

Circuit Description

AF Part:

1. The circuit comprised of Q202, Q203 switch over the level of the stereo signal from JK1/JK4(JK2/JK3)
2. The amplifier IC2 amplifies the signal from step1, and the circuit comprised of IC400, IC402B, DL401 controls the signal's amplitude.
3. The signal from step2 outputs via two ways. One goes to Earphone board, another goes to stereo RF circuit.
4. On the Earphone board, C203 adjust the signal's volume and then send the signal to amplifier circuit. After amplified, the signal is outputted via JK1.
5. On the RF board, the signal compressed and filtered by the circuit comprised of C204, IC4A, IC3B, IC5A, IC6A and IC7A. Then C112 couple the signal to IC8 for stereo coding.

RF Part:

1. The coded stereo signal is processed Frequency Modulation by PLL circuit comprised of VD2, Q10, Q2, Q9, IC12, IC13.
2. The modulated signal is amplified by Q1 and IC11, then filtered by the circuit comprised of L18, L19, L2, C68, L1, C66. After that, the signal is transmitted to the space via the antenna.

Utilized OSC

IC8 utilizes 38kHz OSC

IC12 utilizes 4MHz OSC

IC303 utilizes 4MHz OSC