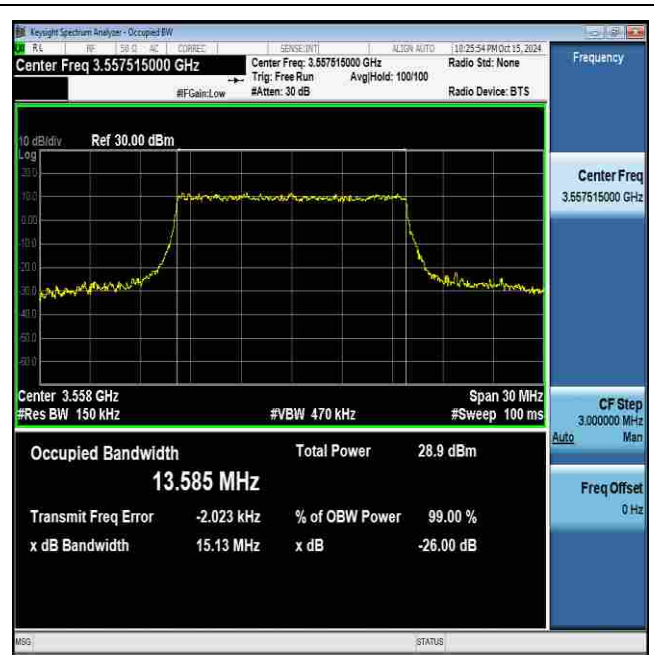
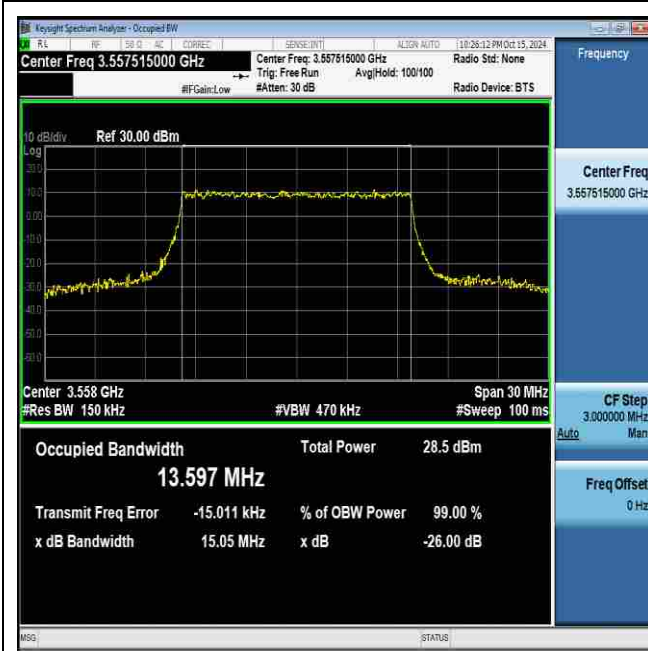


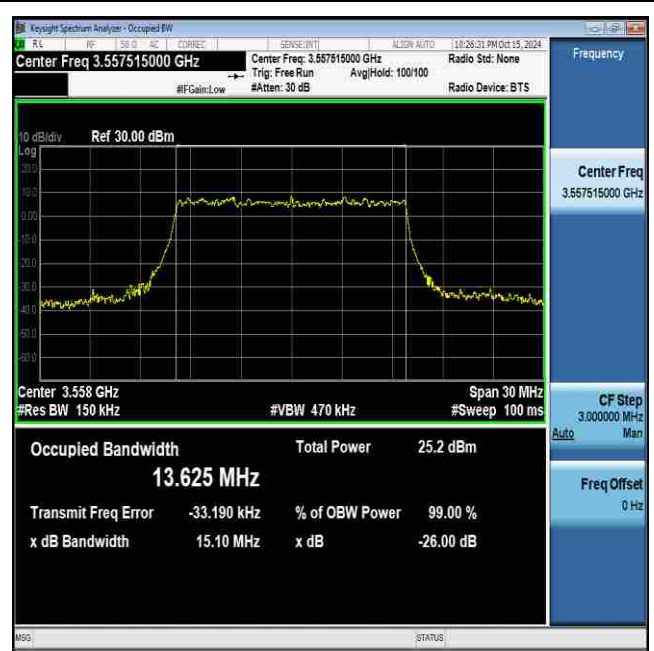
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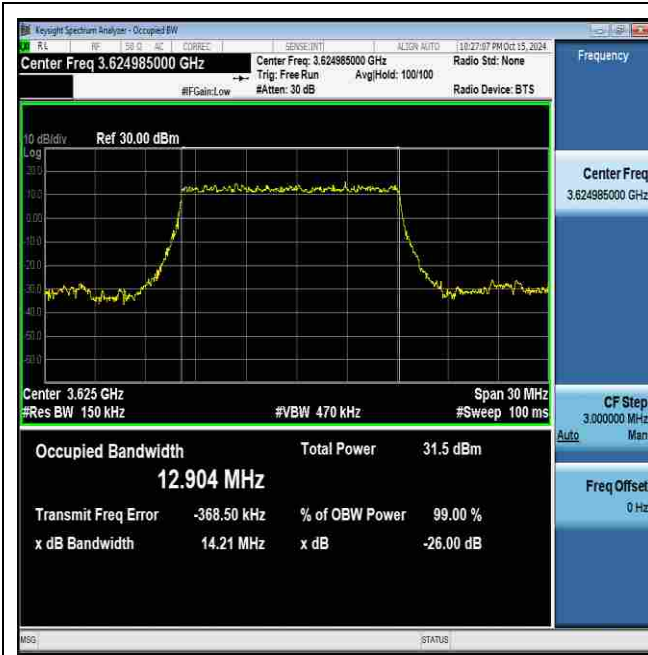
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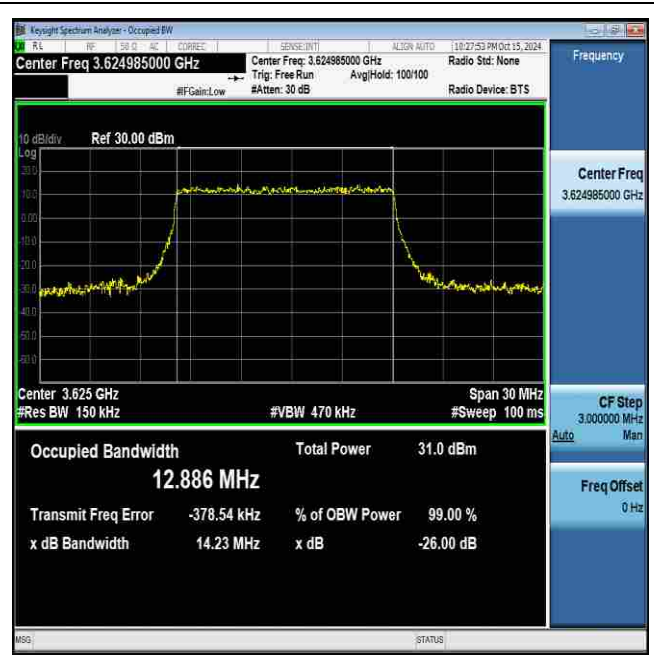
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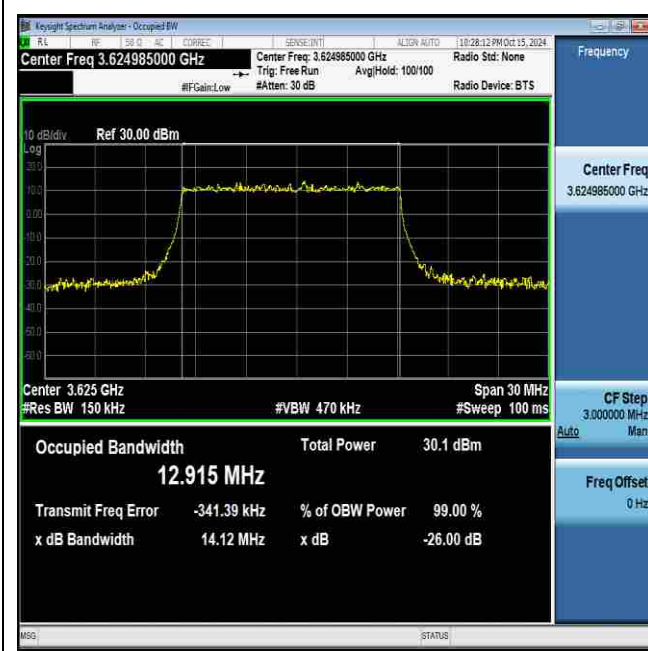
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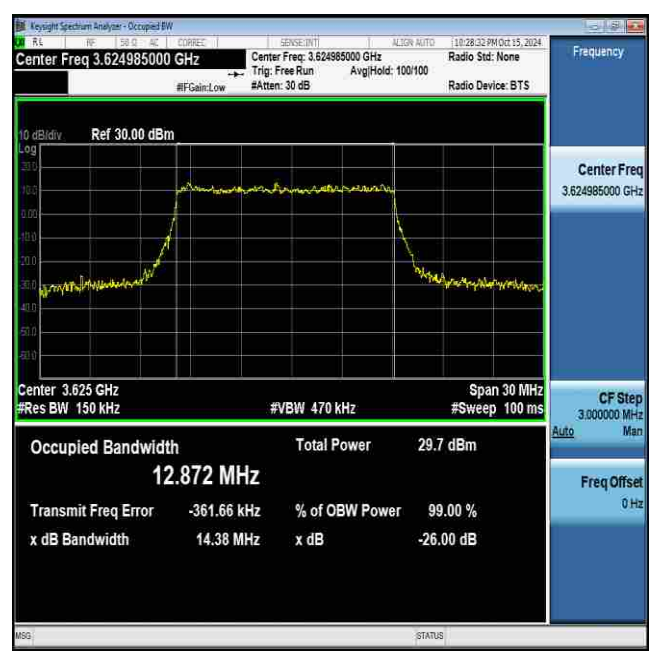
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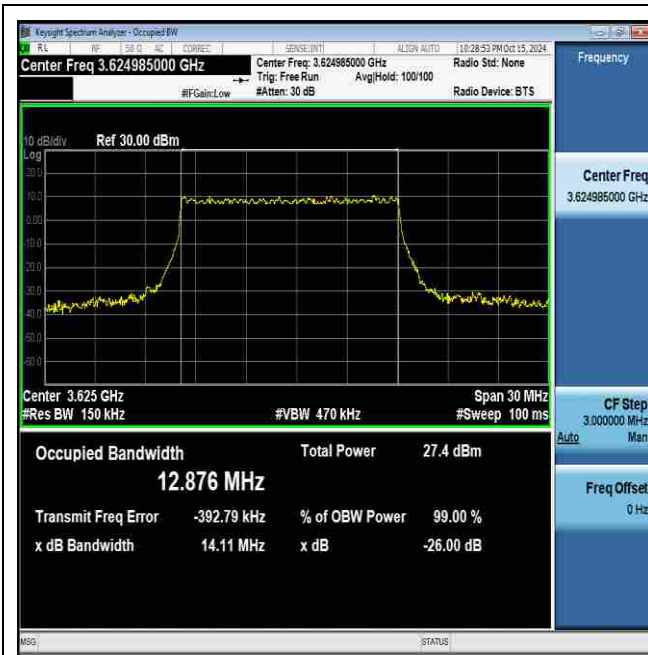
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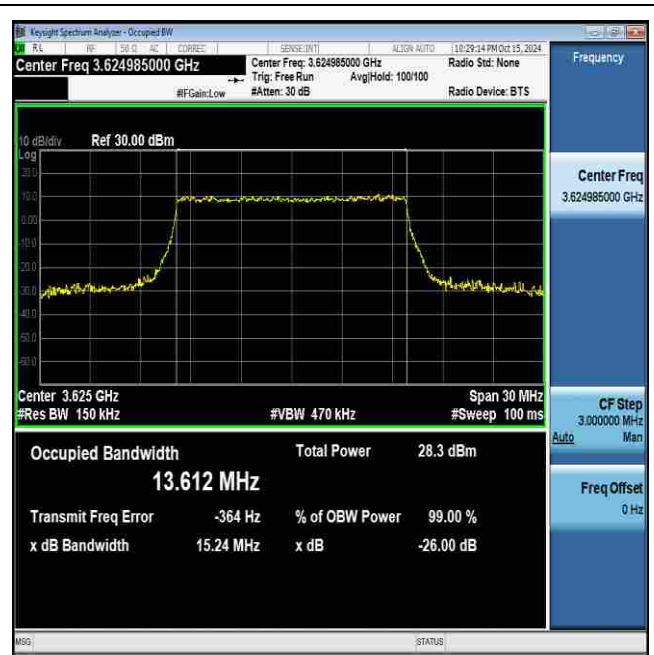
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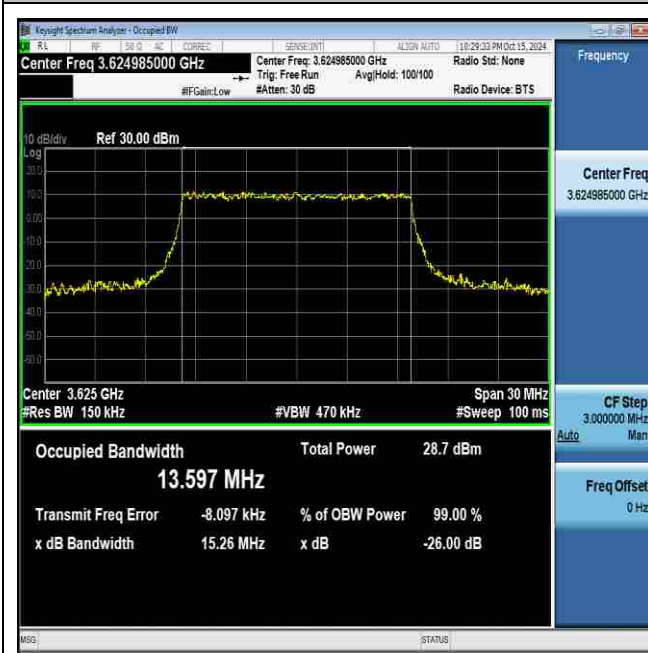
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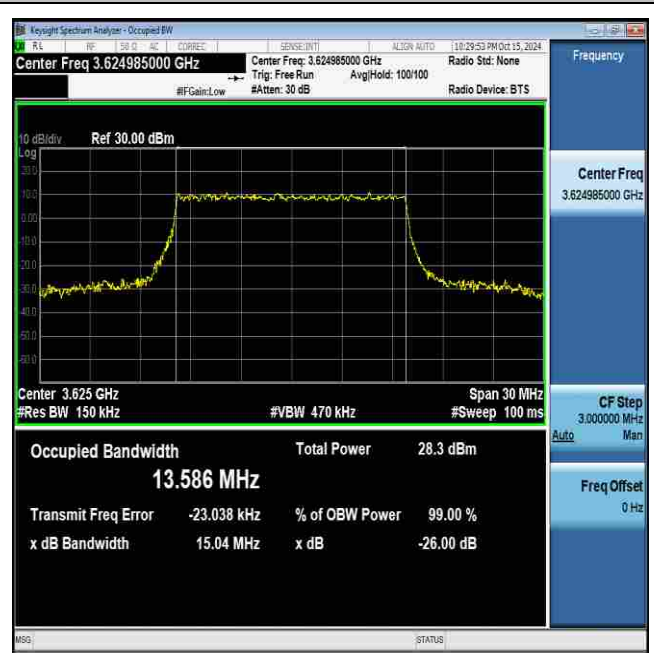
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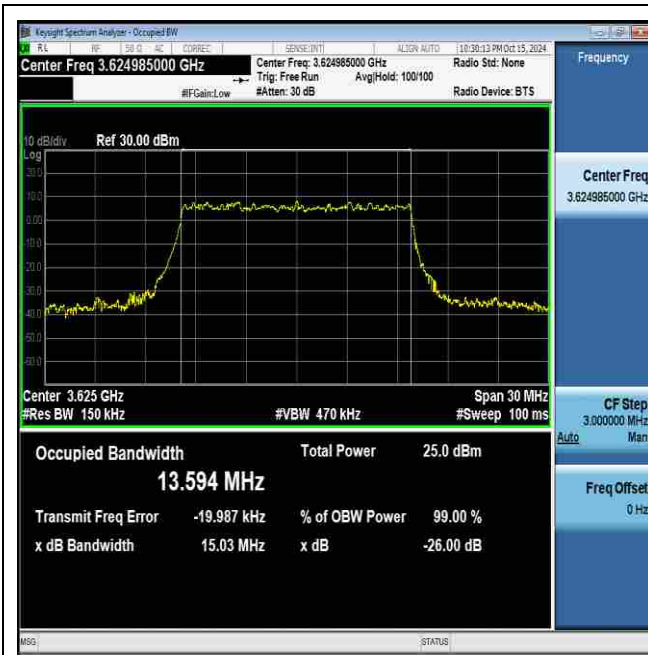
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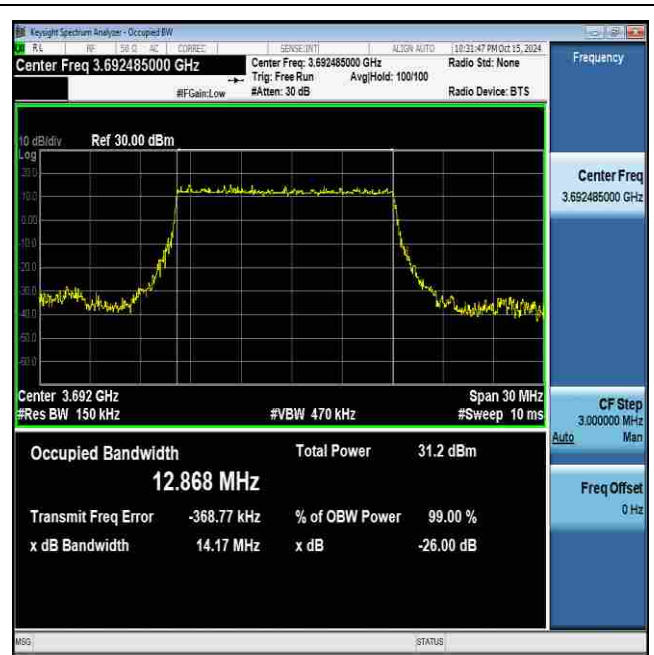
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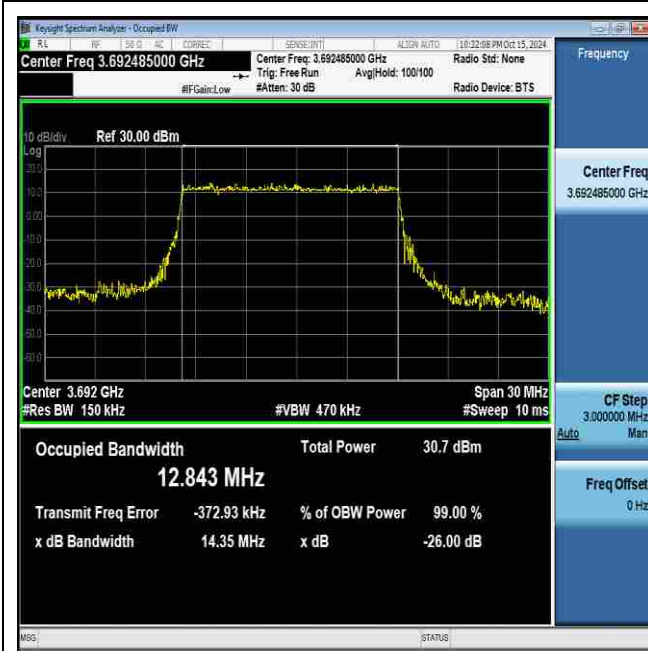
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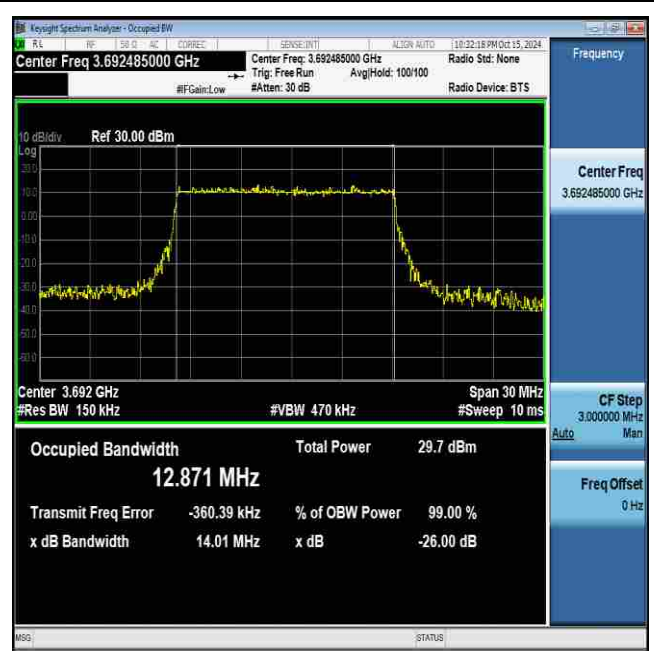
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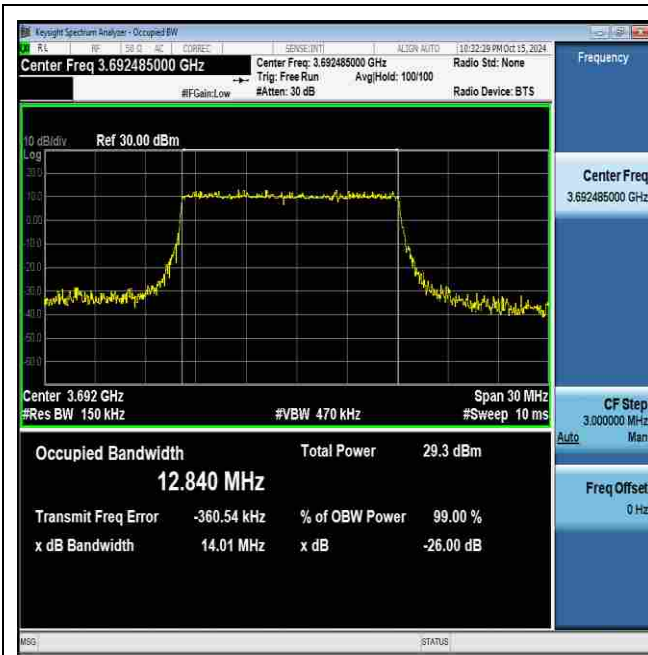
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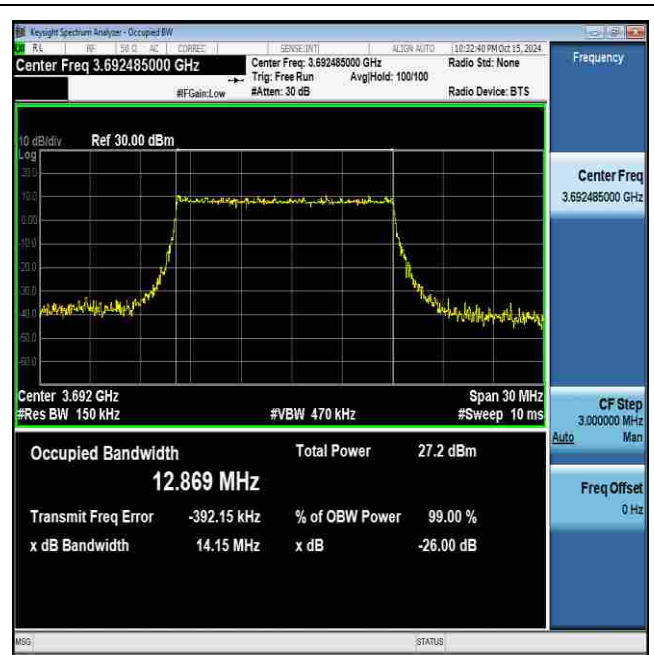
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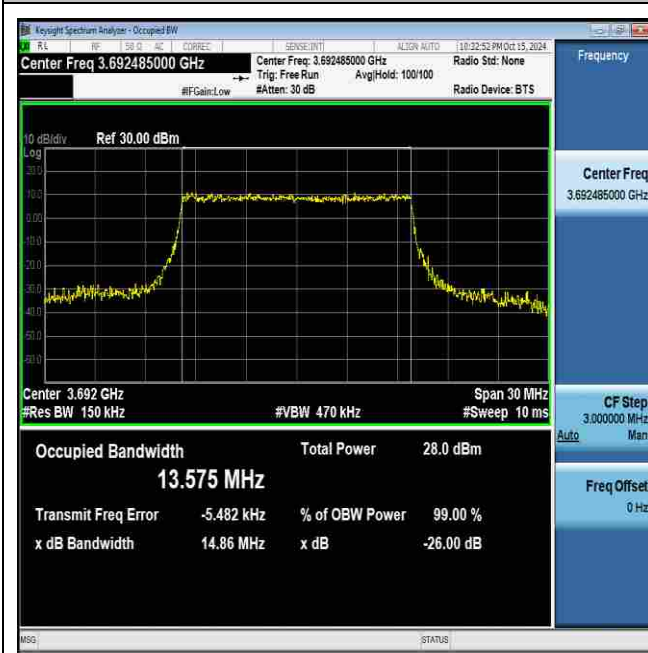
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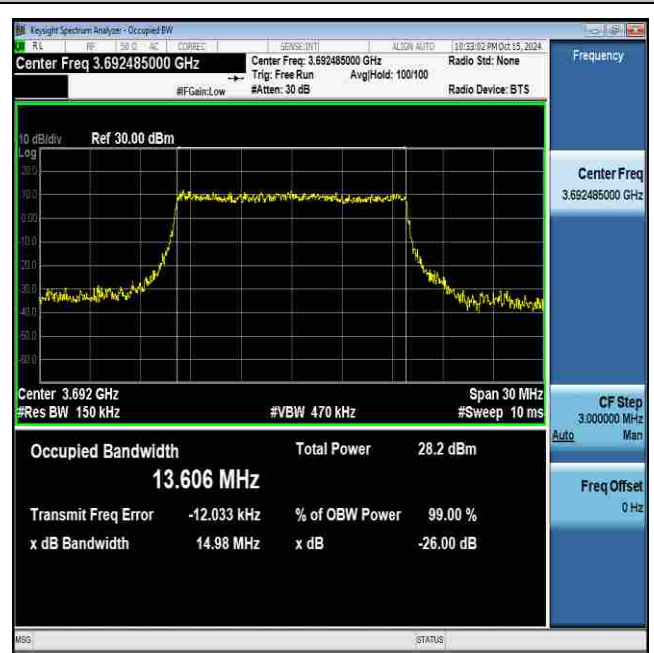
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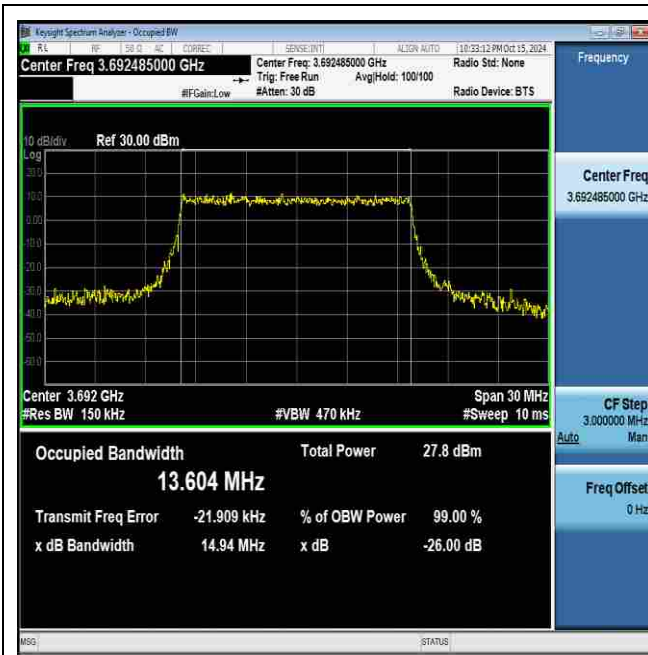
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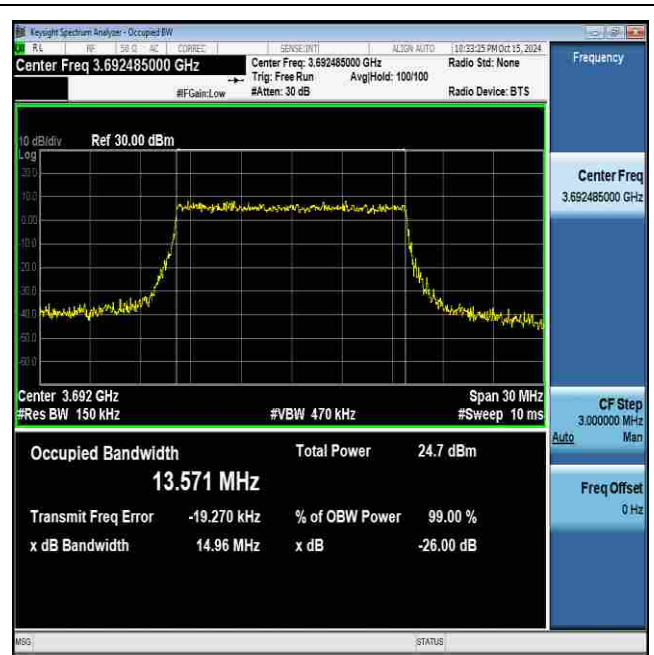
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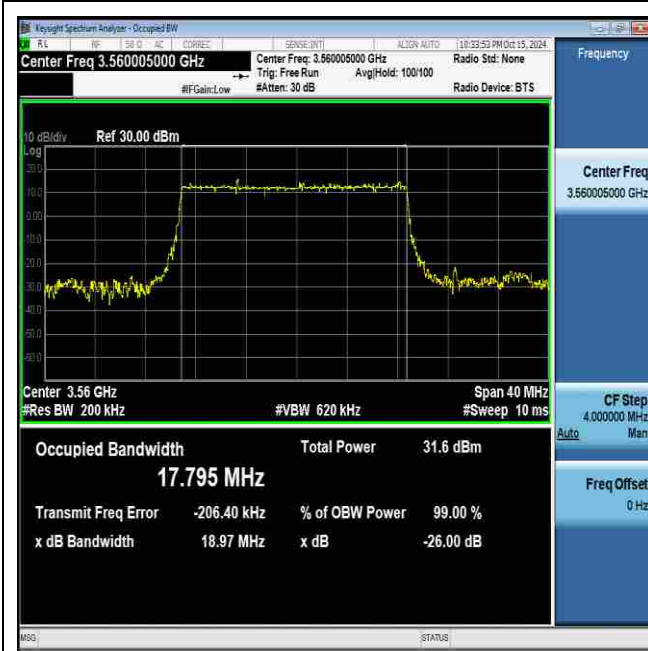
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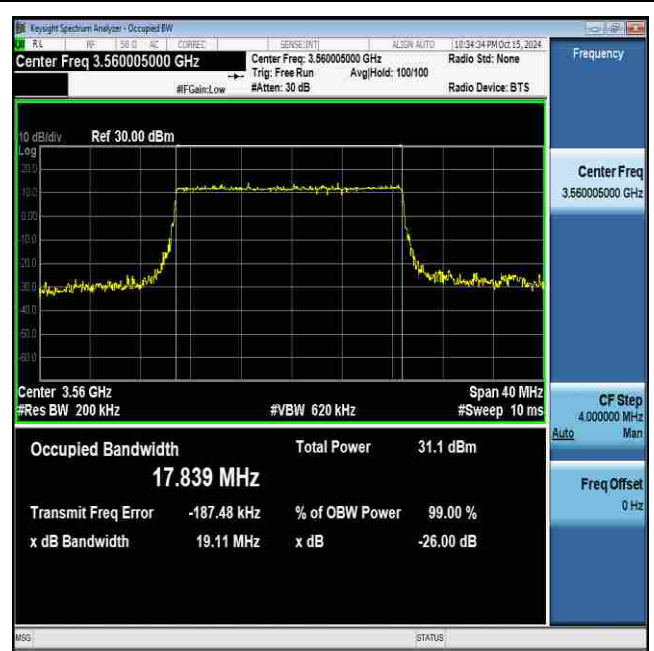
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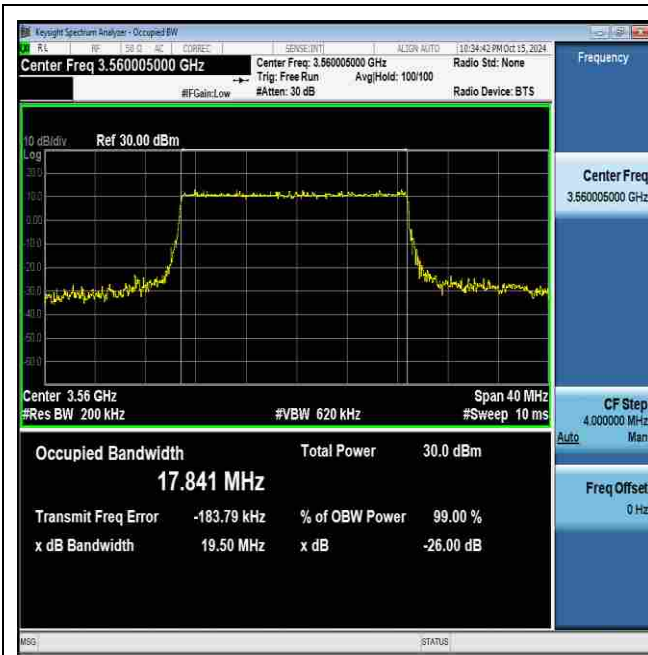
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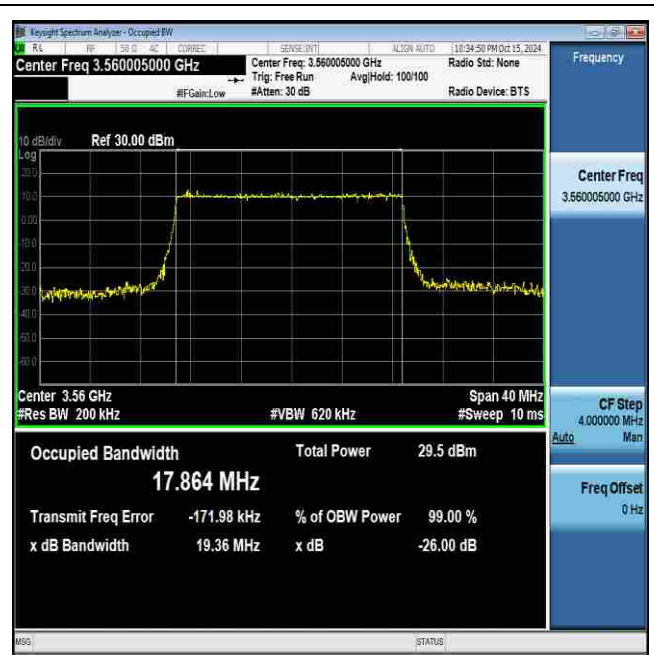
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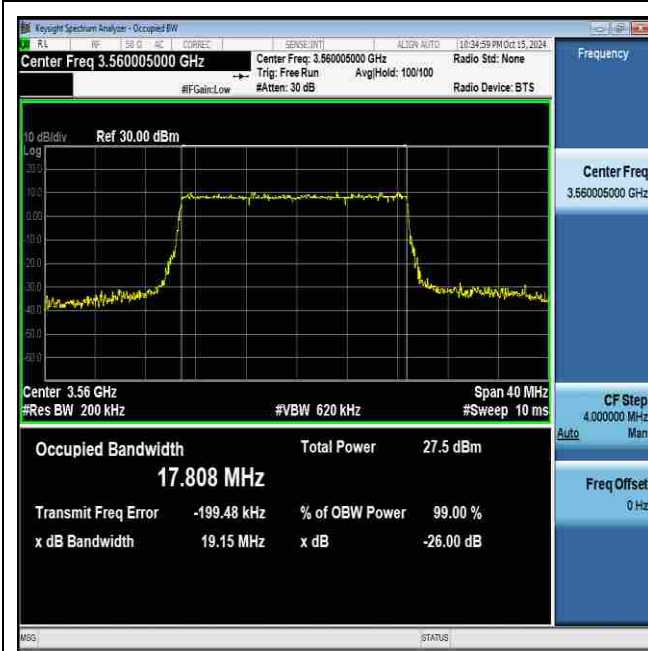
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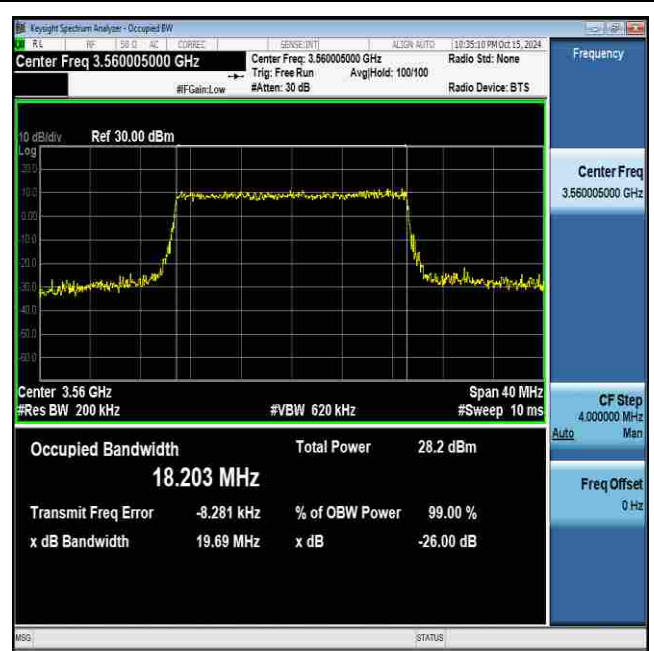
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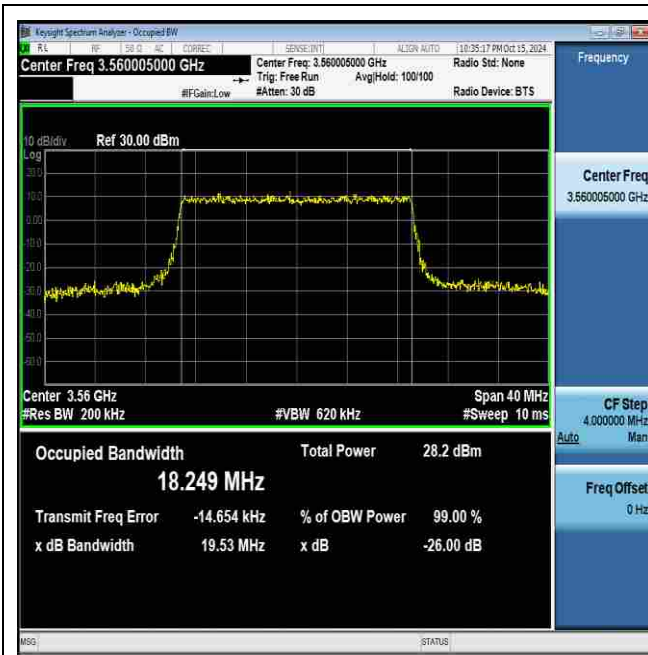
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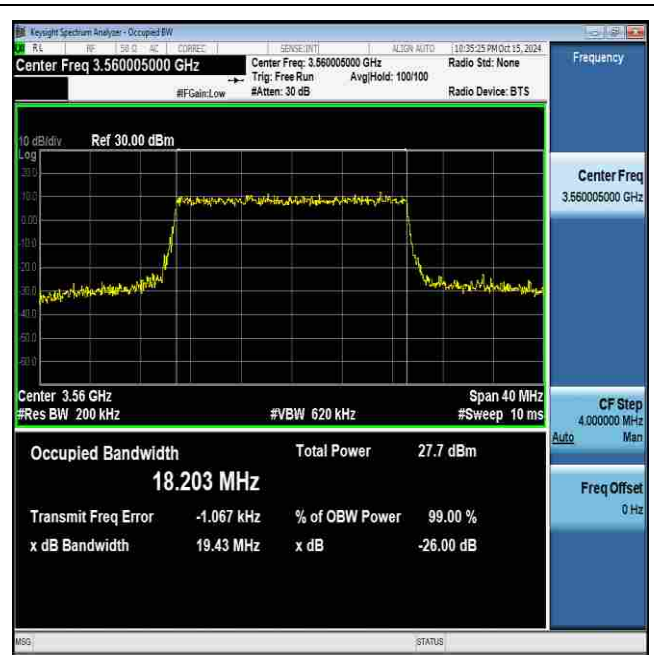
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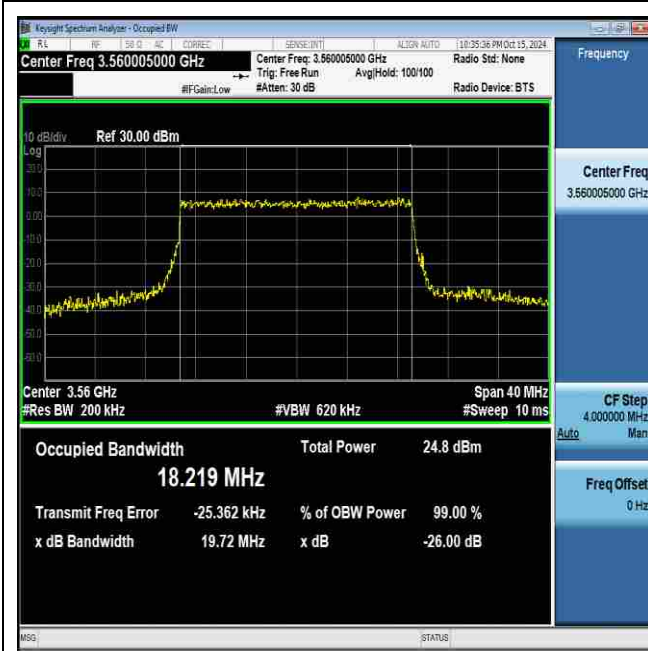
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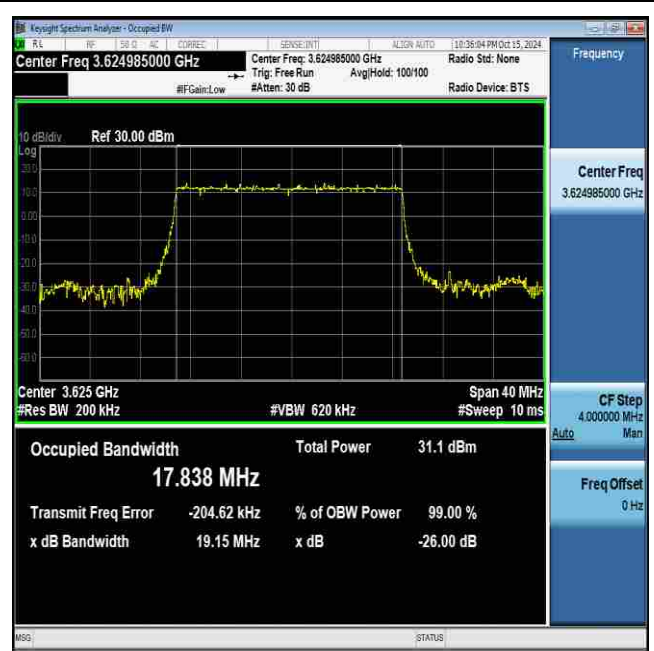
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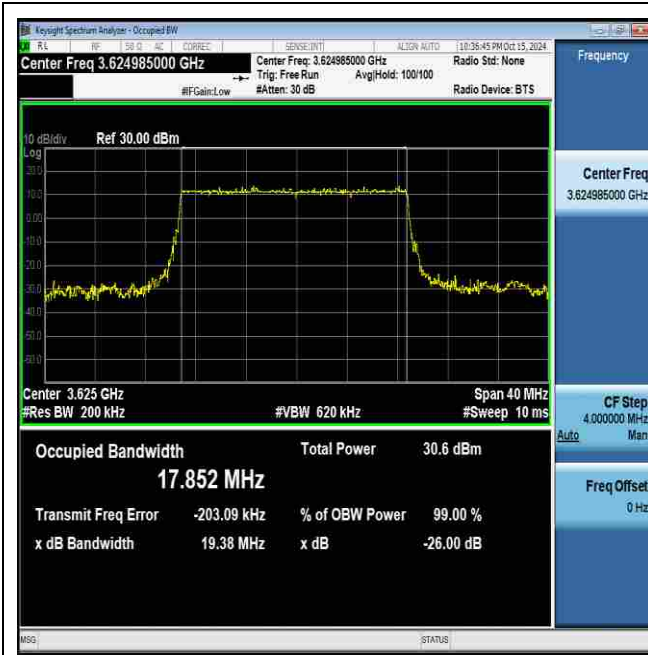
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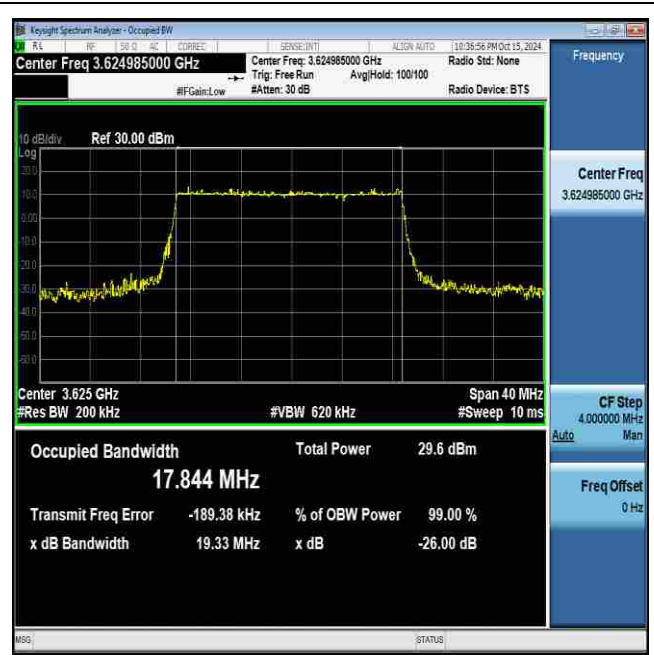
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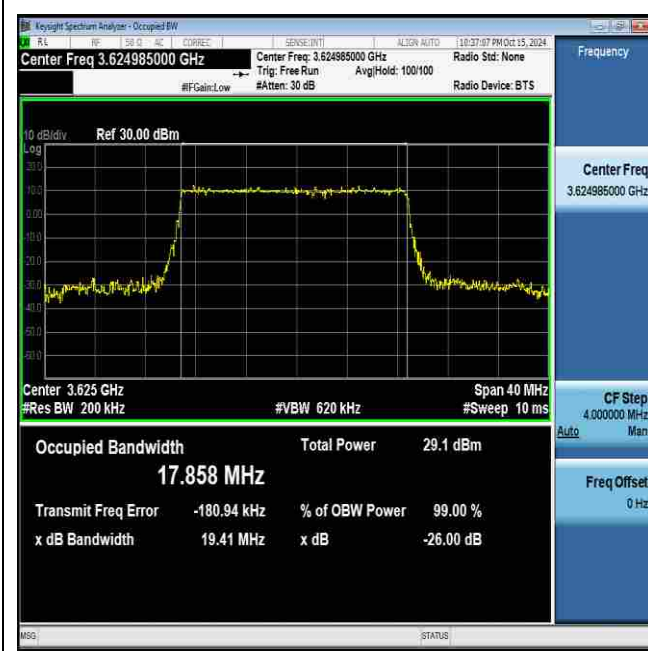
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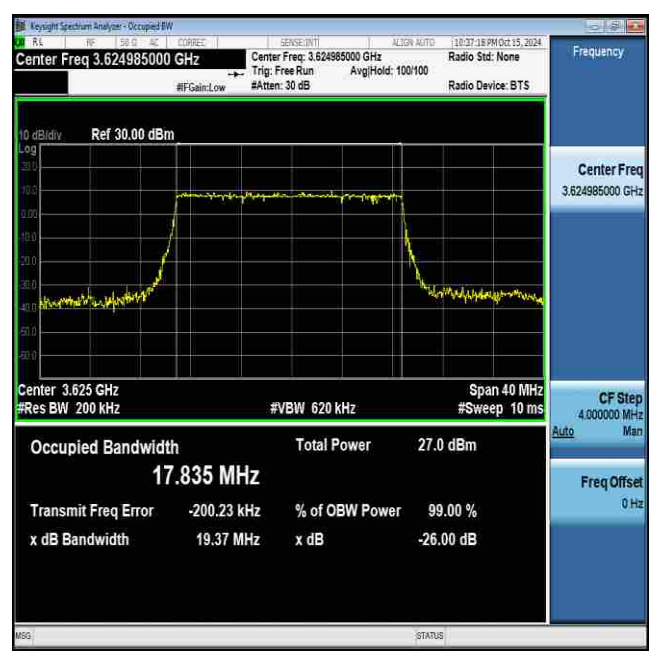
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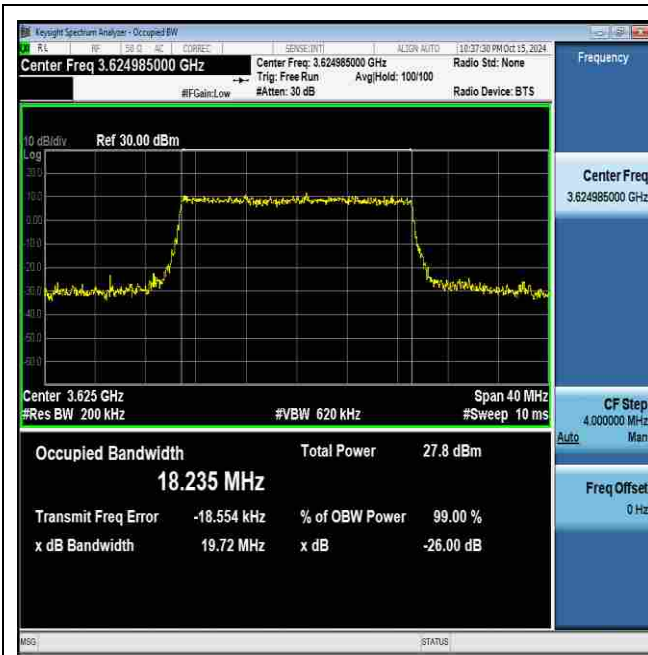
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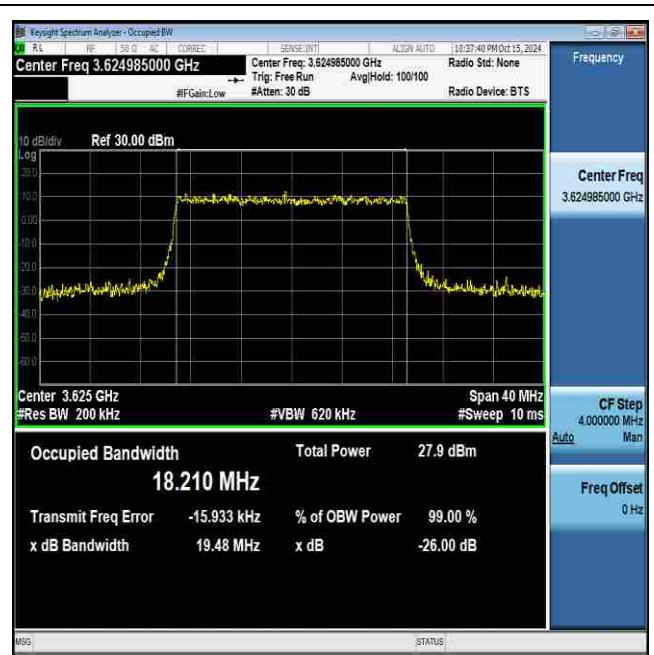
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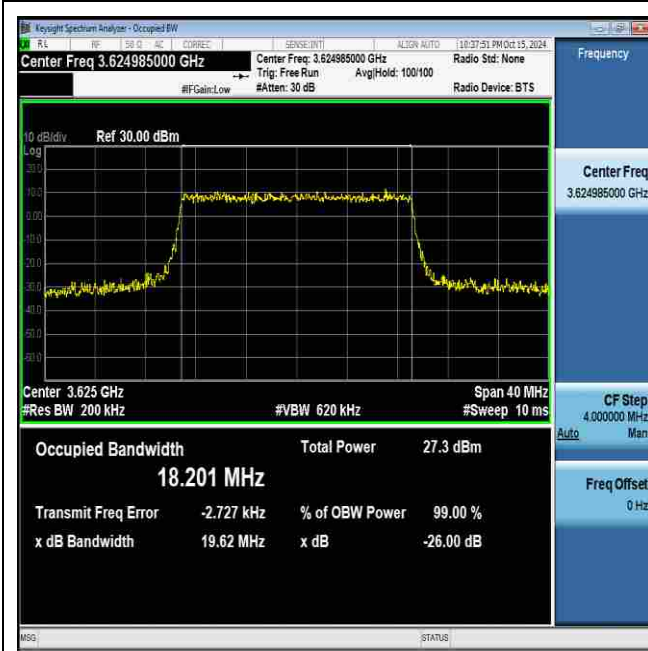
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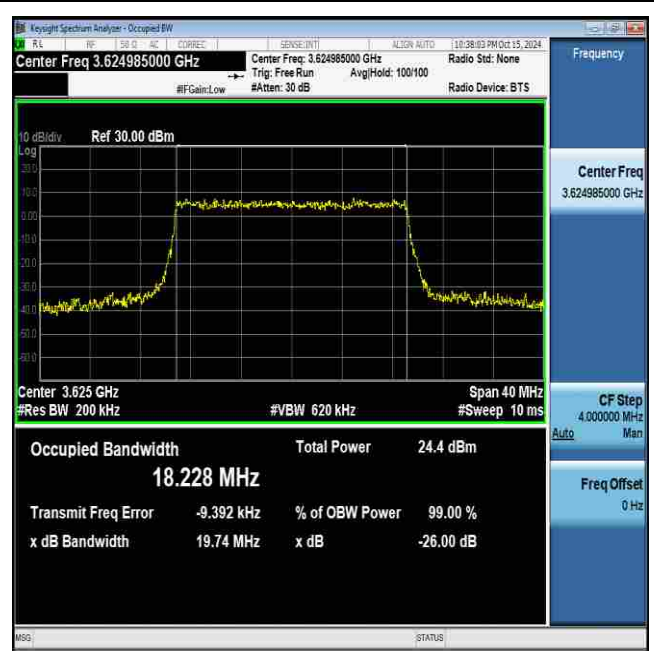
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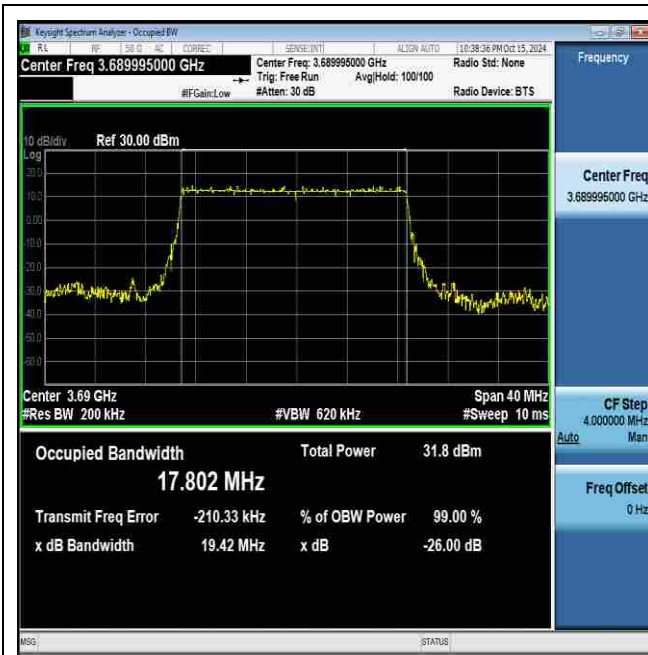
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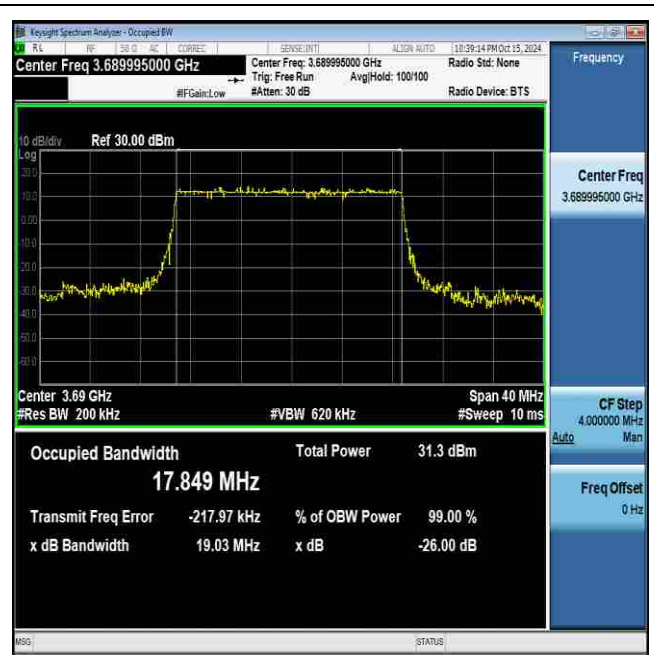
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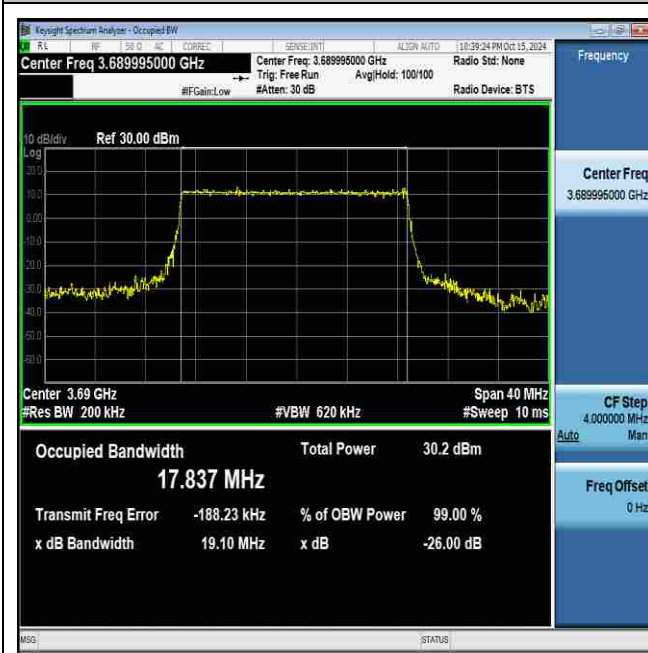
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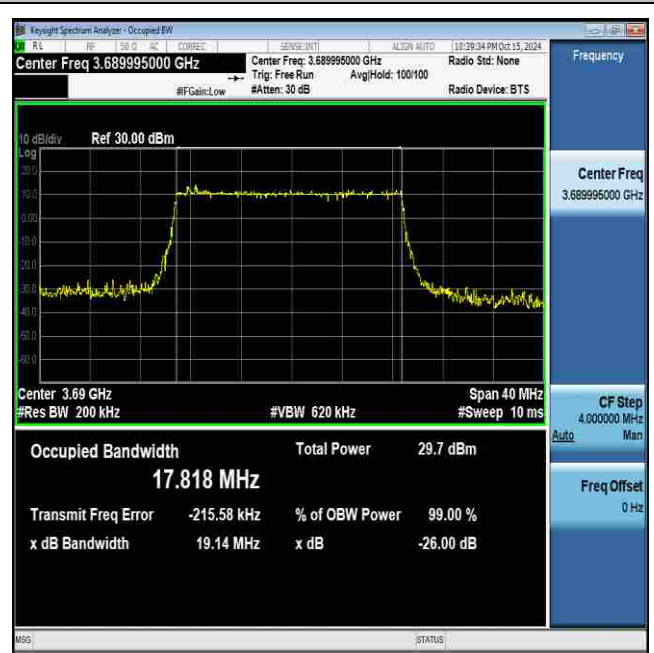
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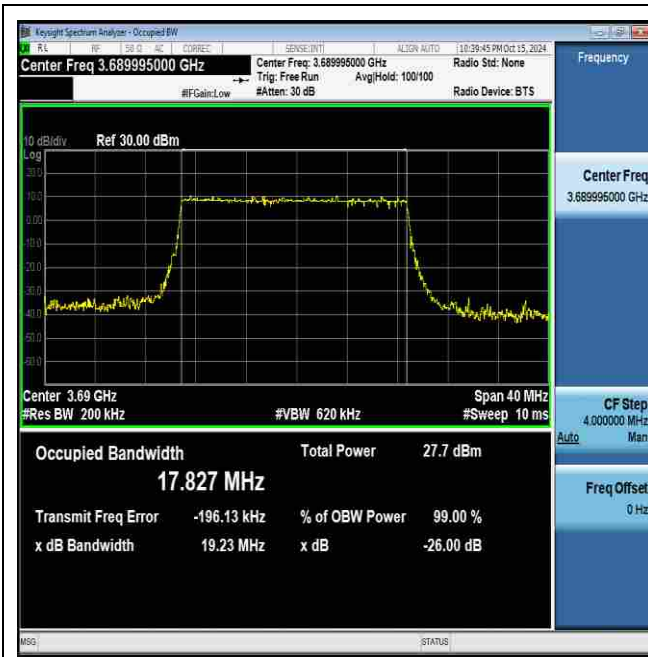
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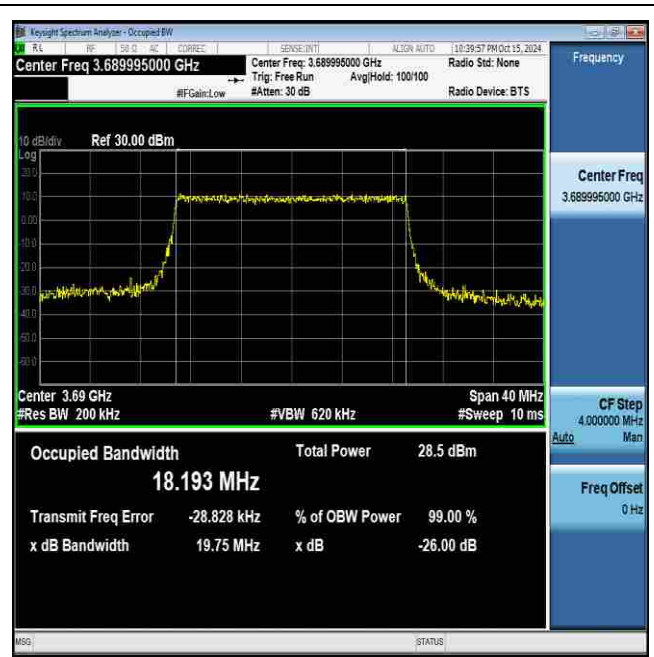
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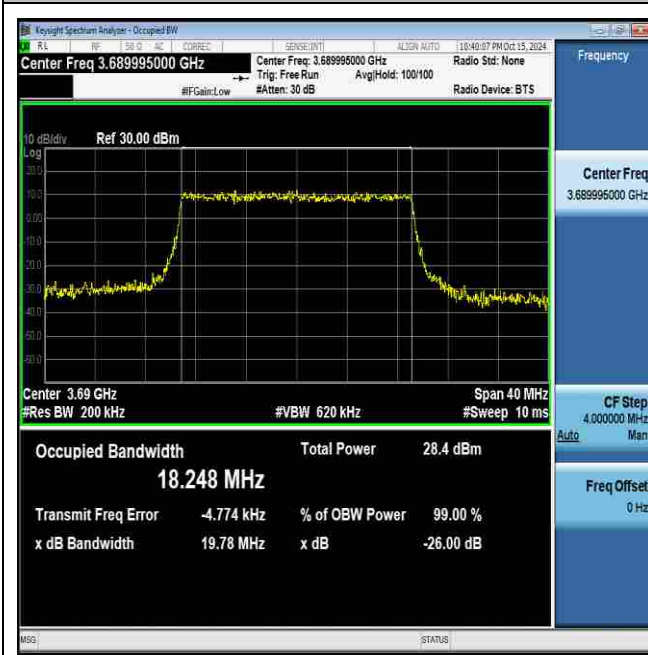
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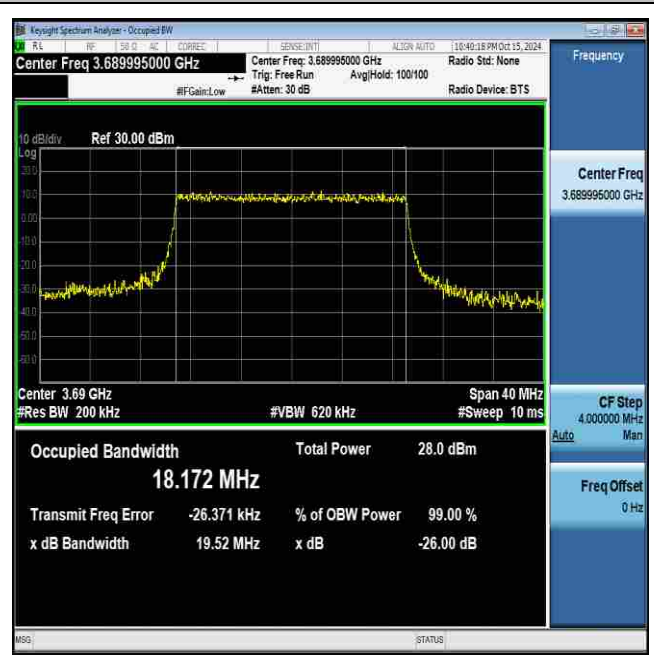
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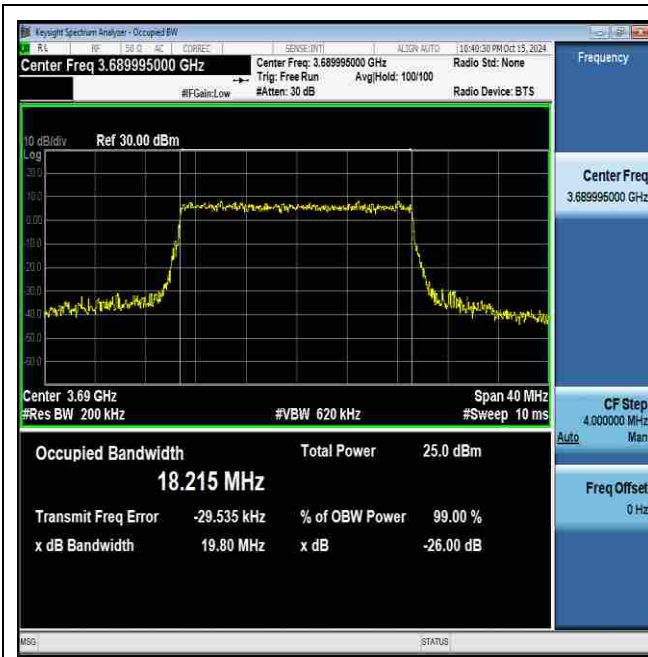
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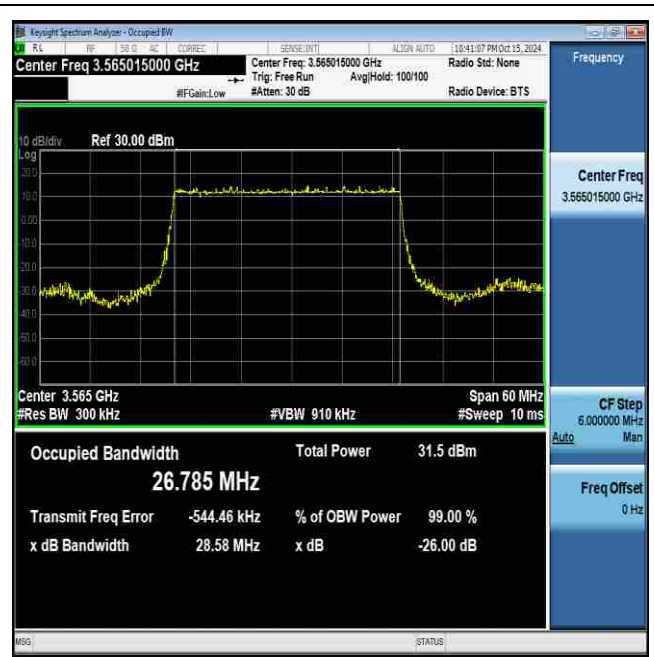
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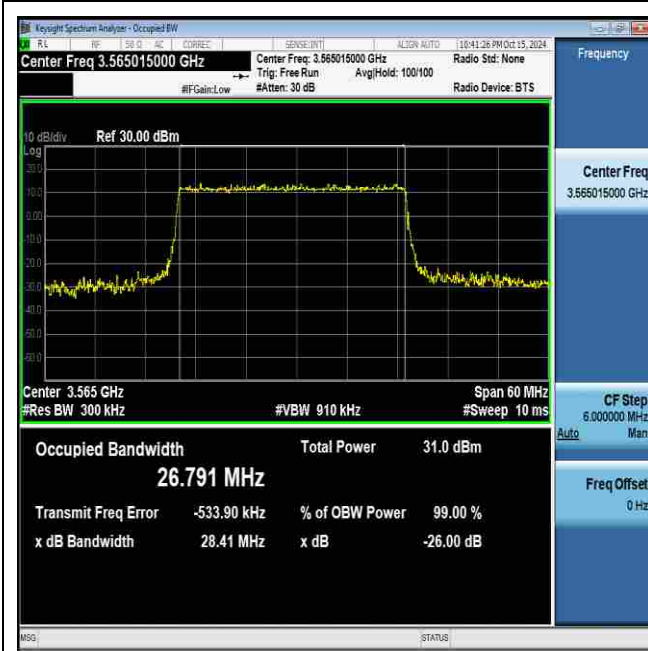
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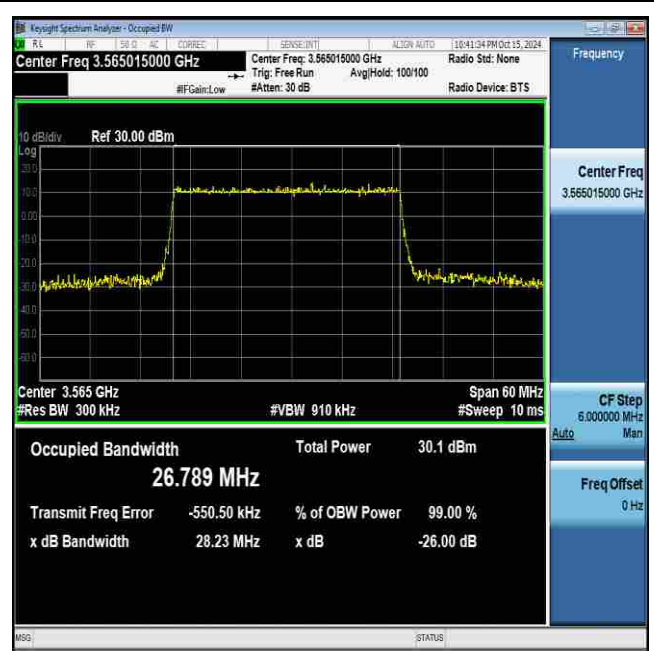
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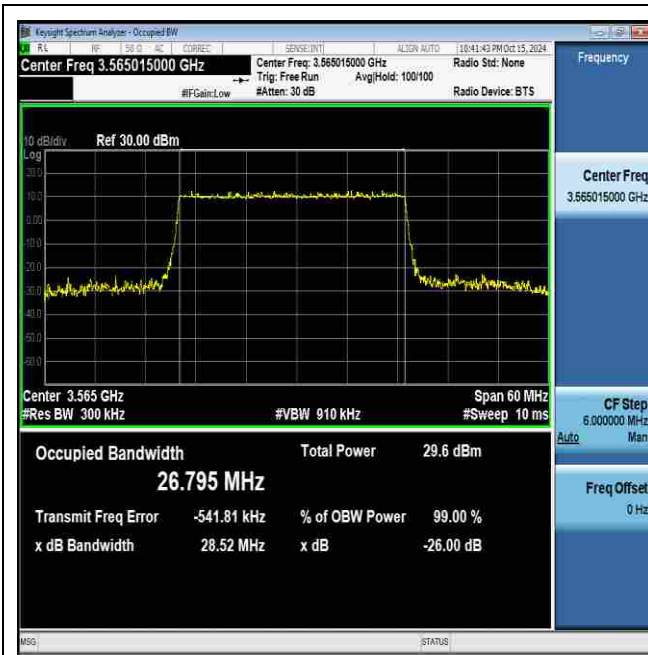
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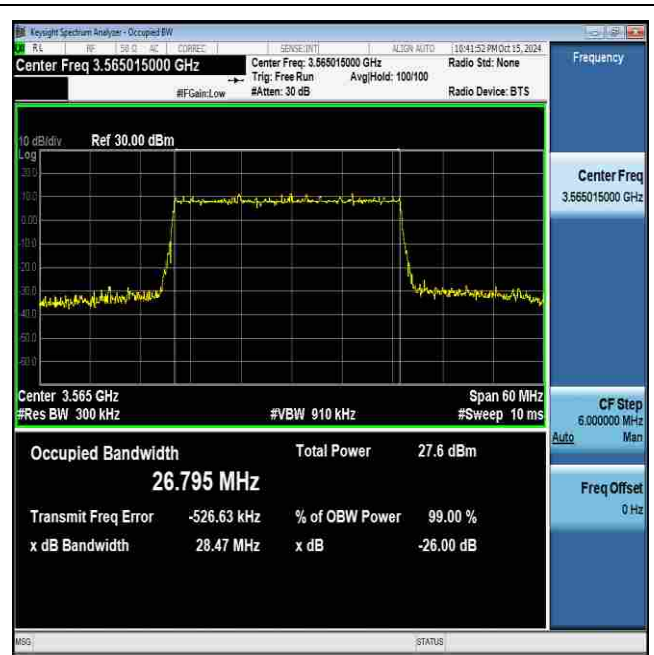
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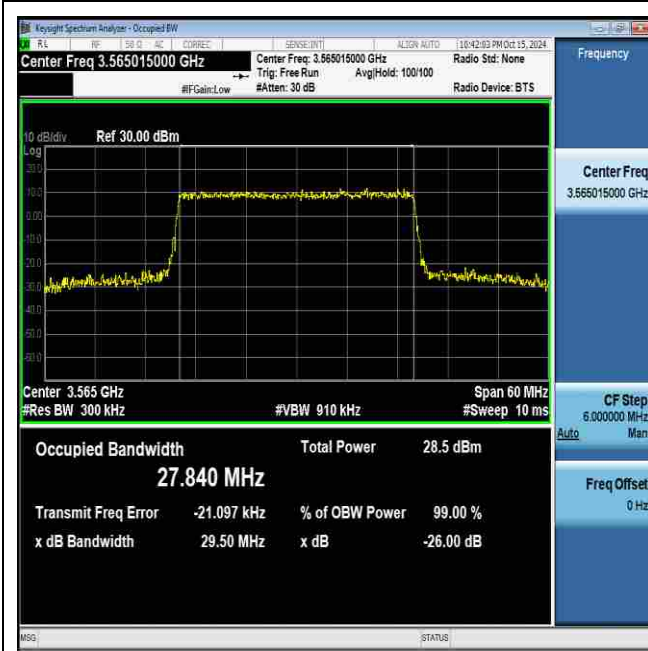
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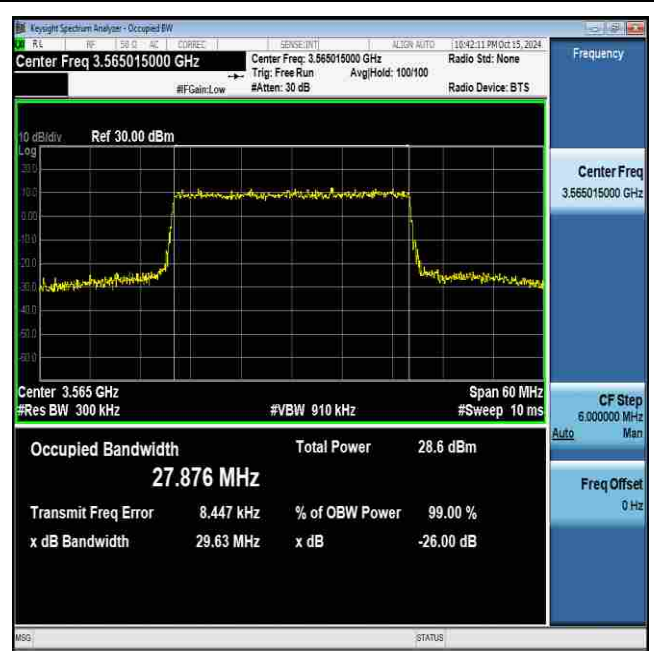
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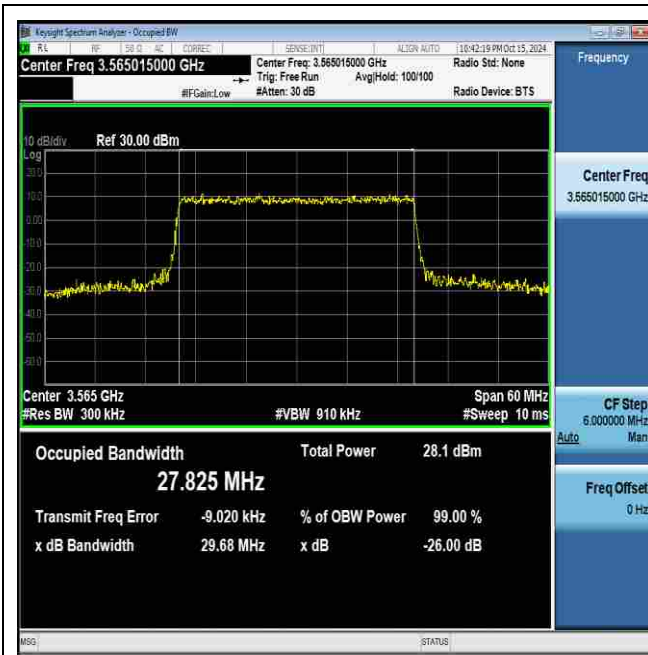
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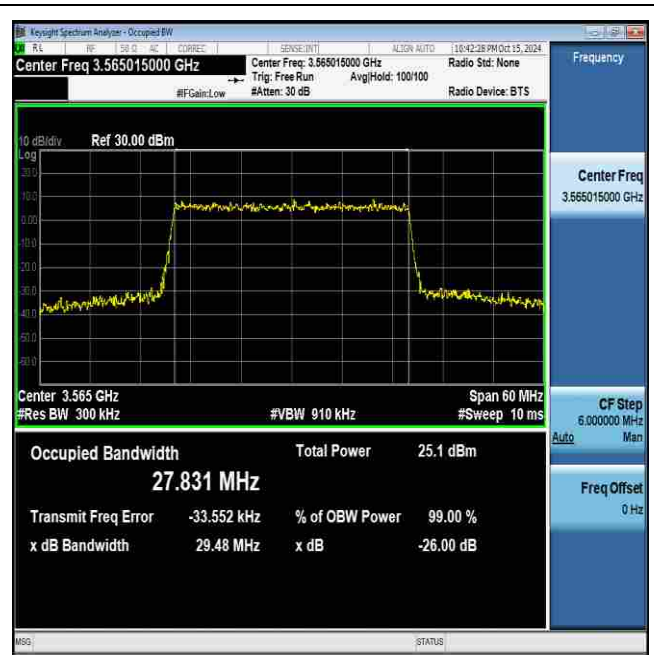
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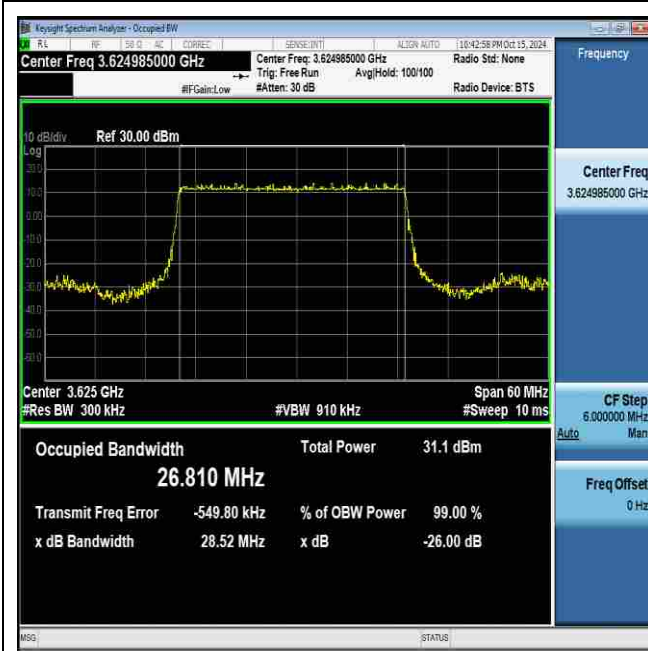
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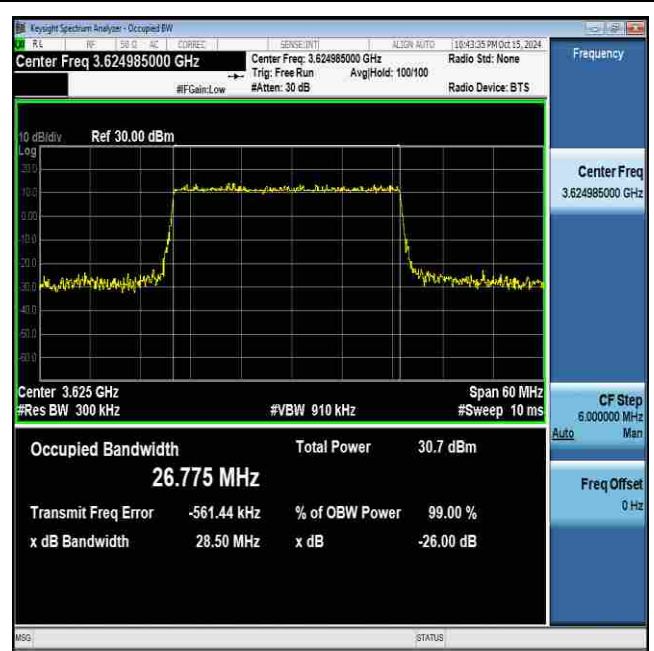
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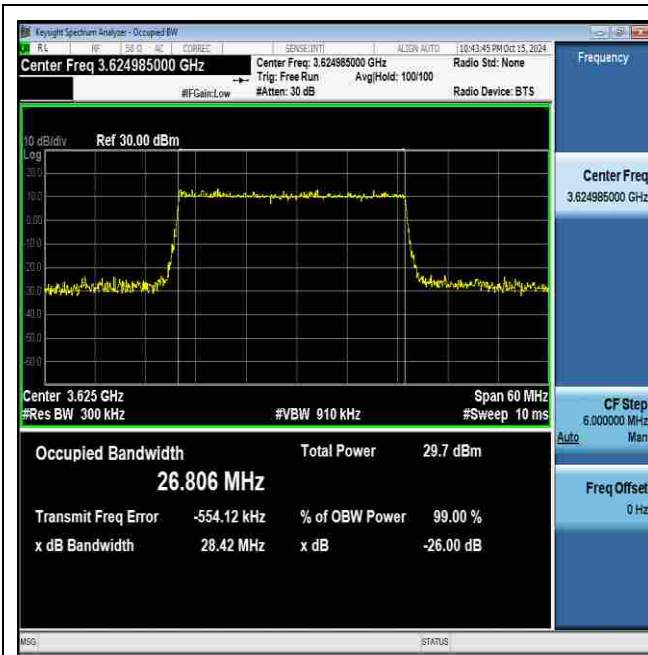
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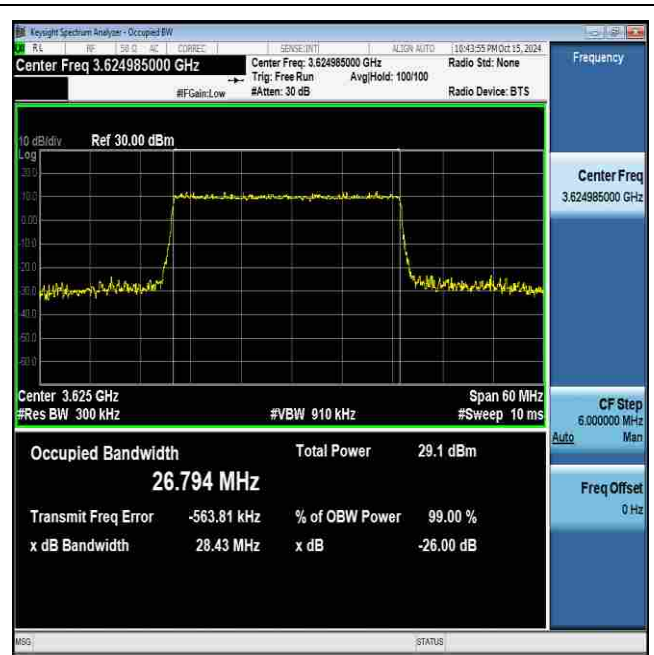
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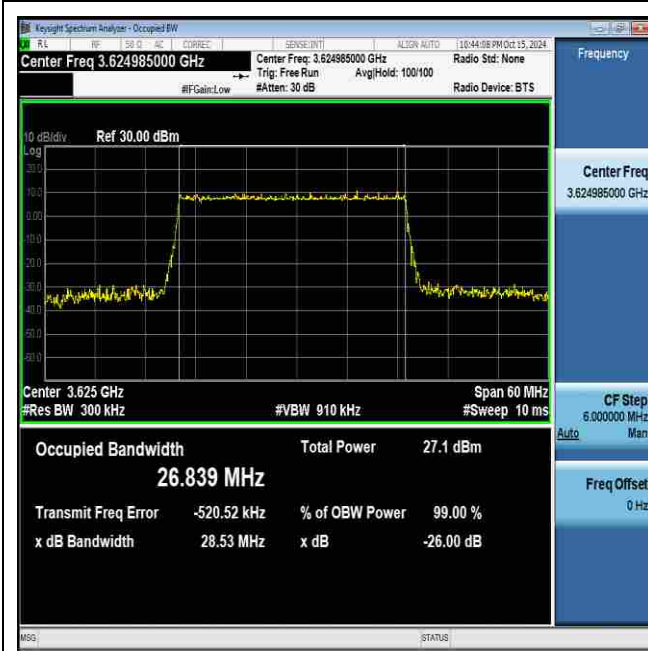
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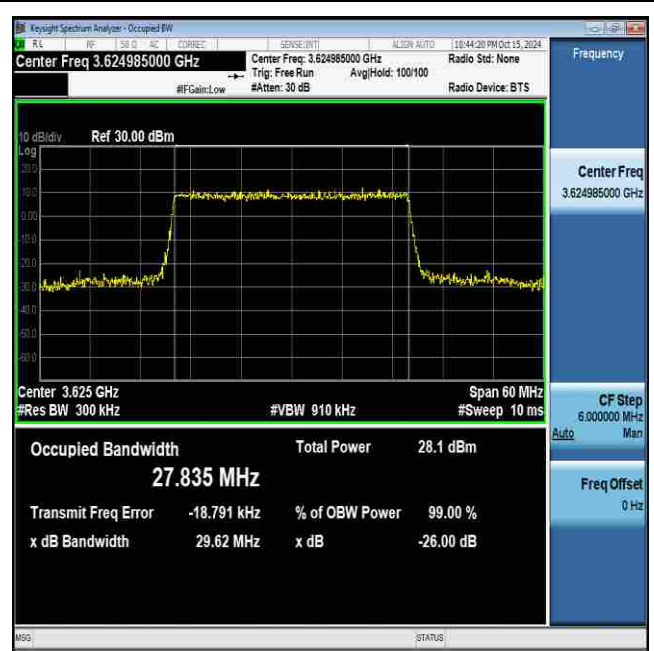
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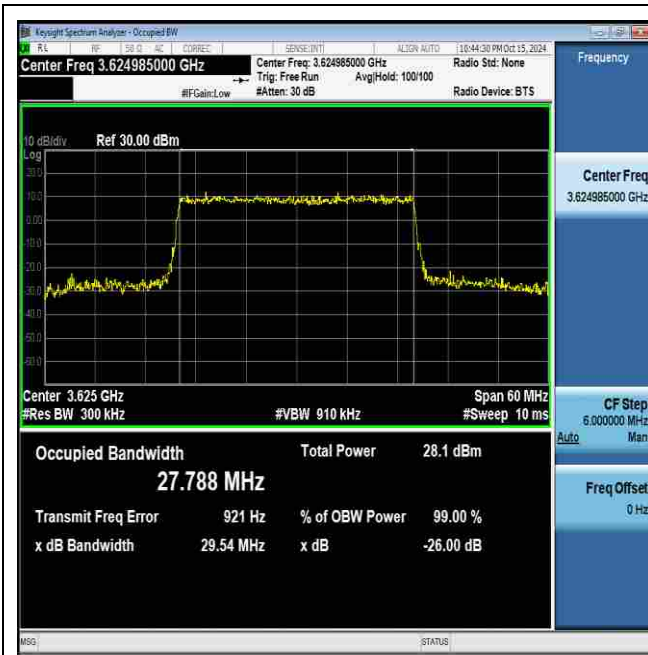
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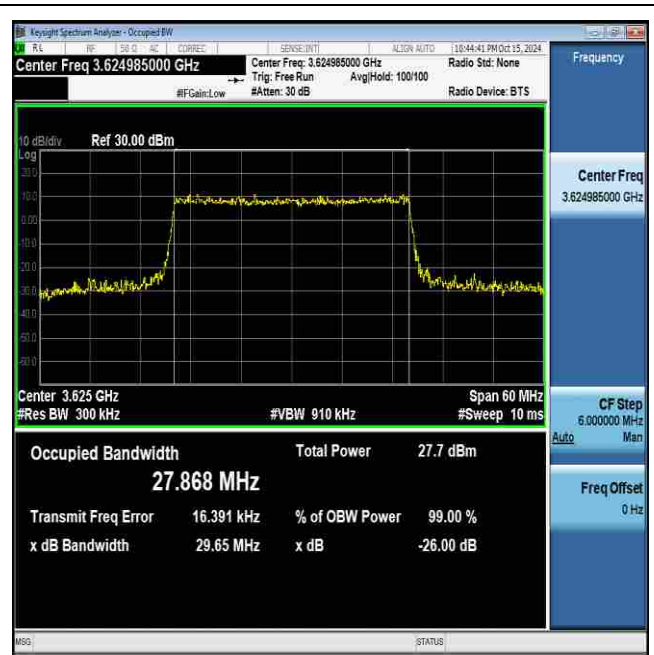
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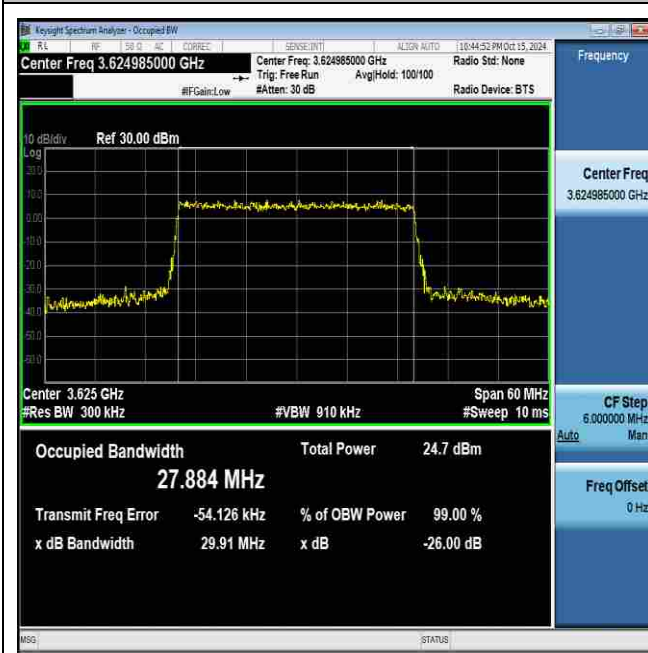
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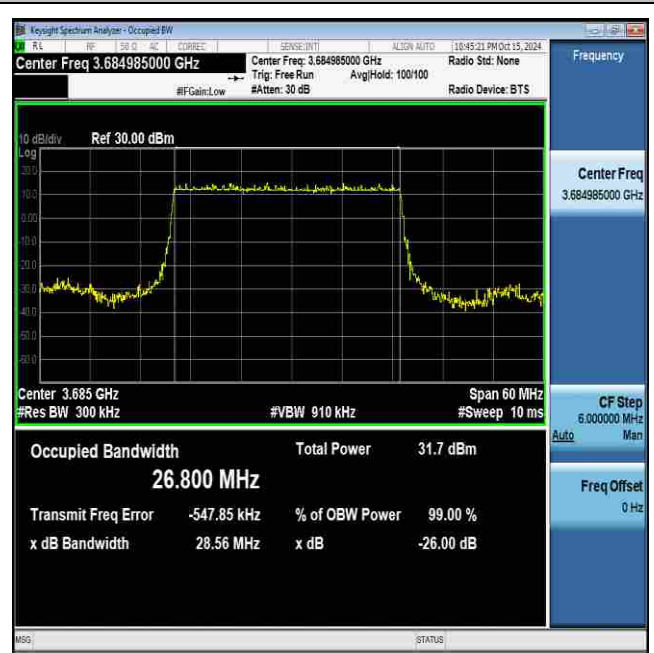
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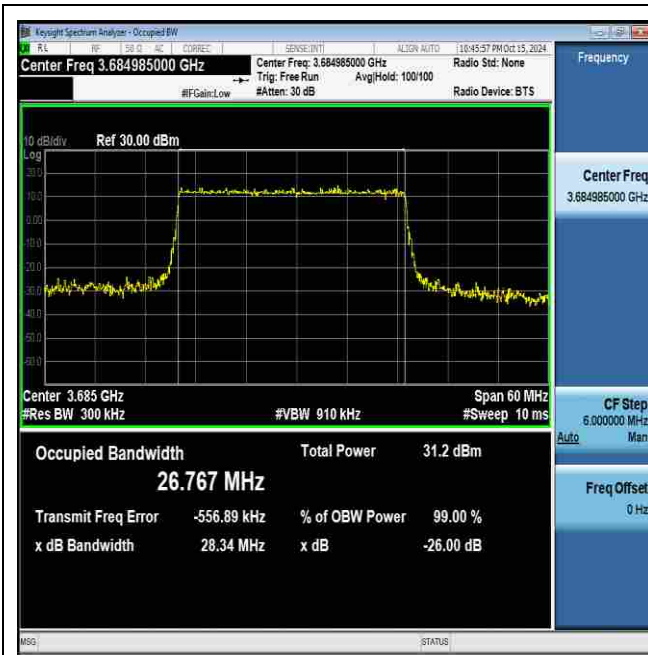
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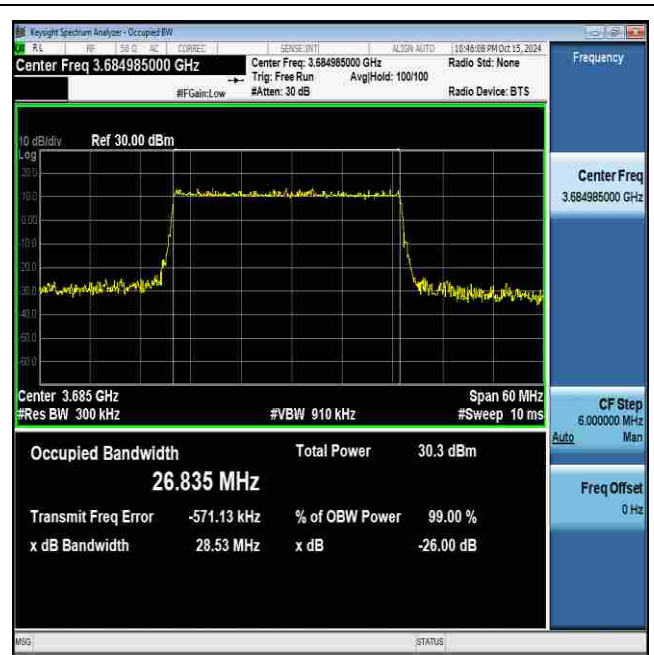
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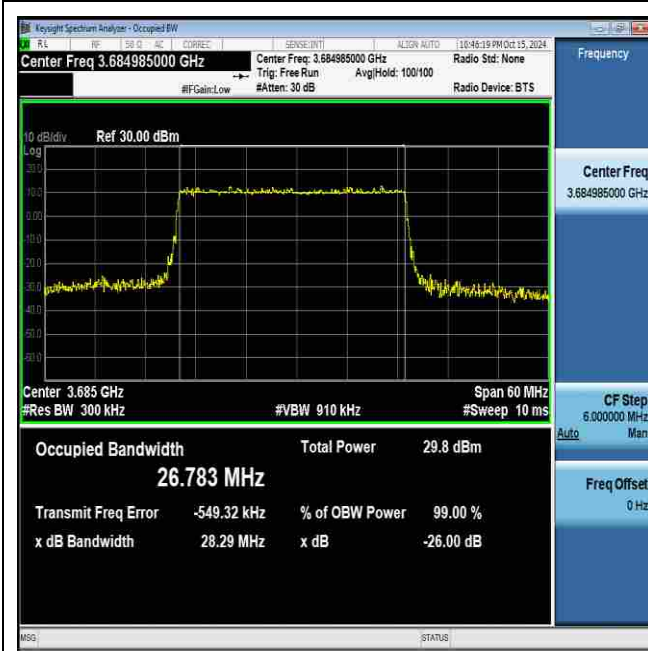
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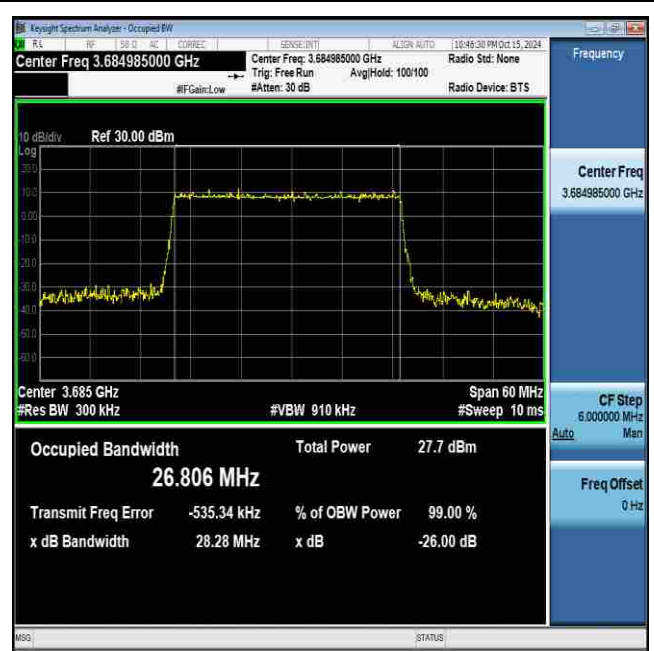
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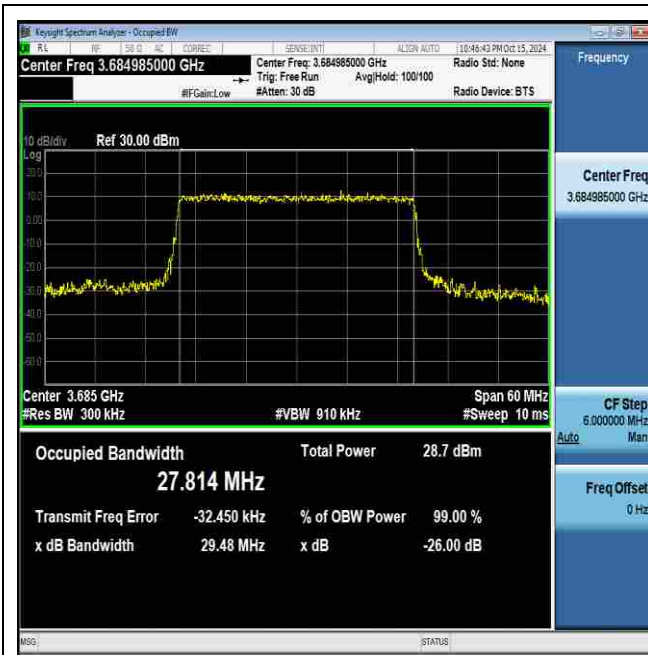
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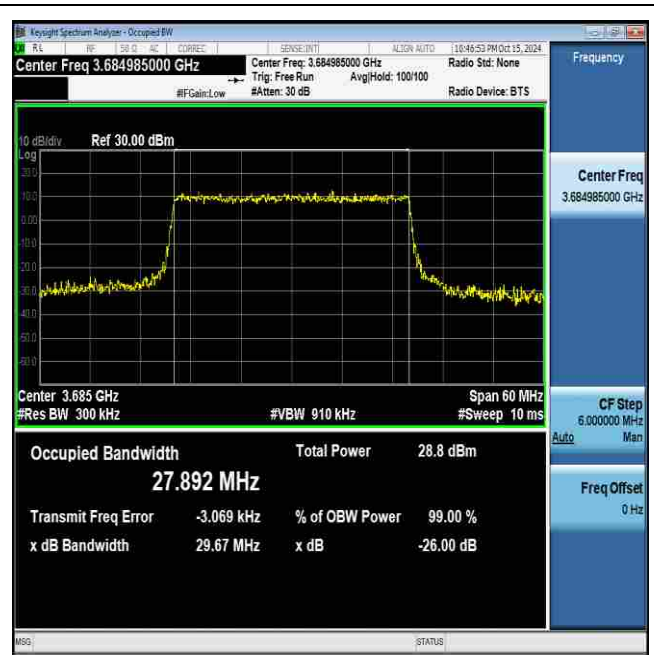
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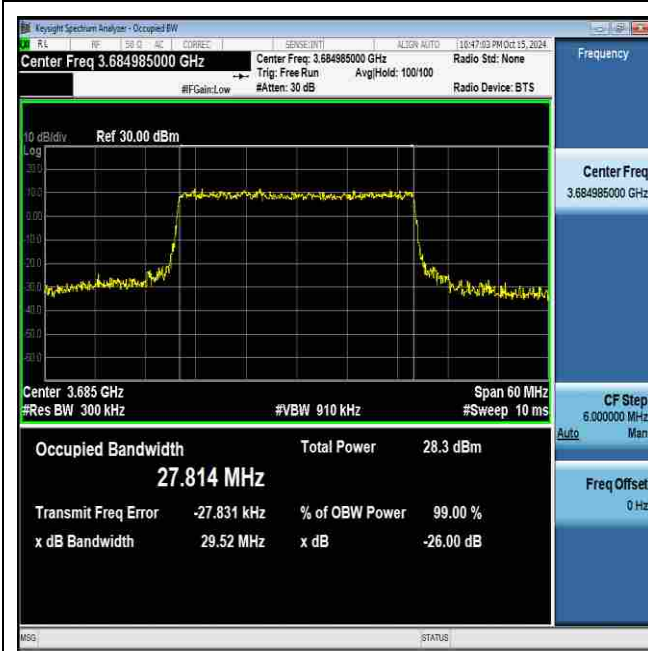
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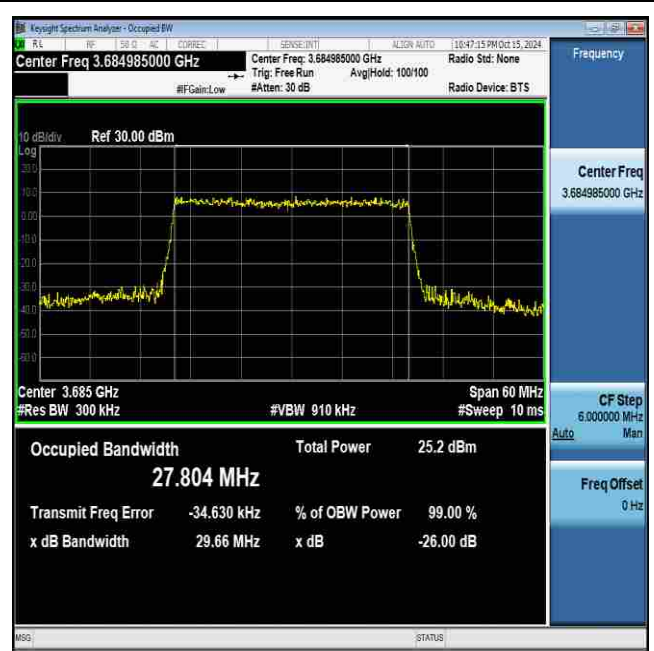
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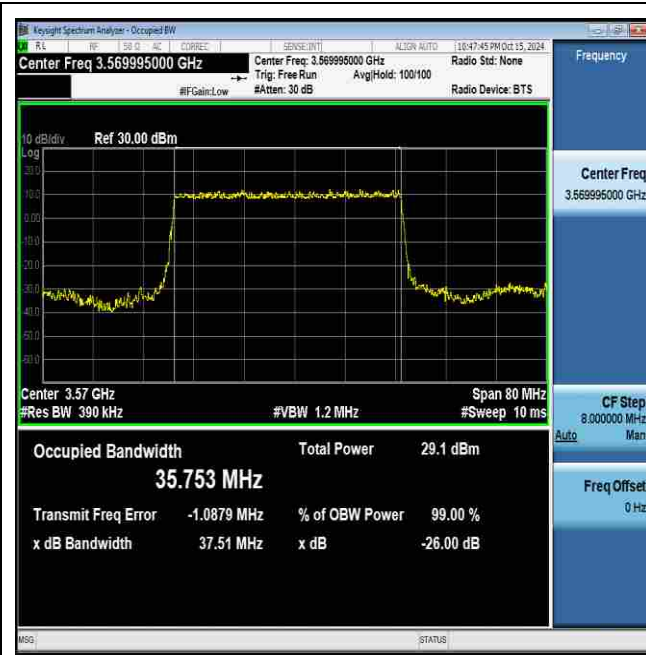
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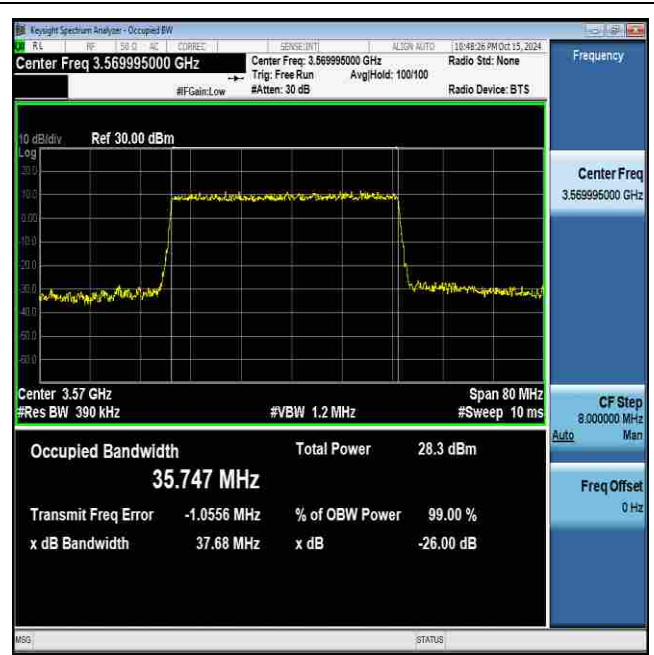
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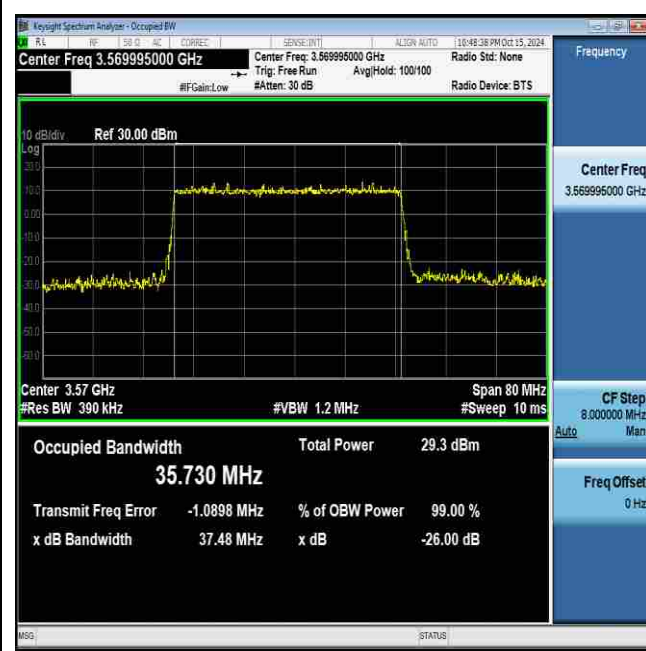
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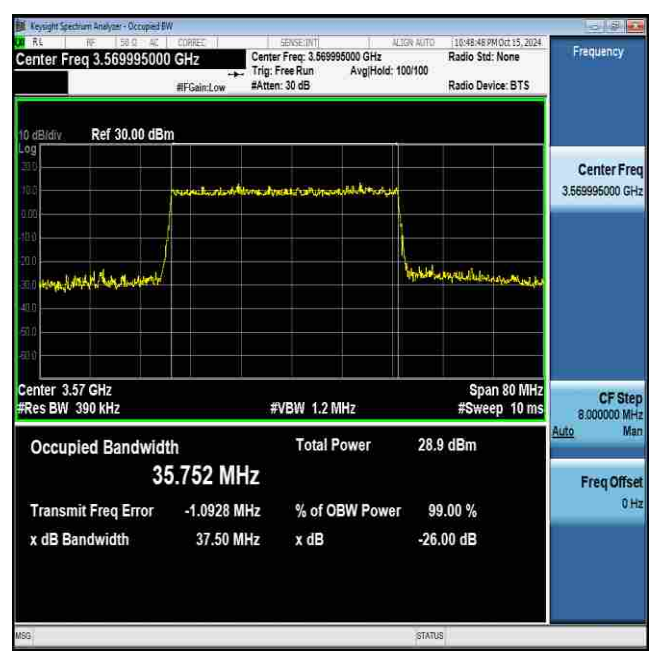
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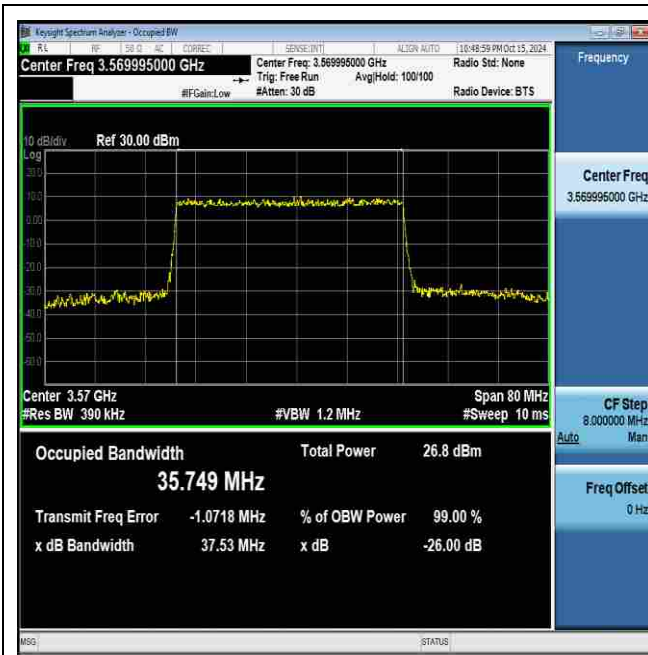
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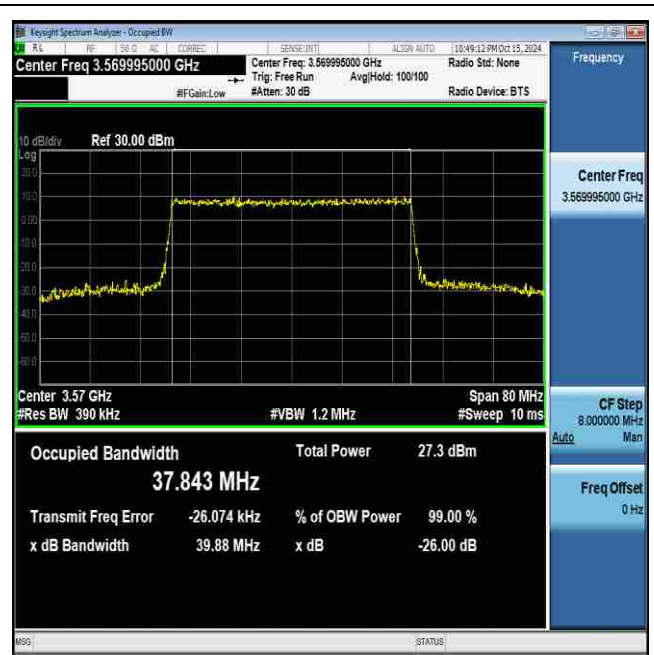
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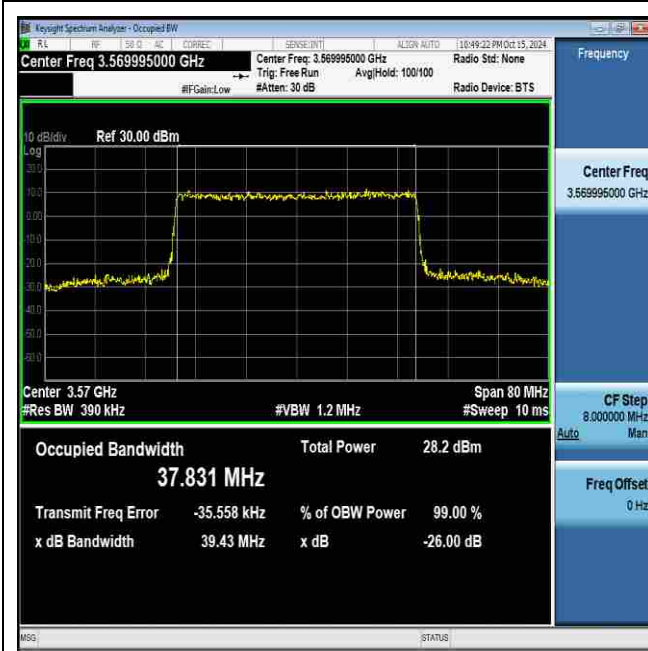
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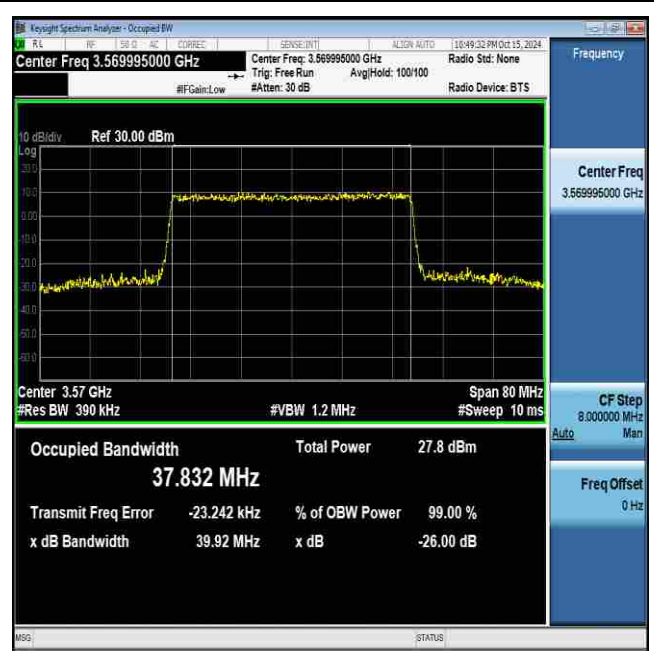
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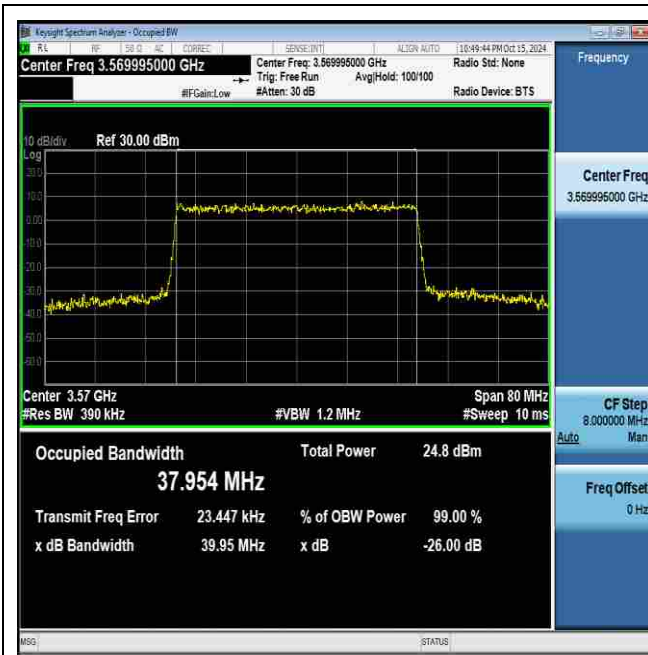
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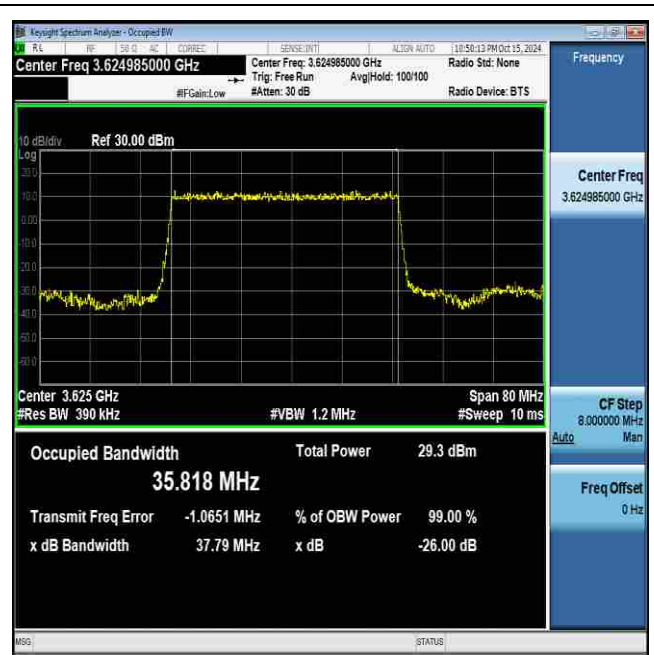
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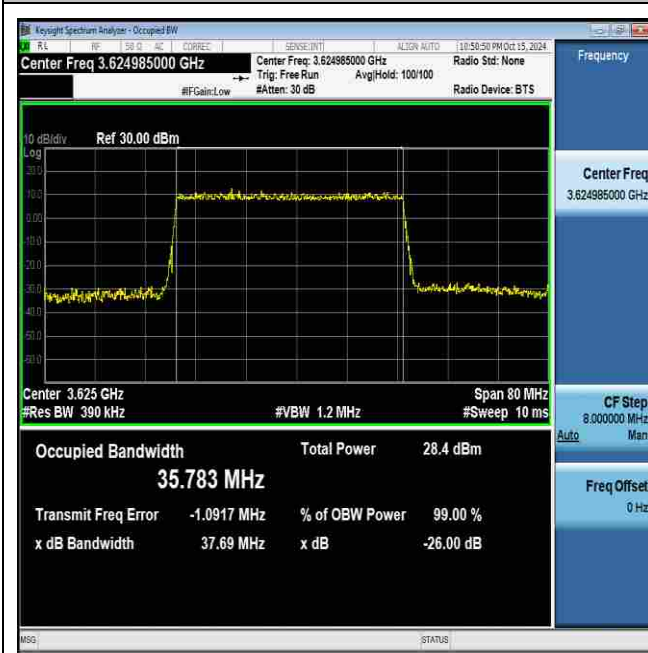
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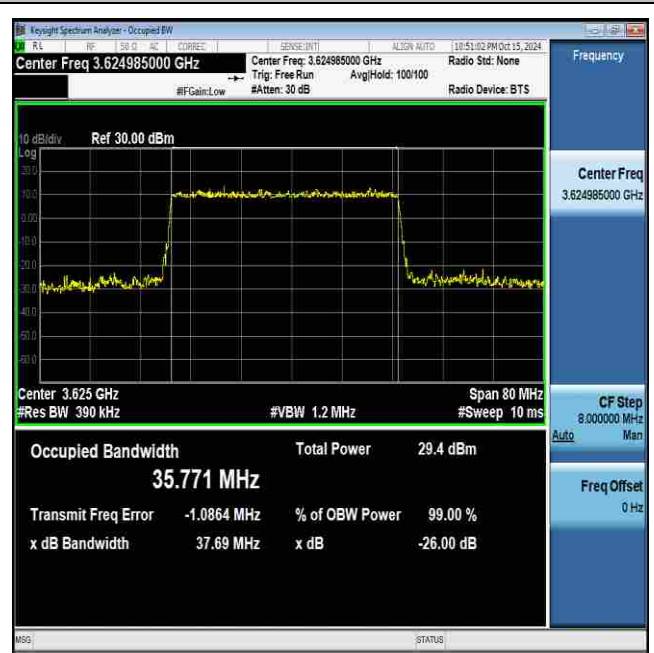
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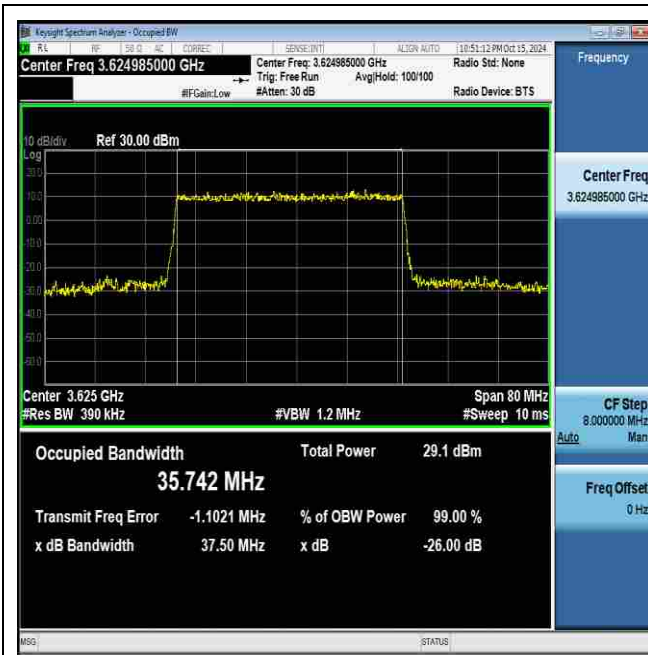
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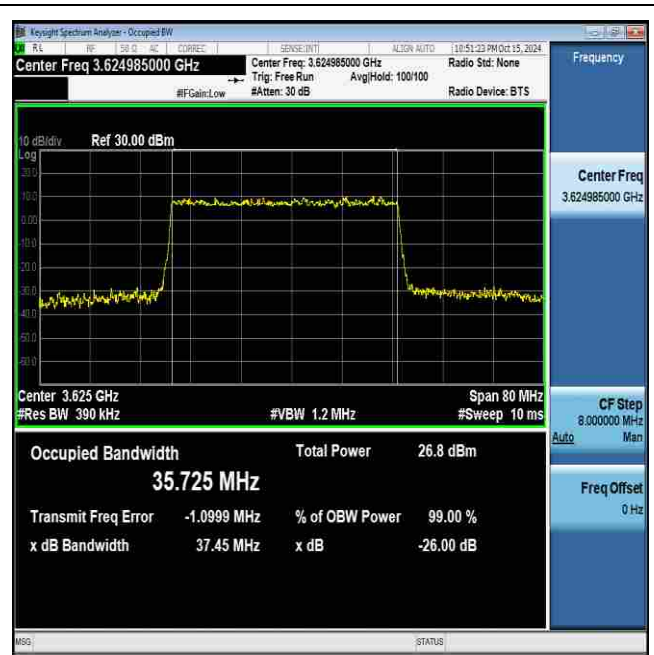
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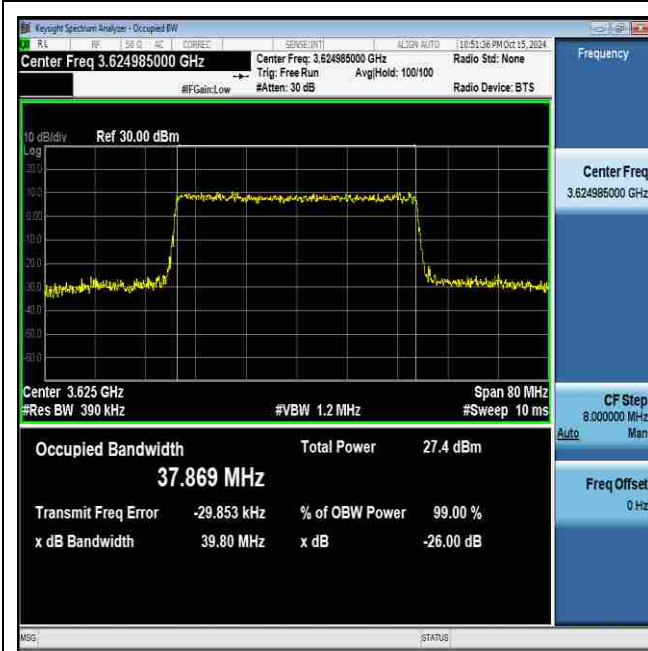
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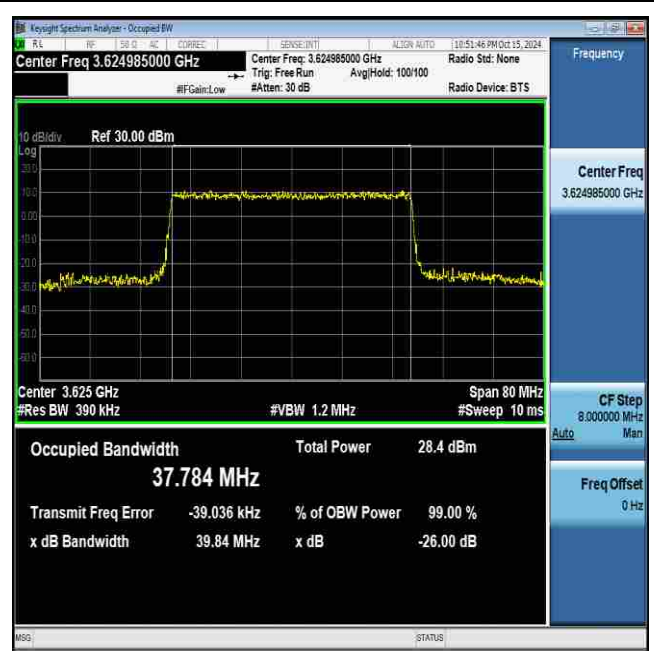
NTNV_N48_PC3_30_40_M_TID4_Ant1#1



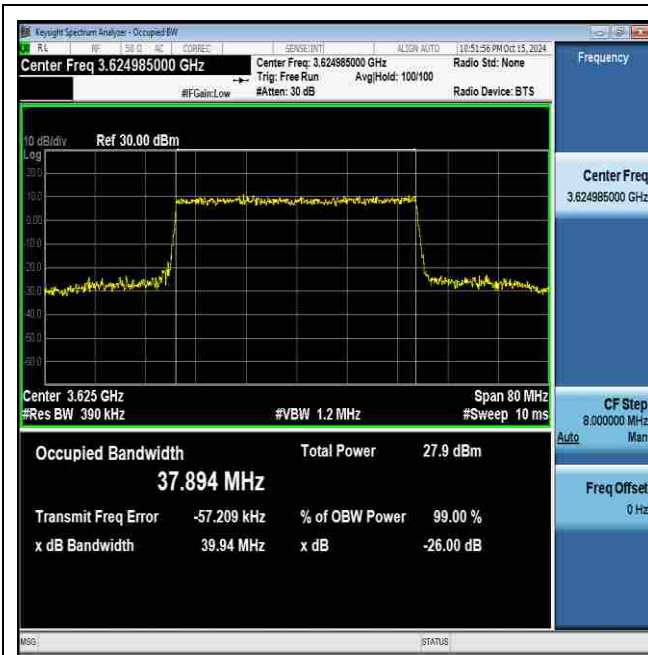
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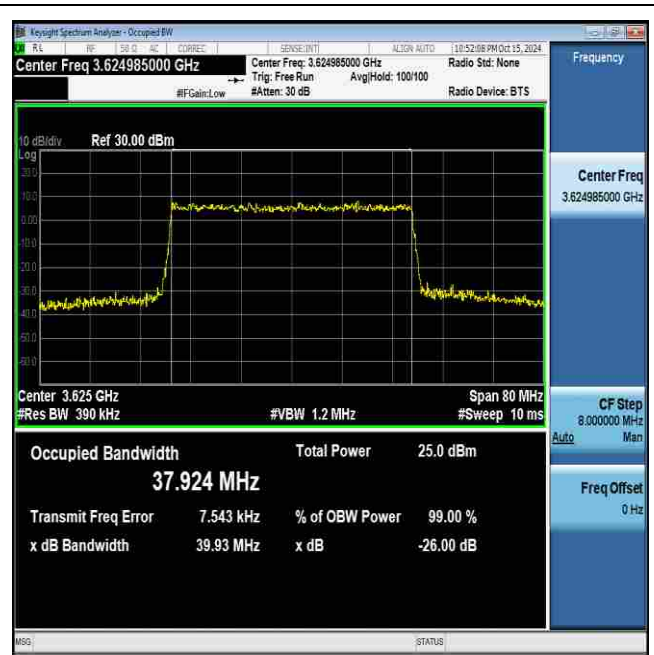
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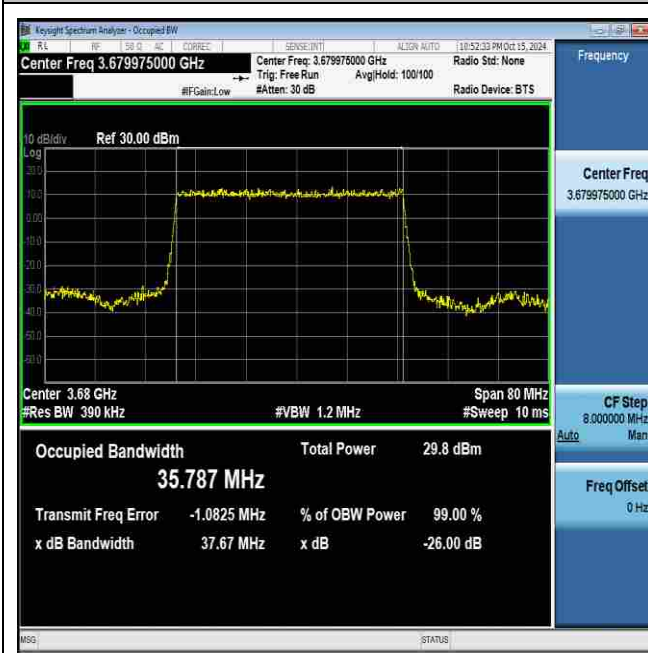
NTNV_N48_PC3_30_40_M_TID7_Ant1#1



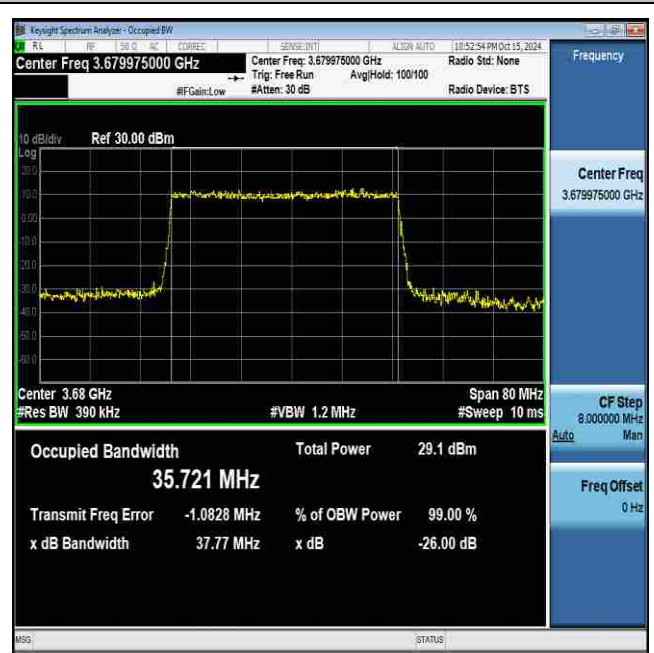
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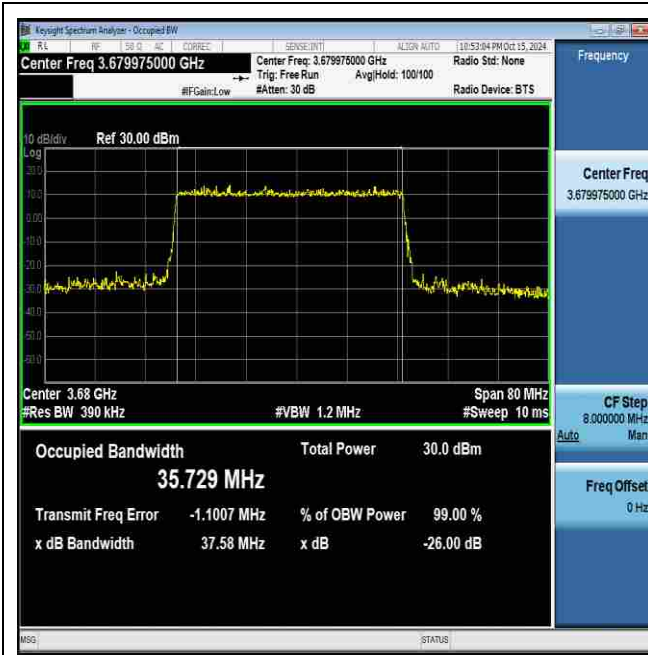
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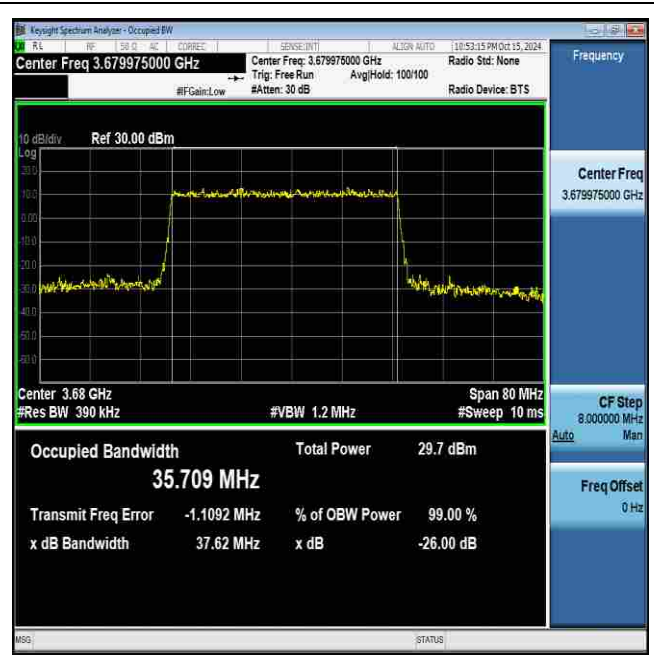
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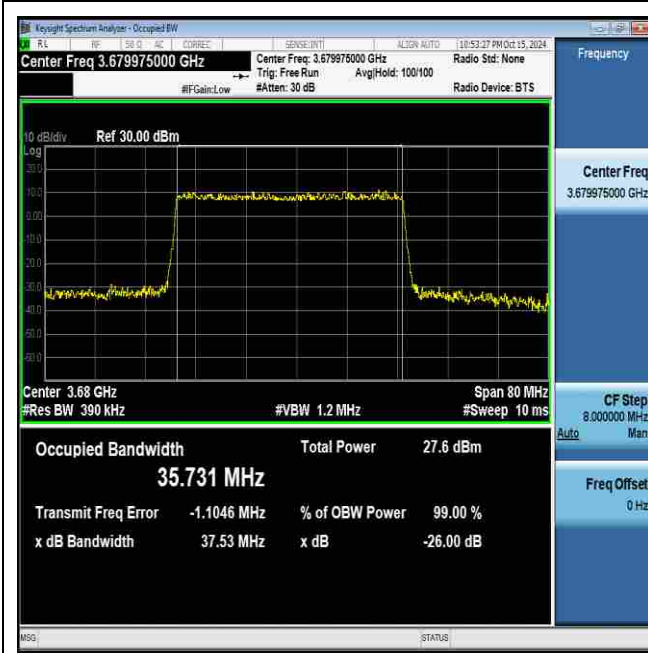
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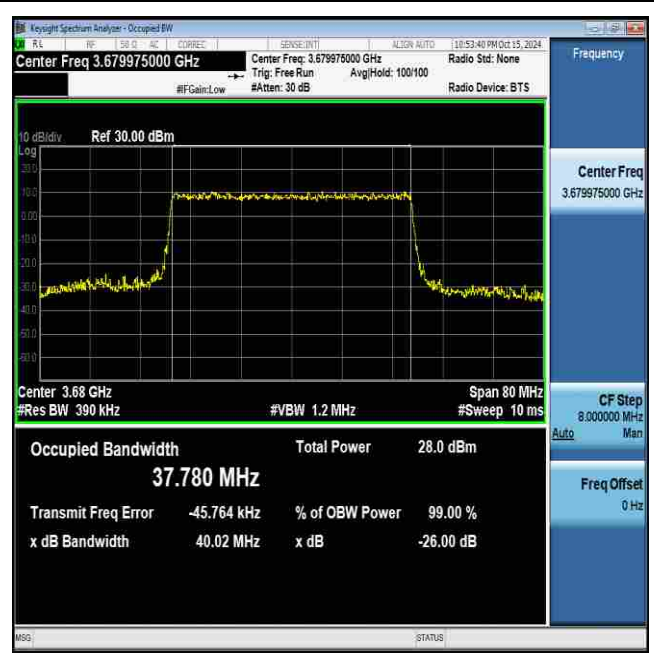
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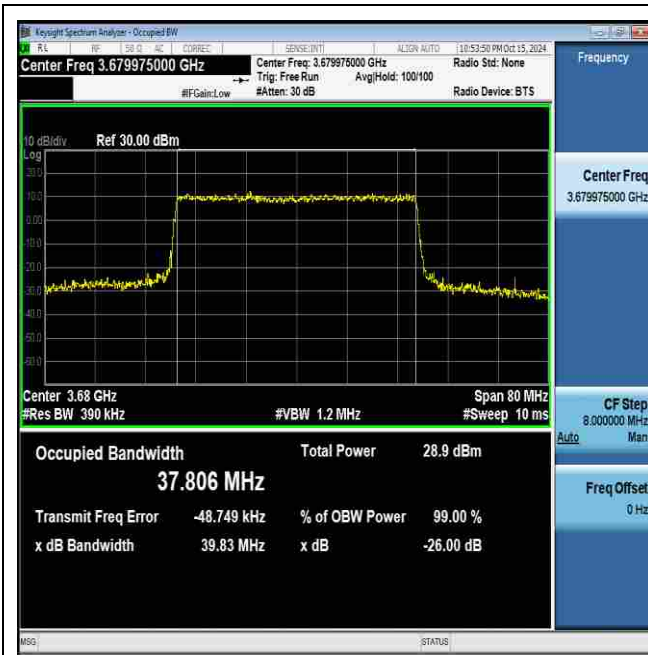
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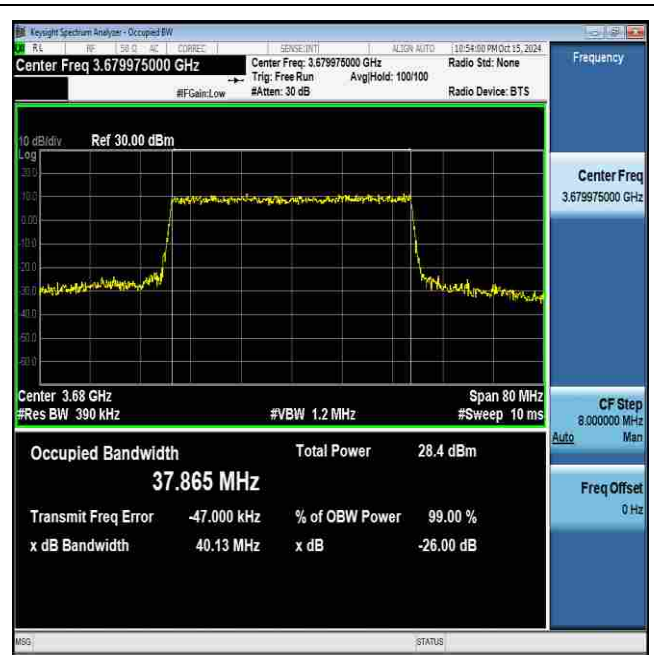
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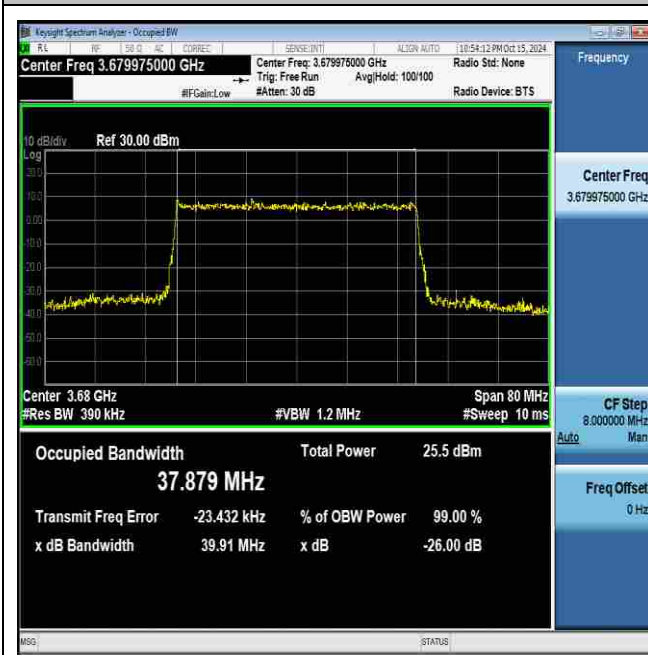
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NTVN_N48_PC3_30_40_H_TID7_Ant1#1



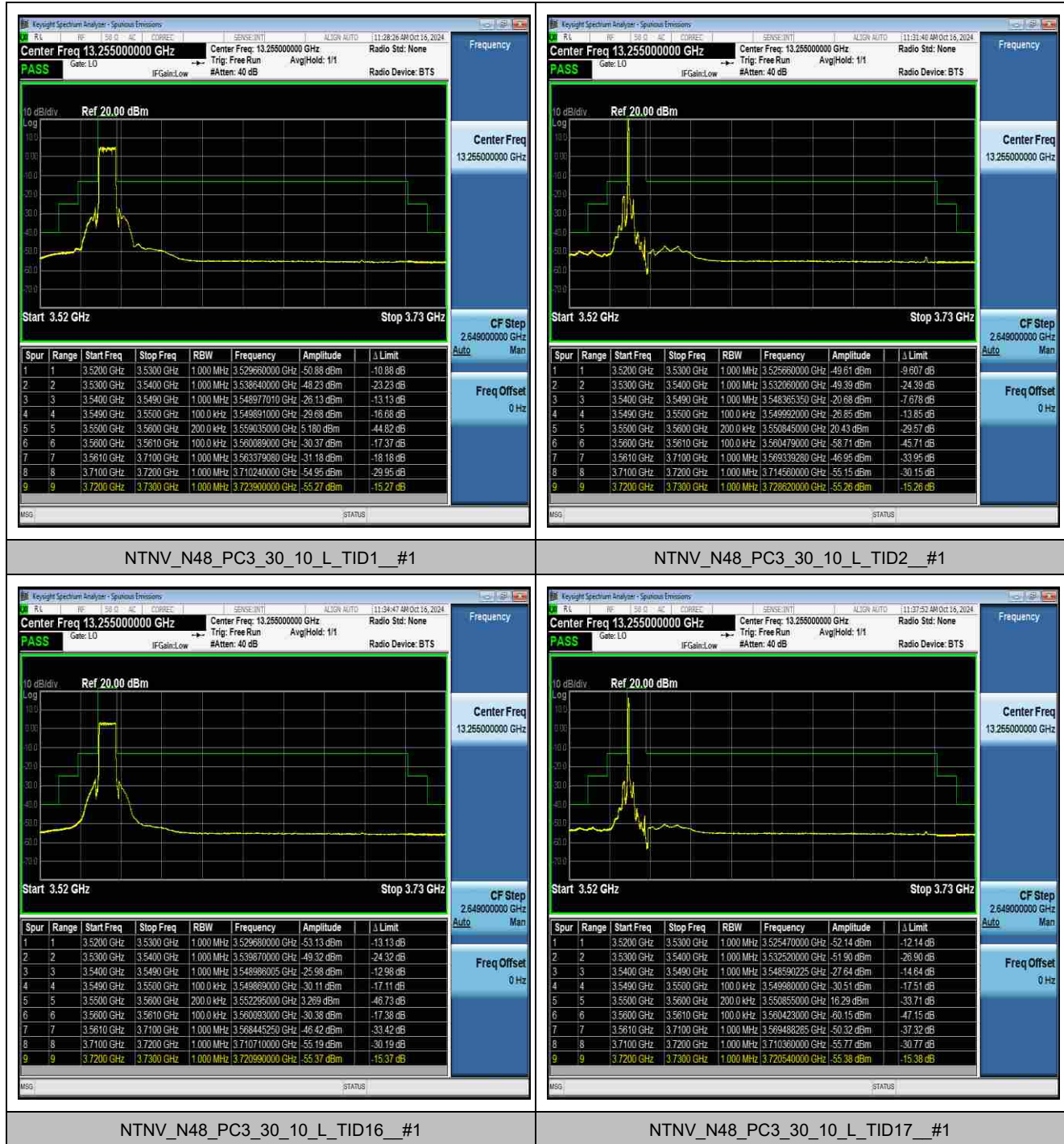
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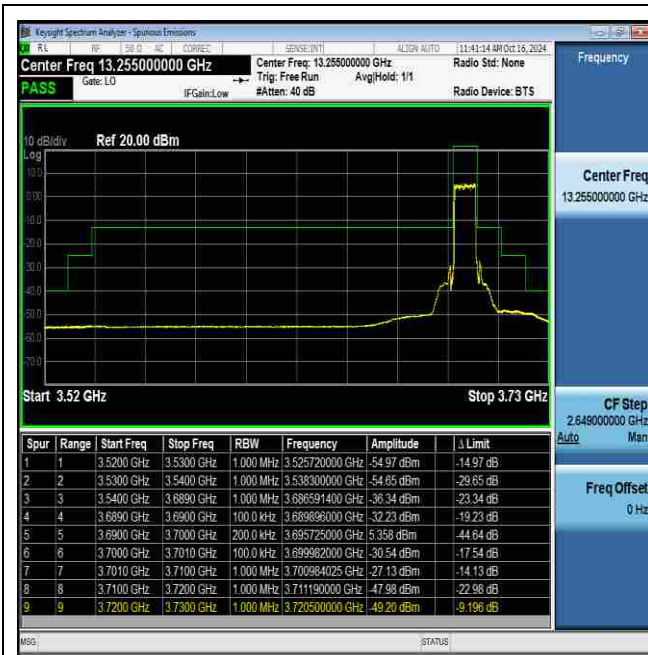


NTVN_N48_PC3_30_40_H_TID9_Ant1#1

Band Edge for SA

Test Graphs

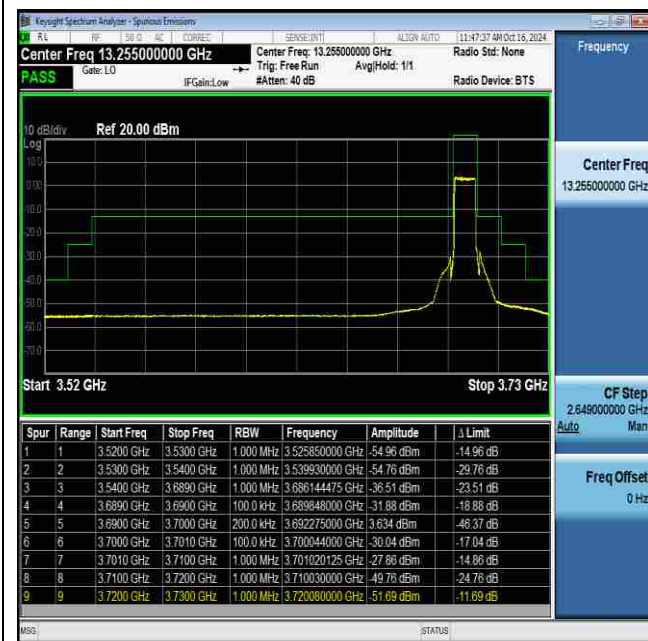




NTNV_N48_PC3_30_10_H_TID1_#1



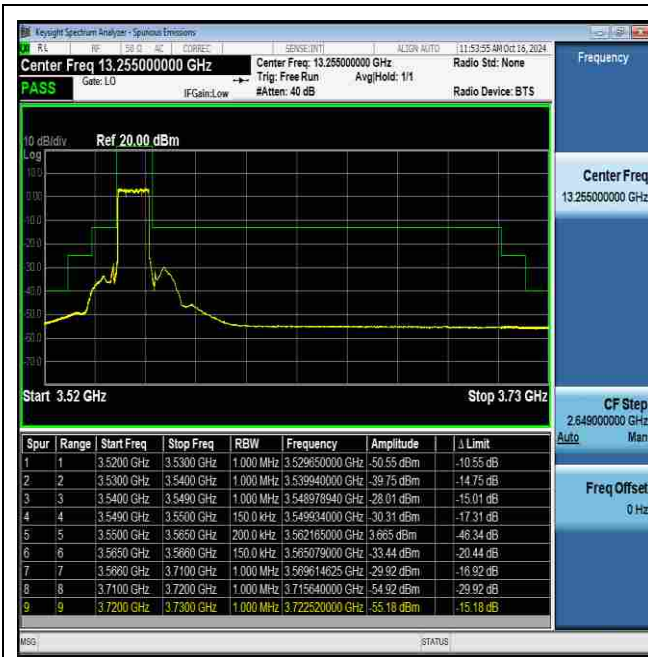
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NTNV_N48_PC3_30_10_H_TID16_#1



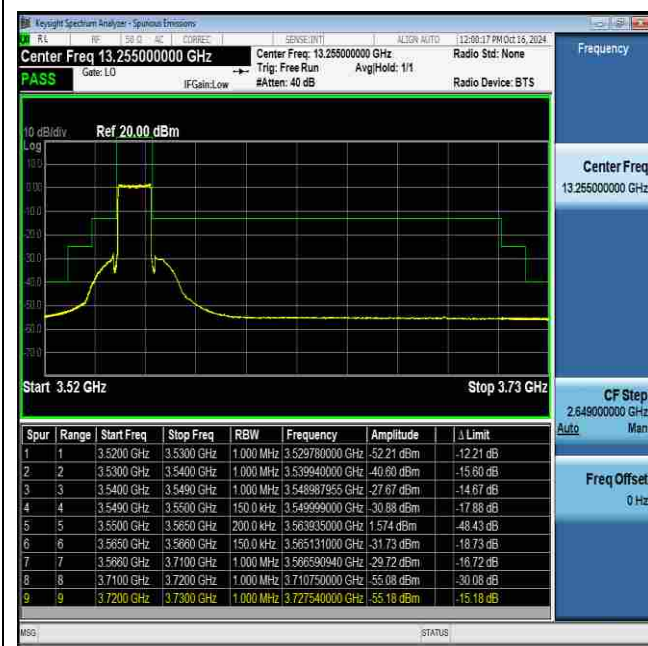
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NTNV_N48_PC3_30_15_L_TID1_#1



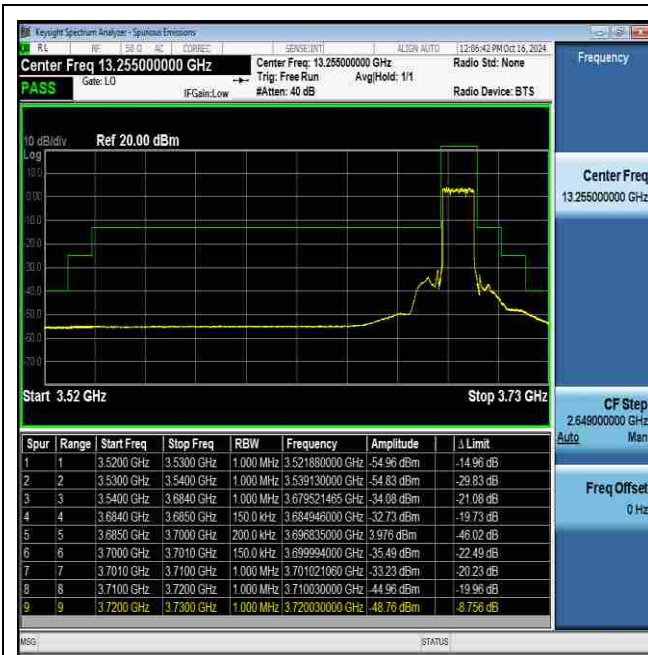
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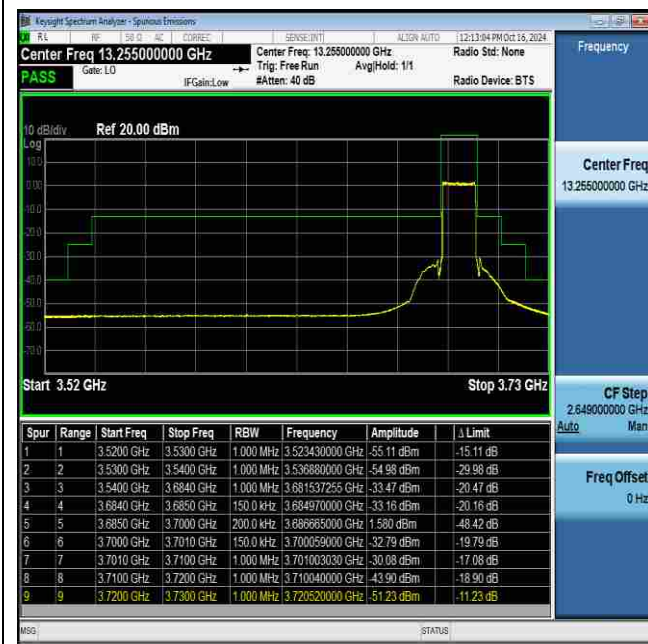
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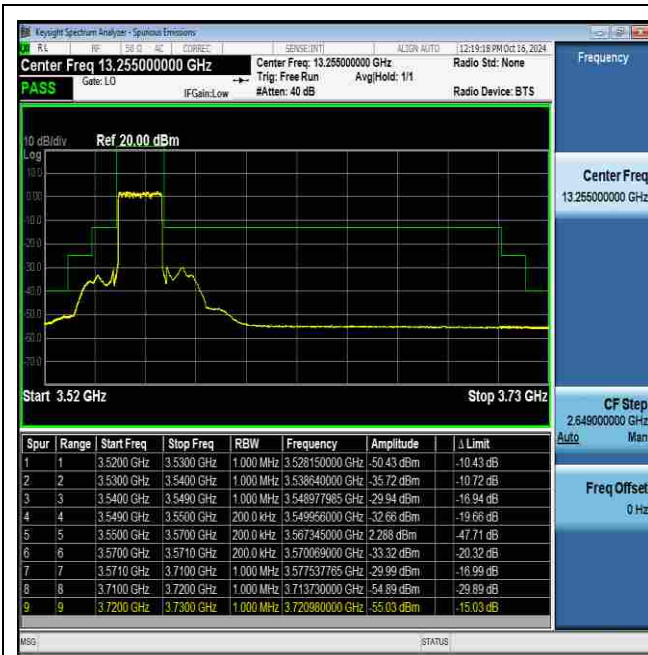
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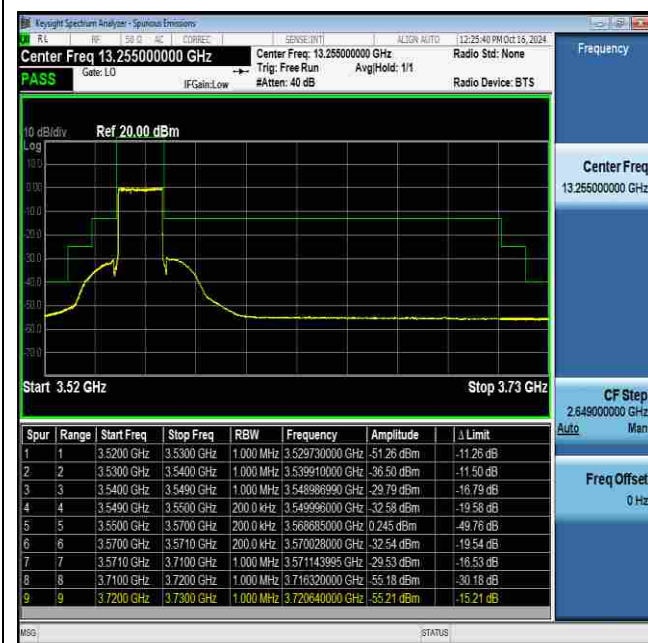
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NTNV_N48_PC3_30_20_L_TID1_#1



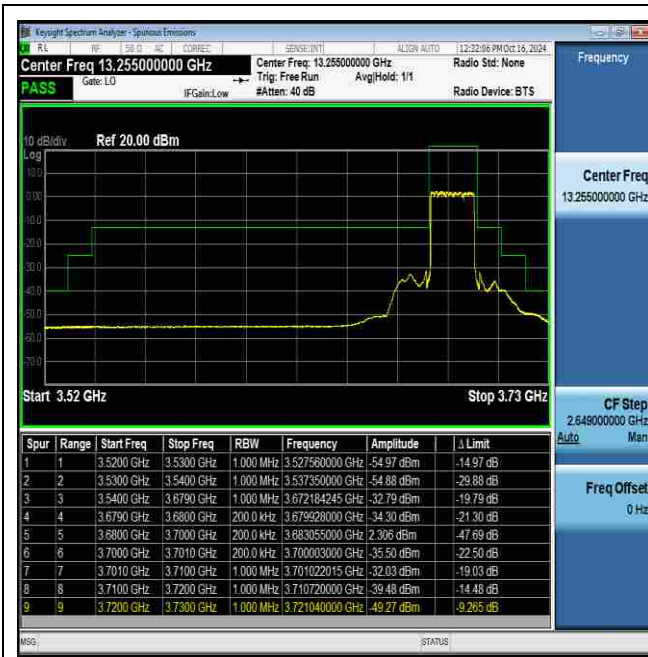
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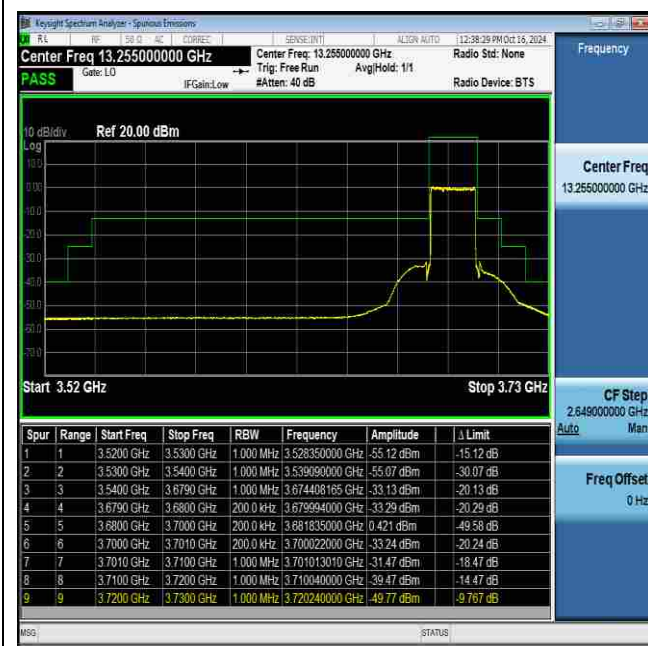
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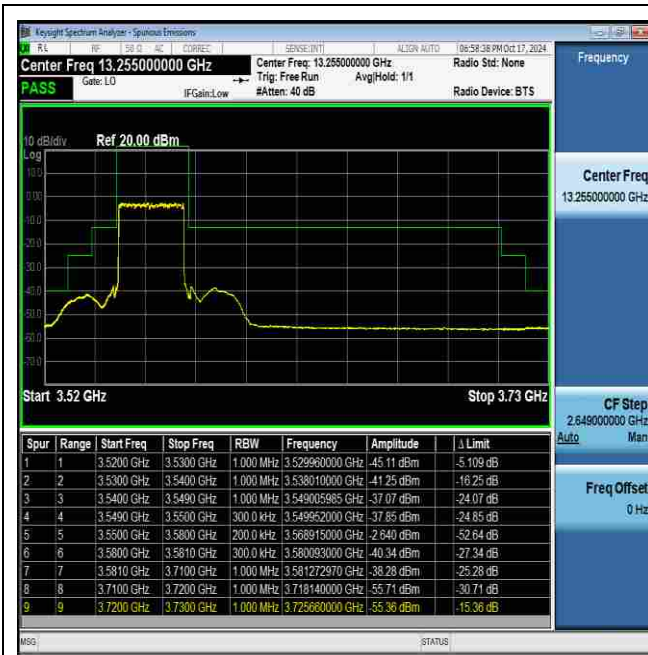
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NTNV_N48_PC3_30_20_H_TID16_#1



NTNV_N48_PC3_30_20_H_TID18_#1



NTVN_N48_PC3_30_30_L_TID1_#1



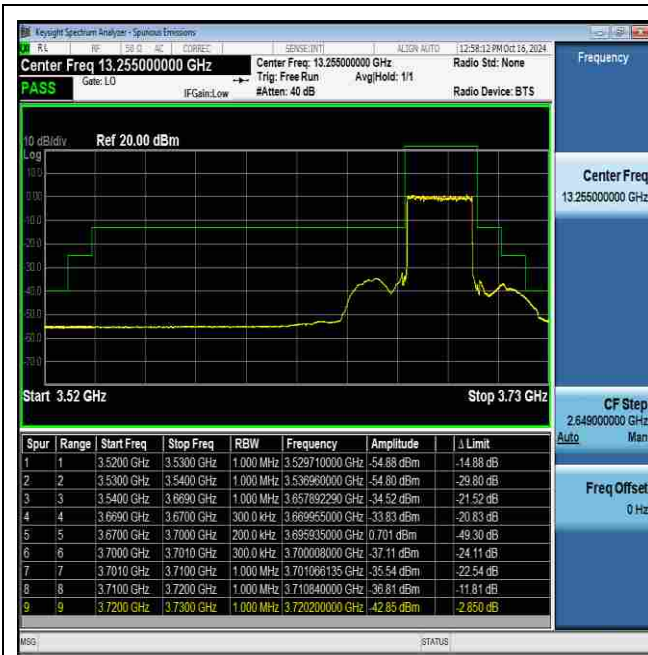
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NTNV_N48_PC3_30_30_H_TID1_#1



NTNV_N48_PC3_30_30_H_TID3_#1



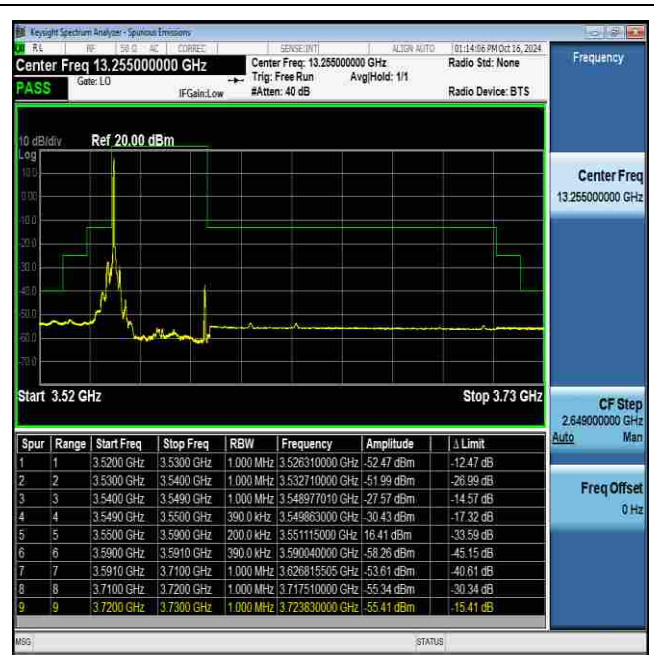
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NTNV_N48_PC3_30_30_H_TID18_#1



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NTVN_N48_PC3_30_40_L_TID2_#1



NTVN_N48_PC3_30_40_L_TID16_#1



NTVN_N48_PC3_30_40_L_TID17_#1



NTNV_N48_PC3_30_40_H_TID1_#1



NTNV_N48_PC3_30_40_H_TID3_#1



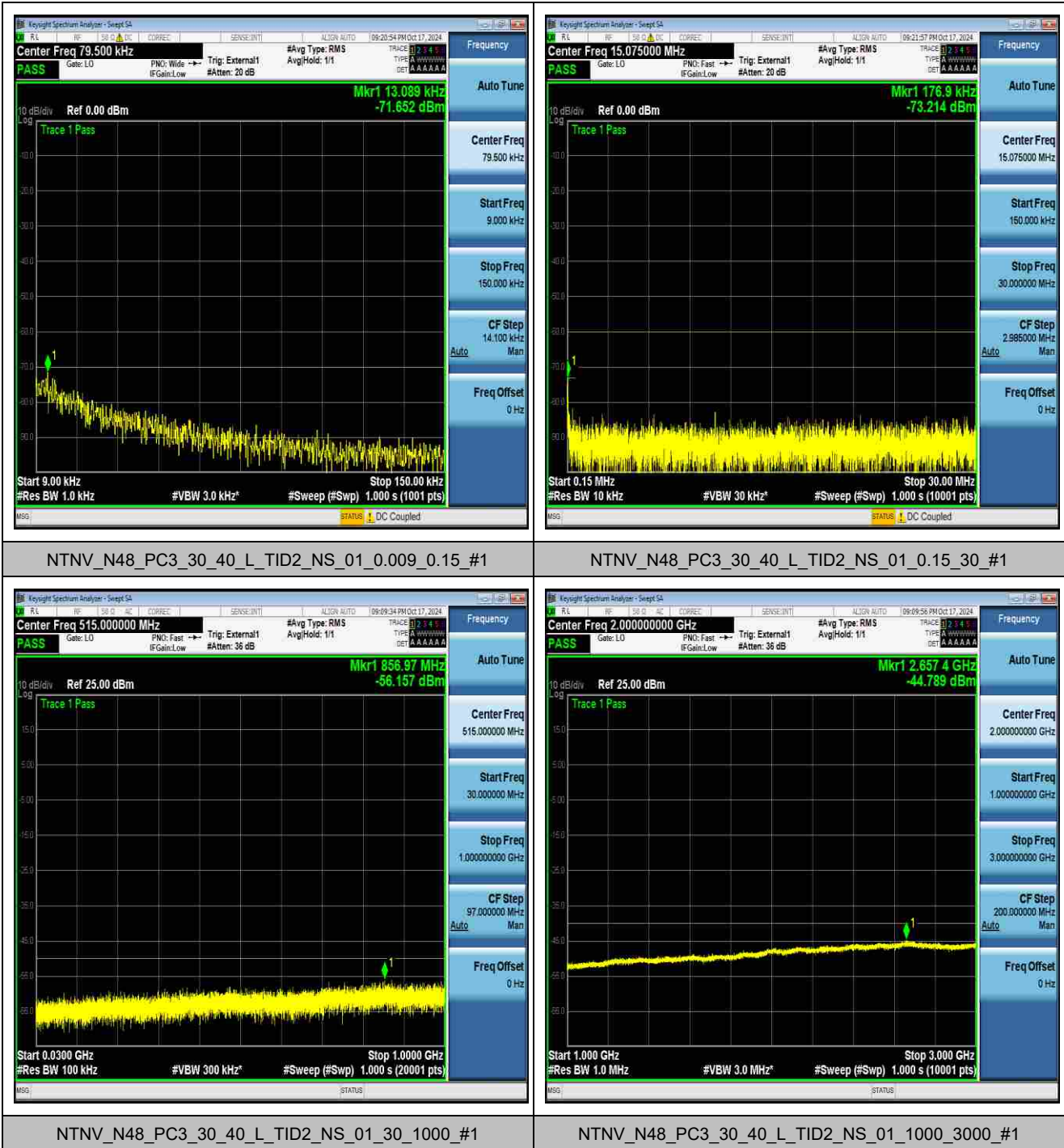
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Conducted Spurious Emission for SA

Test Graphs

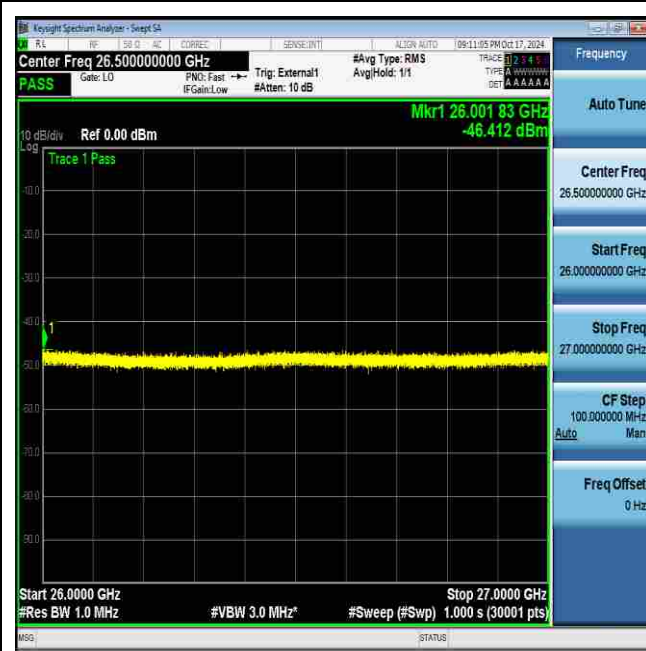




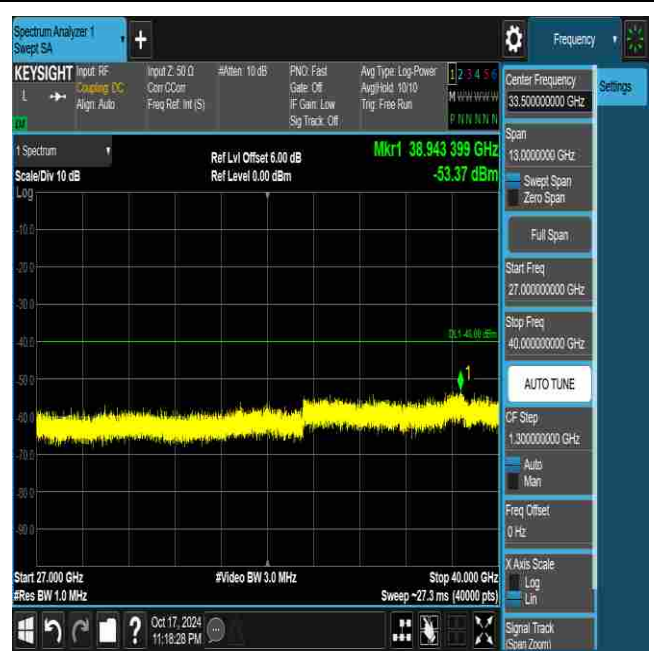
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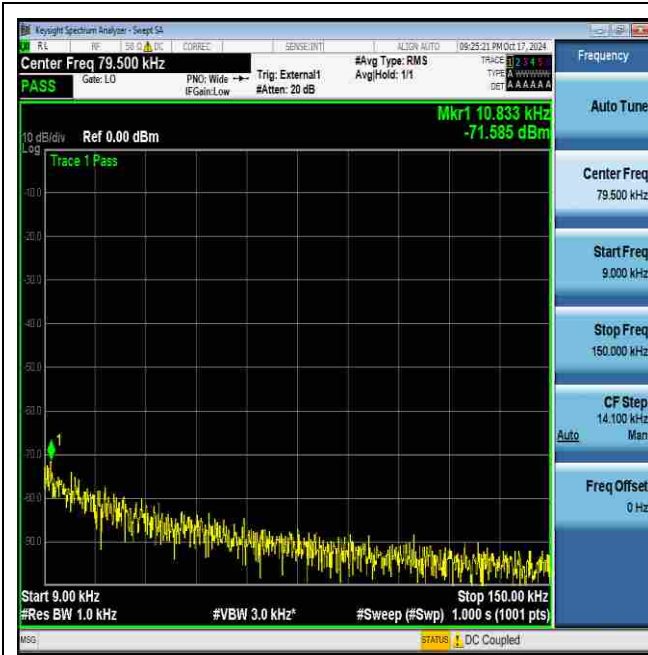
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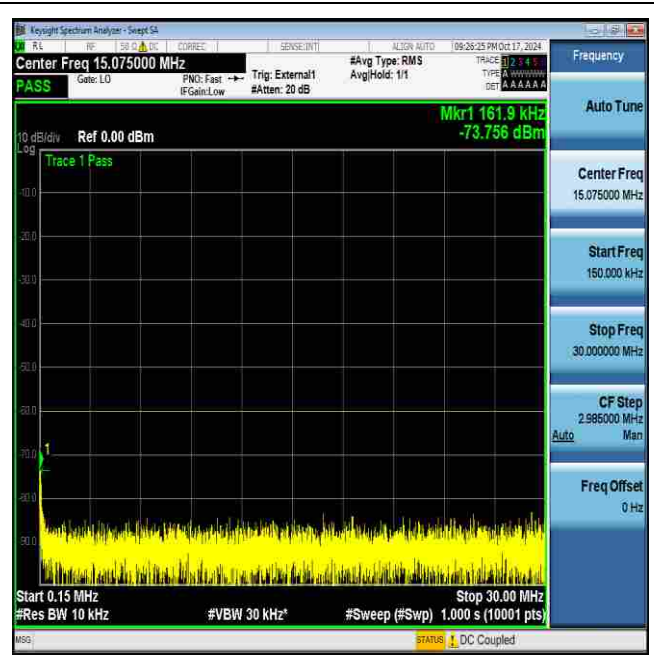
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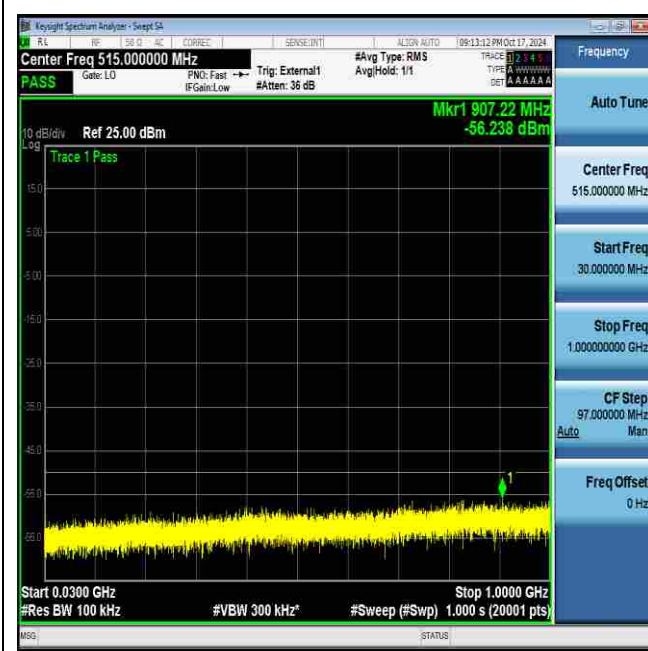
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NTNV_N48_PC3_30_40_H_TID2_NS_01_0.15_30_#1



NTNV_N48_PC3_30_40_H_TID2_NS_01_30_1000_#1



NTNV_N48_PC3_30_40_H_TID2_NS_01_1000_3000_#1



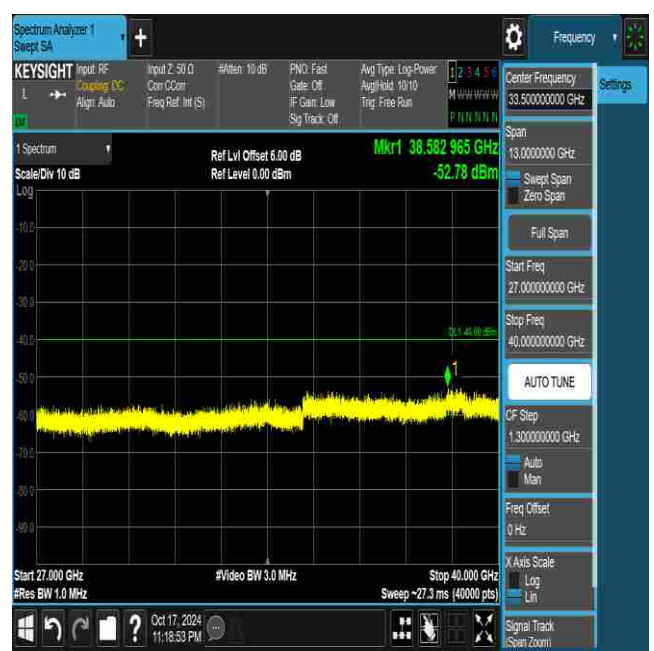
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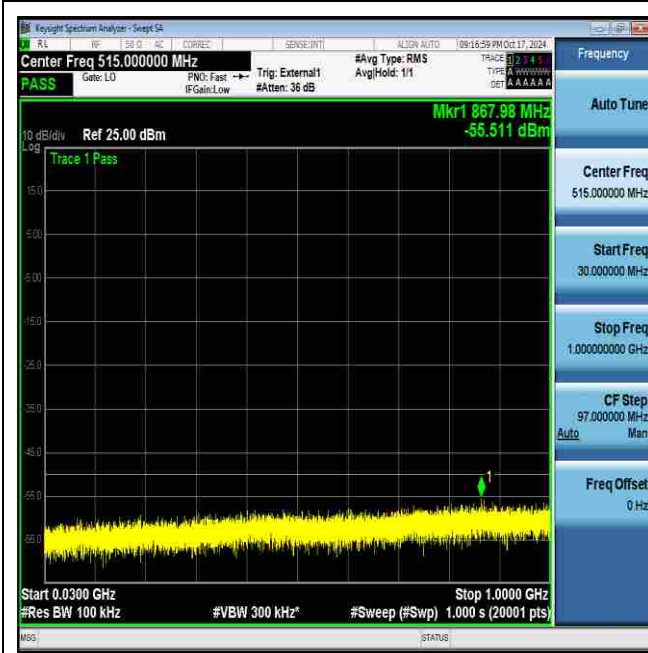
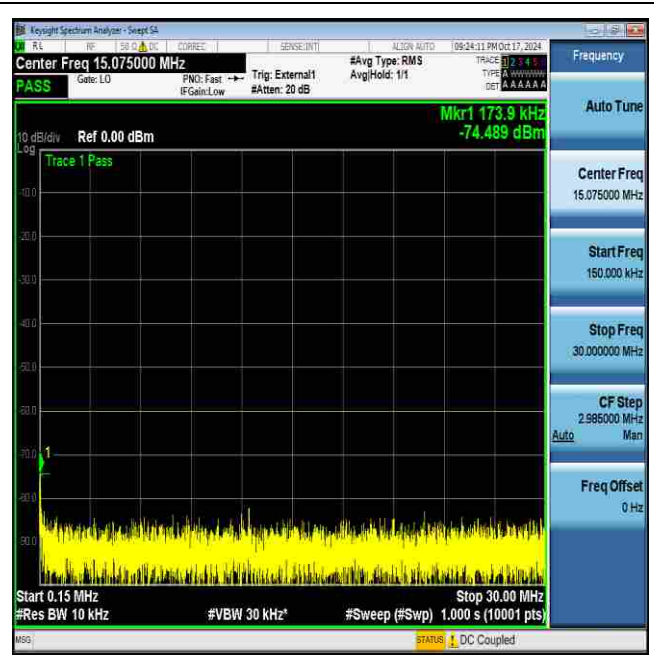
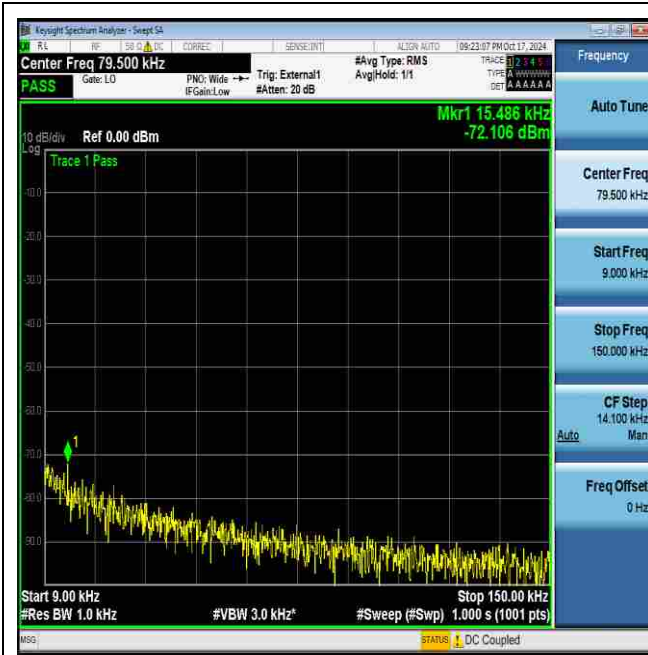
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NTNV_N48_PC3_30_40_H_TID3_NS_01_27000_40000_#1





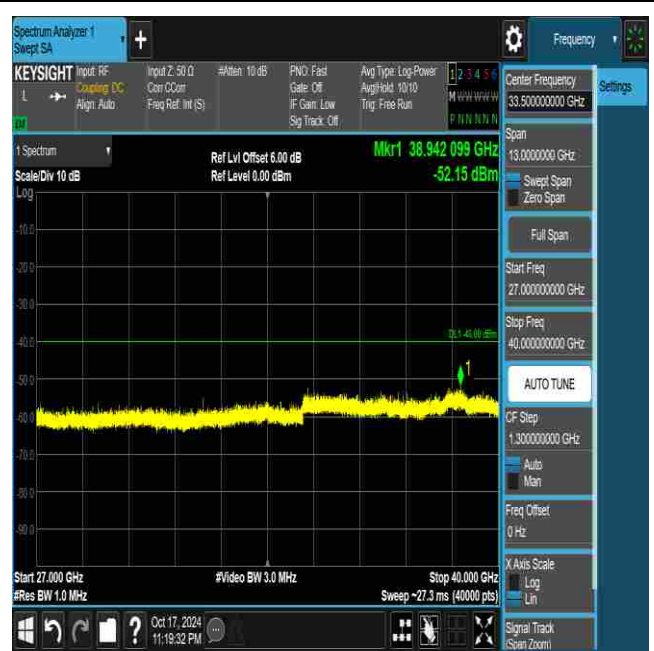
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NTNV_N48_PC3_30_40_M_TID2_NS_01_26000_27000_#1



NTNV_N48_PC3_30_40_M_TID3_NS_01_27000_40000_#1

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N48	30	40	DFT-PI2BPSK	M	Outer_Full	VL	NT	12.000000	0.003361	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	VN	NT	18.000000	0.005042	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	VH	NT	2.000000	0.000560	PASS
N48	30	40	CP-QPSK	M	Outer_Full	VL	NT	14.000000	0.003922	PASS
N48	30	40	CP-QPSK	M	Outer_Full	VN	NT	8.000000	0.002241	PASS
N48	30	40	CP-QPSK	M	Outer_Full	VH	NT	11.000000	0.003081	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	-30	4.000000	0.001103	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	-20	10.000000	0.002759	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	-10	15.000000	0.004138	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	0	9.000000	0.002483	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	10	15.000000	0.004138	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	20	8.000000	0.002207	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	30	11.000000	0.003034	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	40	4.000000	0.001103	PASS
N48	30	40	DFT-PI2BPSK	M	Outer_Full	NV	50	10.000000	0.002759	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	-30	14.000000	0.003862	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	-20	8.000000	0.002207	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	-10	1.000000	0.000276	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	0	4.000000	0.001103	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	10	17.000000	0.004690	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	20	10.000000	0.002759	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	30	13.000000	0.003586	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	40	19.000000	0.005241	PASS
N48	30	40	CP-QPSK	M	Outer_Full	NV	50	5.000000	0.001379	PASS