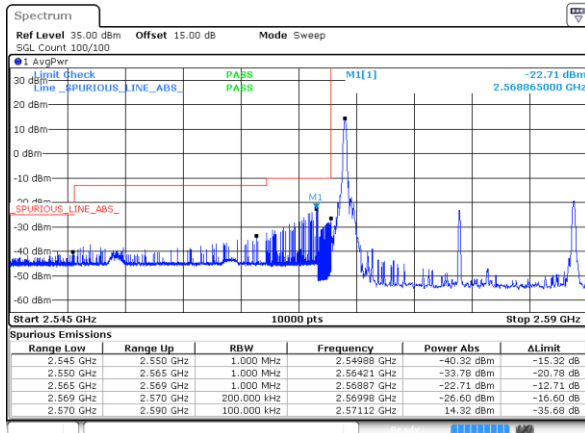
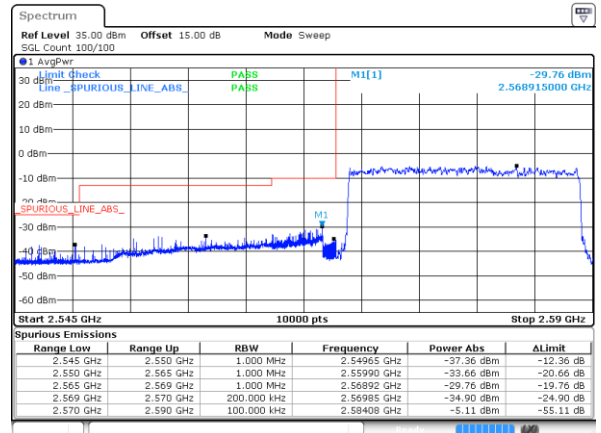


20MHz_Low_QPSK_1@0



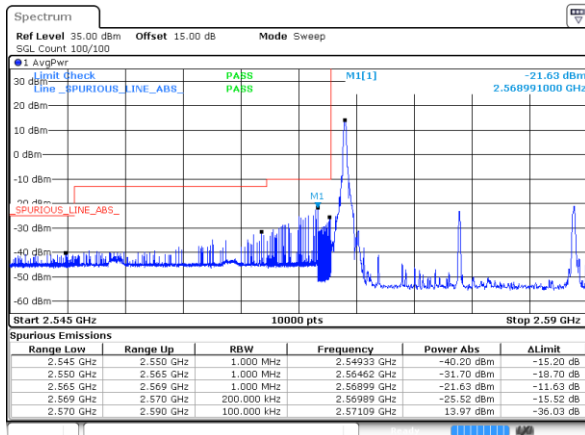
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:58:37

20MHz_Low_QPSK_100@0



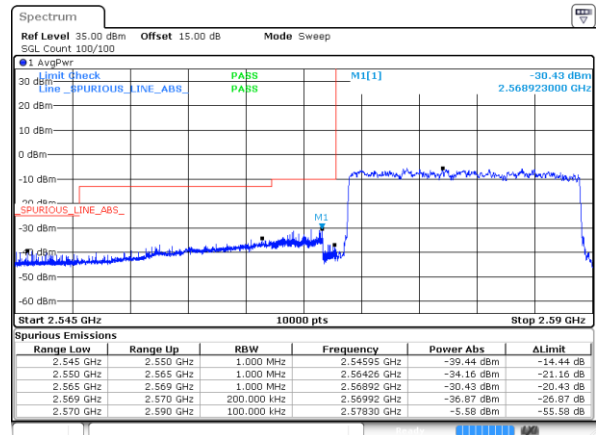
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:58:50

20MHz_Low_16QAM_1@0



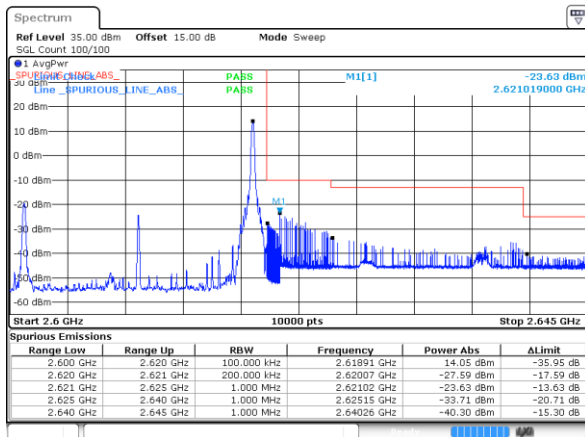
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:59:04

20MHz_Low_16QAM_100@0



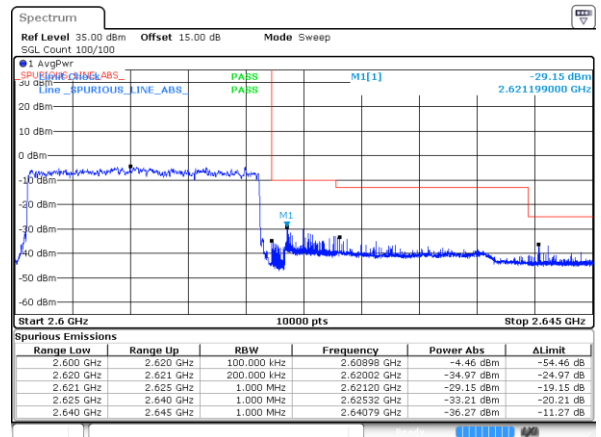
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:59:17

20MHz_High_QPSK_1@99



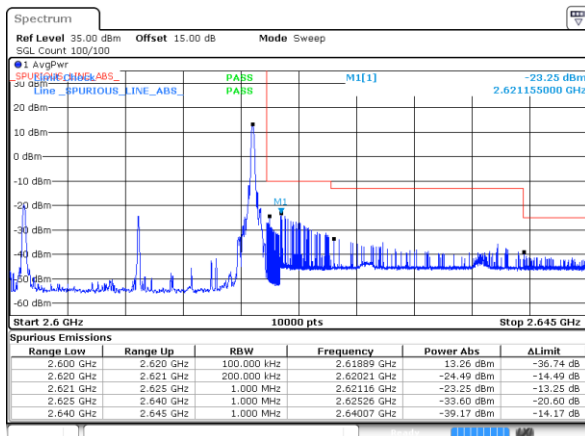
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:59:39

20MHz_High_QPSK_100@0



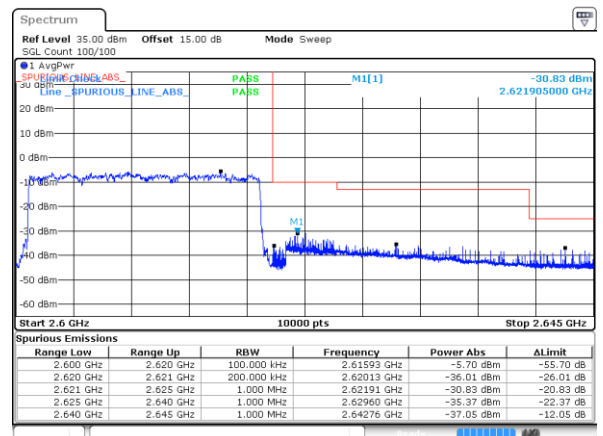
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:59:54

20MHz_High_16QAM_1@99



ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 20:00:08

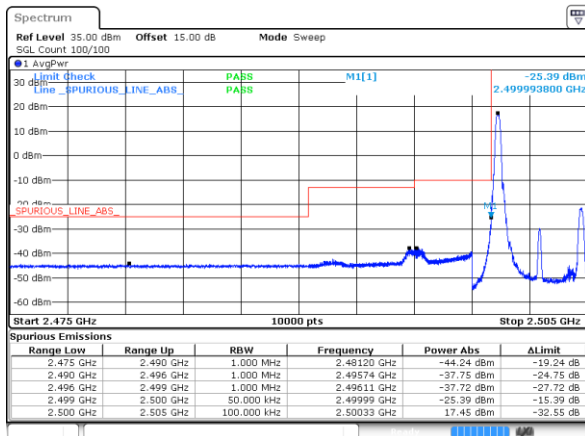
20MHz_High_16QAM_100@0



ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 20:00:23

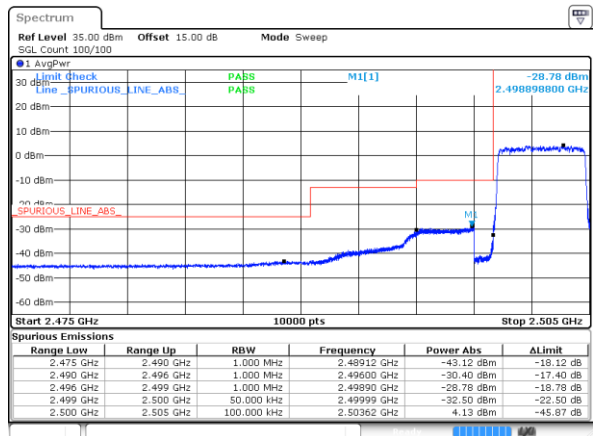
B7 , Normal

5MHz_Low_QPSK_1@0



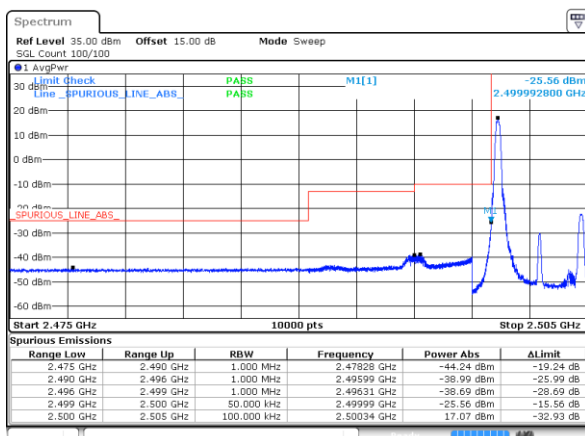
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 19:33:09

5MHz_Low_QPSK_25@0



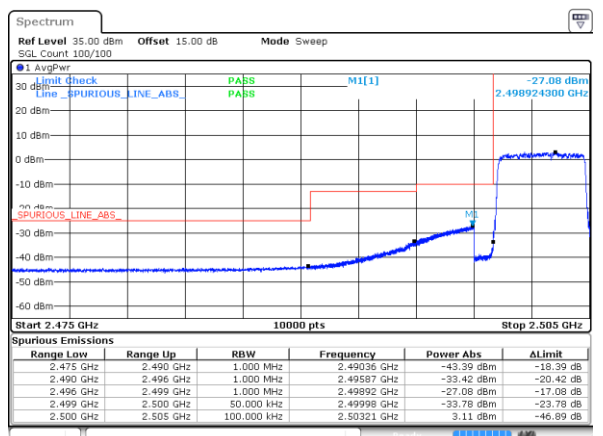
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 19:33:22

5MHz_Low_16QAM_1@0



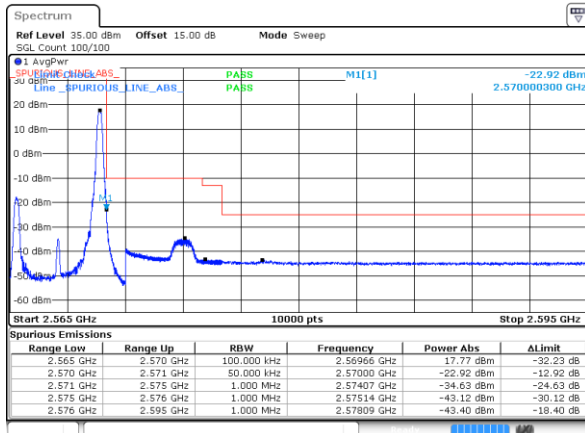
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 19:33:35

5MHz_Low_16QAM_25@0



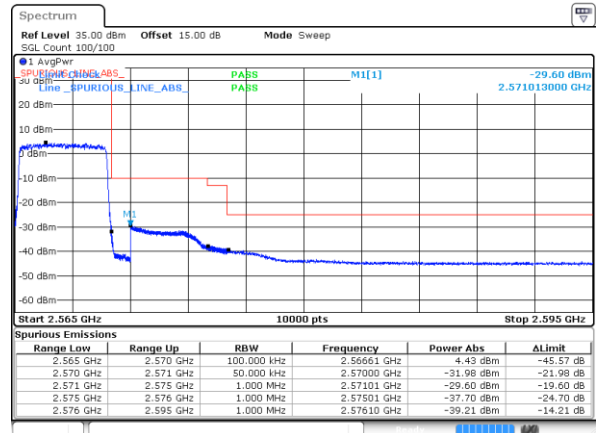
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC. 2024 19:33:48

5MHz_High_QPSK_1@24



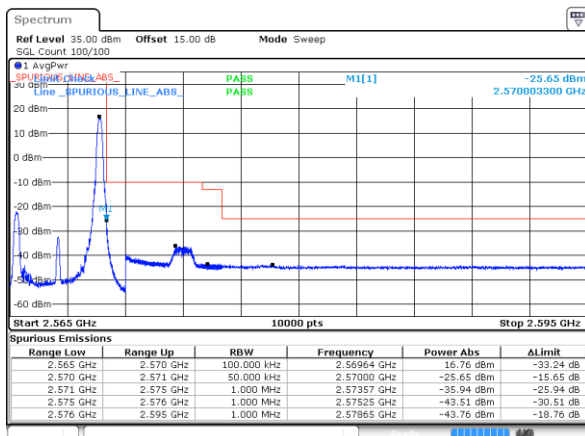
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:34:08

5MHz_High_QPSK_25@0



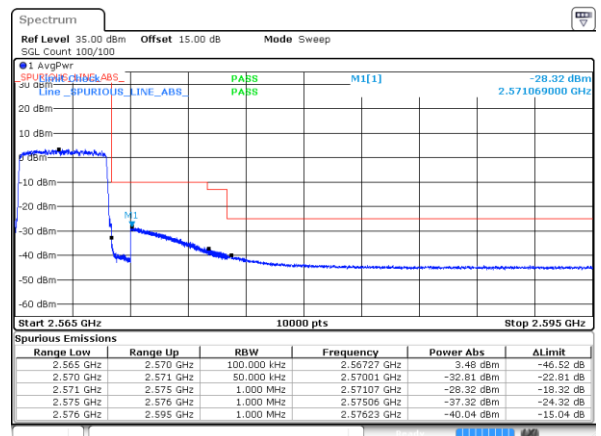
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:34:21

5MHz_High_16QAM_1@24



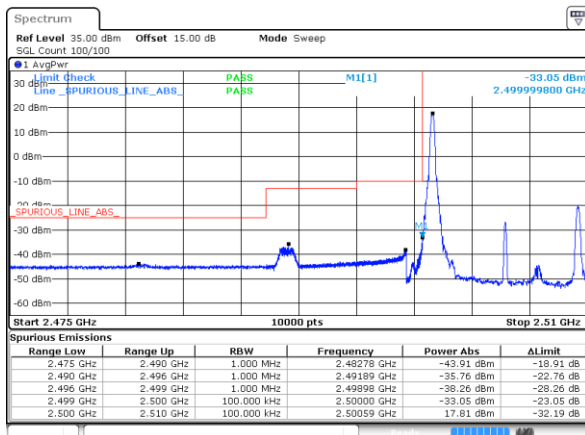
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:34:34

5MHz_High_16QAM_25@0



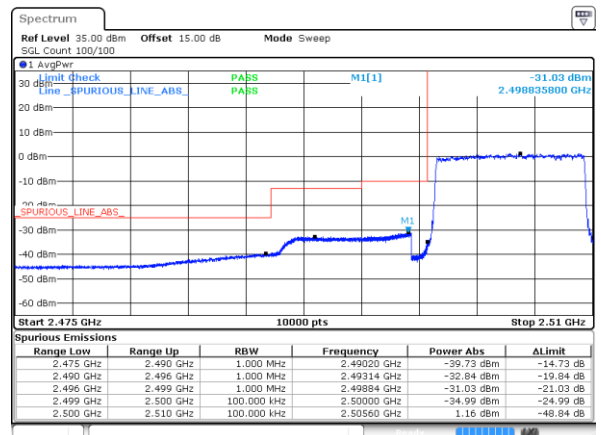
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:34:47

10MHz_Low_QPSK_1@0



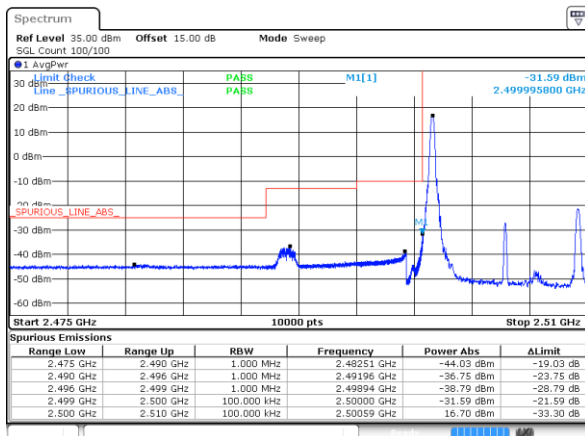
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:37:02

10MHz_Low_QPSK_50@0



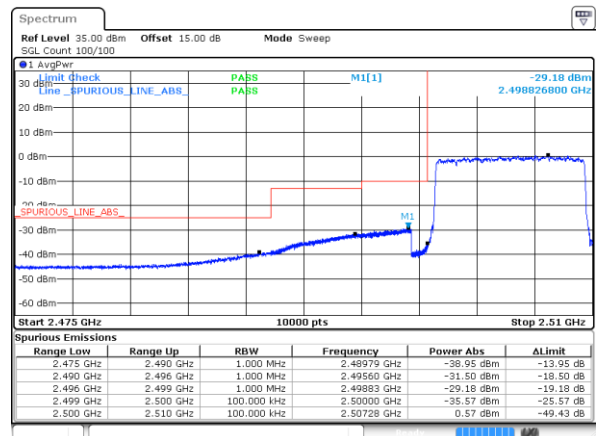
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:37:15

10MHz_Low_16QAM_1@0



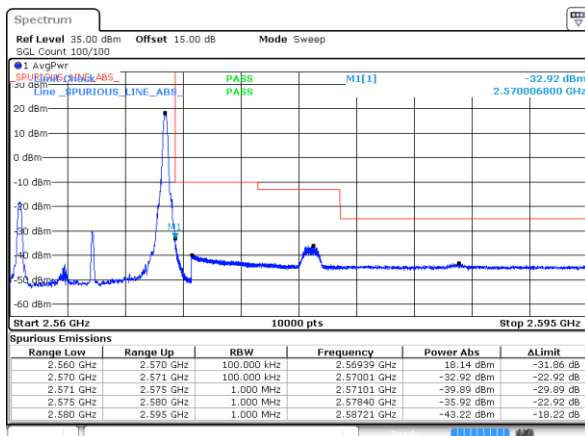
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:37:29

10MHz_Low_16QAM_50@0



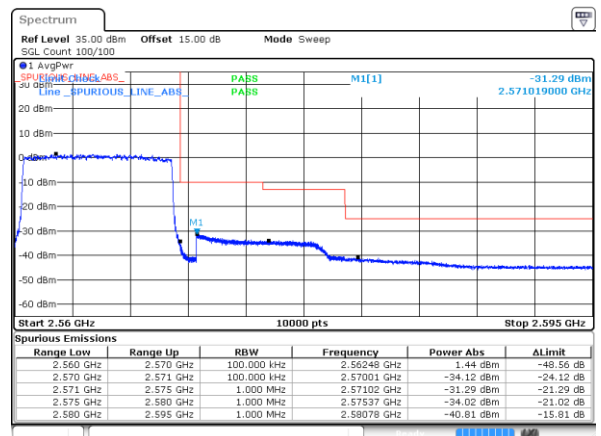
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:37:42

10MHz_High_QPSK_1@49



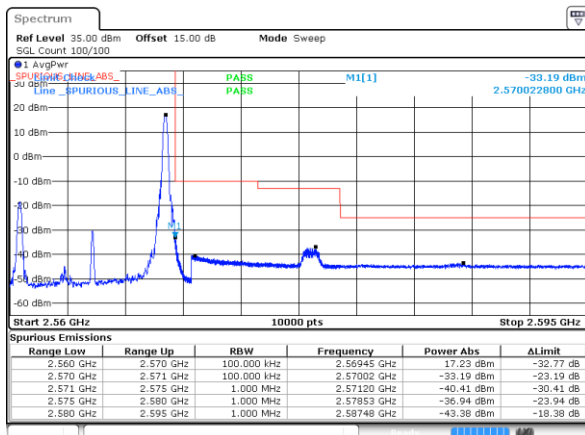
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:38:02

10MHz_High_QPSK_50@0



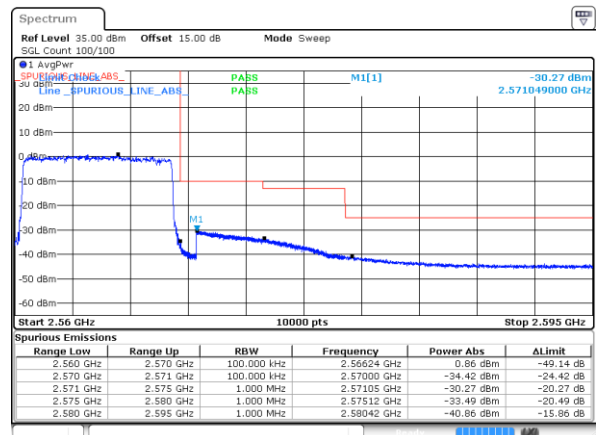
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Date: 4 DEC 2024 19:38:15

10MHz_High_16QAM_1@49



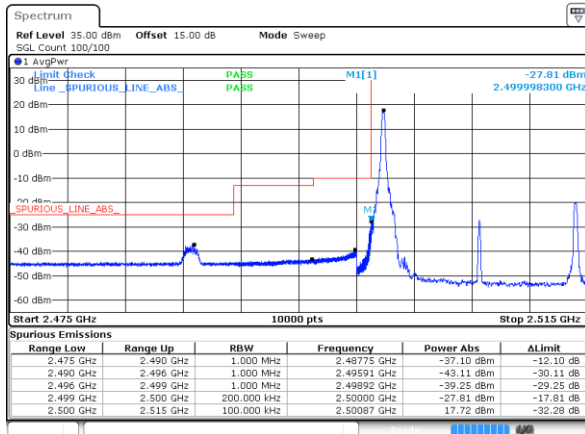
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Date: 4 DEC 2024 19:38:28

10MHz_High_16QAM_50@0



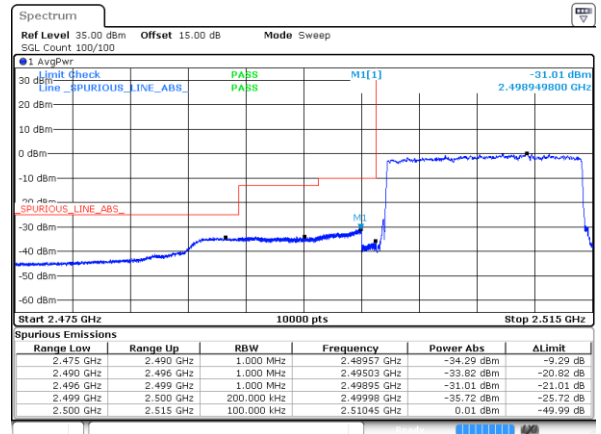
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:38:54

15MHz_Low_QPSK_1@0



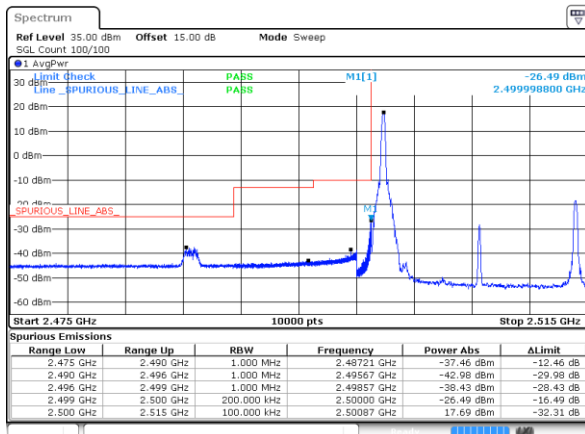
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:40:08

15MHz_Low_QPSK_75@0



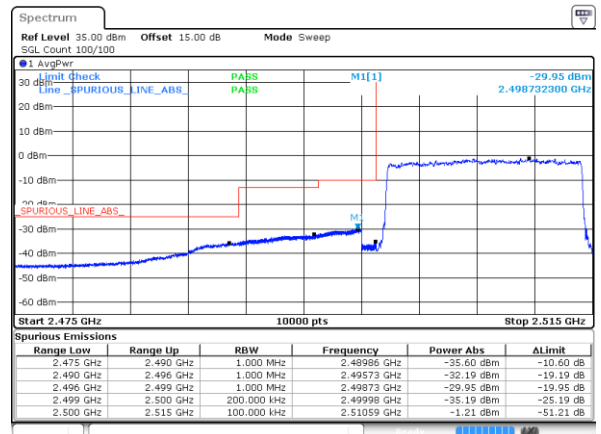
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:40:21

15MHz_Low_16QAM_1@0



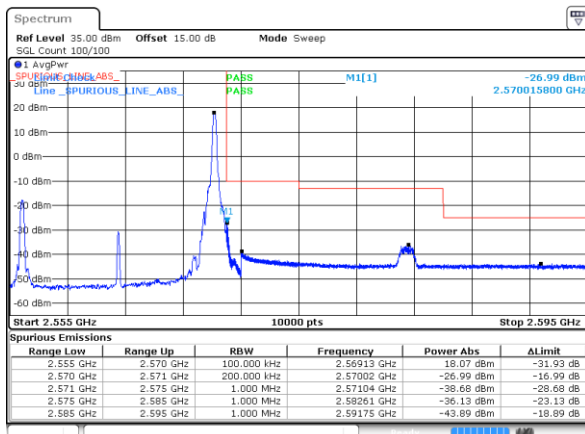
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:40:34

15MHz_Low_16QAM_75@0



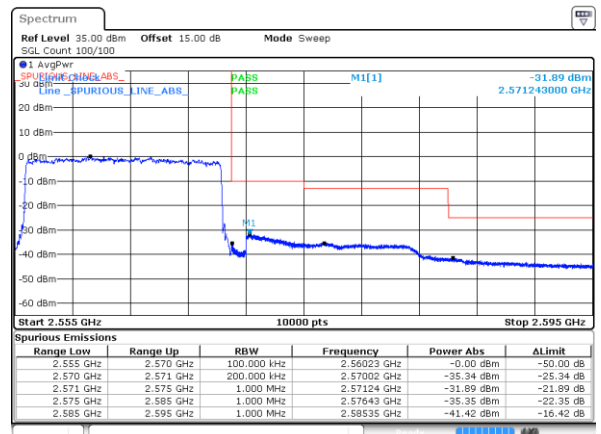
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:40:47

15MHz_High_QPSK_1@74



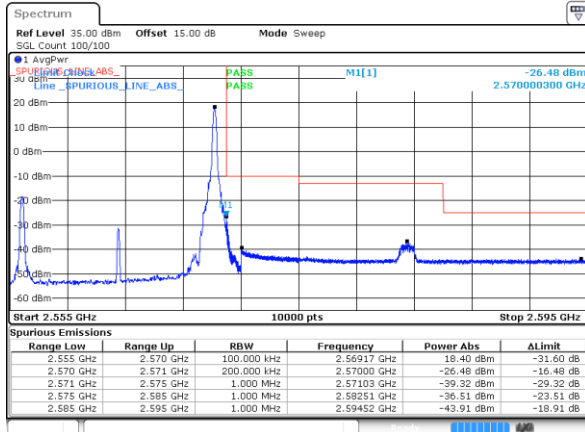
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:41:08

15MHz_High_QPSK_75@0



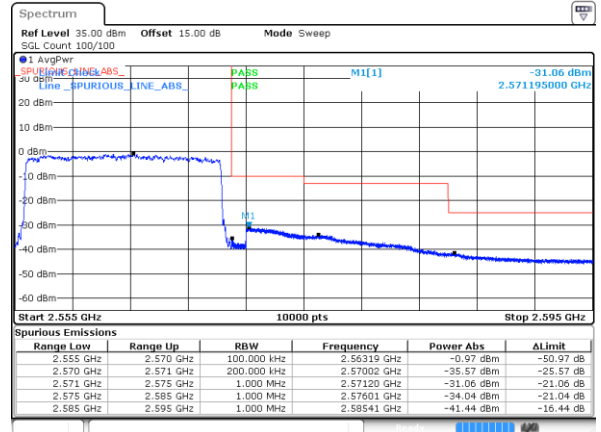
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:41:21

15MHz_High_16QAM_1@74



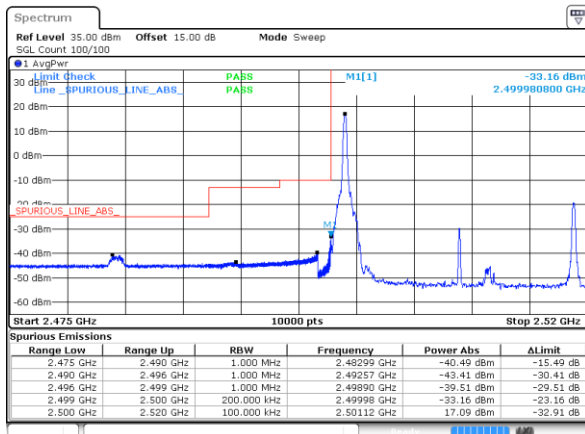
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:41:34

15MHz_High_16QAM_75@0



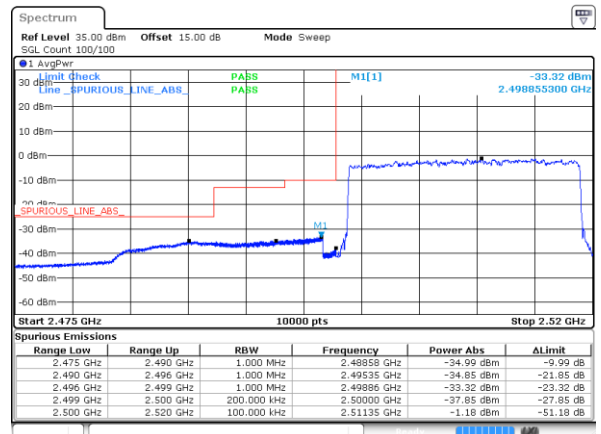
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:41:47

20MHz_Low_QPSK_1@0



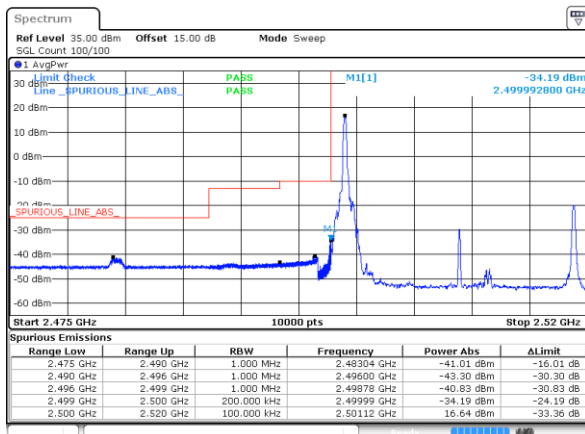
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:42:44

20MHz_Low_QPSK_100@0



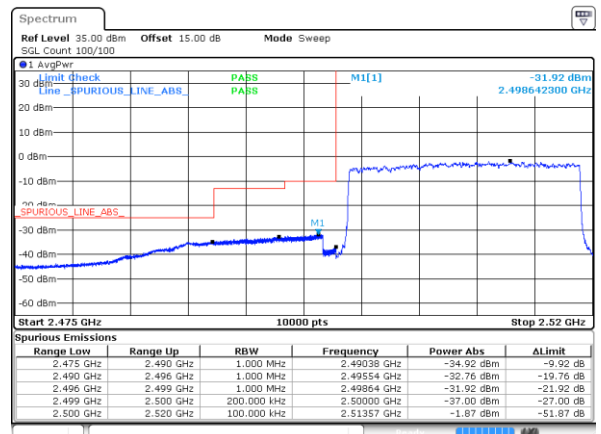
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:42:57

20MHz_Low_16QAM_1@0



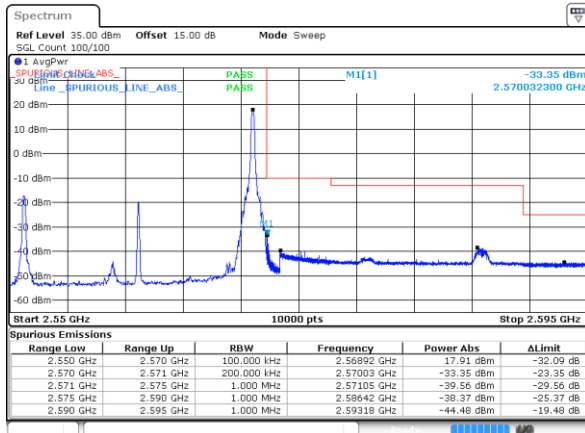
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:43:11

20MHz_Low_16QAM_100@0



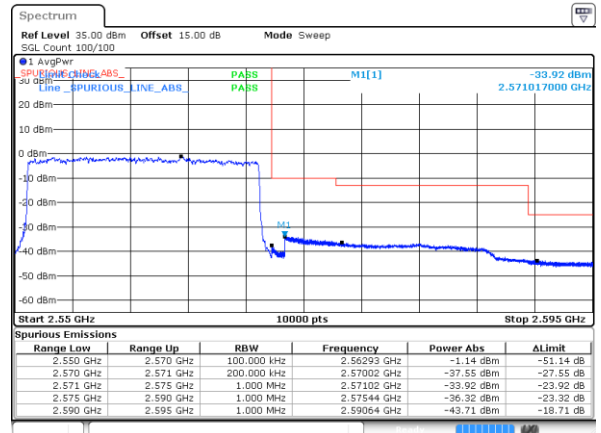
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 19:43:24

20MHz_High_QPSK_1@99



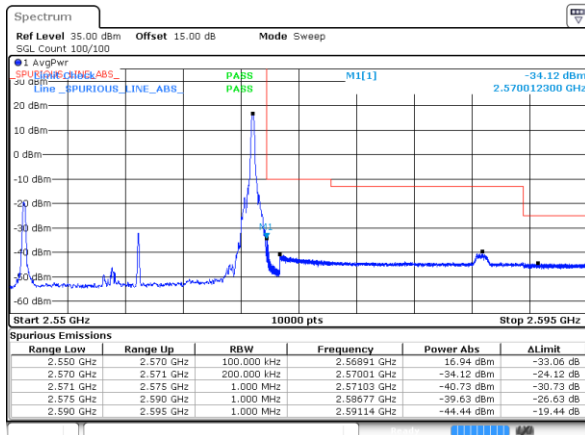
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:43:45

20MHz_High_QPSK_100@0



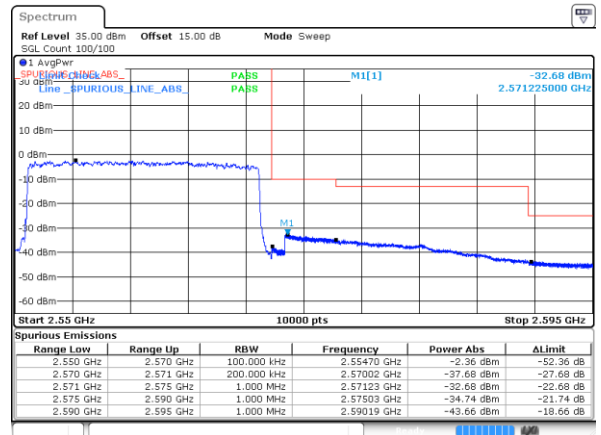
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:43:58

20MHz_High_16QAM_1@99



ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:44:11

20MHz_High_16QAM_100@0



ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 19:44:25

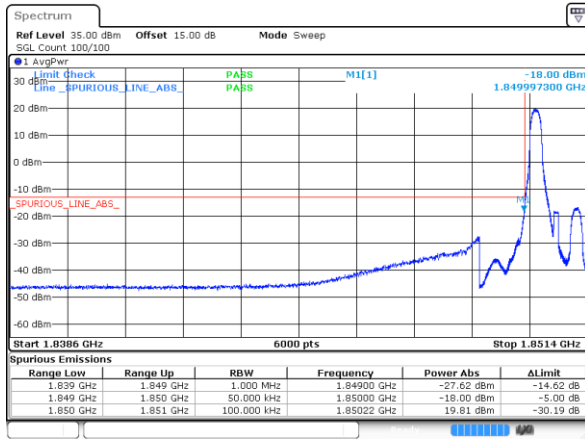
B2 , Normal

Mode	Result (dBm)	Limit	Verdict
1.4MHz_Low_QPSK_1@0	-18.00	See Graphs	Pass
1.4MHz_Low_QPSK_6@0	-21.16	See Graphs	Pass
1.4MHz_Low_16QAM_1@0	-21.44	See Graphs	Pass
1.4MHz_Low_16QAM_6@0	-22.43	See Graphs	Pass
1.4MHz_High_QPSK_1@5	-22.47	See Graphs	Pass
1.4MHz_High_QPSK_6@0	-25.59	See Graphs	Pass
1.4MHz_High_16QAM_1@5	-22.53	See Graphs	Pass
1.4MHz_High_16QAM_6@0	-27.30	See Graphs	Pass
3MHz_Low_QPSK_1@0	-18.95	See Graphs	Pass
3MHz_Low_QPSK_15@0	-21.65	See Graphs	Pass
3MHz_Low_16QAM_1@0	-19.73	See Graphs	Pass
3MHz_Low_16QAM_15@0	-22.65	See Graphs	Pass
3MHz_High_QPSK_1@14	-19.91	See Graphs	Pass
3MHz_High_QPSK_15@0	-24.36	See Graphs	Pass
3MHz_High_16QAM_1@14	-21.05	See Graphs	Pass
3MHz_High_16QAM_15@0	-24.71	See Graphs	Pass
5MHz_Low_QPSK_1@0	-19.36	See Graphs	Pass
5MHz_Low_QPSK_25@0	-24.32	See Graphs	Pass
5MHz_Low_16QAM_1@0	-21.09	See Graphs	Pass
5MHz_Low_16QAM_25@0	-25.18	See Graphs	Pass
5MHz_High_QPSK_1@24	-20.76	See Graphs	Pass
5MHz_High_QPSK_25@0	-26.02	See Graphs	Pass
5MHz_High_16QAM_1@24	-20.93	See Graphs	Pass
5MHz_High_16QAM_25@0	-26.56	See Graphs	Pass
10MHz_Low_QPSK_1@0	-31.39	See Graphs	Pass
10MHz_Low_QPSK_50@0	-28.97	See Graphs	Pass
10MHz_Low_16QAM_1@0	-32.16	See Graphs	Pass
10MHz_Low_16QAM_50@0	-29.23	See Graphs	Pass
10MHz_High_QPSK_1@49	-33.54	See Graphs	Pass
10MHz_High_QPSK_50@0	-28.16	See Graphs	Pass

Mode	Result (dBm)	Limit	Verdict
10MHz_High_16QAM_1@49	-34.62	See Graphs	Pass
10MHz_High_16QAM_50@0	-29.05	See Graphs	Pass
15MHz_Low_QPSK_1@0	-25.52	See Graphs	Pass
15MHz_Low_QPSK_75@0	-28.93	See Graphs	Pass
15MHz_Low_16QAM_1@0	-28.96	See Graphs	Pass
15MHz_Low_16QAM_75@0	-29.50	See Graphs	Pass
15MHz_High_QPSK_1@74	-28.86	See Graphs	Pass
15MHz_High_QPSK_75@0	-28.07	See Graphs	Pass
15MHz_High_16QAM_1@74	-29.27	See Graphs	Pass
15MHz_High_16QAM_75@0	-28.66	See Graphs	Pass
20MHz_Low_QPSK_1@0	-33.24	See Graphs	Pass
20MHz_Low_QPSK_100@0	-29.28	See Graphs	Pass
20MHz_Low_16QAM_1@0	-32.58	See Graphs	Pass
20MHz_Low_16QAM_100@0	-30.39	See Graphs	Pass
20MHz_High_QPSK_1@99	-34.90	See Graphs	Pass
20MHz_High_QPSK_100@0	-29.11	See Graphs	Pass
20MHz_High_16QAM_1@99	-34.44	See Graphs	Pass
20MHz_High_16QAM_100@0	-30.36	See Graphs	Pass

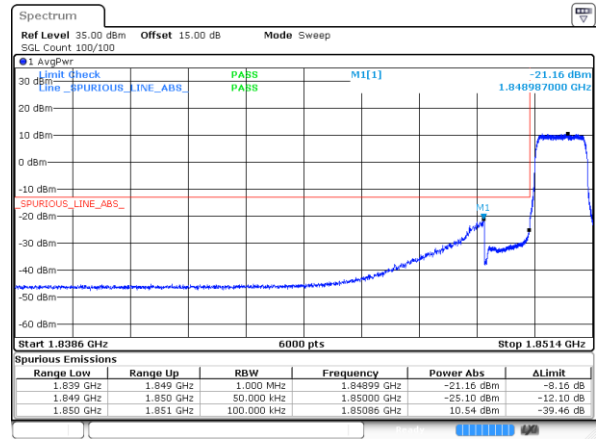
B2 , Normal

1.4MHz_Low_QPSK_1@0



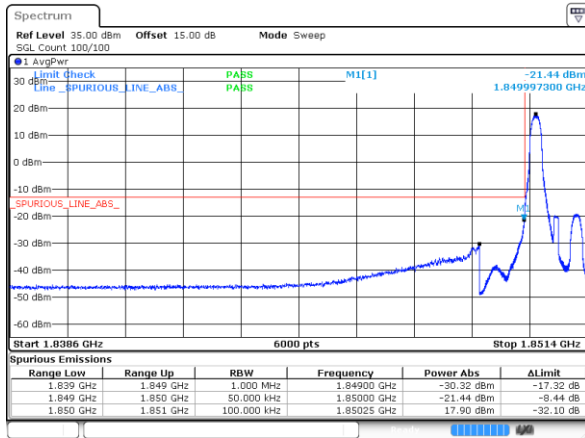
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 18.JAN.2025 13:15:11

1.4MHz_Low_QPSK_6@0



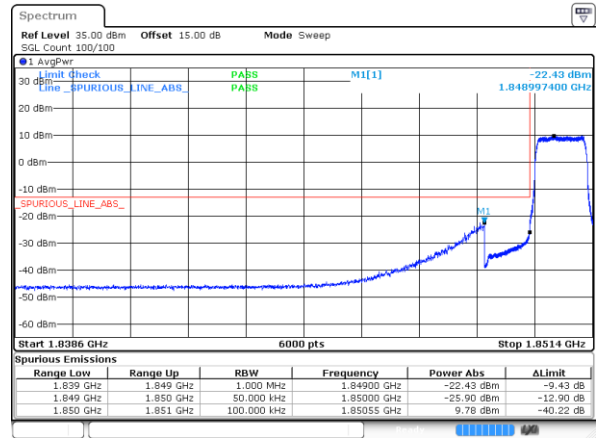
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4.DEC.2024 17:43:55

1.4MHz_Low_16QAM_1@0



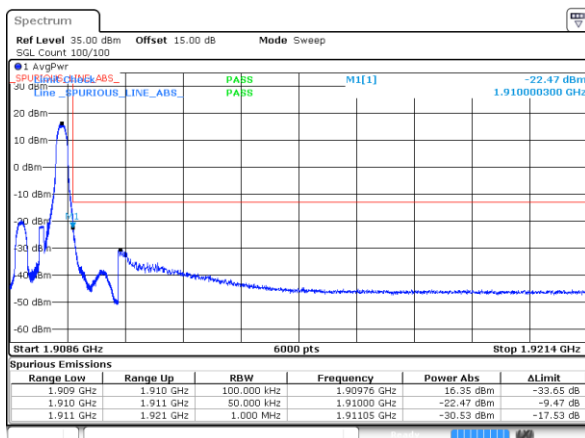
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4.DEC.2024 17:44:17

1.4MHz_Low_16QAM_6@0



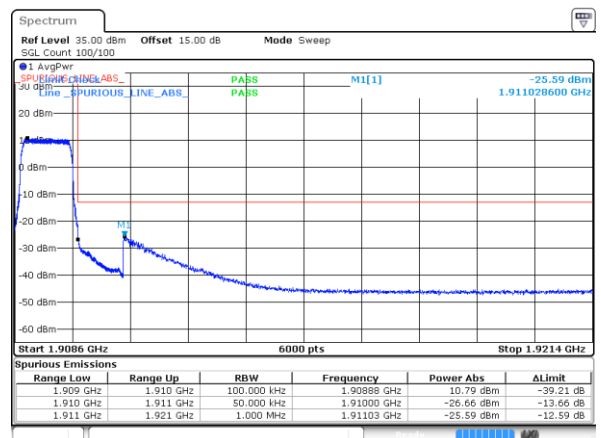
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4.DEC.2024 17:44:38

1.4MHz_High_QPSK_1@5



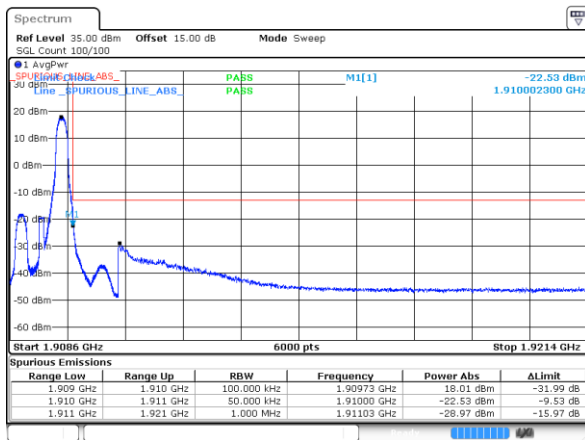
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4.DEC.2024 17:45:09

1.4MHz_High_QPSK_6@0



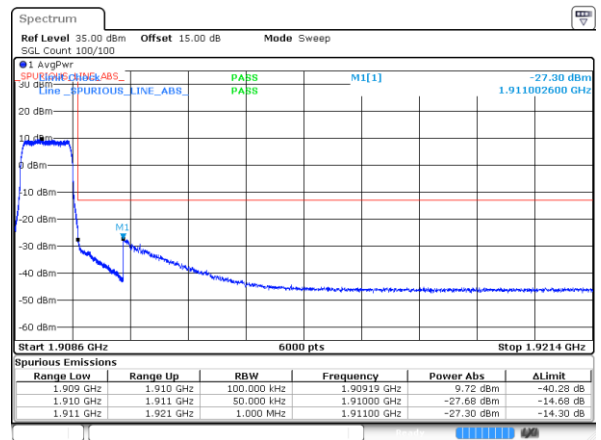
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4.DEC.2024 17:45:41

1.4MHz_High_16QAM_1@5



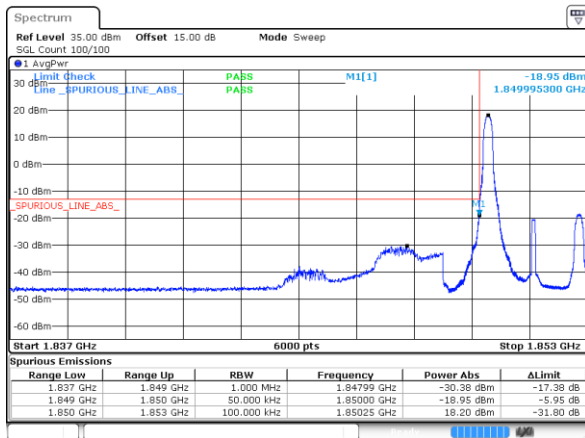
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:46:02

1.4MHz_High_16QAM_6@0



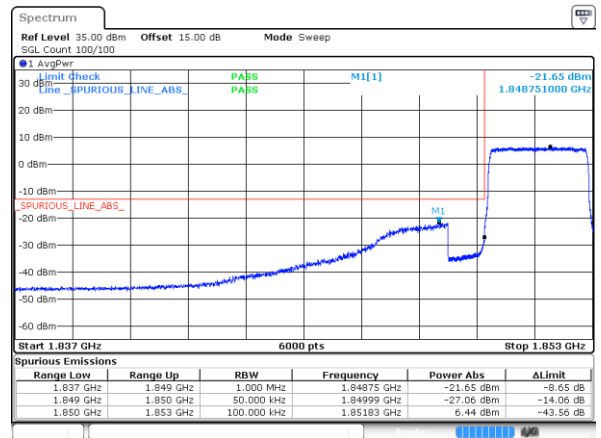
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:46:24

3MHz_Low_QPSK_1@0



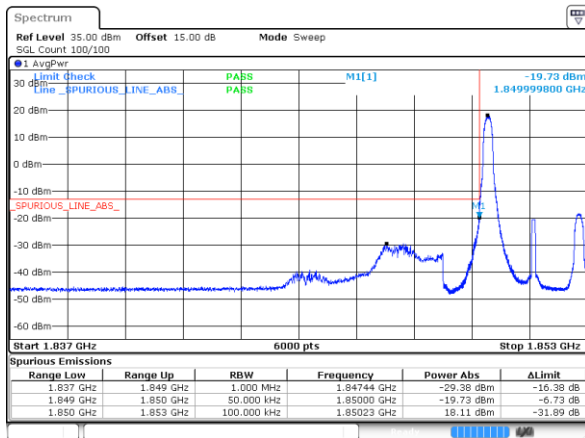
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:48:15

3MHz_Low_QPSK_15@0



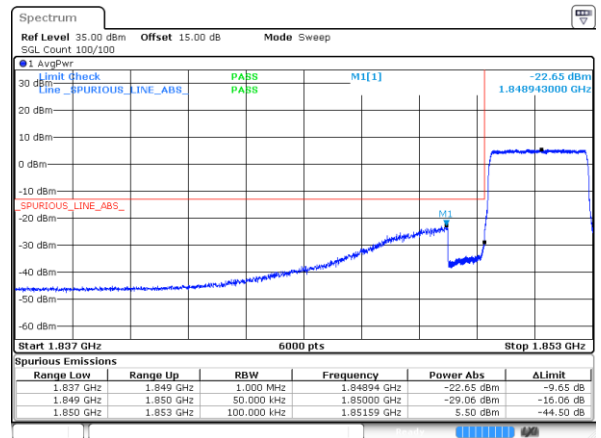
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:48:40

3MHz_Low_16QAM_1@0



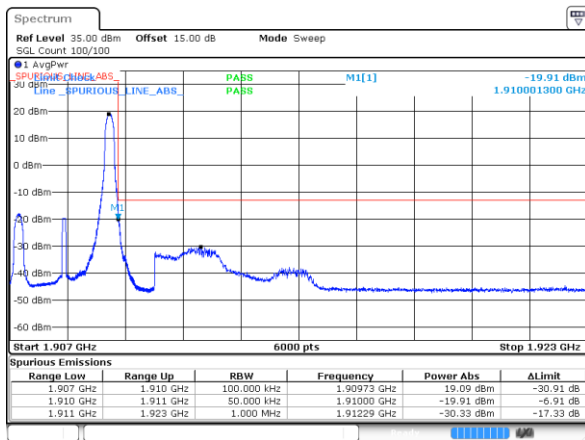
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:49:06

3MHz_Low_16QAM_15@0



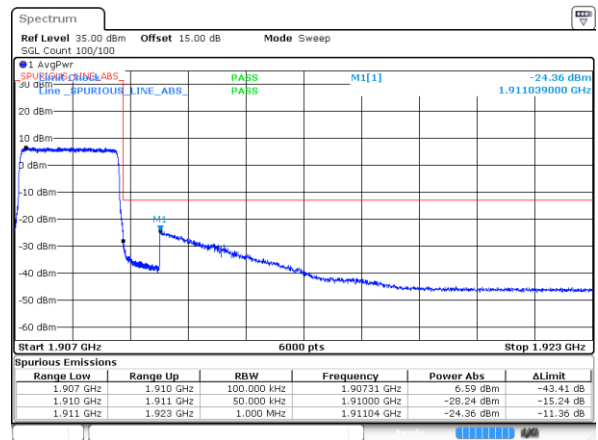
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Date: 4 DEC 2024 17:49:31

3MHz_High_QPSK_1@14



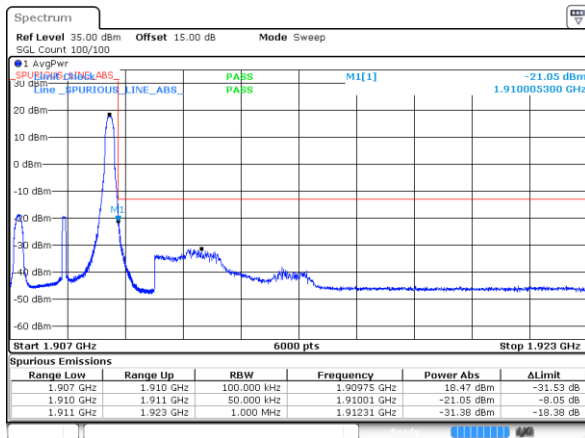
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Date: 4 DEC.2024 17:50:00

3MHz_High_QPSK_15@0



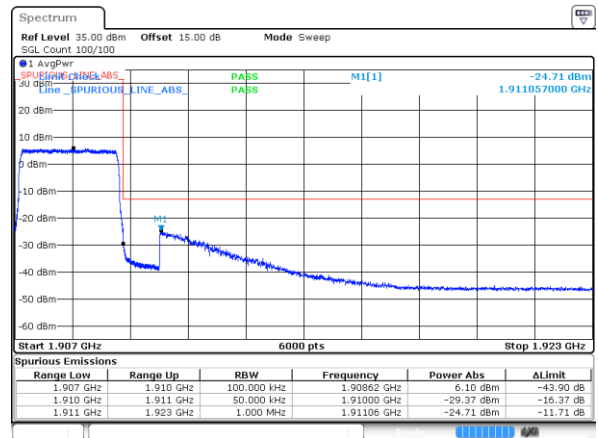
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 17:50:25

3MHz_High_16QAM_1@14



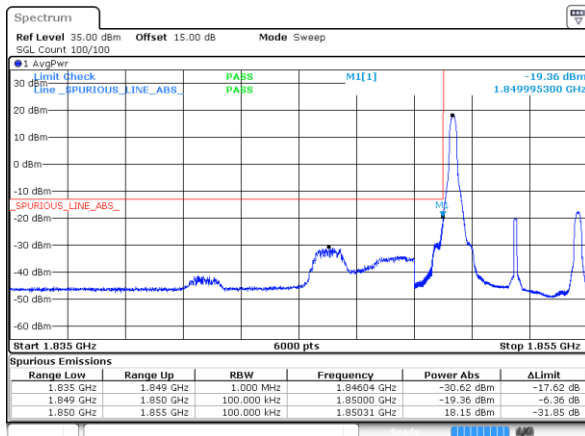
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 17:50:50

3MHz_High_16QAM_15@0



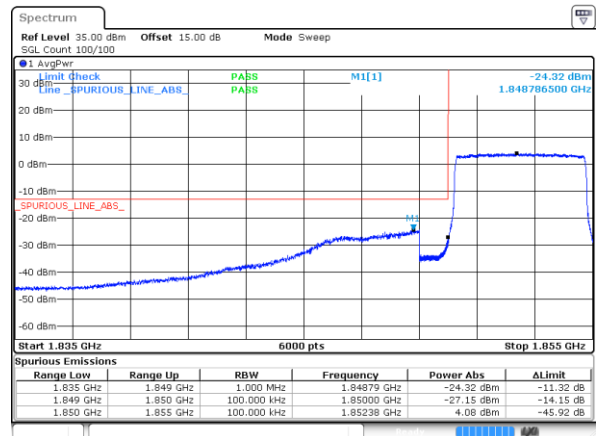
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 17:51:16

5MHz_Low_QPSK_1@0



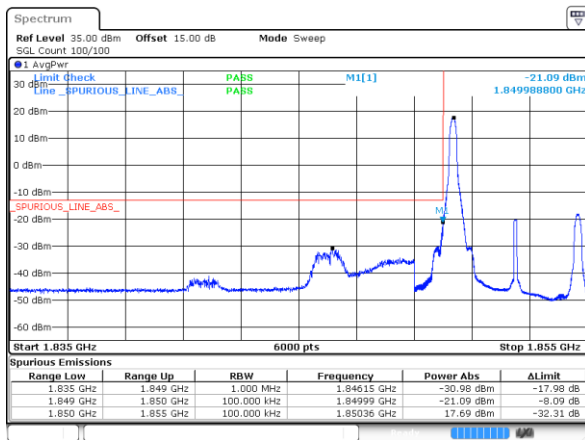
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 17:53:11

5MHz_Low_QPSK_25@0



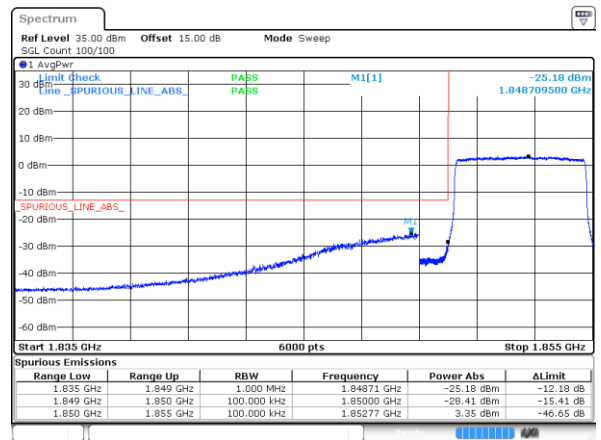
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC.2024 17:53:34

5MHz_Low_16QAM_1@0



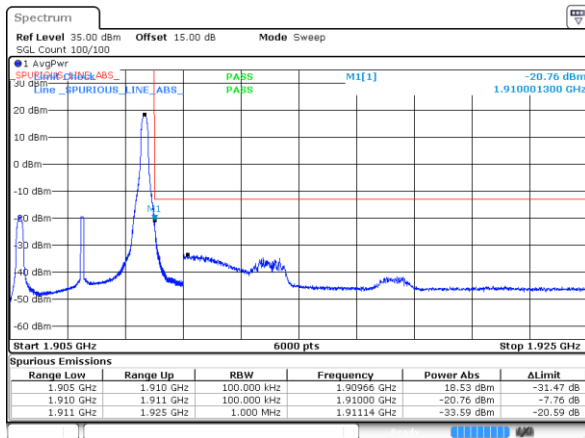
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:53:57

5MHz_Low_16QAM_25@0



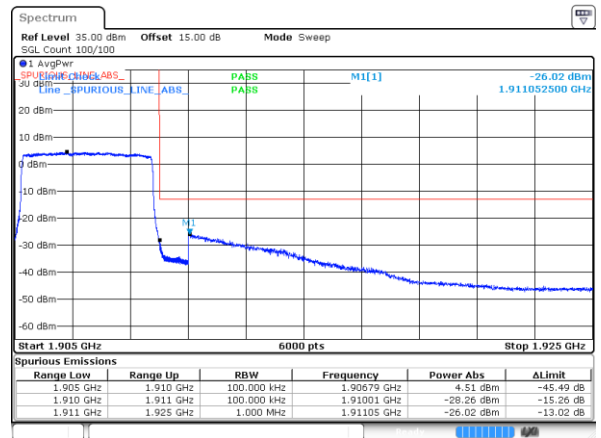
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:54:20

5MHz_High_QPSK_1@24



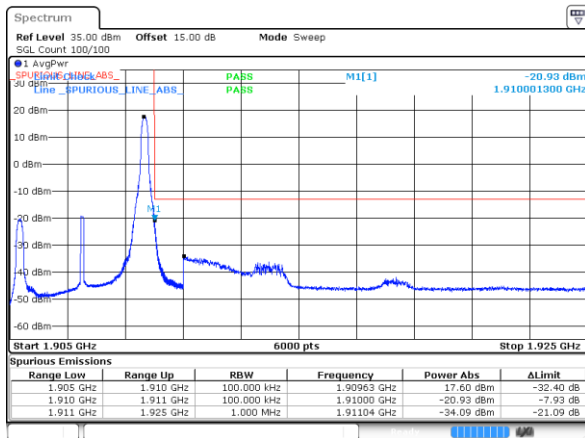
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:54:48

5MHz_High_QPSK_25@0



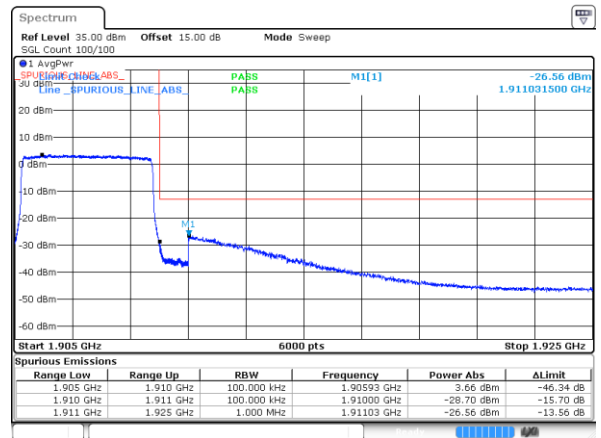
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:55:11

5MHz_High_16QAM_1@24



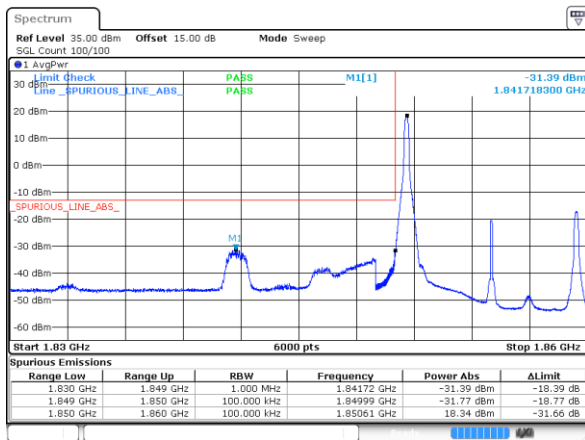
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:55:34

5MHz_High_16QAM_25@0



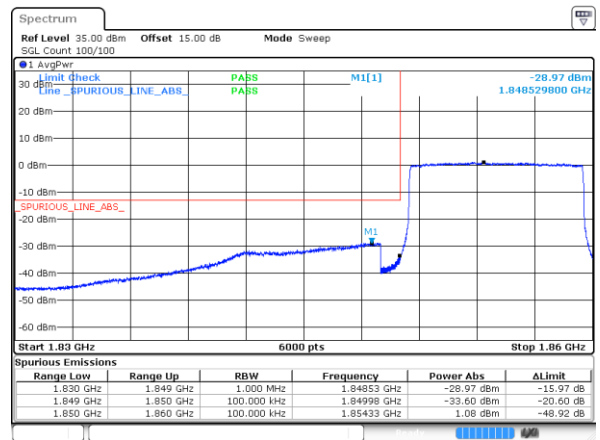
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:55:57

10MHz_Low_QPSK_1@0



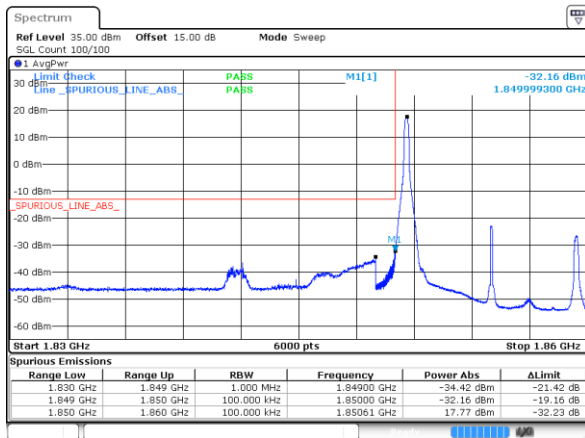
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:58:00

10MHz_Low_QPSK_50@0



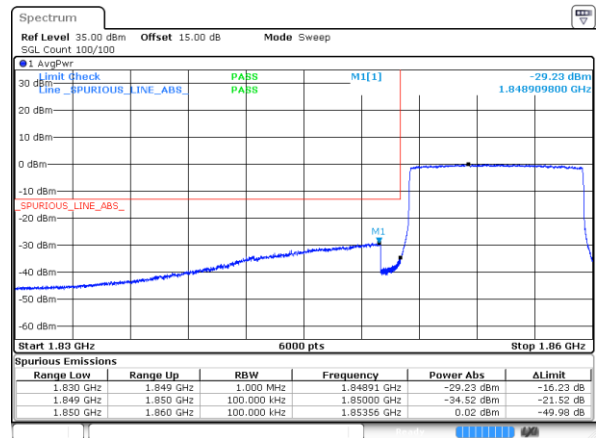
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:58:34

10MHz_Low_16QAM_1@0



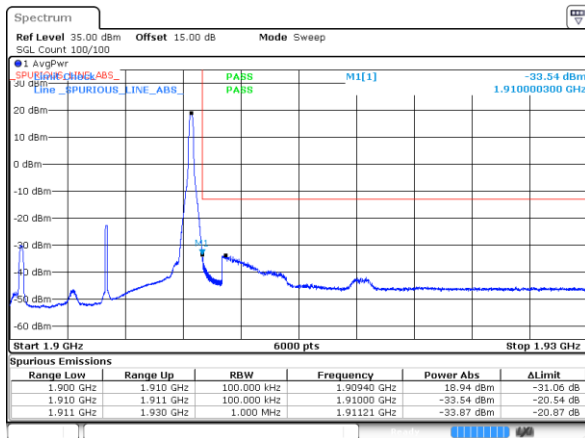
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:59:08

10MHz_Low_16QAM_50@0



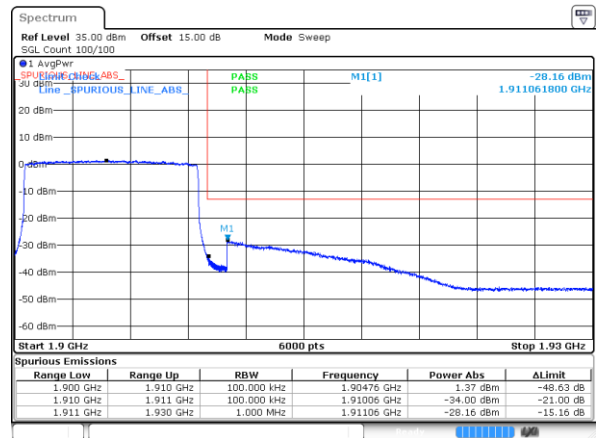
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 17:59:42

10MHz_High_QPSK_1@49



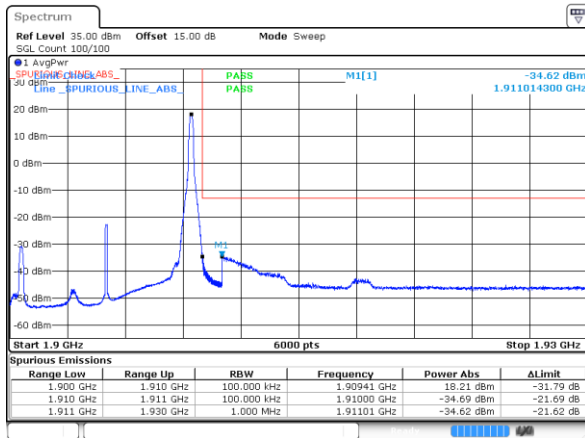
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:00:20

10MHz_High_QPSK_50@0



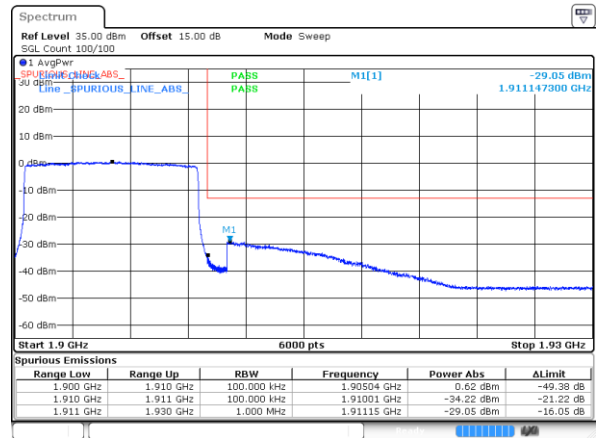
ProjectNo.:RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:00:54

10MHz_High_16QAM_1@49



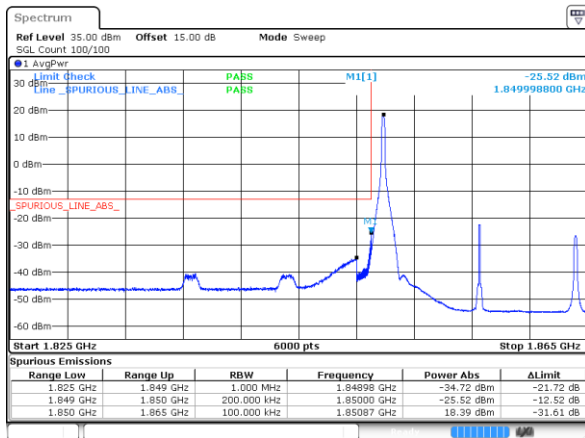
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:01:28

10MHz_High_16QAM_50@0



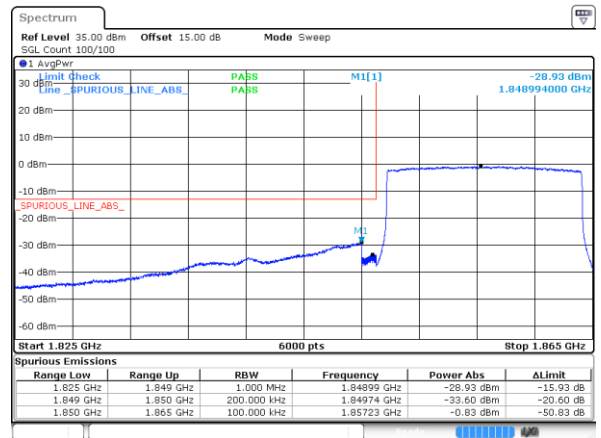
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:02:36

15MHz_Low_QPSK_1@0



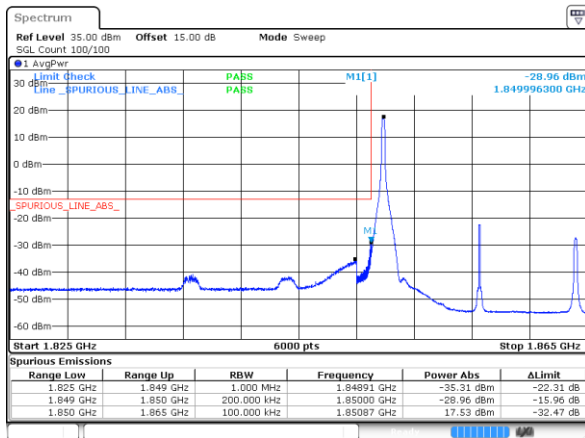
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:04:48

15MHz_Low_QPSK_75@0



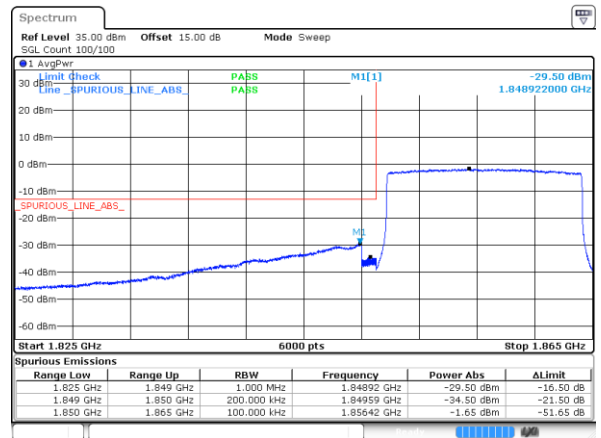
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:05:31

15MHz_Low_16QAM_1@0



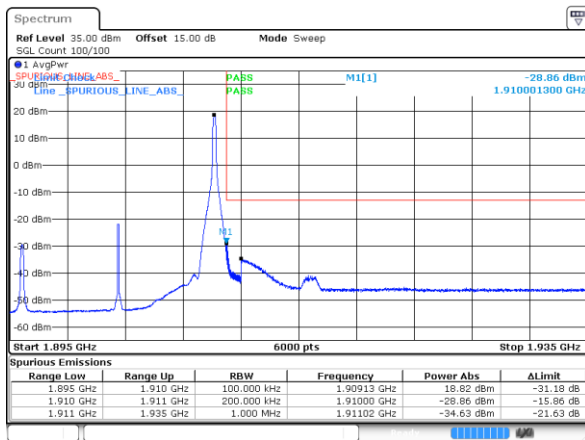
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:06:14

15MHz_Low_16QAM_75@0



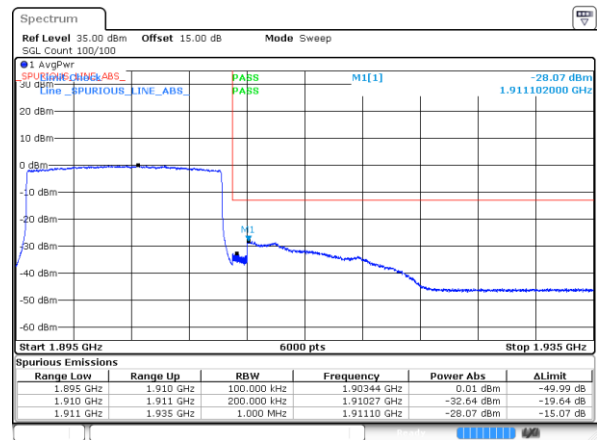
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:06:57

15MHz_High_QPSK_1@74



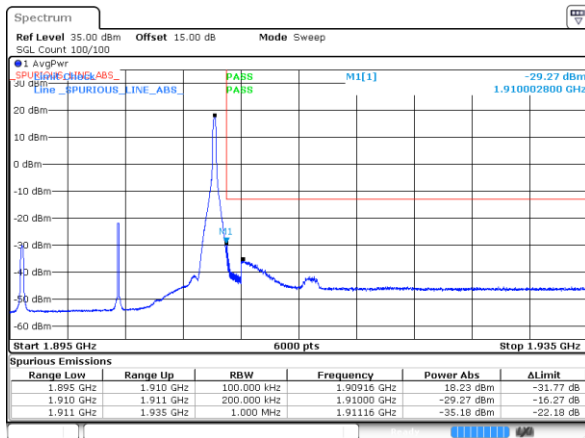
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:07:44

15MHz_High_QPSK_75@0



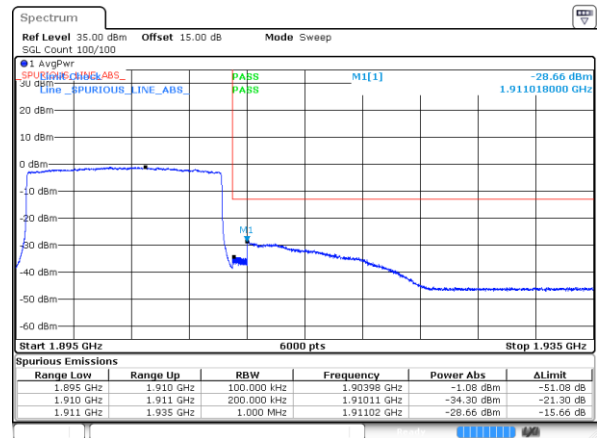
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:08:27

15MHz_High_16QAM_1@74



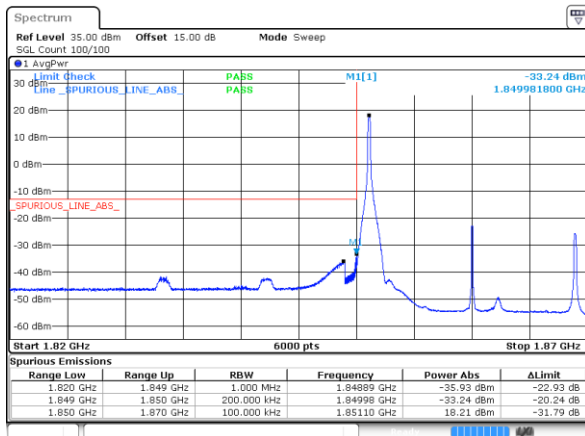
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:09:10

15MHz_High_16QAM_75@0



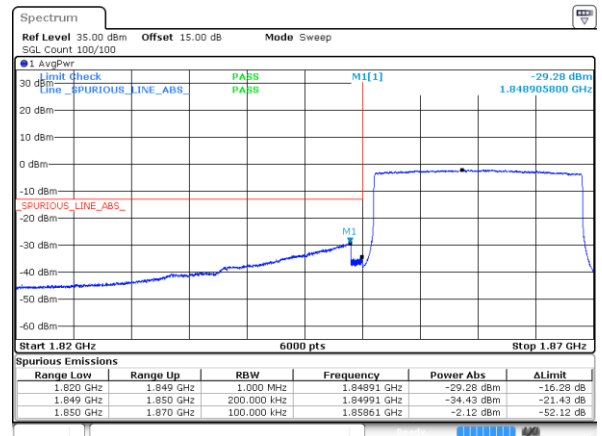
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Date: 4 DEC 2024 18:09:53

20MHz_Low_QPSK_1@0



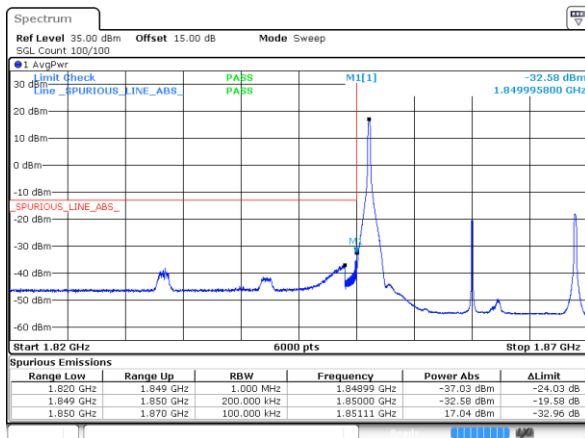
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:12:09

20MHz_Low_QPSK_100@0



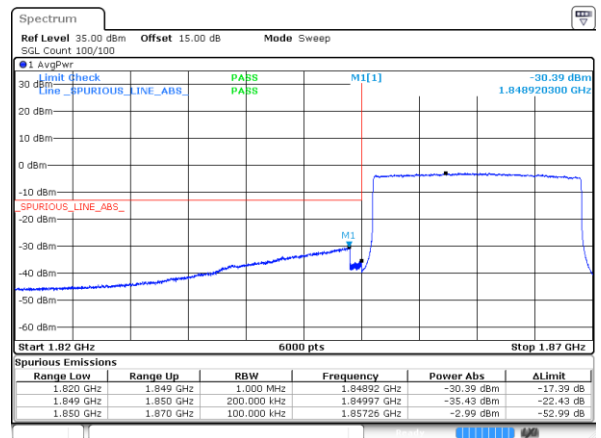
ProjectNo: RKSA240906001 Tester:Jason Lu
Date: 4 DEC 2024 18:13:03

20MHz_Low_16QAM_1@0



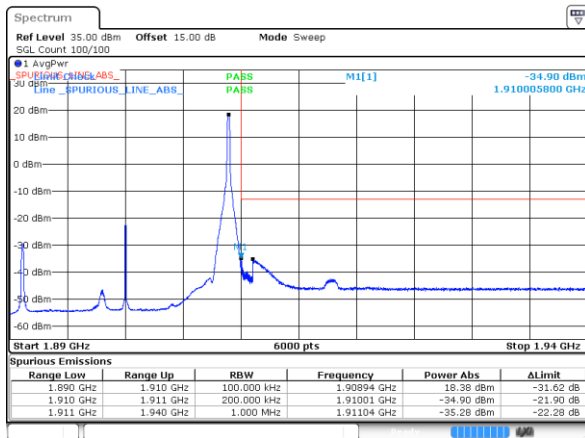
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:13:56

20MHz_Low_16QAM_100@0



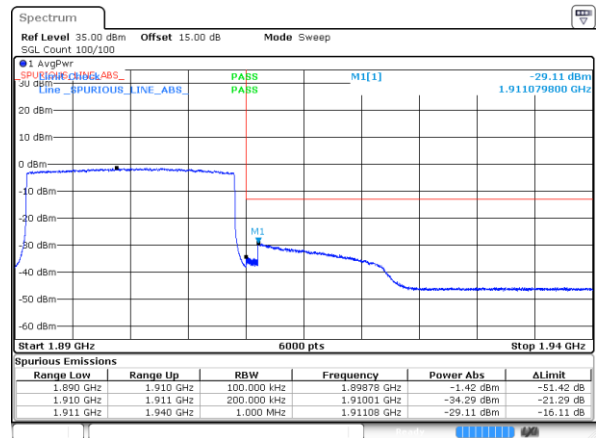
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Date: 4 DEC 2024 18:14:50

20MHz_High_QPSK_1@99



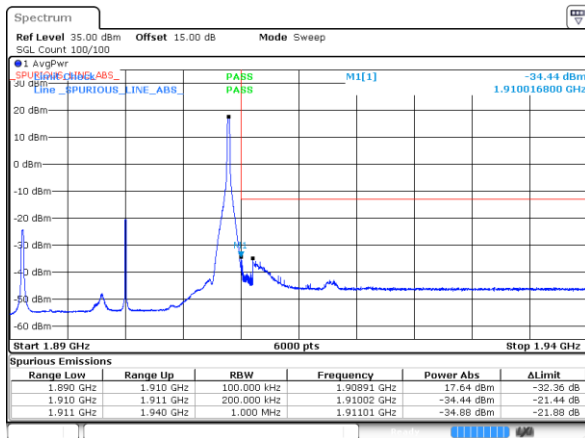
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Date: 4 DEC 2024 18:15:48

20MHz_High_QPSK_100@0



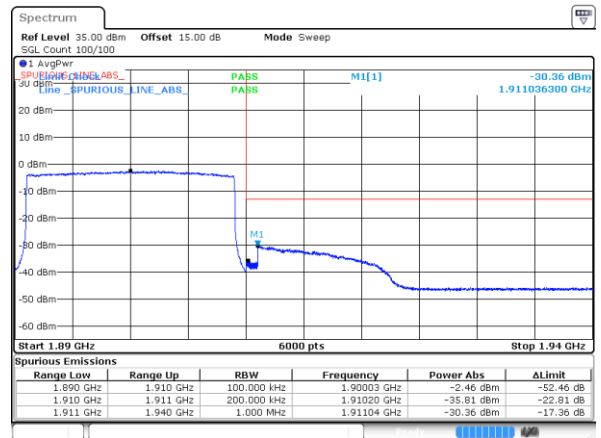
ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:16:41

20MHz_High_16QAM_1@99



ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:17:35

20MHz_High_16QAM_100@0



ProjectNo: RKSA240906001 Tester: Jason Lu
Date: 4 DEC 2024 18:18:29