

Effective (Isotropic) Radiated Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Gain (dBi)	E(I)RP (dBm)	Limit (dBm)	Verdict
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Left	25.68	1.88	27.56	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Right	25.69	1.88	27.57	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_Full	25.57	1.88	27.45	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Outer_Full	25.1	1.88	26.98	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Left	25.7	1.88	27.58	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Right	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_Full	25.55	1.88	27.43	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Outer_Full	24.58	1.88	26.46	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Left	24.22	1.88	26.1	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Right	24.24	1.88	26.12	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_Full	24.56	1.88	26.44	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Outer_Full	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Left	22.72	1.88	24.6	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Right	22.84	1.88	24.72	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_Full	23.03	1.88	24.91	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Outer_Full	23.05	1.88	24.93	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Left	20.97	1.88	22.85	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Right	20.94	1.88	22.82	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_Full	21.16	1.88	23.04	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Outer_Full	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Right	25.84	1.88	27.72	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_Full	25.79	1.88	27.67	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Outer_Full	25.37	1.88	27.25	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Left	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Right	25.7	1.88	27.58	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_Full	25.81	1.88	27.69	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Outer_Full	24.75	1.88	26.63	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Left	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Right	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_Full	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Outer_Full	23.74	1.88	25.62	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Left	23	1.88	24.88	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Right	23.19	1.88	25.07	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_Full	23.19	1.88	25.07	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Outer_Full	23.15	1.88	25.03	30	PASS

DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Left	21.05	1.88	22.93	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Right	21.28	1.88	23.16	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_Full	21.53	1.88	23.41	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Outer_Full	21.25	1.88	23.13	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Left	26.08	1.88	27.96	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Right	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_Full	26.1	1.88	27.98	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Outer_Full	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Left	26.05	1.88	27.93	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Right	26.09	1.88	27.97	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_Full	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Outer_Full	25.08	1.88	26.96	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Left	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Right	25.01	1.88	26.89	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_Full	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Outer_Full	23.88	1.88	25.76	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Left	23.34	1.88	25.22	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Right	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_Full	23.46	1.88	25.34	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Outer_Full	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Left	21.45	1.88	23.33	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Right	21.67	1.88	23.55	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_Full	21.48	1.88	23.36	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Outer_Full	21.52	1.88	23.4	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Left	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Right	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_Full	25.54	1.88	27.42	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Outer_Full	25.04	1.88	26.92	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Left	25.63	1.88	27.51	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Right	25.67	1.88	27.55	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_Full	25.48	1.88	27.36	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Outer_Full	24.48	1.88	26.36	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Left	24.36	1.88	26.24	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Right	24.35	1.88	26.23	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_Full	24.38	1.88	26.26	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Outer_Full	23.44	1.88	25.32	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Left	22.89	1.88	24.77	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Right	22.97	1.88	24.85	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_Full	22.78	1.88	24.66	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Outer_Full	22.97	1.88	24.85	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Left	20.94	1.88	22.82	30	PASS

DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Right	21.04	1.88	22.92	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_Full	20.89	1.88	22.77	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Outer_Full	20.52	1.88	22.4	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Right	25.92	1.88	27.8	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_Full	25.79	1.88	27.67	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Outer_Full	25.18	1.88	27.06	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Left	25.92	1.88	27.8	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Right	25.96	1.88	27.84	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_Full	25.66	1.88	27.54	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Outer_Full	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Left	24.55	1.88	26.43	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Right	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_Full	24.49	1.88	26.37	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Outer_Full	23.64	1.88	25.52	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Left	23.35	1.88	25.23	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Right	23.5	1.88	25.38	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_Full	23.15	1.88	25.03	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Outer_Full	23.15	1.88	25.03	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Left	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Right	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_Full	21.17	1.88	23.05	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Outer_Full	21.07	1.88	22.95	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Left	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Right	25.89	1.88	27.77	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_Full	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Outer_Full	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Left	26.36	1.88	28.24	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Right	26.38	1.88	28.26	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_Full	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Outer_Full	25.01	1.88	26.89	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Left	24.99	1.88	26.87	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Right	24.93	1.88	26.81	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_Full	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Outer_Full	23.85	1.88	25.73	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Left	23.46	1.88	25.34	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Right	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_Full	23.27	1.88	25.15	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Outer_Full	23.36	1.88	25.24	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Left	21.76	1.88	23.64	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Right	21.75	1.88	23.63	30	PASS

DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_Full	21.36	1.88	23.24	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Outer_Full	21.41	1.88	23.29	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Left	25.4	1.88	27.28	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Right	25.39	1.88	27.27	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_Full	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Outer_Full	25	1.88	26.88	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Left	25.29	1.88	27.17	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Right	25.26	1.88	27.14	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_Full	25.48	1.88	27.36	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Outer_Full	24.47	1.88	26.35	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Left	24.33	1.88	26.21	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Right	24.38	1.88	26.26	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_Full	24.31	1.88	26.19	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Outer_Full	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Left	23.21	1.88	25.09	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Right	23.22	1.88	25.1	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_Full	22.91	1.88	24.79	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Outer_Full	22.89	1.88	24.77	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Left	20.91	1.88	22.79	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Right	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_Full	20.93	1.88	22.81	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Outer_Full	20.91	1.88	22.79	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Left	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Right	25.79	1.88	27.67	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_Full	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Outer_Full	25.15	1.88	27.03	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Left	25.81	1.88	27.69	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Right	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.63	1.88	27.51	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Left	24.64	1.88	26.52	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Right	24.83	1.88	26.71	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.54	1.88	26.42	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.66	1.88	25.54	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Left	22.81	1.88	24.69	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Right	22.9	1.88	24.78	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_Full	23.13	1.88	25.01	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Outer_Full	23.1	1.88	24.98	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Left	20.76	1.88	22.64	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Right	20.89	1.88	22.77	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_Full	21.13	1.88	23.01	30	PASS

DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Outer_Full	21.06	1.88	22.94	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Left	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Right	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_Full	26.08	1.88	27.96	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Outer_Full	25.48	1.88	27.36	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Left	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Right	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_Full	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.91	1.88	26.79	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Left	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Right	24.97	1.88	26.85	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_Full	24.79	1.88	26.67	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Outer_Full	23.88	1.88	25.76	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Left	23.62	1.88	25.5	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Right	23.53	1.88	25.41	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_Full	23.31	1.88	25.19	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Outer_Full	23.37	1.88	25.25	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Left	21.12	1.88	23	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Right	21.09	1.88	22.97	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_Full	21.37	1.88	23.25	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Outer_Full	21.29	1.88	23.17	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Left	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Right	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_Full	25.76	1.88	27.64	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Outer_Full	25.18	1.88	27.06	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Right	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_Full	25.64	1.88	27.52	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Outer_Full	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Left	24.77	1.88	26.65	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Right	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_Full	24.11	1.88	25.99	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Outer_Full	23.61	1.88	25.49	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Left	23.27	1.88	25.15	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Right	23.33	1.88	25.21	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_Full	23.03	1.88	24.91	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Outer_Full	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Left	20.92	1.88	22.8	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Right	21.03	1.88	22.91	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_Full	21.17	1.88	23.05	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Outer_Full	21.11	1.88	22.99	30	PASS

DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Left	25.91	1.88	27.79	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Right	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_Full	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Outer_Full	25.42	1.88	27.3	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Left	25.96	1.88	27.84	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Right	26.13	1.88	28.01	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_Full	25.74	1.88	27.62	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Outer_Full	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Left	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Right	24.73	1.88	26.61	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_Full	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Outer_Full	23.76	1.88	25.64	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Left	23.74	1.88	25.62	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Right	23.81	1.88	25.69	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_Full	23.21	1.88	25.09	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Outer_Full	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Left	21.49	1.88	23.37	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Right	21.61	1.88	23.49	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_Full	21.32	1.88	23.2	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Outer_Full	21.29	1.88	23.17	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Left	26.12	1.88	28	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Right	26.12	1.88	28	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_Full	26.14	1.88	28.02	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Outer_Full	25.5	1.88	27.38	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Left	25.92	1.88	27.8	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Right	25.82	1.88	27.7	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_Full	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Outer_Full	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Left	25.05	1.88	26.93	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Right	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_Full	24.93	1.88	26.81	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Outer_Full	23.95	1.88	25.83	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Left	23.59	1.88	25.47	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Right	23.47	1.88	25.35	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_Full	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Outer_Full	23.47	1.88	25.35	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Left	21.42	1.88	23.3	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Right	21.37	1.88	23.25	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_Full	21.54	1.88	23.42	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Outer_Full	21.41	1.88	23.29	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Left	25.71	1.88	27.59	30	PASS

DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Right	25.75	1.88	27.63	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_Full	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Outer_Full	25.27	1.88	27.15	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Left	25.82	1.88	27.7	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Right	25.76	1.88	27.64	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_Full	25.76	1.88	27.64	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Outer_Full	24.75	1.88	26.63	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Left	24.53	1.88	26.41	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Right	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.61	1.88	26.49	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Outer_Full	23.66	1.88	25.54	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Left	23.2	1.88	25.08	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Right	23.18	1.88	25.06	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_Full	23.15	1.88	25.03	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Outer_Full	23.18	1.88	25.06	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Left	21.3	1.88	23.18	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Right	21.35	1.88	23.23	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_Full	21.12	1.88	23	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Outer_Full	21.15	1.88	23.03	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Left	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Right	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_Full	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Outer_Full	25.42	1.88	27.3	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Left	25.66	1.88	27.54	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Right	25.91	1.88	27.79	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.86	1.88	27.74	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.87	1.88	26.75	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Left	24.73	1.88	26.61	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Right	24.97	1.88	26.85	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.78	1.88	25.66	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Left	22.84	1.88	24.72	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Right	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_Full	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Outer_Full	23.36	1.88	25.24	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Left	20.97	1.88	22.85	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Right	21.24	1.88	23.12	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_Full	21.37	1.88	23.25	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Outer_Full	21.31	1.88	23.19	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Left	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Right	25.8	1.88	27.68	30	PASS

DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_Full	26.18	1.88	28.06	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Outer_Full	25.54	1.88	27.42	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Left	26.31	1.88	28.19	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Right	26.28	1.88	28.16	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_Full	26.05	1.88	27.93	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Outer_Full	25.04	1.88	26.92	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Left	25.3	1.88	27.18	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Right	25.27	1.88	27.15	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_Full	25.05	1.88	26.93	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Outer_Full	23.97	1.88	25.85	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Left	23.61	1.88	25.49	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Right	23.58	1.88	25.46	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_Full	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Outer_Full	23.5	1.88	25.38	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Left	21.07	1.88	22.95	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Right	21.11	1.88	22.99	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_Full	21.46	1.88	23.34	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Outer_Full	21.46	1.88	23.34	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Left	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Right	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_Full	25.79	1.88	27.67	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Outer_Full	25.21	1.88	27.09	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Right	25.72	1.88	27.6	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_Full	25.72	1.88	27.6	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Outer_Full	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Left	24.5	1.88	26.38	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Right	24.48	1.88	26.36	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.68	1.88	26.56	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Outer_Full	23.66	1.88	25.54	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Left	23.17	1.88	25.05	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Right	23.16	1.88	25.04	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_Full	23.17	1.88	25.05	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Outer_Full	23.11	1.88	24.99	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Left	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Right	21.09	1.88	22.97	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_Full	21.18	1.88	23.06	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Outer_Full	21.19	1.88	23.07	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Right	25.93	1.88	27.81	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_Full	25.95	1.88	27.83	30	PASS

DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Outer_Full	25.39	1.88	27.27	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Left	25.84	1.88	27.72	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Right	26.16	1.88	28.04	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Left	24.57	1.88	26.45	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Right	24.76	1.88	26.64	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.75	1.88	26.63	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.87	1.88	25.75	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Right	23.62	1.88	25.5	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_Full	23.25	1.88	25.13	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Outer_Full	23.29	1.88	25.17	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Left	21.36	1.88	23.24	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Right	21.59	1.88	23.47	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_Full	21.28	1.88	23.16	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Outer_Full	21.4	1.88	23.28	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Left	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Right	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_Full	26.07	1.88	27.95	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Outer_Full	25.58	1.88	27.46	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Left	26.2	1.88	28.08	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Right	26.15	1.88	28.03	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.98	1.88	27.86	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Outer_Full	25.06	1.88	26.94	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Left	24.98	1.88	26.86	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Right	24.9	1.88	26.78	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_Full	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Outer_Full	23.91	1.88	25.79	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Left	23.69	1.88	25.57	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Right	23.57	1.88	25.45	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_Full	23.51	1.88	25.39	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Outer_Full	23.44	1.88	25.32	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Left	21.46	1.88	23.34	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Right	21.44	1.88	23.32	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_Full	21.45	1.88	23.33	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Outer_Full	21.43	1.88	23.31	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Left	25.86	1.88	27.74	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Right	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_Full	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Outer_Full	25.47	1.88	27.35	30	PASS

DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Right	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_Full	25.98	1.88	27.86	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Outer_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Left	24.61	1.88	26.49	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Right	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_Full	24.8	1.88	26.68	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Outer_Full	23.84	1.88	25.72	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Left	23.34	1.88	25.22	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Right	23.46	1.88	25.34	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_Full	23.37	1.88	25.25	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Outer_Full	23.32	1.88	25.2	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Left	21.25	1.88	23.13	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Right	21.45	1.88	23.33	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_Full	21.36	1.88	23.24	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Outer_Full	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Left	25.93	1.88	27.81	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Right	26.13	1.88	28.01	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_Full	26.14	1.88	28.02	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Outer_Full	25.57	1.88	27.45	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Left	25.87	1.88	27.75	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Right	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_Full	26.06	1.88	27.94	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Outer_Full	25.1	1.88	26.98	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Left	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Right	25.04	1.88	26.92	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Outer_Full	23.95	1.88	25.83	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Left	23.3	1.88	25.18	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Right	23.49	1.88	25.37	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_Full	23.5	1.88	25.38	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Outer_Full	23.48	1.88	25.36	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Left	21.2	1.88	23.08	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Right	21.43	1.88	23.31	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_Full	21.49	1.88	23.37	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Outer_Full	21.54	1.88	23.42	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Left	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Right	26.12	1.88	28	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_Full	26.26	1.88	28.14	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Outer_Full	25.68	1.88	27.56	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Left	26.09	1.88	27.97	30	PASS

DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Right	26.16	1.88	28.04	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_Full	26.15	1.88	28.03	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Outer_Full	25.16	1.88	27.04	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Left	25.19	1.88	27.07	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Right	25.24	1.88	27.12	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_Full	25.04	1.88	26.92	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Outer_Full	24.13	1.88	26.01	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Left	23.54	1.88	25.42	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Right	23.55	1.88	25.43	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_Full	23.6	1.88	25.48	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Outer_Full	23.67	1.88	25.55	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Left	21.34	1.88	23.22	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Right	21.48	1.88	23.36	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_Full	21.59	1.88	23.47	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Outer_Full	21.63	1.88	23.51	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Left	25.58	1.88	27.46	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Right	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_Full	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Outer_Full	25.25	1.88	27.13	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Left	25.64	1.88	27.52	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Right	25.96	1.88	27.84	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_Full	25.64	1.88	27.52	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.62	1.88	26.5	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Left	24.4	1.88	26.28	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Right	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.49	1.88	26.37	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Outer_Full	23.62	1.88	25.5	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Left	22.89	1.88	24.77	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Right	23.18	1.88	25.06	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_Full	23.09	1.88	24.97	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Outer_Full	23.19	1.88	25.07	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Left	20.98	1.88	22.86	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Right	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_Full	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Outer_Full	21.17	1.88	23.05	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Right	25.93	1.88	27.81	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_Full	25.86	1.88	27.74	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Outer_Full	25.4	1.88	27.28	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Left	25.72	1.88	27.6	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Right	25.91	1.88	27.79	30	PASS

DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_Full	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Outer_Full	24.9	1.88	26.78	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Left	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Right	24.62	1.88	26.5	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.63	1.88	26.51	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Outer_Full	23.74	1.88	25.62	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Left	23.38	1.88	25.26	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Right	23.56	1.88	25.44	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_Full	23.21	1.88	25.09	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Outer_Full	23.29	1.88	25.17	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Left	21.3	1.88	23.18	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Right	21.49	1.88	23.37	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_Full	21.28	1.88	23.16	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Outer_Full	21.26	1.88	23.14	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Left	25.7	1.88	27.58	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Right	26.02	1.88	27.9	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_Full	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Outer_Full	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Left	25.79	1.88	27.67	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Right	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_Full	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Outer_Full	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Left	24.73	1.88	26.61	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Right	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Outer_Full	23.82	1.88	25.7	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Left	23.16	1.88	25.04	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Right	23.51	1.88	25.39	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_Full	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Outer_Full	23.44	1.88	25.32	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Left	21.25	1.88	23.13	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Right	21.57	1.88	23.45	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_Full	21.38	1.88	23.26	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Outer_Full	21.44	1.88	23.32	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Left	25.62	1.88	27.5	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Right	25.92	1.88	27.8	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_Full	25.97	1.88	27.85	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Outer_Full	25.38	1.88	27.26	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Left	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Right	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_Full	25.9	1.88	27.78	30	PASS

DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Outer_Full	24.87	1.88	26.75	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Left	24.4	1.88	26.28	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Right	24.81	1.88	26.69	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_Full	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Outer_Full	23.73	1.88	25.61	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Left	23.19	1.88	25.07	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Right	23.38	1.88	25.26	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_Full	23.22	1.88	25.1	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Outer_Full	23.25	1.88	25.13	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Left	21.21	1.88	23.09	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Right	21.5	1.88	23.38	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_Full	21.23	1.88	23.11	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Outer_Full	21.34	1.88	23.22	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Left	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Right	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_Full	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Outer_Full	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Left	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Right	25.96	1.88	27.84	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_Full	25.82	1.88	27.7	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Outer_Full	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Left	24.55	1.88	26.43	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Right	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_Full	24.73	1.88	26.61	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Outer_Full	23.86	1.88	25.74	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Left	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Right	23.49	1.88	25.37	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_Full	23.33	1.88	25.21	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Outer_Full	23.3	1.88	25.18	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Left	20.93	1.88	22.81	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Right	21.28	1.88	23.16	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_Full	21.27	1.88	23.15	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Outer_Full	21.33	1.88	23.21	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Left	25.82	1.88	27.7	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Right	26.08	1.88	27.96	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_Full	26.26	1.88	28.14	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Outer_Full	25.62	1.88	27.5	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Left	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Right	26.05	1.88	27.93	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_Full	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Outer_Full	25.04	1.88	26.92	30	PASS

DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Left	24.83	1.88	26.71	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Right	25.14	1.88	27.02	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_Full	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Outer_Full	24.02	1.88	25.9	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Left	23.34	1.88	25.22	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Right	23.55	1.88	25.43	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_Full	23.5	1.88	25.38	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Outer_Full	23.4	1.88	25.28	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Left	21.18	1.88	23.06	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Right	21.33	1.88	23.21	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_Full	21.51	1.88	23.39	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Outer_Full	21.52	1.88	23.4	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Left	25.82	1.88	27.7	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Right	26.18	1.88	28.06	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_Full	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Outer_Full	25.37	1.88	27.25	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Left	25.81	1.88	27.69	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Right	26.09	1.88	27.97	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_Full	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Outer_Full	24.84	1.88	26.72	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Left	24.64	1.88	26.52	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Right	24.94	1.88	26.82	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.77	1.88	26.65	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Outer_Full	23.82	1.88	25.7	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Left	23.13	1.88	25.01	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Right	23.55	1.88	25.43	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_Full	23.36	1.88	25.24	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Outer_Full	23.31	1.88	25.19	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Left	21.27	1.88	23.15	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Right	21.61	1.88	23.49	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_Full	21.3	1.88	23.18	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Outer_Full	21.24	1.88	23.12	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Right	26.21	1.88	28.09	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_Full	26.05	1.88	27.93	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Outer_Full	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Right	26.08	1.88	27.96	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_Full	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Outer_Full	24.94	1.88	26.82	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Left	24.45	1.88	26.33	30	PASS

DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Right	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Outer_Full	23.78	1.88	25.66	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Right	23.76	1.88	25.64	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_Full	23.3	1.88	25.18	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Outer_Full	23.32	1.88	25.2	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Left	21.36	1.88	23.24	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Right	21.72	1.88	23.6	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_Full	21.24	1.88	23.12	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Outer_Full	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Left	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Right	26.16	1.88	28.04	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_Full	26.13	1.88	28.01	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Outer_Full	25.54	1.88	27.42	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Left	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Right	26.16	1.88	28.04	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_Full	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Outer_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Left	24.77	1.88	26.65	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Right	25.07	1.88	26.95	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.83	1.88	26.71	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.84	1.88	25.72	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Right	23.66	1.88	25.54	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_Full	23.38	1.88	25.26	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Outer_Full	23.42	1.88	25.3	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Left	21.42	1.88	23.3	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Right	21.62	1.88	23.5	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_Full	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Outer_Full	21.38	1.88	23.26	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Left	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Right	26.17	1.88	28.05	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_Full	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Outer_Full	25.47	1.88	27.35	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Left	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Right	26.28	1.88	28.16	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_Full	25.85	1.88	27.73	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Outer_Full	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Left	24.61	1.88	26.49	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Right	25.05	1.88	26.93	30	PASS

DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.76	1.88	26.64	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Outer_Full	23.77	1.88	25.65	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Right	23.73	1.88	25.61	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_Full	23.26	1.88	25.14	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Outer_Full	23.35	1.88	25.23	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Left	21.34	1.88	23.22	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Right	21.73	1.88	23.61	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_Full	21.32	1.88	23.2	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Outer_Full	21.32	1.88	23.2	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Right	26.24	1.88	28.12	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_Full	26.01	1.88	27.89	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Outer_Full	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Right	26.08	1.88	27.96	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_Full	25.84	1.88	27.72	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Outer_Full	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Left	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Right	25.14	1.88	27.02	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Outer_Full	23.8	1.88	25.68	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Left	23.13	1.88	25.01	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Right	23.57	1.88	25.45	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_Full	23.35	1.88	25.23	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Outer_Full	23.32	1.88	25.2	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Left	21.02	1.88	22.9	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Right	21.45	1.88	23.33	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_Full	21.34	1.88	23.22	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Outer_Full	21.34	1.88	23.22	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Left	25.86	1.88	27.74	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Right	26.4	1.88	28.28	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_Full	26.07	1.88	27.95	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Outer_Full	25.45	1.88	27.33	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Left	25.97	1.88	27.85	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Right	26.26	1.88	28.14	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.97	1.88	27.85	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Outer_Full	24.93	1.88	26.81	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Left	24.94	1.88	26.82	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Right	25.23	1.88	27.11	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.79	1.88	26.67	30	PASS

DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.84	1.88	25.72	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Right	23.7	1.88	25.58	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_Full	23.42	1.88	25.3	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Outer_Full	23.4	1.88	25.28	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Left	21.18	1.88	23.06	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Right	21.47	1.88	23.35	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_Full	21.31	1.88	23.19	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Outer_Full	21.43	1.88	23.31	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Left	25.94	1.88	27.82	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Right	26.22	1.88	28.1	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+L	Inner_Full	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+L	Outer_Full	25.48	1.88	27.36	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Left	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Right	26.24	1.88	28.12	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+L	Inner_Full	25.84	1.88	27.72	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+L	Outer_Full	24.98	1.88	26.86	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Left	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Right	25.09	1.88	26.97	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+L	Inner_Full	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+L	Outer_Full	23.86	1.88	25.74	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Left	23.26	1.88	25.14	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Right	23.6	1.88	25.48	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+L	Inner_Full	23.35	1.88	25.23	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+L	Outer_Full	23.43	1.88	25.31	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Left	21.3	1.88	23.18	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Right	21.75	1.88	23.63	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+L	Inner_Full	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+L	Outer_Full	21.33	1.88	23.21	30	PASS

Peak-to-Average Ratio for NSA

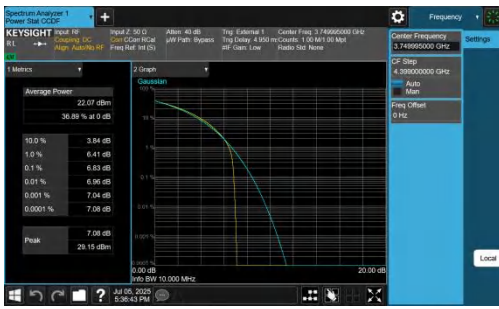
Peak-to-Average Ratio(CCDF)

Test Result

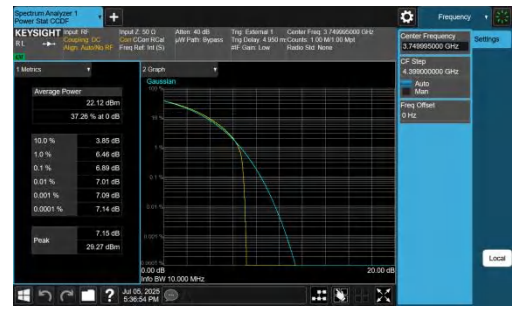
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_5A_n78A	30	5+10	CP-QPSK	M+M	Outer_Full	6.83	≤13	PASS
DC_5A_n78A	30	5+10	CP-16QAM	M+M	Outer_Full	6.89	≤13	PASS
DC_5A_n78A	30	5+10	CP-64QAM	M+M	Outer_Full	7.26	≤13	PASS
DC_5A_n78A	30	5+10	CP-256QAM	M+M	Outer_Full	8.74	≤13	PASS
DC_5A_n78A	30	5+15	CP-QPSK	M+M	Outer_Full	6.69	≤13	PASS
DC_5A_n78A	30	5+15	CP-16QAM	M+M	Outer_Full	6.73	≤13	PASS
DC_5A_n78A	30	5+15	CP-64QAM	M+M	Outer_Full	7.27	≤13	PASS
DC_5A_n78A	30	5+15	CP-256QAM	M+M	Outer_Full	8.39	≤13	PASS
DC_5A_n78A	30	5+20	CP-QPSK	M+M	Outer_Full	6.67	≤13	PASS
DC_5A_n78A	30	5+20	CP-16QAM	M+M	Outer_Full	6.64	≤13	PASS
DC_5A_n78A	30	5+20	CP-64QAM	M+M	Outer_Full	7.22	≤13	PASS
DC_5A_n78A	30	5+20	CP-256QAM	M+M	Outer_Full	8.23	≤13	PASS
DC_5A_n78A	30	5+25	CP-QPSK	M+M	Outer_Full	6.85	≤13	PASS
DC_5A_n78A	30	5+25	CP-16QAM	M+M	Outer_Full	6.89	≤13	PASS
DC_5A_n78A	30	5+25	CP-64QAM	M+M	Outer_Full	7.3	≤13	PASS
DC_5A_n78A	30	5+25	CP-256QAM	M+M	Outer_Full	8.24	≤13	PASS
DC_5A_n78A	30	5+30	CP-QPSK	M+M	Outer_Full	6.73	≤13	PASS
DC_5A_n78A	30	5+30	CP-16QAM	M+M	Outer_Full	6.88	≤13	PASS
DC_5A_n78A	30	5+30	CP-64QAM	M+M	Outer_Full	7.3	≤13	PASS
DC_5A_n78A	30	5+30	CP-256QAM	M+M	Outer_Full	8.43	≤13	PASS
DC_5A_n78A	30	5+40	CP-QPSK	M+M	Outer_Full	6.71	≤13	PASS
DC_5A_n78A	30	5+40	CP-16QAM	M+M	Outer_Full	6.84	≤13	PASS
DC_5A_n78A	30	5+40	CP-64QAM	M+M	Outer_Full	7.28	≤13	PASS
DC_5A_n78A	30	5+40	CP-256QAM	M+M	Outer_Full	8.43	≤13	PASS
DC_5A_n78A	30	5+50	CP-QPSK	M+M	Outer_Full	6.78	≤13	PASS
DC_5A_n78A	30	5+50	CP-16QAM	M+M	Outer_Full	6.77	≤13	PASS
DC_5A_n78A	30	5+50	CP-64QAM	M+M	Outer_Full	7.34	≤13	PASS
DC_5A_n78A	30	5+50	CP-256QAM	M+M	Outer_Full	8.28	≤13	PASS
DC_5A_n78A	30	5+60	CP-QPSK	M+M	Outer_Full	6.92	≤13	PASS
DC_5A_n78A	30	5+60	CP-16QAM	M+M	Outer_Full	6.91	≤13	PASS
DC_5A_n78A	30	5+60	CP-64QAM	M+M	Outer_Full	7.41	≤13	PASS
DC_5A_n78A	30	5+60	CP-256QAM	M+M	Outer_Full	8.45	≤13	PASS
DC_5A_n78A	30	5+70	CP-QPSK	M+M	Outer_Full	6.9	≤13	PASS
DC_5A_n78A	30	5+70	CP-16QAM	M+M	Outer_Full	6.9	≤13	PASS
DC_5A_n78A	30	5+70	CP-64QAM	M+M	Outer_Full	7.4	≤13	PASS
DC_5A_n78A	30	5+70	CP-256QAM	M+M	Outer_Full	8.49	≤13	PASS

DC_5A_n78A	30	5+80	CP-QPSK	M+M	Outer_Full	6.94	≤13	PASS
DC_5A_n78A	30	5+80	CP-16QAM	M+M	Outer_Full	6.97	≤13	PASS
DC_5A_n78A	30	5+80	CP-64QAM	M+M	Outer_Full	7.3	≤13	PASS
DC_5A_n78A	30	5+80	CP-256QAM	M+M	Outer_Full	8.43	≤13	PASS
DC_5A_n78A	30	5+90	CP-QPSK	M+M	Outer_Full	6.84	≤13	PASS
DC_5A_n78A	30	5+90	CP-16QAM	M+M	Outer_Full	6.86	≤13	PASS
DC_5A_n78A	30	5+90	CP-64QAM	M+M	Outer_Full	7.38	≤13	PASS
DC_5A_n78A	30	5+90	CP-256QAM	M+M	Outer_Full	8.62	≤13	PASS
DC_5A_n78A	30	5+100	CP-QPSK	M+M	Outer_Full	7.12	≤13	PASS
DC_5A_n78A	30	5+100	CP-16QAM	M+M	Outer_Full	6.94	≤13	PASS
DC_5A_n78A	30	5+100	CP-64QAM	M+M	Outer_Full	7.34	≤13	PASS
DC_5A_n78A	30	5+100	CP-256QAM	M+M	Outer_Full	8.44	≤13	PASS

Test Graphs



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

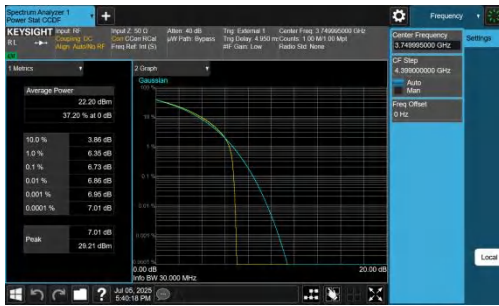
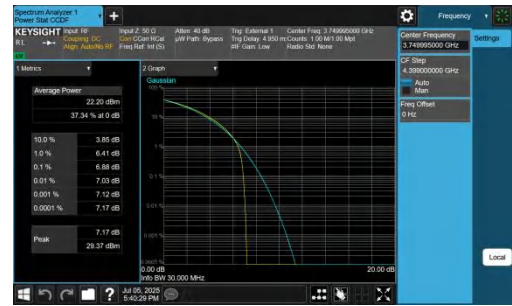
NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

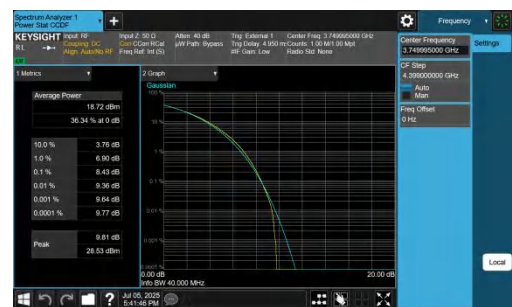

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS



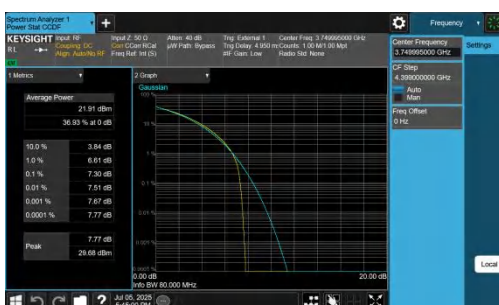
NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-QPSK-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-16QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-64QAM
-Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-256QAM
-Outter_Full-PASS

26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n78A	30	5+10	CP-QPSK	M+M	Outer_Full	8.5964	9.978	PASS
DC_5A_n78A	30	5+10	CP-16QAM	M+M	Outer_Full	8.6237	9.916	PASS
DC_5A_n78A	30	5+10	CP-64QAM	M+M	Outer_Full	8.5931	10.03	PASS
DC_5A_n78A	30	5+10	CP-256QAM	M+M	Outer_Full	8.6216	9.763	PASS
DC_5A_n78A	30	5+15	CP-QPSK	M+M	Outer_Full	13.644	15.40	PASS
DC_5A_n78A	30	5+15	CP-16QAM	M+M	Outer_Full	13.657	15.44	PASS
DC_5A_n78A	30	5+15	CP-64QAM	M+M	Outer_Full	13.629	15.26	PASS
DC_5A_n78A	30	5+15	CP-256QAM	M+M	Outer_Full	13.657	15.15	PASS
DC_5A_n78A	30	5+20	CP-QPSK	M+M	Outer_Full	18.318	20.06	PASS
DC_5A_n78A	30	5+20	CP-16QAM	M+M	Outer_Full	18.271	19.98	PASS
DC_5A_n78A	30	5+20	CP-64QAM	M+M	Outer_Full	18.269	19.99	PASS
DC_5A_n78A	30	5+20	CP-256QAM	M+M	Outer_Full	18.271	20.18	PASS
DC_5A_n78A	30	5+25	CP-QPSK	M+M	Outer_Full	23.271	25.27	PASS
DC_5A_n78A	30	5+25	CP-16QAM	M+M	Outer_Full	23.293	25.09	PASS
DC_5A_n78A	30	5+25	CP-64QAM	M+M	Outer_Full	23.319	25.16	PASS
DC_5A_n78A	30	5+25	CP-256QAM	M+M	Outer_Full	23.262	25.25	PASS
DC_5A_n78A	30	5+30	CP-QPSK	M+M	Outer_Full	27.861	30.03	PASS
DC_5A_n78A	30	5+30	CP-16QAM	M+M	Outer_Full	27.931	30.06	PASS
DC_5A_n78A	30	5+30	CP-64QAM	M+M	Outer_Full	27.948	30.03	PASS
DC_5A_n78A	30	5+30	CP-256QAM	M+M	Outer_Full	27.859	30.00	PASS
DC_5A_n78A	30	5+40	CP-QPSK	M+M	Outer_Full	37.925	40.54	PASS
DC_5A_n78A	30	5+40	CP-16QAM	M+M	Outer_Full	37.875	40.31	PASS
DC_5A_n78A	30	5+40	CP-64QAM	M+M	Outer_Full	37.948	40.51	PASS
DC_5A_n78A	30	5+40	CP-256QAM	M+M	Outer_Full	37.897	40.34	PASS
DC_5A_n78A	30	5+50	CP-QPSK	M+M	Outer_Full	47.638	50.25	PASS
DC_5A_n78A	30	5+50	CP-16QAM	M+M	Outer_Full	47.552	50.33	PASS
DC_5A_n78A	30	5+50	CP-64QAM	M+M	Outer_Full	47.627	50.48	PASS
DC_5A_n78A	30	5+50	CP-256QAM	M+M	Outer_Full	47.522	50.17	PASS
DC_5A_n78A	30	5+60	CP-QPSK	M+M	Outer_Full	57.851	60.90	PASS
DC_5A_n78A	30	5+60	CP-16QAM	M+M	Outer_Full	58.041	60.85	PASS
DC_5A_n78A	30	5+60	CP-64QAM	M+M	Outer_Full	57.883	61.01	PASS
DC_5A_n78A	30	5+60	CP-256QAM	M+M	Outer_Full	57.982	61.12	PASS
DC_5A_n78A	30	5+70	CP-QPSK	M+M	Outer_Full	67.638	71.12	PASS
DC_5A_n78A	30	5+70	CP-16QAM	M+M	Outer_Full	67.541	70.94	PASS
DC_5A_n78A	30	5+70	CP-64QAM	M+M	Outer_Full	67.486	70.82	PASS
DC_5A_n78A	30	5+70	CP-256QAM	M+M	Outer_Full	67.442	70.94	PASS

DC_5A_n78A	30	5+80	CP-QPSK	M+M	Outer_Full	77.449	81.12	PASS
DC_5A_n78A	30	5+80	CP-16QAM	M+M	Outer_Full	77.558	81.16	PASS
DC_5A_n78A	30	5+80	CP-64QAM	M+M	Outer_Full	77.455	81.15	PASS
DC_5A_n78A	30	5+80	CP-256QAM	M+M	Outer_Full	77.542	81.22	PASS
DC_5A_n78A	30	5+90	CP-QPSK	M+M	Outer_Full	87.609	91.54	PASS
DC_5A_n78A	30	5+90	CP-16QAM	M+M	Outer_Full	87.638	91.35	PASS
DC_5A_n78A	30	5+90	CP-64QAM	M+M	Outer_Full	87.492	91.24	PASS
DC_5A_n78A	30	5+90	CP-256QAM	M+M	Outer_Full	87.408	91.53	PASS
DC_5A_n78A	30	5+100	CP-QPSK	M+M	Outer_Full	97.698	101.7	PASS
DC_5A_n78A	30	5+100	CP-16QAM	M+M	Outer_Full	97.569	101.7	PASS
DC_5A_n78A	30	5+100	CP-64QAM	M+M	Outer_Full	97.610	101.8	PASS
DC_5A_n78A	30	5+100	CP-256QAM	M+M	Outer_Full	97.446	101.8	PASS

Test Graphs



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



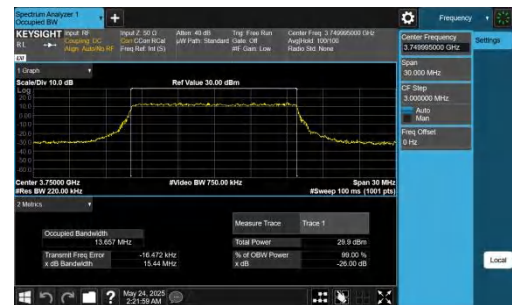
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NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



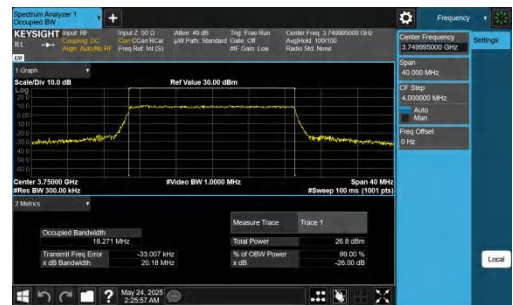
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NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



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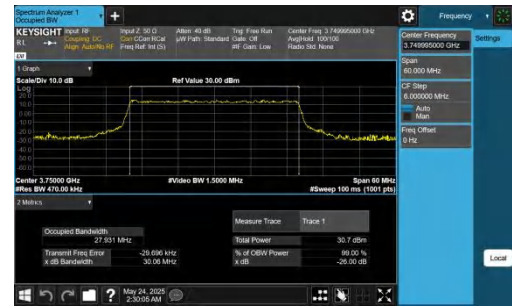
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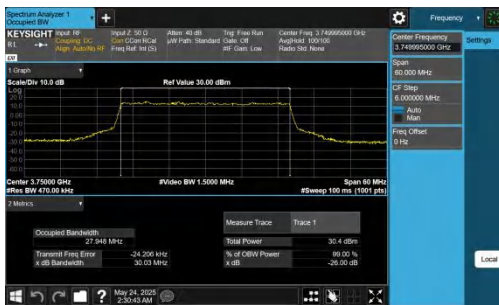
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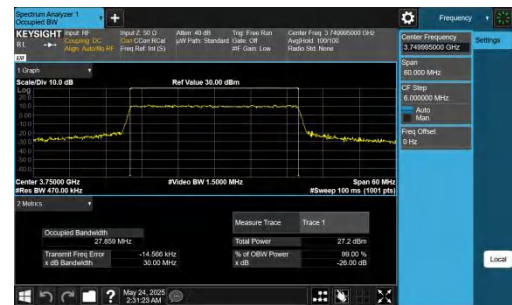
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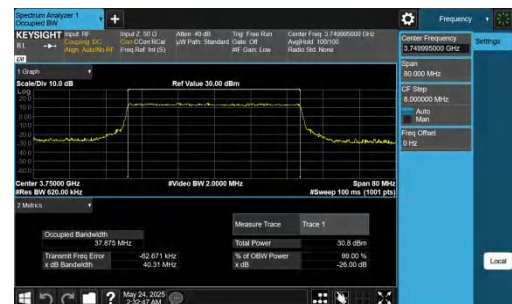
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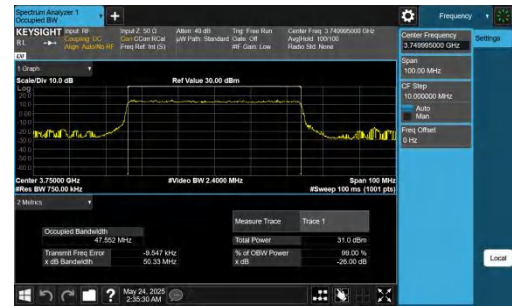
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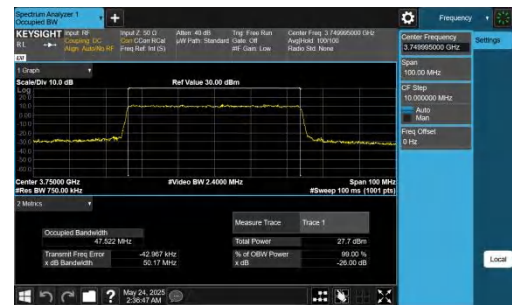
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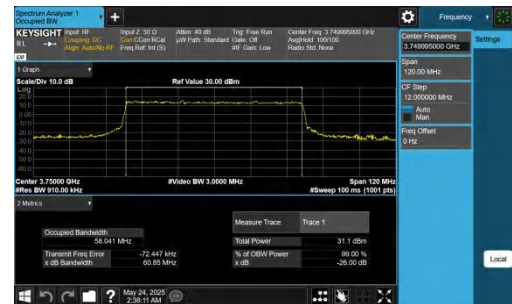
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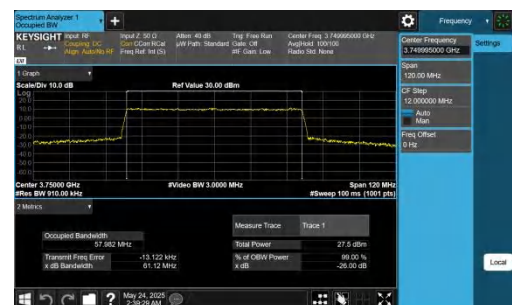
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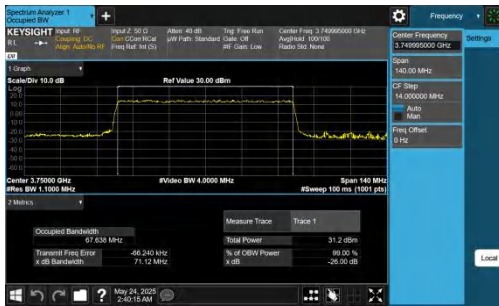
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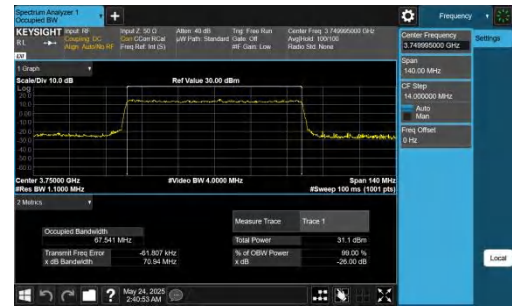
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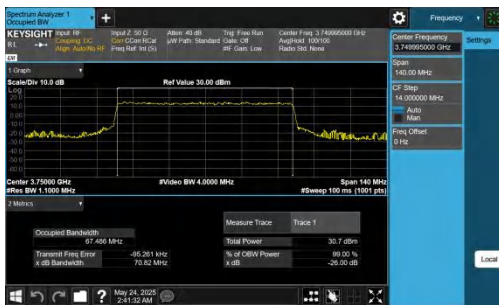
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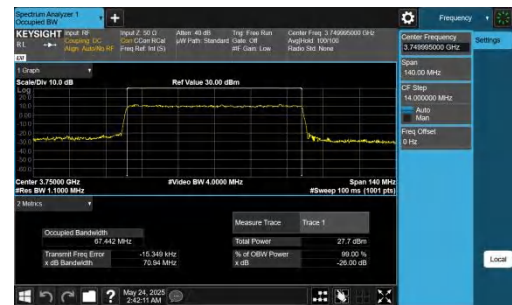
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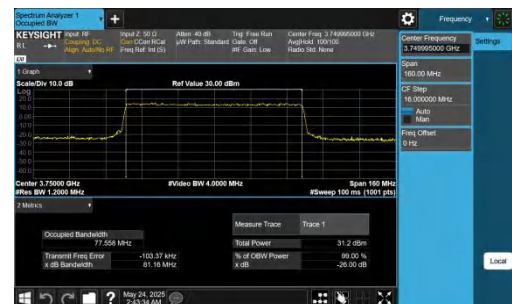
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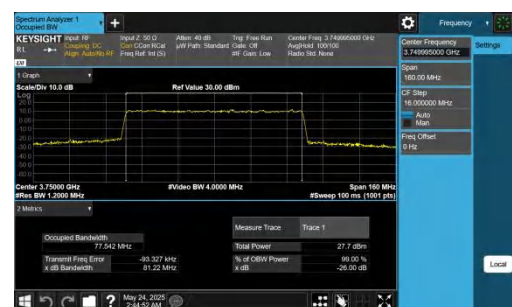
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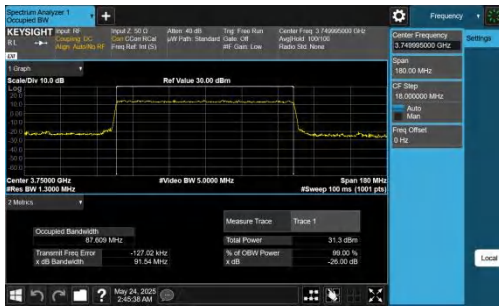
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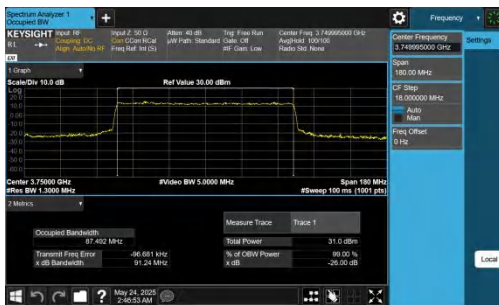
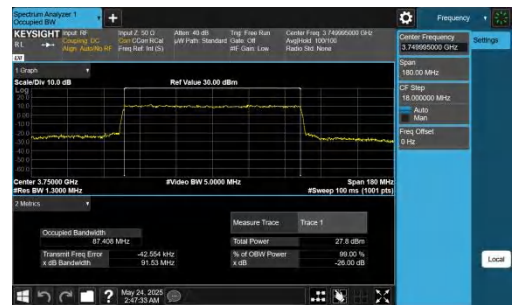


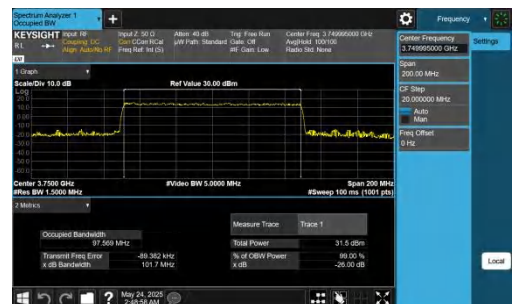
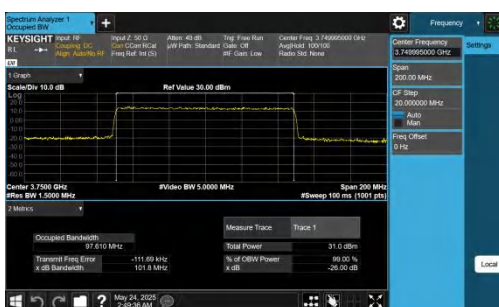
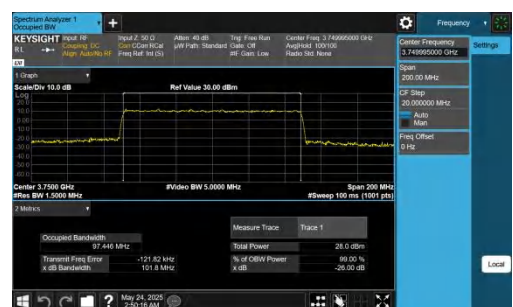
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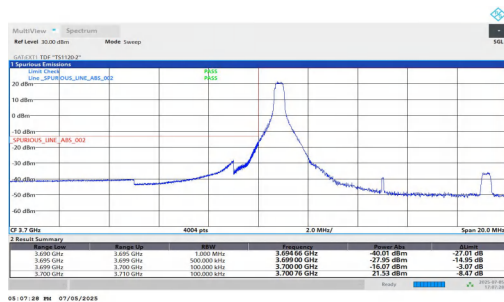

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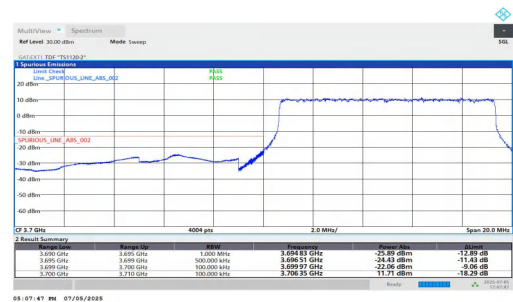
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Outer_Full-Ant1-PASS

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Band Edge for NSA

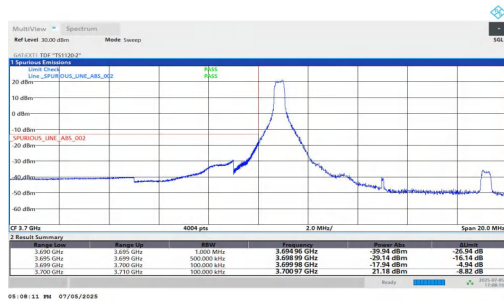
Test Graphs



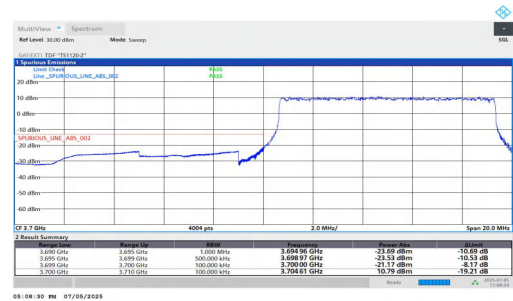
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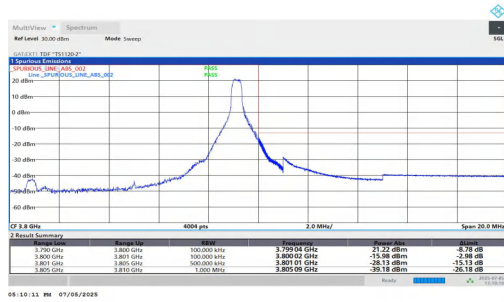
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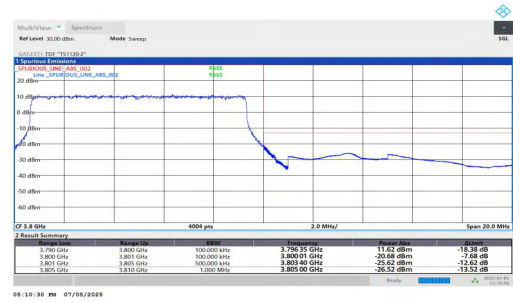
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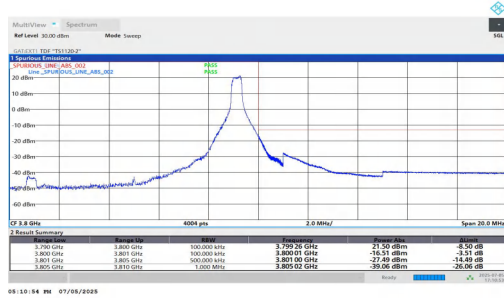
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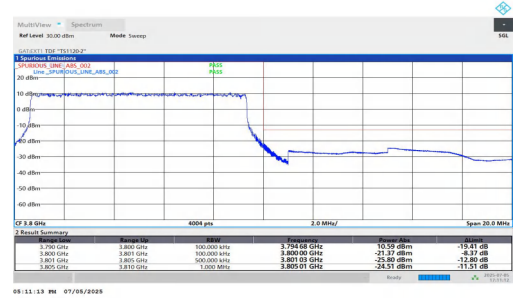
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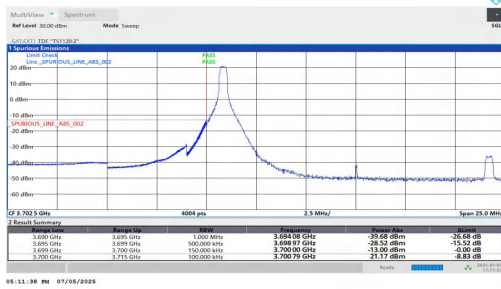
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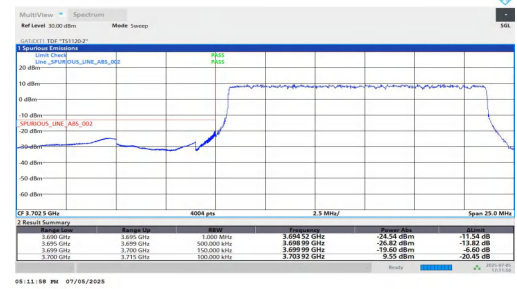
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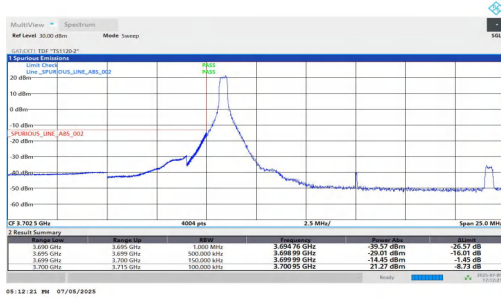
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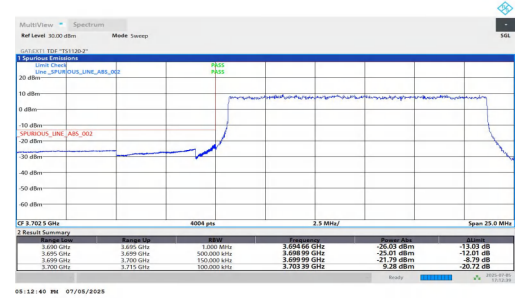
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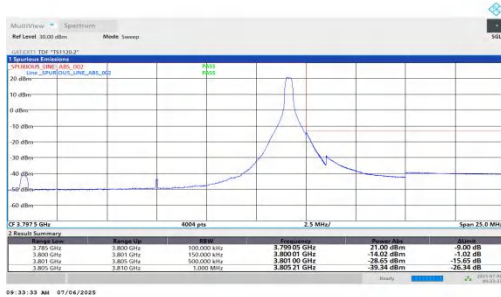
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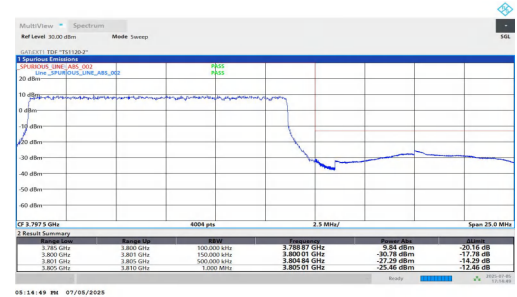
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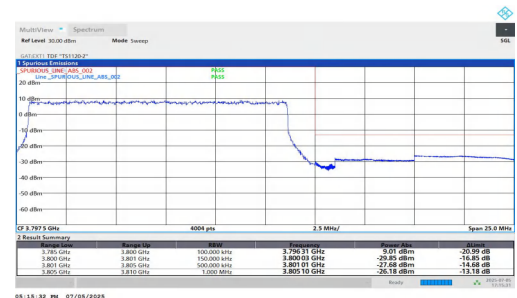
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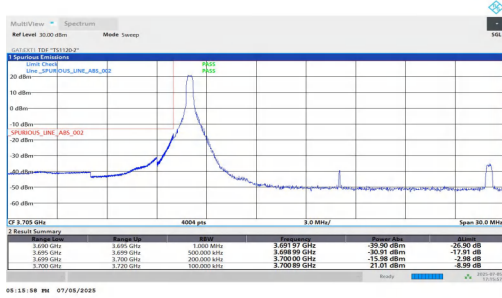
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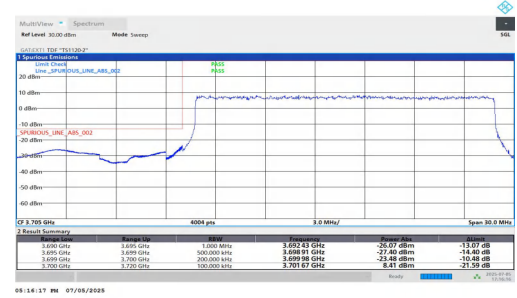
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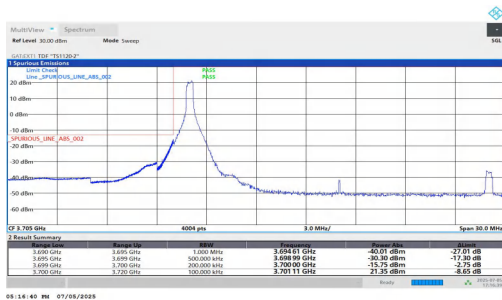
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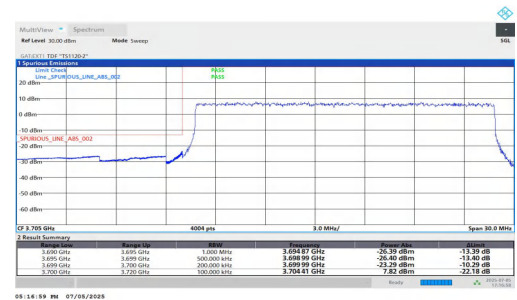
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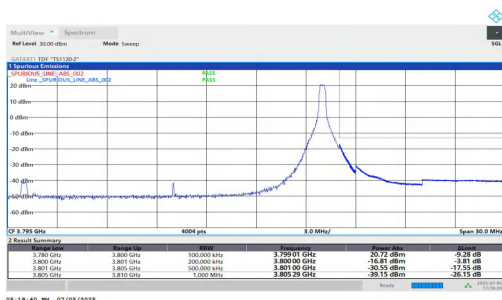
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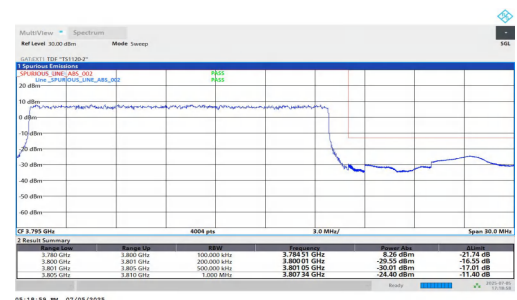
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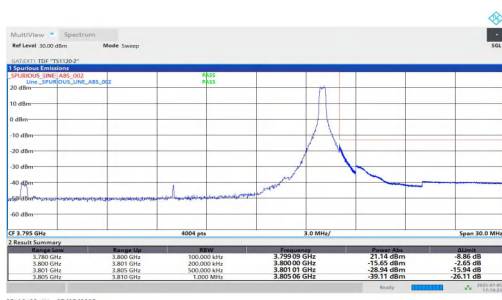
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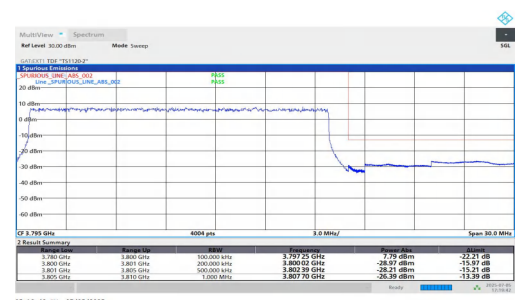
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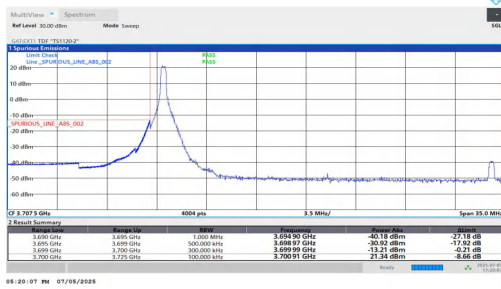
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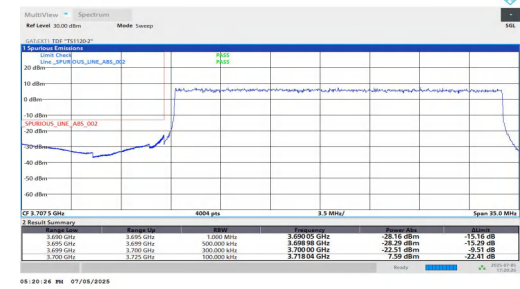
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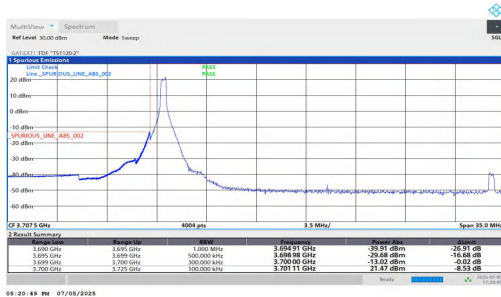
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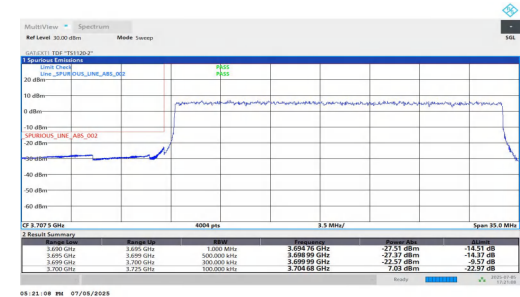
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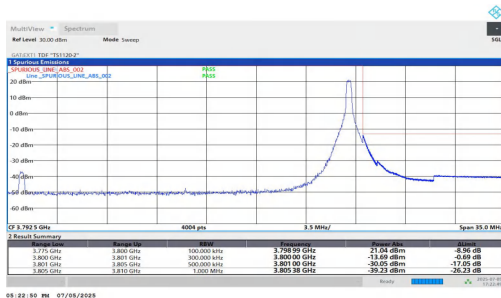
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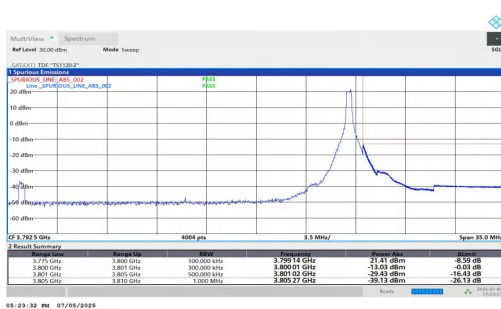
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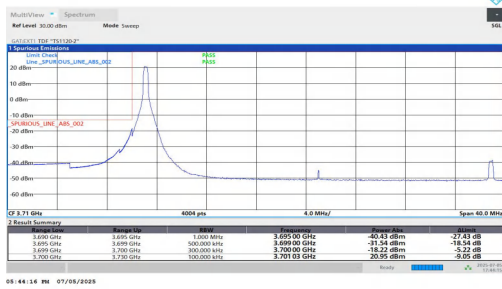
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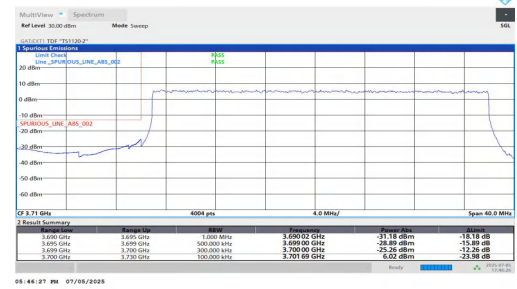
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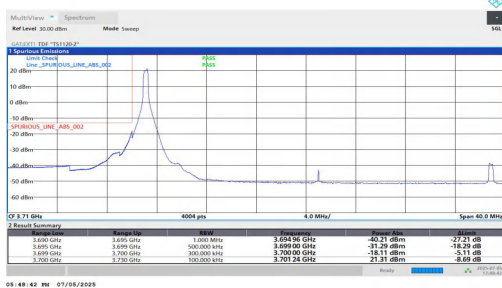
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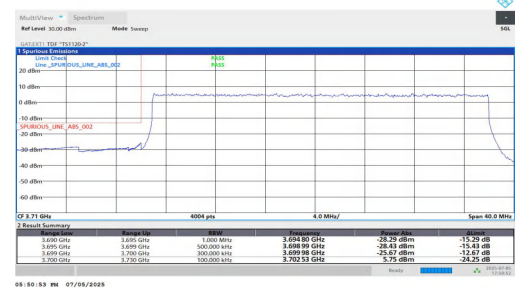
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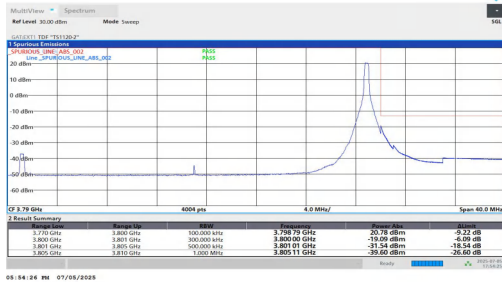
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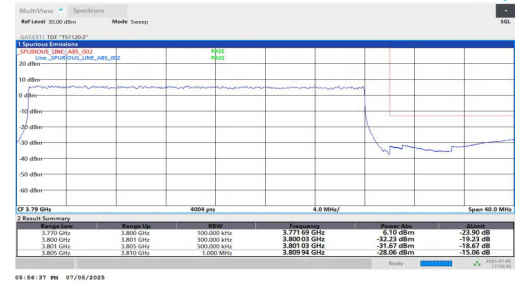
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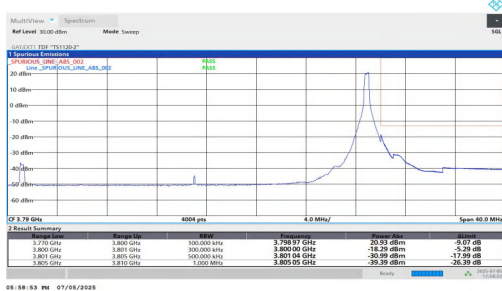
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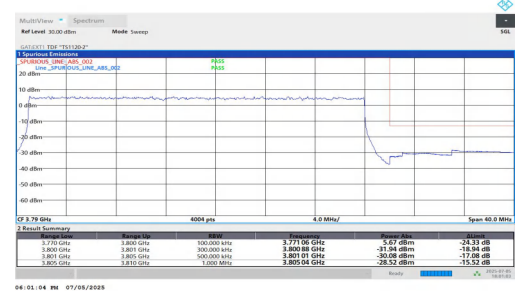
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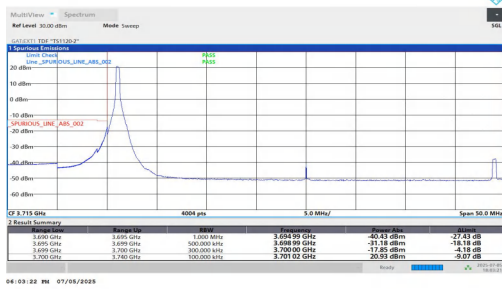
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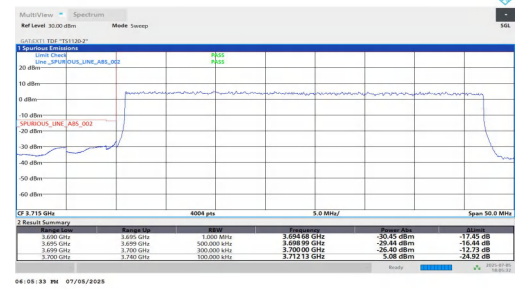
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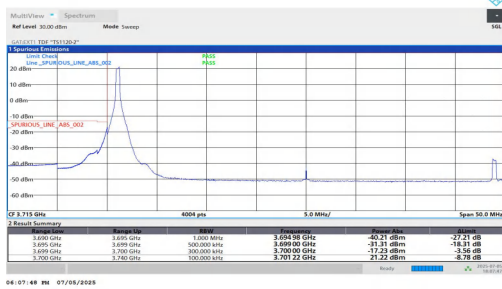
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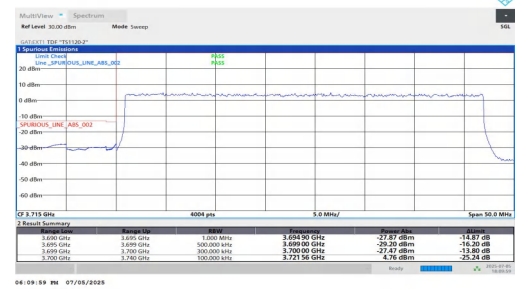
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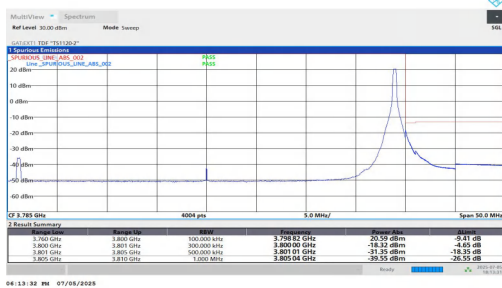
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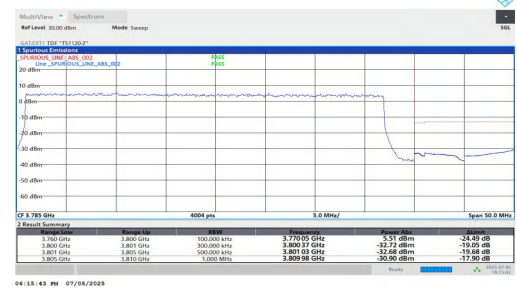
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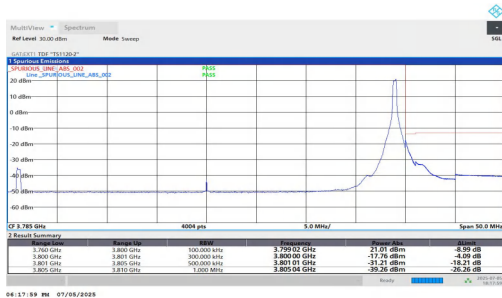
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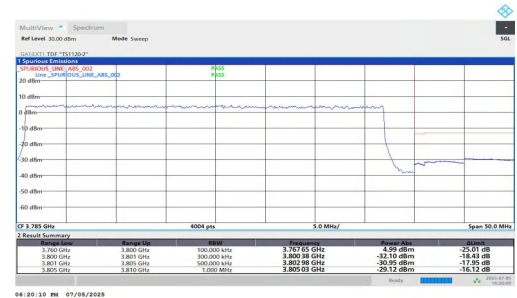
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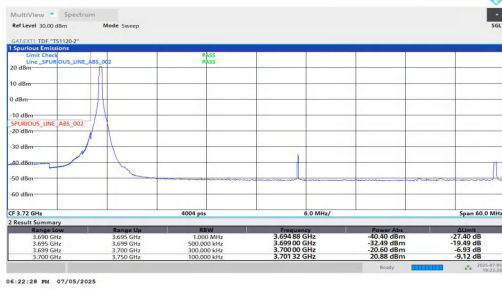
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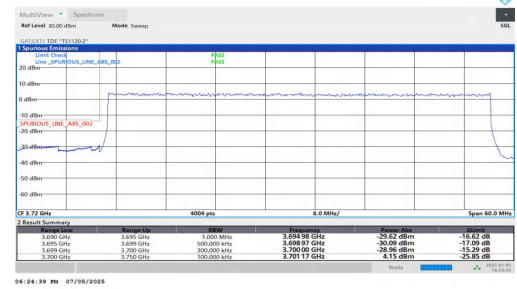
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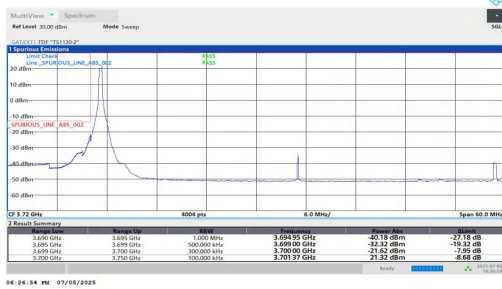
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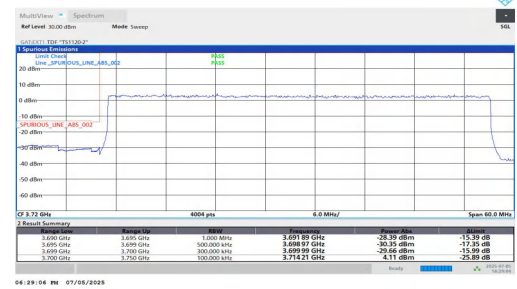
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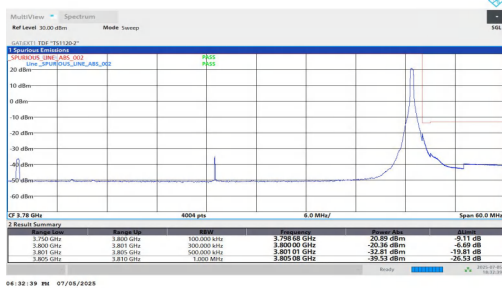
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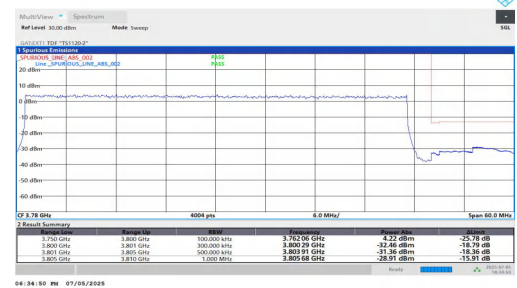
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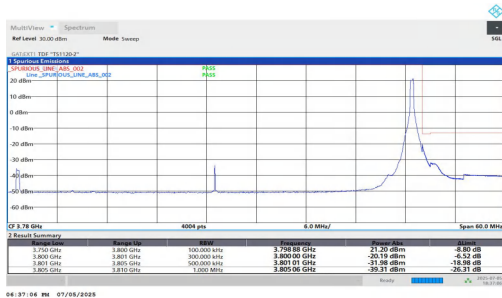
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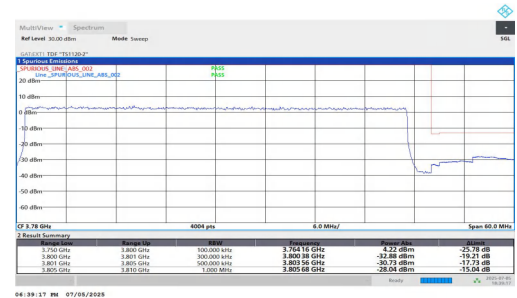
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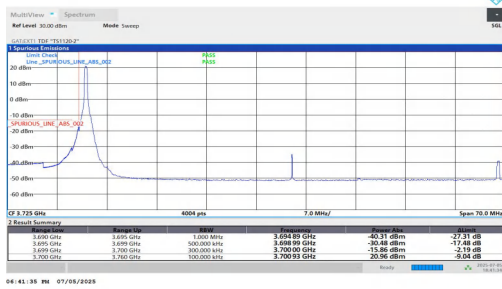
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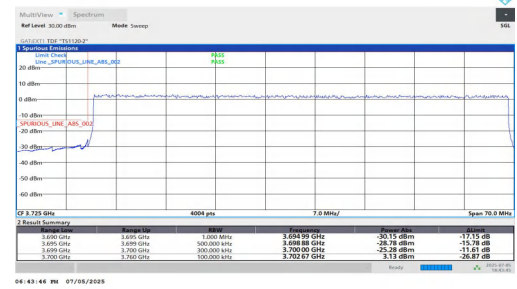
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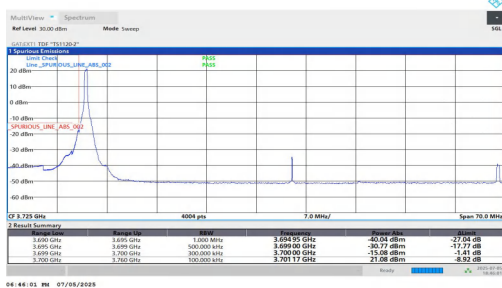
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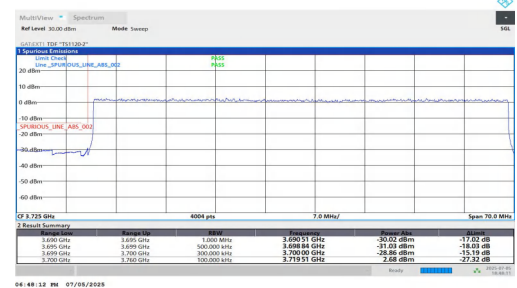
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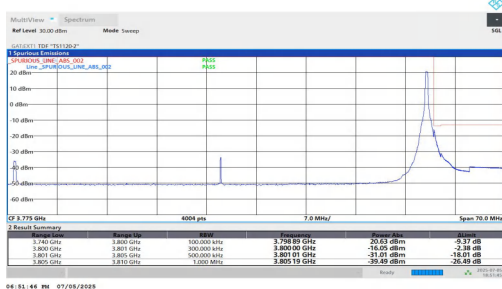
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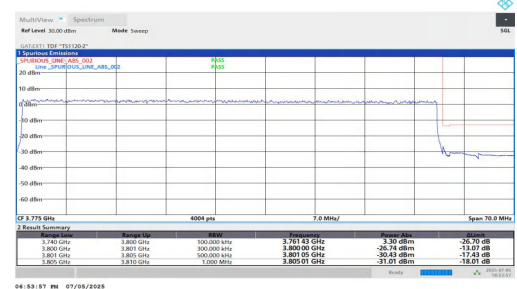
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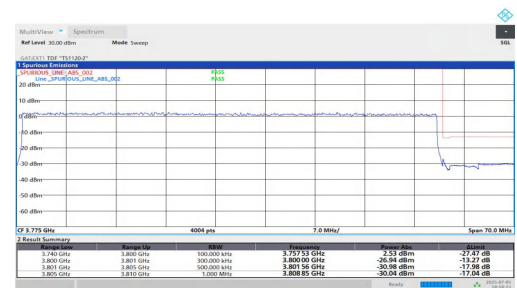
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NTVN-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



NTVN-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-QPSK-Edg e_1RB_Right-PASS



NTVN-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS