

Effective (Isotropic) Radiated Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Gain (dBi)	E(I)RP (dBm)	Limit (dBm)	Verdict
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Left	25.98	1.88	27.86	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Right	25.86	1.88	27.74	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Inner_Full	25.83	1.88	27.71	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+L	Outer_Full	25.42	1.88	27.3	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Left	26.09	1.88	27.97	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Right	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Inner_Full	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+L	Outer_Full	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Left	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Right	24.49	1.88	26.37	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Inner_Full	24.76	1.88	26.64	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+L	Outer_Full	23.77	1.88	25.65	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Left	23.11	1.88	24.99	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Right	22.89	1.88	24.77	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Inner_Full	23.37	1.88	25.25	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+L	Outer_Full	23.32	1.88	25.2	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Left	21.29	1.88	23.17	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Right	21.14	1.88	23.02	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Inner_Full	21.47	1.88	23.35	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+L	Outer_Full	21.27	1.88	23.15	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Left	25.3	1.88	27.18	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Right	25.13	1.88	27.01	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Inner_Full	25.37	1.88	27.25	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+M	Outer_Full	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Left	25.23	1.88	27.11	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Right	25.07	1.88	26.95	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Inner_Full	25.24	1.88	27.12	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+M	Outer_Full	24.25	1.88	26.13	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Left	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Right	24.3	1.88	26.18	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Inner_Full	24.23	1.88	26.11	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+M	Outer_Full	23.23	1.88	25.11	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Left	22.63	1.88	24.51	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Right	22.58	1.88	24.46	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Inner_Full	22.8	1.88	24.68	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+M	Outer_Full	22.68	1.88	24.56	30	PASS

DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Left	20.75	1.88	22.63	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Right	20.58	1.88	22.46	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Inner_Full	20.75	1.88	22.63	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+M	Outer_Full	20.81	1.88	22.69	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Left	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Right	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Inner_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+10	DFT-BPSK	M+H	Outer_Full	24.41	1.88	26.29	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Left	24.78	1.88	26.66	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Right	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Inner_Full	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+10	DFT-QPSK	M+H	Outer_Full	24.42	1.88	26.3	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Left	23.73	1.88	25.61	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Right	23.98	1.88	25.86	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Inner_Full	23.81	1.88	25.69	30	PASS
DC_5A_n78A	30	5+10	DFT-16QAM	M+H	Outer_Full	22.72	1.88	24.6	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Left	22.04	1.88	23.92	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Right	22.3	1.88	24.18	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Inner_Full	22.29	1.88	24.17	30	PASS
DC_5A_n78A	30	5+10	DFT-64QAM	M+H	Outer_Full	22.25	1.88	24.13	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Left	20.24	1.88	22.12	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Right	20.48	1.88	22.36	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Inner_Full	20.33	1.88	22.21	30	PASS
DC_5A_n78A	30	5+10	DFT-256QAM	M+H	Outer_Full	20.47	1.88	22.35	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Right	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Inner_Full	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+L	Outer_Full	25.26	1.88	27.14	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Left	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Right	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Inner_Full	25.72	1.88	27.6	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+L	Outer_Full	24.75	1.88	26.63	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Left	24.64	1.88	26.52	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Right	24.33	1.88	26.21	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Inner_Full	24.63	1.88	26.51	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+L	Outer_Full	23.61	1.88	25.49	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Left	23.23	1.88	25.11	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Right	23.01	1.88	24.89	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Inner_Full	23.05	1.88	24.93	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+L	Outer_Full	23.09	1.88	24.97	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Left	21.29	1.88	23.17	30	PASS

DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Right	21.09	1.88	22.97	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Inner_Full	21.14	1.88	23.02	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+L	Outer_Full	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Left	25.54	1.88	27.42	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Right	25.35	1.88	27.23	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Inner_Full	25.25	1.88	27.13	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+M	Outer_Full	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Left	25.59	1.88	27.47	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Right	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Inner_Full	25.14	1.88	27.02	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+M	Outer_Full	24.2	1.88	26.08	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Left	24.29	1.88	26.17	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Right	24.11	1.88	25.99	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Inner_Full	24.05	1.88	25.93	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+M	Outer_Full	23.18	1.88	25.06	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Left	23.16	1.88	25.04	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Right	22.86	1.88	24.74	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Inner_Full	22.77	1.88	24.65	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+M	Outer_Full	22.71	1.88	24.59	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Left	20.82	1.88	22.7	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Right	20.63	1.88	22.51	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Inner_Full	20.72	1.88	22.6	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+M	Outer_Full	20.62	1.88	22.5	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Left	24.57	1.88	26.45	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Right	24.91	1.88	26.79	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Inner_Full	24.9	1.88	26.78	30	PASS
DC_5A_n78A	30	5+15	DFT-BPSK	M+H	Outer_Full	24.24	1.88	26.12	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Left	25.02	1.88	26.9	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Right	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Inner_Full	24.68	1.88	26.56	30	PASS
DC_5A_n78A	30	5+15	DFT-QPSK	M+H	Outer_Full	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Left	23.73	1.88	25.61	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Right	23.94	1.88	25.82	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Inner_Full	23.65	1.88	25.53	30	PASS
DC_5A_n78A	30	5+15	DFT-16QAM	M+H	Outer_Full	22.6	1.88	24.48	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Left	22.15	1.88	24.03	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Right	22.61	1.88	24.49	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Inner_Full	22.21	1.88	24.09	30	PASS
DC_5A_n78A	30	5+15	DFT-64QAM	M+H	Outer_Full	22.15	1.88	24.03	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Left	20.45	1.88	22.33	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Right	20.89	1.88	22.77	30	PASS

DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Inner_Full	20.24	1.88	22.12	30	PASS
DC_5A_n78A	30	5+15	DFT-256QAM	M+H	Outer_Full	20.16	1.88	22.04	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Right	25.38	1.88	27.26	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Inner_Full	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+L	Outer_Full	25.15	1.88	27.03	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Left	25.63	1.88	27.51	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Right	25.3	1.88	27.18	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Inner_Full	25.66	1.88	27.54	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+L	Outer_Full	24.61	1.88	26.49	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Left	24.7	1.88	26.58	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Right	24.38	1.88	26.26	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Inner_Full	24.42	1.88	26.3	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+L	Outer_Full	23.59	1.88	25.47	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Left	23.56	1.88	25.44	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Right	23.22	1.88	25.1	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Inner_Full	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+L	Outer_Full	23.1	1.88	24.98	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Left	21.41	1.88	23.29	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Right	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Inner_Full	21.11	1.88	22.99	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+L	Outer_Full	21.1	1.88	22.98	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Left	25.59	1.88	27.47	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Right	25.26	1.88	27.14	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Inner_Full	25.31	1.88	27.19	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+M	Outer_Full	24.78	1.88	26.66	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Left	25.66	1.88	27.54	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Right	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.18	1.88	27.06	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Left	24.58	1.88	26.46	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Right	24.27	1.88	26.15	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.02	1.88	25.9	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.17	1.88	25.05	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Left	22.81	1.88	24.69	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Right	22.47	1.88	24.35	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Inner_Full	22.65	1.88	24.53	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+M	Outer_Full	22.7	1.88	24.58	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Left	20.64	1.88	22.52	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Right	20.31	1.88	22.19	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Inner_Full	20.68	1.88	22.56	30	PASS

DC_5A_n78A	30	5+20	DFT-256QAM	M+M	Outer_Full	20.75	1.88	22.63	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Left	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Right	25.2	1.88	27.08	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Inner_Full	24.8	1.88	26.68	30	PASS
DC_5A_n78A	30	5+20	DFT-BPSK	M+H	Outer_Full	24.37	1.88	26.25	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Left	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Right	25	1.88	26.88	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Inner_Full	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Left	23.91	1.88	25.79	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Right	24.13	1.88	26.01	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Inner_Full	23.54	1.88	25.42	30	PASS
DC_5A_n78A	30	5+20	DFT-16QAM	M+H	Outer_Full	22.62	1.88	24.5	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Left	22.5	1.88	24.38	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Right	22.74	1.88	24.62	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Inner_Full	22.11	1.88	23.99	30	PASS
DC_5A_n78A	30	5+20	DFT-64QAM	M+H	Outer_Full	22.1	1.88	23.98	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Left	20.05	1.88	21.93	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Right	20.31	1.88	22.19	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Inner_Full	20.13	1.88	22.01	30	PASS
DC_5A_n78A	30	5+20	DFT-256QAM	M+H	Outer_Full	20.09	1.88	21.97	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Left	26.03	1.88	27.91	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Right	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Inner_Full	25.69	1.88	27.57	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+L	Outer_Full	25.16	1.88	27.04	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Left	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Right	25.7	1.88	27.58	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Inner_Full	25.55	1.88	27.43	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+L	Outer_Full	24.67	1.88	26.55	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Left	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Right	24.62	1.88	26.5	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Inner_Full	24.41	1.88	26.29	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+L	Outer_Full	23.58	1.88	25.46	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Left	23.51	1.88	25.39	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Right	23.21	1.88	25.09	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Inner_Full	22.96	1.88	24.84	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+L	Outer_Full	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Left	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Right	20.89	1.88	22.77	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Inner_Full	21.05	1.88	22.93	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+L	Outer_Full	21.12	1.88	23	30	PASS

DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Right	25.47	1.88	27.35	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Inner_Full	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+M	Outer_Full	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Left	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Right	25.55	1.88	27.43	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Inner_Full	25.27	1.88	27.15	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+M	Outer_Full	24.38	1.88	26.26	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Left	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Right	24.12	1.88	26	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Inner_Full	24.18	1.88	26.06	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+M	Outer_Full	23.32	1.88	25.2	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Left	23.58	1.88	25.46	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Right	23.26	1.88	25.14	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Inner_Full	22.64	1.88	24.52	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+M	Outer_Full	22.8	1.88	24.68	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Left	21.32	1.88	23.2	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Right	21.09	1.88	22.97	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Inner_Full	20.75	1.88	22.63	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+M	Outer_Full	20.83	1.88	22.71	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Left	25.26	1.88	27.14	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Right	25.18	1.88	27.06	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Inner_Full	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+25	DFT-BPSK	M+H	Outer_Full	24.42	1.88	26.3	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Left	25.07	1.88	26.95	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Right	24.97	1.88	26.85	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Inner_Full	24.67	1.88	26.55	30	PASS
DC_5A_n78A	30	5+25	DFT-QPSK	M+H	Outer_Full	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Left	24.09	1.88	25.97	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Right	24.08	1.88	25.96	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Inner_Full	23.62	1.88	25.5	30	PASS
DC_5A_n78A	30	5+25	DFT-16QAM	M+H	Outer_Full	22.73	1.88	24.61	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Left	22.73	1.88	24.61	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Right	22.63	1.88	24.51	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Inner_Full	22.13	1.88	24.01	30	PASS
DC_5A_n78A	30	5+25	DFT-64QAM	M+H	Outer_Full	22.28	1.88	24.16	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Left	20.56	1.88	22.44	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Right	20.58	1.88	22.46	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Inner_Full	20.23	1.88	22.11	30	PASS
DC_5A_n78A	30	5+25	DFT-256QAM	M+H	Outer_Full	20.39	1.88	22.27	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Left	25.9	1.88	27.78	30	PASS

DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Right	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Inner_Full	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+L	Outer_Full	25.23	1.88	27.11	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Left	25.96	1.88	27.84	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Right	25.78	1.88	27.66	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Inner_Full	25.63	1.88	27.51	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+L	Outer_Full	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Left	24.7	1.88	26.58	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Right	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+L	Outer_Full	23.63	1.88	25.51	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Left	23.4	1.88	25.28	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Right	23.11	1.88	24.99	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Inner_Full	23.01	1.88	24.89	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+L	Outer_Full	23.2	1.88	25.08	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Left	21.57	1.88	23.45	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Right	21.23	1.88	23.11	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Inner_Full	20.96	1.88	22.84	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+L	Outer_Full	21.14	1.88	23.02	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Left	25.76	1.88	27.64	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Right	25.34	1.88	27.22	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Inner_Full	25.49	1.88	27.37	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+M	Outer_Full	24.93	1.88	26.81	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Right	25.31	1.88	27.19	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.4	1.88	27.28	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.42	1.88	26.3	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Left	24.67	1.88	26.55	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Right	24.31	1.88	26.19	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.19	1.88	26.07	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.36	1.88	25.24	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Left	22.86	1.88	24.74	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Right	22.49	1.88	24.37	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Inner_Full	22.84	1.88	24.72	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+M	Outer_Full	22.89	1.88	24.77	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Left	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Right	20.59	1.88	22.47	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Inner_Full	20.79	1.88	22.67	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+M	Outer_Full	20.94	1.88	22.82	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Left	25.19	1.88	27.07	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Right	24.8	1.88	26.68	30	PASS

DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Inner_Full	24.92	1.88	26.8	30	PASS
DC_5A_n78A	30	5+30	DFT-BPSK	M+H	Outer_Full	24.48	1.88	26.36	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Left	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Right	25.28	1.88	27.16	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Inner_Full	24.87	1.88	26.75	30	PASS
DC_5A_n78A	30	5+30	DFT-QPSK	M+H	Outer_Full	24.48	1.88	26.36	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Left	24.69	1.88	26.57	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Right	24.3	1.88	26.18	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Inner_Full	23.78	1.88	25.66	30	PASS
DC_5A_n78A	30	5+30	DFT-16QAM	M+H	Outer_Full	22.91	1.88	24.79	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Left	22.82	1.88	24.7	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Right	22.57	1.88	24.45	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Inner_Full	22.33	1.88	24.21	30	PASS
DC_5A_n78A	30	5+30	DFT-64QAM	M+H	Outer_Full	22.36	1.88	24.24	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Left	20.44	1.88	22.32	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Right	20.12	1.88	22	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Inner_Full	20.22	1.88	22.1	30	PASS
DC_5A_n78A	30	5+30	DFT-256QAM	M+H	Outer_Full	20.45	1.88	22.33	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Left	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Right	25.56	1.88	27.44	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Inner_Full	25.66	1.88	27.54	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+L	Outer_Full	25.11	1.88	26.99	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Left	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Right	25.74	1.88	27.62	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Inner_Full	25.51	1.88	27.39	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+L	Outer_Full	24.62	1.88	26.5	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Left	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Right	24.43	1.88	26.31	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.5	1.88	26.38	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+L	Outer_Full	23.61	1.88	25.49	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Left	23.22	1.88	25.1	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Right	23.02	1.88	24.9	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Inner_Full	23.03	1.88	24.91	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+L	Outer_Full	23.14	1.88	25.02	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Left	21.32	1.88	23.2	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Right	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Inner_Full	21	1.88	22.88	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+L	Outer_Full	21.13	1.88	23.01	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Left	25.68	1.88	27.56	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Right	25.35	1.88	27.23	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Inner_Full	25.51	1.88	27.39	30	PASS

DC_5A_n78A	30	5+40	DFT-BPSK	M+M	Outer_Full	24.96	1.88	26.84	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Left	25.67	1.88	27.55	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Right	25.37	1.88	27.25	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.39	1.88	27.27	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.43	1.88	26.31	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Left	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Right	24.13	1.88	26.01	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.27	1.88	26.15	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.41	1.88	25.29	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Left	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Right	23.01	1.88	24.89	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Inner_Full	22.78	1.88	24.66	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+M	Outer_Full	22.93	1.88	24.81	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Left	21.27	1.88	23.15	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Right	20.93	1.88	22.81	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Inner_Full	20.69	1.88	22.57	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+M	Outer_Full	20.98	1.88	22.86	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Left	25.4	1.88	27.28	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Right	25.04	1.88	26.92	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Inner_Full	25.07	1.88	26.95	30	PASS
DC_5A_n78A	30	5+40	DFT-BPSK	M+H	Outer_Full	24.63	1.88	26.51	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Left	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Right	25.06	1.88	26.94	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Inner_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+40	DFT-QPSK	M+H	Outer_Full	24.59	1.88	26.47	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Left	24.22	1.88	26.1	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Right	23.87	1.88	25.75	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Inner_Full	23.86	1.88	25.74	30	PASS
DC_5A_n78A	30	5+40	DFT-16QAM	M+H	Outer_Full	22.96	1.88	24.84	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Left	23.02	1.88	24.9	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Right	22.56	1.88	24.44	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Inner_Full	22.48	1.88	24.36	30	PASS
DC_5A_n78A	30	5+40	DFT-64QAM	M+H	Outer_Full	22.49	1.88	24.37	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Left	20.87	1.88	22.75	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Right	20.46	1.88	22.34	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Inner_Full	20.34	1.88	22.22	30	PASS
DC_5A_n78A	30	5+40	DFT-256QAM	M+H	Outer_Full	20.49	1.88	22.37	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Left	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Right	25.45	1.88	27.33	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Inner_Full	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+L	Outer_Full	25.29	1.88	27.17	30	PASS

DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Left	25.98	1.88	27.86	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Right	25.52	1.88	27.4	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Inner_Full	25.8	1.88	27.68	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+L	Outer_Full	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Left	24.8	1.88	26.68	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Right	24.33	1.88	26.21	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Inner_Full	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+L	Outer_Full	23.71	1.88	25.59	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Left	23.42	1.88	25.3	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Right	23.06	1.88	24.94	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Inner_Full	23.25	1.88	25.13	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+L	Outer_Full	23.21	1.88	25.09	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Left	21.56	1.88	23.44	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Right	20.99	1.88	22.87	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Inner_Full	21.19	1.88	23.07	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+L	Outer_Full	21.3	1.88	23.18	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Left	25.74	1.88	27.62	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Right	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Inner_Full	25.74	1.88	27.62	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+M	Outer_Full	25.12	1.88	27	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Left	25.61	1.88	27.49	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Right	25.35	1.88	27.23	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Inner_Full	25.59	1.88	27.47	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+M	Outer_Full	24.63	1.88	26.51	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Left	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Right	24.29	1.88	26.17	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Inner_Full	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+M	Outer_Full	23.65	1.88	25.53	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Left	23.12	1.88	25	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Right	22.75	1.88	24.63	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Inner_Full	23.08	1.88	24.96	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+M	Outer_Full	23.11	1.88	24.99	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Left	21.05	1.88	22.93	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Right	20.72	1.88	22.6	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Inner_Full	21.02	1.88	22.9	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+M	Outer_Full	21.14	1.88	23.02	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Left	25.42	1.88	27.3	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Right	25.12	1.88	27	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Inner_Full	25.44	1.88	27.32	30	PASS
DC_5A_n78A	30	5+50	DFT-BPSK	M+H	Outer_Full	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Left	25.44	1.88	27.32	30	PASS

DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Right	25.23	1.88	27.11	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Inner_Full	25.36	1.88	27.24	30	PASS
DC_5A_n78A	30	5+50	DFT-QPSK	M+H	Outer_Full	24.55	1.88	26.43	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Left	24.54	1.88	26.42	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Right	24.32	1.88	26.2	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Inner_Full	24.17	1.88	26.05	30	PASS
DC_5A_n78A	30	5+50	DFT-16QAM	M+H	Outer_Full	23.24	1.88	25.12	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Left	22.96	1.88	24.84	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Right	22.69	1.88	24.57	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Inner_Full	22.7	1.88	24.58	30	PASS
DC_5A_n78A	30	5+50	DFT-64QAM	M+H	Outer_Full	22.77	1.88	24.65	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Left	20.76	1.88	22.64	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Right	20.54	1.88	22.42	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Inner_Full	20.69	1.88	22.57	30	PASS
DC_5A_n78A	30	5+50	DFT-256QAM	M+H	Outer_Full	20.76	1.88	22.64	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Left	25.65	1.88	27.53	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Right	25.27	1.88	27.15	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Inner_Full	25.5	1.88	27.38	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+L	Outer_Full	24.92	1.88	26.8	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Right	25.21	1.88	27.09	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Inner_Full	25.42	1.88	27.3	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.42	1.88	26.3	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Left	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Right	24.03	1.88	25.91	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.38	1.88	26.26	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+L	Outer_Full	23.39	1.88	25.27	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Left	23.06	1.88	24.94	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Right	22.54	1.88	24.42	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Inner_Full	22.92	1.88	24.8	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+L	Outer_Full	22.88	1.88	24.76	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Left	21.24	1.88	23.12	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Right	20.72	1.88	22.6	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Inner_Full	21	1.88	22.88	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+L	Outer_Full	20.84	1.88	22.72	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Left	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Right	24.89	1.88	26.77	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Inner_Full	25.34	1.88	27.22	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+M	Outer_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Left	25.35	1.88	27.23	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Right	24.91	1.88	26.79	30	PASS

DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Inner_Full	25.28	1.88	27.16	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+M	Outer_Full	24.58	1.88	26.46	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Left	24.1	1.88	25.98	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Right	23.56	1.88	25.44	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.24	1.88	26.12	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+M	Outer_Full	23.33	1.88	25.21	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Left	23.03	1.88	24.91	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Right	22.56	1.88	24.44	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Inner_Full	22.71	1.88	24.59	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+M	Outer_Full	22.82	1.88	24.7	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Left	21.02	1.88	22.9	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Right	20.45	1.88	22.33	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Inner_Full	20.8	1.88	22.68	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+M	Outer_Full	20.88	1.88	22.76	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Left	25.49	1.88	27.37	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Right	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Inner_Full	25.07	1.88	26.95	30	PASS
DC_5A_n78A	30	5+60	DFT-BPSK	M+H	Outer_Full	24.58	1.88	26.46	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Left	25.54	1.88	27.42	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Right	24.94	1.88	26.82	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Inner_Full	24.95	1.88	26.83	30	PASS
DC_5A_n78A	30	5+60	DFT-QPSK	M+H	Outer_Full	24.65	1.88	26.53	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Left	24.35	1.88	26.23	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Right	23.87	1.88	25.75	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.06	1.88	25.94	30	PASS
DC_5A_n78A	30	5+60	DFT-16QAM	M+H	Outer_Full	23.01	1.88	24.89	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Left	23.02	1.88	24.9	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Right	22.56	1.88	24.44	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Inner_Full	22.64	1.88	24.52	30	PASS
DC_5A_n78A	30	5+60	DFT-64QAM	M+H	Outer_Full	22.55	1.88	24.43	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Left	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Right	20.39	1.88	22.27	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Inner_Full	20.5	1.88	22.38	30	PASS
DC_5A_n78A	30	5+60	DFT-256QAM	M+H	Outer_Full	20.54	1.88	22.42	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Right	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Inner_Full	25.75	1.88	27.63	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+L	Outer_Full	25.05	1.88	26.93	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Left	25.9	1.88	27.78	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Right	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Inner_Full	25.69	1.88	27.57	30	PASS

DC_5A_n78A	30	5+70	DFT-QPSK	M+L	Outer_Full	24.59	1.88	26.47	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Left	24.55	1.88	26.43	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Right	24.19	1.88	26.07	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Inner_Full	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+L	Outer_Full	23.51	1.88	25.39	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Left	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Right	23	1.88	24.88	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Inner_Full	23.02	1.88	24.9	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+L	Outer_Full	22.69	1.88	24.57	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Left	21.25	1.88	23.13	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Right	20.96	1.88	22.84	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Inner_Full	21.01	1.88	22.89	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+L	Outer_Full	20.99	1.88	22.87	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Left	25.57	1.88	27.45	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Right	24.8	1.88	26.68	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Inner_Full	25.51	1.88	27.39	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+M	Outer_Full	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Left	25.39	1.88	27.27	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Right	24.72	1.88	26.6	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Inner_Full	25.38	1.88	27.26	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+M	Outer_Full	24.43	1.88	26.31	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Left	24.52	1.88	26.4	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Right	23.81	1.88	25.69	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Inner_Full	24.3	1.88	26.18	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+M	Outer_Full	23.47	1.88	25.35	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Left	22.76	1.88	24.64	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Right	22.26	1.88	24.14	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Inner_Full	22.92	1.88	24.8	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+M	Outer_Full	22.83	1.88	24.71	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Left	20.83	1.88	22.71	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Right	20.17	1.88	22.05	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Inner_Full	20.87	1.88	22.75	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+M	Outer_Full	20.87	1.88	22.75	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Right	25.06	1.88	26.94	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Inner_Full	25.44	1.88	27.32	30	PASS
DC_5A_n78A	30	5+70	DFT-BPSK	M+H	Outer_Full	24.79	1.88	26.67	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Left	25.77	1.88	27.65	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Right	25.03	1.88	26.91	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Inner_Full	25.37	1.88	27.25	30	PASS
DC_5A_n78A	30	5+70	DFT-QPSK	M+H	Outer_Full	24.34	1.88	26.22	30	PASS

DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Left	24.71	1.88	26.59	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Right	24.1	1.88	25.98	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Inner_Full	24.19	1.88	26.07	30	PASS
DC_5A_n78A	30	5+70	DFT-16QAM	M+H	Outer_Full	23.25	1.88	25.13	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Left	23.14	1.88	25.02	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Right	22.52	1.88	24.4	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Inner_Full	22.69	1.88	24.57	30	PASS
DC_5A_n78A	30	5+70	DFT-64QAM	M+H	Outer_Full	22.65	1.88	24.53	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Left	20.93	1.88	22.81	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Right	20.31	1.88	22.19	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Inner_Full	20.68	1.88	22.56	30	PASS
DC_5A_n78A	30	5+70	DFT-256QAM	M+H	Outer_Full	20.71	1.88	22.59	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Left	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Right	25.22	1.88	27.1	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Inner_Full	25.69	1.88	27.57	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+L	Outer_Full	24.99	1.88	26.87	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Left	25.91	1.88	27.79	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Right	25.15	1.88	27.03	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Inner_Full	25.6	1.88	27.48	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+L	Outer_Full	24.54	1.88	26.42	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Left	24.68	1.88	26.56	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Right	24.05	1.88	25.93	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+L	Outer_Full	23.47	1.88	25.35	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Left	23.25	1.88	25.13	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Right	22.49	1.88	24.37	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Inner_Full	22.96	1.88	24.84	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+L	Outer_Full	22.94	1.88	24.82	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Left	21.39	1.88	23.27	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Right	20.61	1.88	22.49	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Inner_Full	21.02	1.88	22.9	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+L	Outer_Full	20.96	1.88	22.84	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Left	25.61	1.88	27.49	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Right	24.86	1.88	26.74	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Inner_Full	25.57	1.88	27.45	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+M	Outer_Full	25	1.88	26.88	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Left	25.67	1.88	27.55	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Right	24.85	1.88	26.73	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Inner_Full	25.51	1.88	27.39	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+M	Outer_Full	24.39	1.88	26.27	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Left	24.35	1.88	26.23	30	PASS

DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Right	23.65	1.88	25.53	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.34	1.88	26.22	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+M	Outer_Full	23.31	1.88	25.19	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Left	23.33	1.88	25.21	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Right	22.51	1.88	24.39	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Inner_Full	22.96	1.88	24.84	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+M	Outer_Full	22.88	1.88	24.76	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Left	21.2	1.88	23.08	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Right	20.46	1.88	22.34	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Inner_Full	20.88	1.88	22.76	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+M	Outer_Full	20.78	1.88	22.66	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Left	25.67	1.88	27.55	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Right	25.16	1.88	27.04	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Inner_Full	25.47	1.88	27.35	30	PASS
DC_5A_n78A	30	5+80	DFT-BPSK	M+H	Outer_Full	24.94	1.88	26.82	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Left	25.63	1.88	27.51	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Right	25.09	1.88	26.97	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Inner_Full	25.26	1.88	27.14	30	PASS
DC_5A_n78A	30	5+80	DFT-QPSK	M+H	Outer_Full	24.66	1.88	26.54	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Left	24.57	1.88	26.45	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Right	24.05	1.88	25.93	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.17	1.88	26.05	30	PASS
DC_5A_n78A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.17	1.88	25.05	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Left	23.19	1.88	25.07	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Right	22.64	1.88	24.52	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Inner_Full	22.78	1.88	24.66	30	PASS
DC_5A_n78A	30	5+80	DFT-64QAM	M+H	Outer_Full	22.69	1.88	24.57	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Left	21.15	1.88	23.03	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Right	20.62	1.88	22.5	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Inner_Full	20.77	1.88	22.65	30	PASS
DC_5A_n78A	30	5+80	DFT-256QAM	M+H	Outer_Full	20.69	1.88	22.57	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Left	25.95	1.88	27.83	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Right	24.88	1.88	26.76	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Inner_Full	25.48	1.88	27.36	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+L	Outer_Full	24.97	1.88	26.85	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Left	25.99	1.88	27.87	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Right	24.9	1.88	26.78	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Inner_Full	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+L	Outer_Full	24.45	1.88	26.33	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Left	24.76	1.88	26.64	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Right	23.71	1.88	25.59	30	PASS

DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.41	1.88	26.29	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+L	Outer_Full	23.37	1.88	25.25	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Left	23.45	1.88	25.33	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Right	22.37	1.88	24.25	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Inner_Full	22.97	1.88	24.85	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+L	Outer_Full	22.92	1.88	24.8	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Left	21.46	1.88	23.34	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Right	20.42	1.88	22.3	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Inner_Full	21.02	1.88	22.9	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+L	Outer_Full	20.9	1.88	22.78	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Left	25.88	1.88	27.76	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Right	25.03	1.88	26.91	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Inner_Full	25.46	1.88	27.34	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+M	Outer_Full	24.87	1.88	26.75	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Left	25.73	1.88	27.61	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Right	24.79	1.88	26.67	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Inner_Full	25.4	1.88	27.28	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+M	Outer_Full	24.35	1.88	26.23	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Left	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Right	23.94	1.88	25.82	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.35	1.88	26.23	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+M	Outer_Full	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Left	23.13	1.88	25.01	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Right	22.39	1.88	24.27	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Inner_Full	22.85	1.88	24.73	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+M	Outer_Full	22.8	1.88	24.68	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Left	21.16	1.88	23.04	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Right	20.2	1.88	22.08	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Inner_Full	20.93	1.88	22.81	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+M	Outer_Full	20.81	1.88	22.69	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Left	25.65	1.88	27.53	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Right	25.19	1.88	27.07	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Inner_Full	25.5	1.88	27.38	30	PASS
DC_5A_n78A	30	5+90	DFT-BPSK	M+H	Outer_Full	24.79	1.88	26.67	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Left	25.69	1.88	27.57	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Right	25.24	1.88	27.12	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.43	1.88	27.31	30	PASS
DC_5A_n78A	30	5+90	DFT-QPSK	M+H	Outer_Full	24.27	1.88	26.15	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Left	24.74	1.88	26.62	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Right	24.27	1.88	26.15	30	PASS
DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.28	1.88	26.16	30	PASS

DC_5A_n78A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.28	1.88	25.16	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Left	23.18	1.88	25.06	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Right	22.67	1.88	24.55	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Inner_Full	22.83	1.88	24.71	30	PASS
DC_5A_n78A	30	5+90	DFT-64QAM	M+H	Outer_Full	22.77	1.88	24.65	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Left	21.09	1.88	22.97	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Right	20.45	1.88	22.33	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Inner_Full	20.81	1.88	22.69	30	PASS
DC_5A_n78A	30	5+90	DFT-256QAM	M+H	Outer_Full	20.78	1.88	22.66	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+M	Inner_1RB_Left	25.98	1.88	27.86	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+M	Inner_1RB_Right	25.22	1.88	27.1	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+M	Inner_Full	25.53	1.88	27.41	30	PASS
DC_5A_n78A	30	5+100	DFT-BPSK	M+M	Outer_Full	24.97	1.88	26.85	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+M	Inner_1RB_Left	26	1.88	27.88	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+M	Inner_1RB_Right	25.16	1.88	27.04	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+M	Inner_Full	25.41	1.88	27.29	30	PASS
DC_5A_n78A	30	5+100	DFT-QPSK	M+M	Outer_Full	24.37	1.88	26.25	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+M	Inner_1RB_Left	24.82	1.88	26.7	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+M	Inner_1RB_Right	23.96	1.88	25.84	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+M	Inner_Full	24.44	1.88	26.32	30	PASS
DC_5A_n78A	30	5+100	DFT-16QAM	M+M	Outer_Full	23.38	1.88	25.26	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+M	Inner_1RB_Left	23.36	1.88	25.24	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+M	Inner_1RB_Right	22.62	1.88	24.5	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+M	Inner_Full	22.88	1.88	24.76	30	PASS
DC_5A_n78A	30	5+100	DFT-64QAM	M+M	Outer_Full	22.91	1.88	24.79	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+M	Inner_1RB_Left	21.45	1.88	23.33	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+M	Inner_1RB_Right	20.64	1.88	22.52	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+M	Inner_Full	20.92	1.88	22.8	30	PASS
DC_5A_n78A	30	5+100	DFT-256QAM	M+M	Outer_Full	20.86	1.88	22.74	30	PASS

Peak-to-Average Ratio for NSA

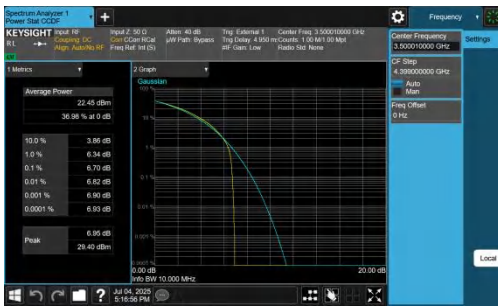
Peak-to-Average Ratio(CCDF)

Test Result

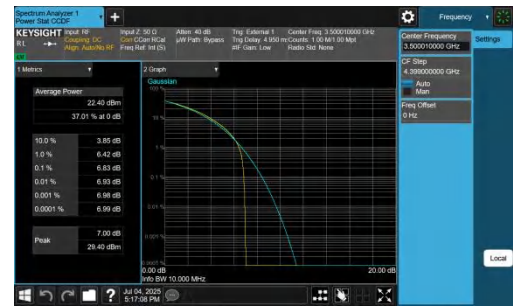
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_5A_n78A	30	5+10	CP-QPSK	M+M	Outer_Full	6.7	≤13	PASS
DC_5A_n78A	30	5+10	CP-16QAM	M+M	Outer_Full	6.83	≤13	PASS
DC_5A_n78A	30	5+10	CP-64QAM	M+M	Outer_Full	7.22	≤13	PASS
DC_5A_n78A	30	5+10	CP-256QAM	M+M	Outer_Full	8.74	≤13	PASS
DC_5A_n78A	30	5+15	CP-QPSK	M+M	Outer_Full	6.68	≤13	PASS
DC_5A_n78A	30	5+15	CP-16QAM	M+M	Outer_Full	6.71	≤13	PASS
DC_5A_n78A	30	5+15	CP-64QAM	M+M	Outer_Full	7.29	≤13	PASS
DC_5A_n78A	30	5+15	CP-256QAM	M+M	Outer_Full	8.43	≤13	PASS
DC_5A_n78A	30	5+20	CP-QPSK	M+M	Outer_Full	6.66	≤13	PASS
DC_5A_n78A	30	5+20	CP-16QAM	M+M	Outer_Full	6.58	≤13	PASS
DC_5A_n78A	30	5+20	CP-64QAM	M+M	Outer_Full	7.2	≤13	PASS
DC_5A_n78A	30	5+20	CP-256QAM	M+M	Outer_Full	8.37	≤13	PASS
DC_5A_n78A	30	5+25	CP-QPSK	M+M	Outer_Full	6.83	≤13	PASS
DC_5A_n78A	30	5+25	CP-16QAM	M+M	Outer_Full	6.85	≤13	PASS
DC_5A_n78A	30	5+25	CP-64QAM	M+M	Outer_Full	7.28	≤13	PASS
DC_5A_n78A	30	5+25	CP-256QAM	M+M	Outer_Full	8.3	≤13	PASS
DC_5A_n78A	30	5+30	CP-QPSK	M+M	Outer_Full	6.69	≤13	PASS
DC_5A_n78A	30	5+30	CP-16QAM	M+M	Outer_Full	6.82	≤13	PASS
DC_5A_n78A	30	5+30	CP-64QAM	M+M	Outer_Full	7.23	≤13	PASS
DC_5A_n78A	30	5+30	CP-256QAM	M+M	Outer_Full	8.45	≤13	PASS
DC_5A_n78A	30	5+40	CP-QPSK	M+M	Outer_Full	6.66	≤13	PASS
DC_5A_n78A	30	5+40	CP-16QAM	M+M	Outer_Full	6.79	≤13	PASS
DC_5A_n78A	30	5+40	CP-64QAM	M+M	Outer_Full	7.24	≤13	PASS
DC_5A_n78A	30	5+40	CP-256QAM	M+M	Outer_Full	8.47	≤13	PASS
DC_5A_n78A	30	5+50	CP-QPSK	M+M	Outer_Full	6.74	≤13	PASS
DC_5A_n78A	30	5+50	CP-16QAM	M+M	Outer_Full	6.72	≤13	PASS
DC_5A_n78A	30	5+50	CP-64QAM	M+M	Outer_Full	7.36	≤13	PASS
DC_5A_n78A	30	5+50	CP-256QAM	M+M	Outer_Full	8.36	≤13	PASS
DC_5A_n78A	30	5+60	CP-QPSK	M+M	Outer_Full	6.9	≤13	PASS
DC_5A_n78A	30	5+60	CP-16QAM	M+M	Outer_Full	6.92	≤13	PASS
DC_5A_n78A	30	5+60	CP-64QAM	M+M	Outer_Full	7.43	≤13	PASS
DC_5A_n78A	30	5+60	CP-256QAM	M+M	Outer_Full	8.51	≤13	PASS
DC_5A_n78A	30	5+70	CP-QPSK	M+M	Outer_Full	6.86	≤13	PASS
DC_5A_n78A	30	5+70	CP-16QAM	M+M	Outer_Full	6.85	≤13	PASS
DC_5A_n78A	30	5+70	CP-64QAM	M+M	Outer_Full	7.35	≤13	PASS
DC_5A_n78A	30	5+70	CP-256QAM	M+M	Outer_Full	8.57	≤13	PASS

DC_5A_n78A	30	5+80	CP-QPSK	M+M	Outer_Full	6.86	≤13	PASS
DC_5A_n78A	30	5+80	CP-16QAM	M+M	Outer_Full	6.89	≤13	PASS
DC_5A_n78A	30	5+80	CP-64QAM	M+M	Outer_Full	7.28	≤13	PASS
DC_5A_n78A	30	5+80	CP-256QAM	M+M	Outer_Full	8.49	≤13	PASS
DC_5A_n78A	30	5+90	CP-QPSK	M+M	Outer_Full	6.84	≤13	PASS
DC_5A_n78A	30	5+90	CP-16QAM	M+M	Outer_Full	6.84	≤13	PASS
DC_5A_n78A	30	5+90	CP-64QAM	M+M	Outer_Full	7.37	≤13	PASS
DC_5A_n78A	30	5+90	CP-256QAM	M+M	Outer_Full	8.59	≤13	PASS
DC_5A_n78A	30	5+100	CP-QPSK	M+M	Outer_Full	7.09	≤13	PASS
DC_5A_n78A	30	5+100	CP-16QAM	M+M	Outer_Full	7.01	≤13	PASS
DC_5A_n78A	30	5+100	CP-64QAM	M+M	Outer_Full	7.41	≤13	PASS
DC_5A_n78A	30	5+100	CP-256QAM	M+M	Outer_Full	8.51	≤13	PASS

Test Graphs



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-PASS



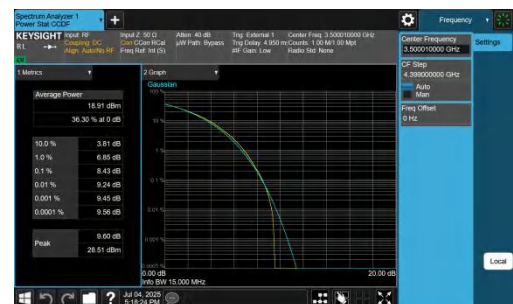
NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS

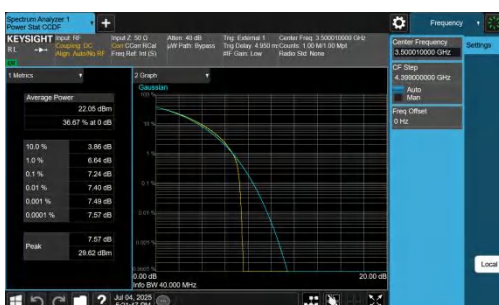
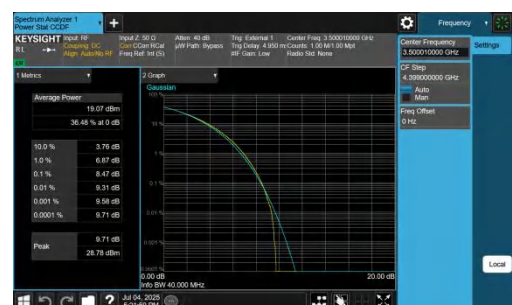

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

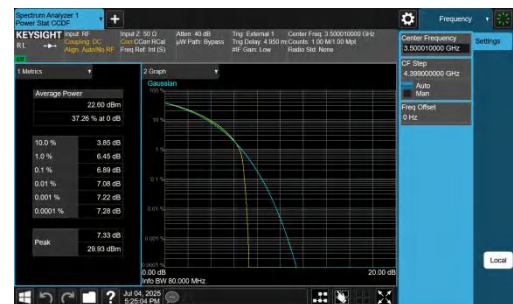
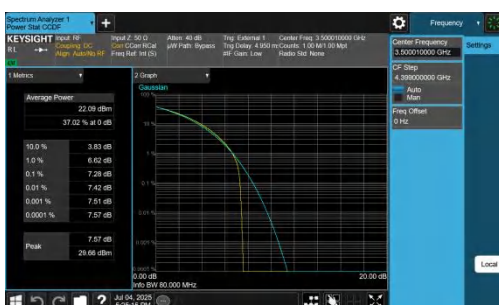
NTNV-DC_5A_n78A-PC2-30-5+60-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-64QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-256QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-QPSK-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-16QAM-
Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-64QAM
-Outter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-CP-256QAM
-Outter_Full-PASS

26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n78A	30	5+10	CP-QPSK	M+M	Outer_Full	8.6134	10.01	PASS
DC_5A_n78A	30	5+10	CP-16QAM	M+M	Outer_Full	8.6269	9.938	PASS
DC_5A_n78A	30	5+10	CP-64QAM	M+M	Outer_Full	8.6694	10.05	PASS
DC_5A_n78A	30	5+10	CP-256QAM	M+M	Outer_Full	8.6794	10.20	PASS
DC_5A_n78A	30	5+15	CP-QPSK	M+M	Outer_Full	13.622	15.21	PASS
DC_5A_n78A	30	5+15	CP-16QAM	M+M	Outer_Full	13.598	15.10	PASS
DC_5A_n78A	30	5+15	CP-64QAM	M+M	Outer_Full	13.611	15.23	PASS
DC_5A_n78A	30	5+15	CP-256QAM	M+M	Outer_Full	13.621	15.11	PASS
DC_5A_n78A	30	5+20	CP-QPSK	M+M	Outer_Full	18.243	20.06	PASS
DC_5A_n78A	30	5+20	CP-16QAM	M+M	Outer_Full	18.253	20.08	PASS
DC_5A_n78A	30	5+20	CP-64QAM	M+M	Outer_Full	18.270	20.03	PASS
DC_5A_n78A	30	5+20	CP-256QAM	M+M	Outer_Full	18.251	20.16	PASS
DC_5A_n78A	30	5+25	CP-QPSK	M+M	Outer_Full	23.227	25.27	PASS
DC_5A_n78A	30	5+25	CP-16QAM	M+M	Outer_Full	23.323	25.10	PASS
DC_5A_n78A	30	5+25	CP-64QAM	M+M	Outer_Full	23.251	25.25	PASS
DC_5A_n78A	30	5+25	CP-256QAM	M+M	Outer_Full	23.307	25.39	PASS
DC_5A_n78A	30	5+30	CP-QPSK	M+M	Outer_Full	27.907	30.12	PASS
DC_5A_n78A	30	5+30	CP-16QAM	M+M	Outer_Full	27.974	30.10	PASS
DC_5A_n78A	30	5+30	CP-64QAM	M+M	Outer_Full	27.904	32.27	PASS
DC_5A_n78A	30	5+30	CP-256QAM	M+M	Outer_Full	27.930	30.22	PASS
DC_5A_n78A	30	5+40	CP-QPSK	M+M	Outer_Full	37.977	40.58	PASS
DC_5A_n78A	30	5+40	CP-16QAM	M+M	Outer_Full	37.939	40.44	PASS
DC_5A_n78A	30	5+40	CP-64QAM	M+M	Outer_Full	37.902	40.37	PASS
DC_5A_n78A	30	5+40	CP-256QAM	M+M	Outer_Full	37.892	40.42	PASS
DC_5A_n78A	30	5+50	CP-QPSK	M+M	Outer_Full	47.570	50.16	PASS
DC_5A_n78A	30	5+50	CP-16QAM	M+M	Outer_Full	47.508	50.29	PASS
DC_5A_n78A	30	5+50	CP-64QAM	M+M	Outer_Full	47.641	50.25	PASS
DC_5A_n78A	30	5+50	CP-256QAM	M+M	Outer_Full	47.534	50.26	PASS
DC_5A_n78A	30	5+60	CP-QPSK	M+M	Outer_Full	57.940	61.04	PASS
DC_5A_n78A	30	5+60	CP-16QAM	M+M	Outer_Full	57.900	60.88	PASS
DC_5A_n78A	30	5+60	CP-64QAM	M+M	Outer_Full	57.925	60.95	PASS
DC_5A_n78A	30	5+60	CP-256QAM	M+M	Outer_Full	58.032	60.82	PASS
DC_5A_n78A	30	5+70	CP-QPSK	M+M	Outer_Full	67.505	71.03	PASS
DC_5A_n78A	30	5+70	CP-16QAM	M+M	Outer_Full	67.368	70.80	PASS
DC_5A_n78A	30	5+70	CP-64QAM	M+M	Outer_Full	67.558	71.15	PASS
DC_5A_n78A	30	5+70	CP-256QAM	M+M	Outer_Full	67.506	70.97	PASS

DC_5A_n78A	30	5+80	CP-QPSK	M+M	Outer_Full	77.474	81.15	PASS
DC_5A_n78A	30	5+80	CP-16QAM	M+M	Outer_Full	77.663	81.14	PASS
DC_5A_n78A	30	5+80	CP-64QAM	M+M	Outer_Full	77.483	81.36	PASS
DC_5A_n78A	30	5+80	CP-256QAM	M+M	Outer_Full	77.560	81.23	PASS
DC_5A_n78A	30	5+90	CP-QPSK	M+M	Outer_Full	87.594	91.33	PASS
DC_5A_n78A	30	5+90	CP-16QAM	M+M	Outer_Full	87.498	91.56	PASS
DC_5A_n78A	30	5+90	CP-64QAM	M+M	Outer_Full	87.533	91.40	PASS
DC_5A_n78A	30	5+90	CP-256QAM	M+M	Outer_Full	87.511	91.38	PASS
DC_5A_n78A	30	5+100	CP-QPSK	M+M	Outer_Full	97.714	102.0	PASS
DC_5A_n78A	30	5+100	CP-16QAM	M+M	Outer_Full	97.442	101.7	PASS
DC_5A_n78A	30	5+100	CP-64QAM	M+M	Outer_Full	97.419	101.7	PASS
DC_5A_n78A	30	5+100	CP-256QAM	M+M	Outer_Full	97.484	101.8	PASS

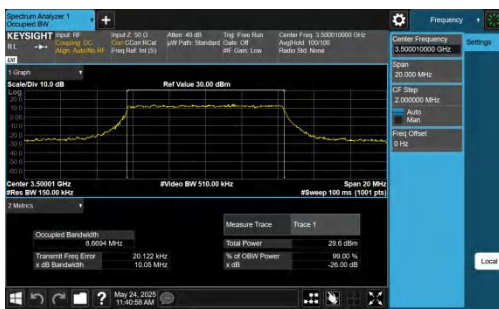
Test Graphs



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-CP-256QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+15-M+M-QPSK-IDLE-CP-64QAM-Outer_Full-Ant1-PASS



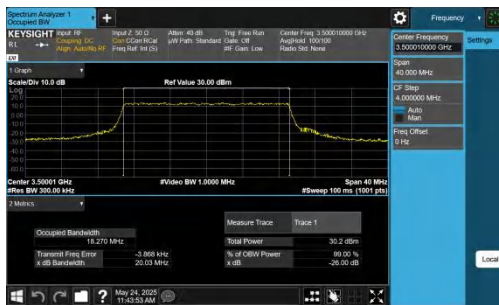
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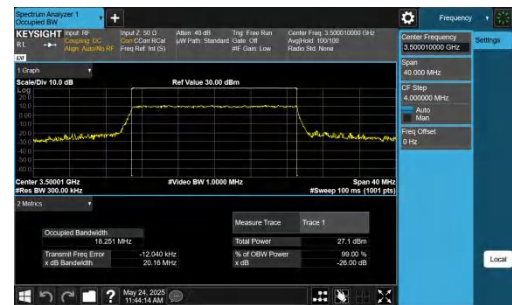
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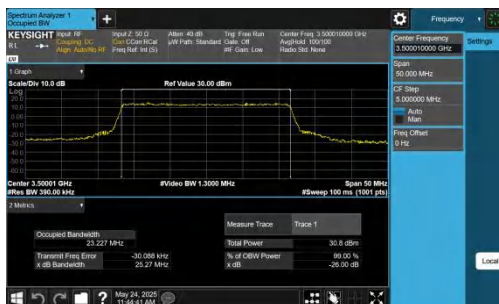
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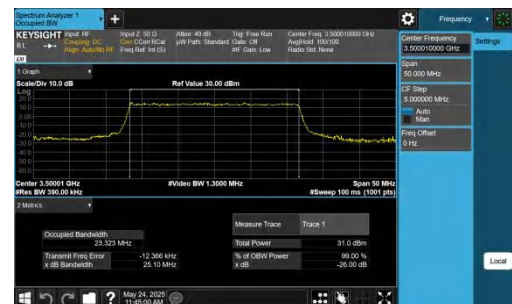
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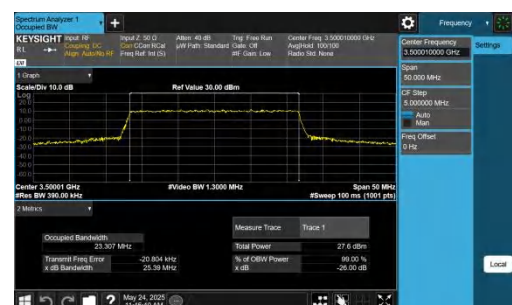
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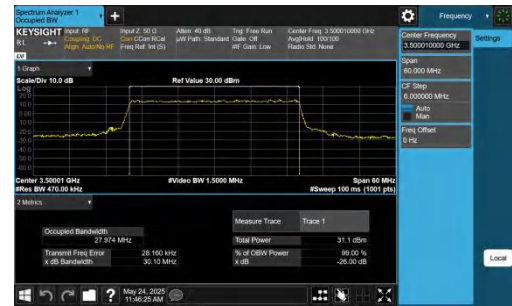
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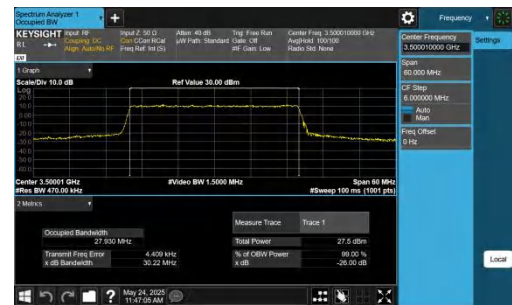
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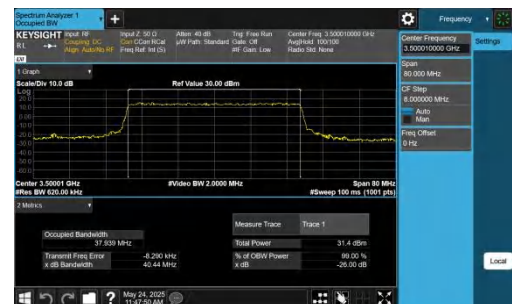
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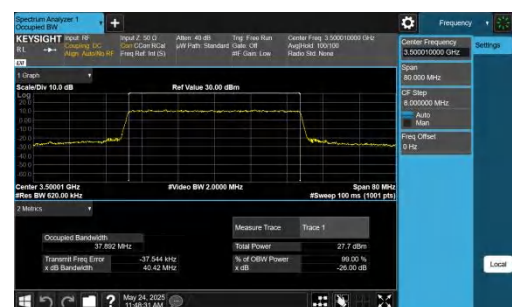
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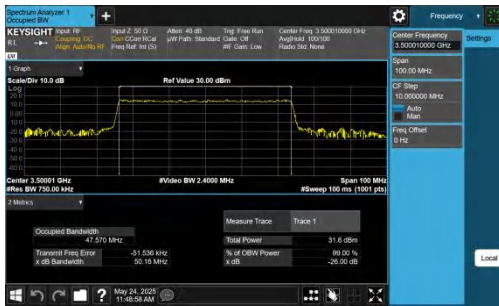
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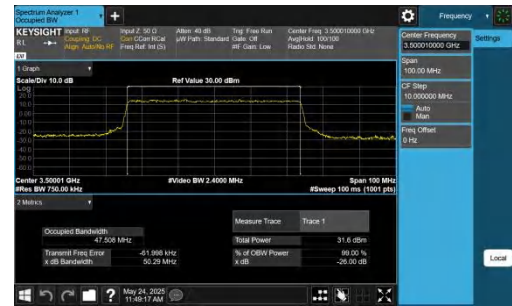
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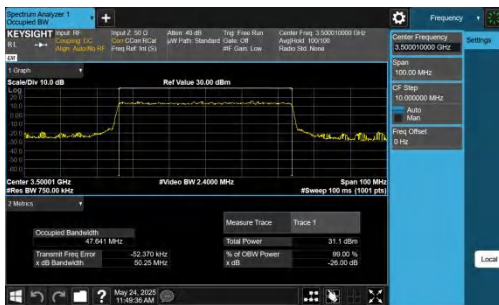
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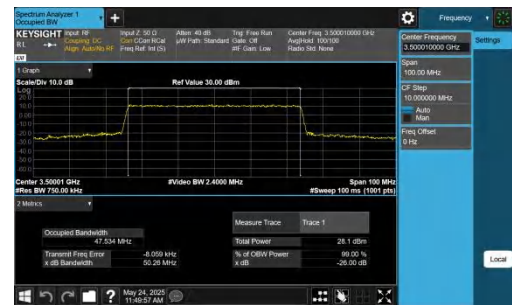
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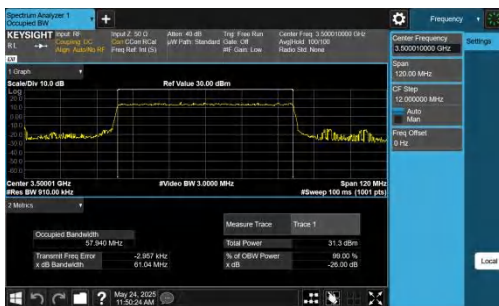
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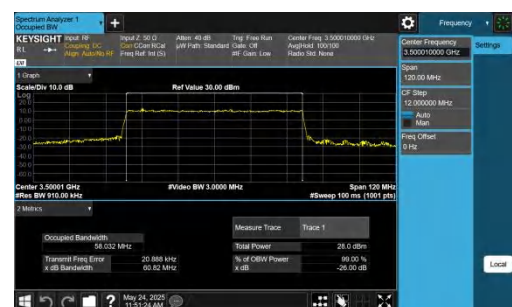
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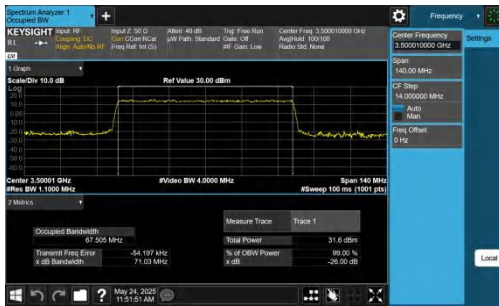
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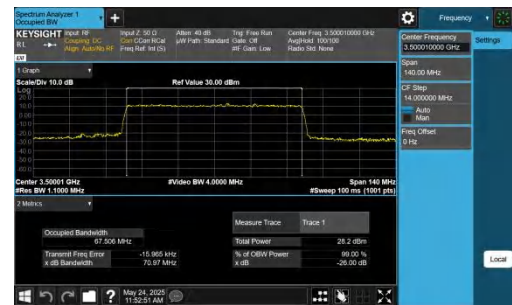
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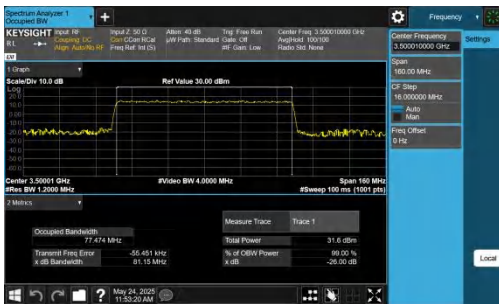
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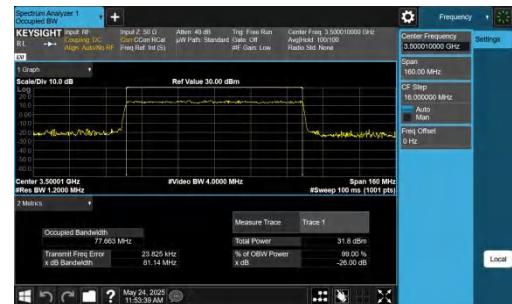
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Outer_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-Ant1-PASS



NTNV-DC_5A_n78A-PC2-30-5+80-M+M-QPSK-IDLE-CP-16QAM-O
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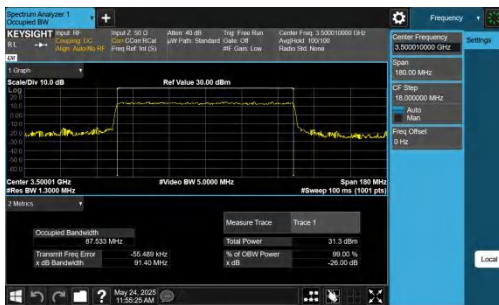
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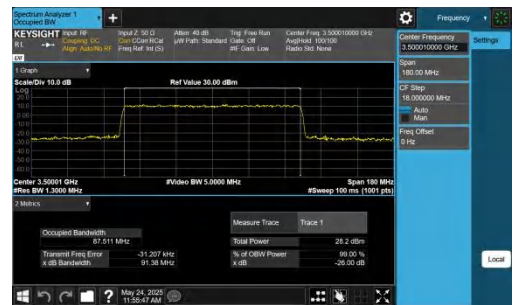
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NTNV-DC_5A_n78A-PC2-30-5+90-M+M-QPSK-IDLE-CP-16QAM-O
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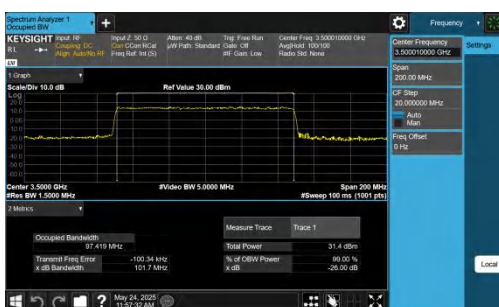
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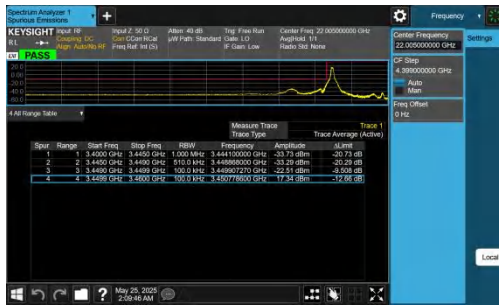
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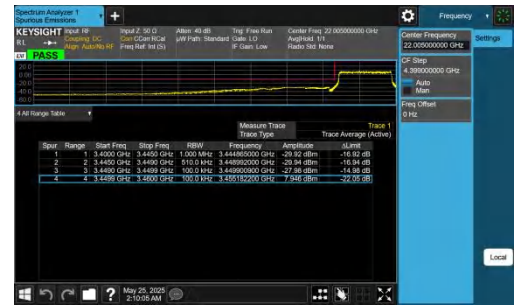
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Band Edge for NSA

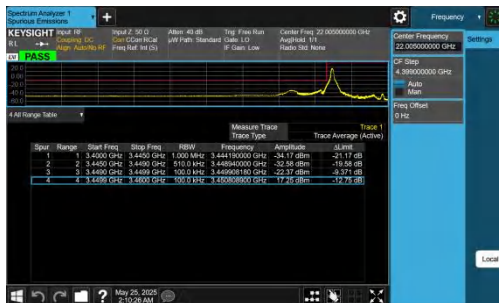
Test Graphs



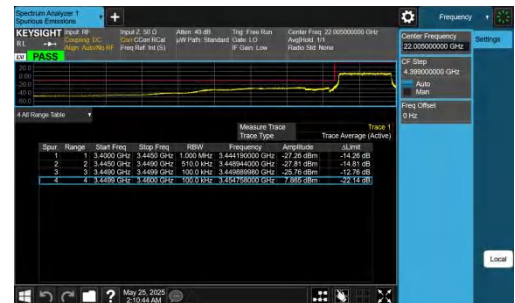
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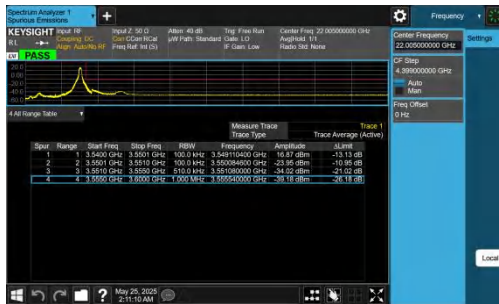
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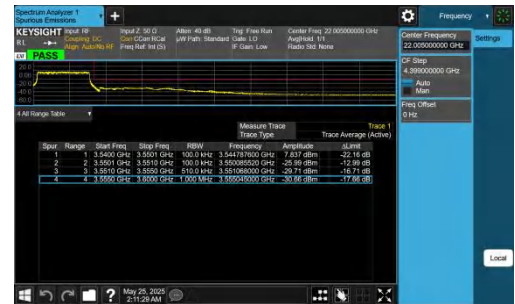
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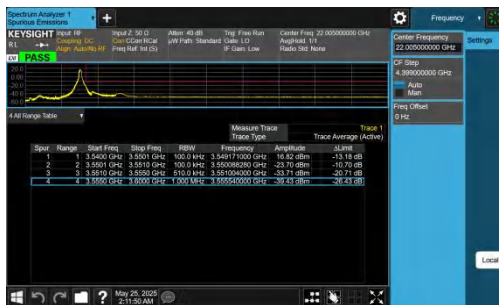
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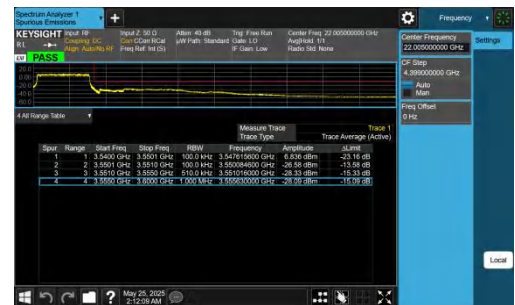
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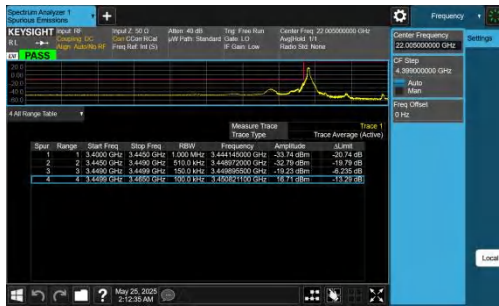
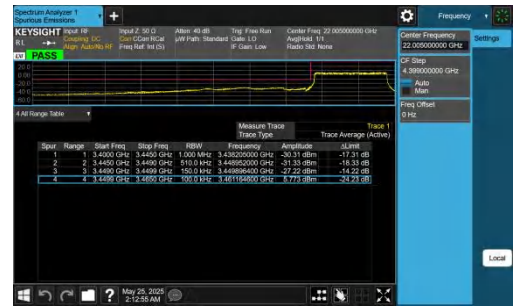
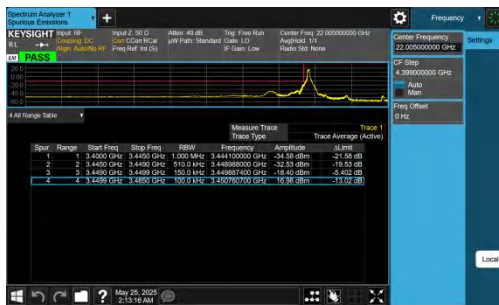
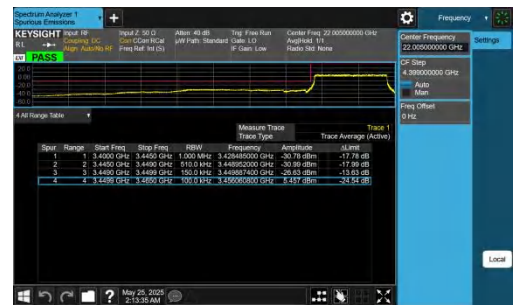
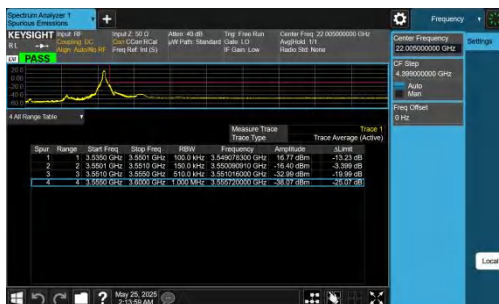
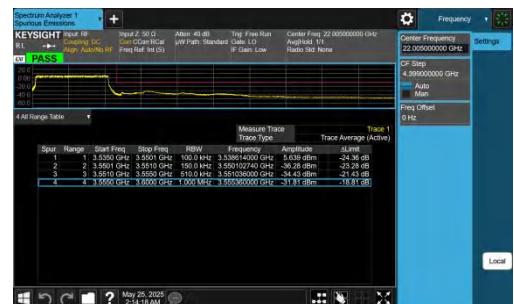
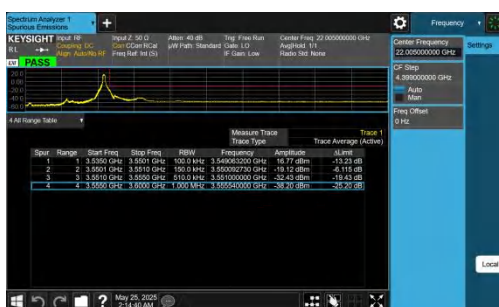
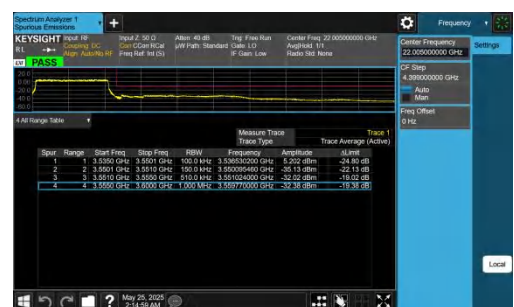
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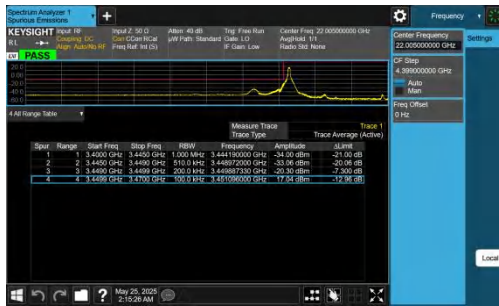
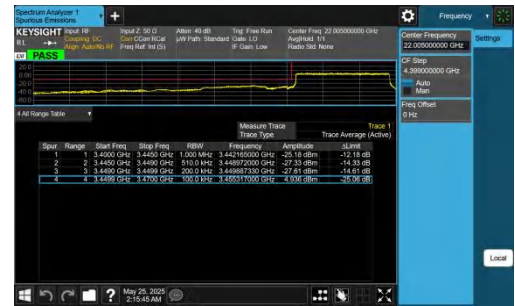
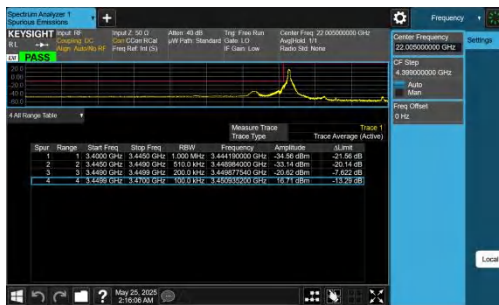
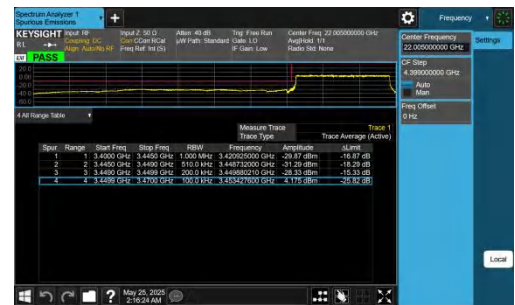
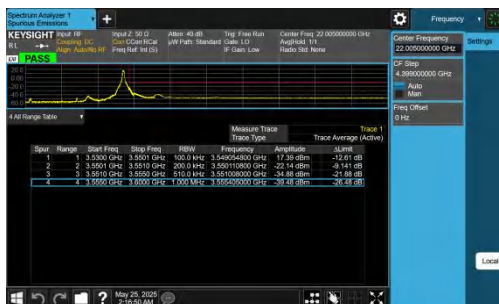
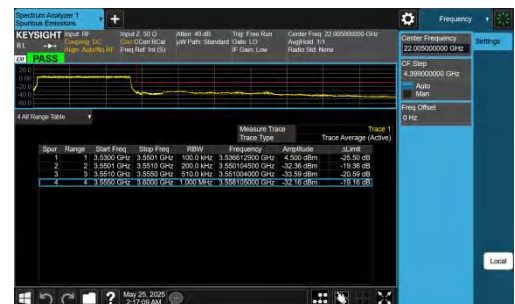
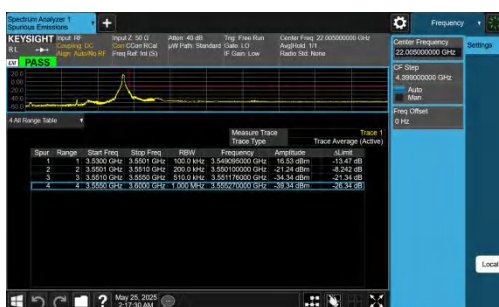
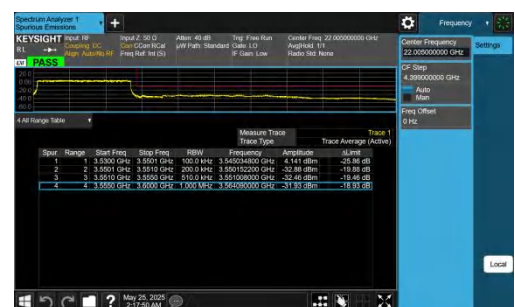


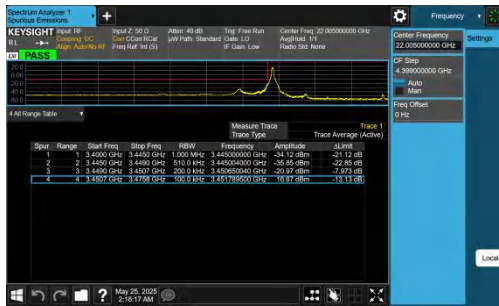
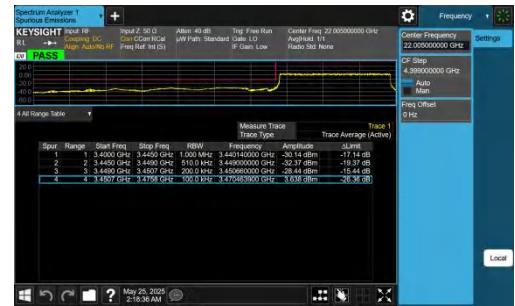
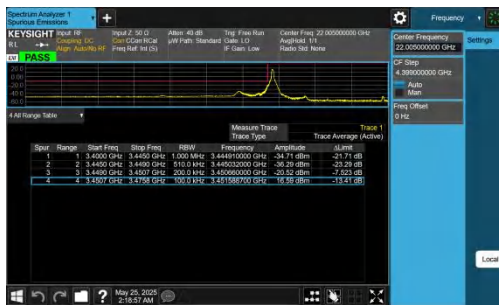
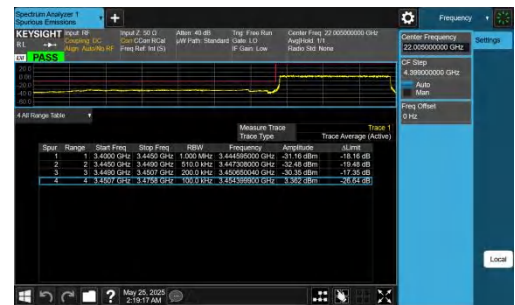
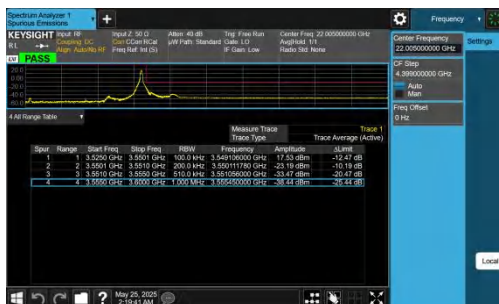
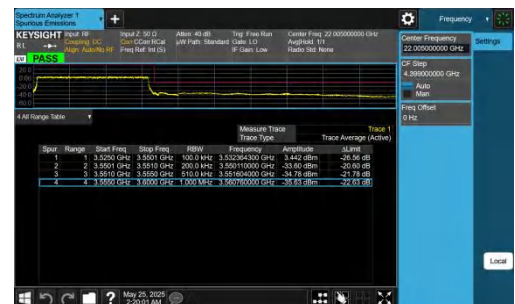
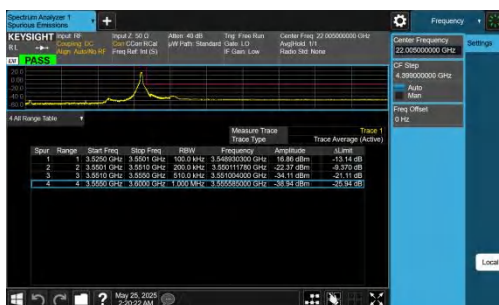
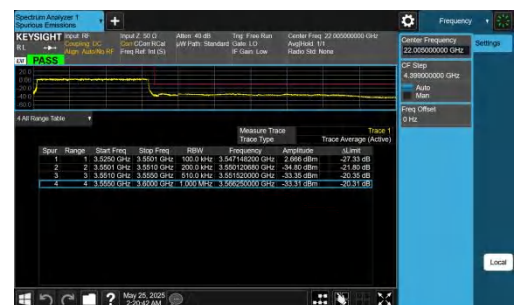
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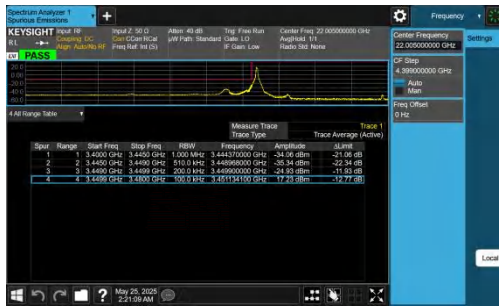
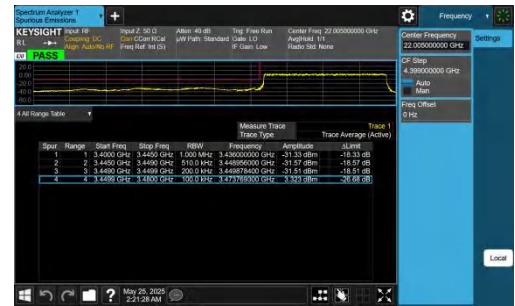
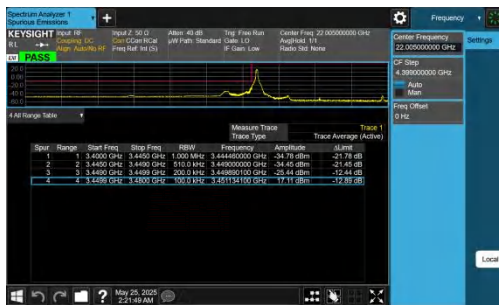
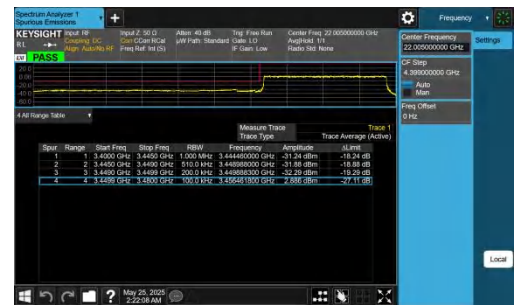
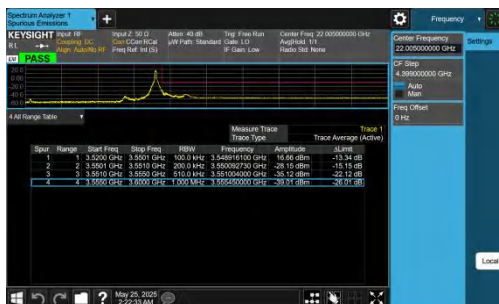
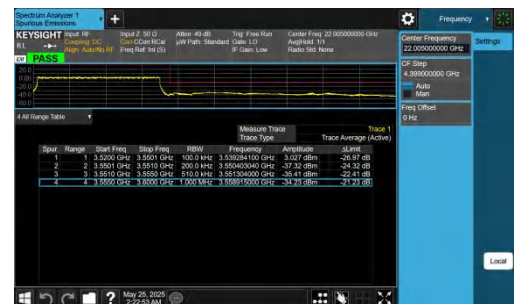
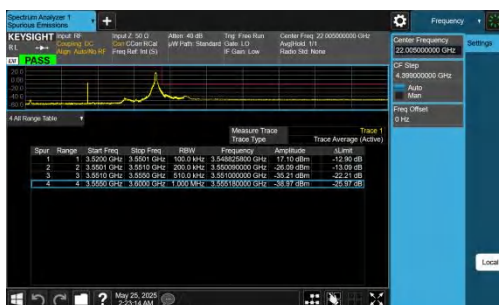
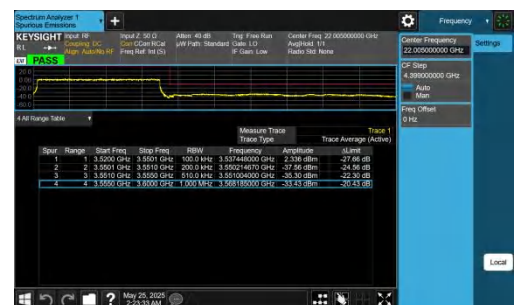


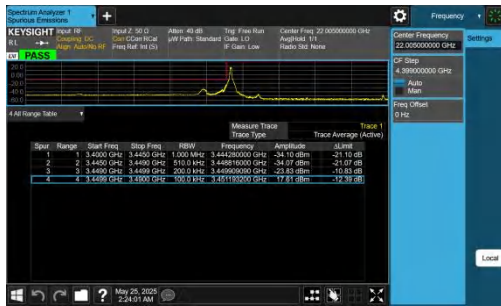
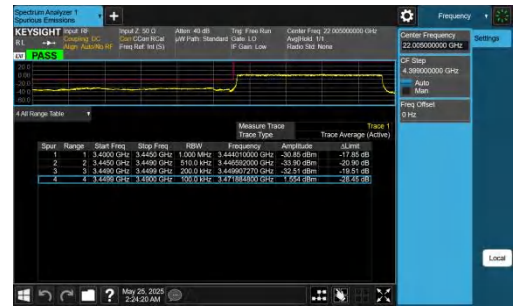
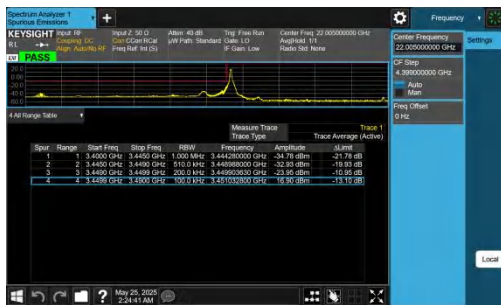
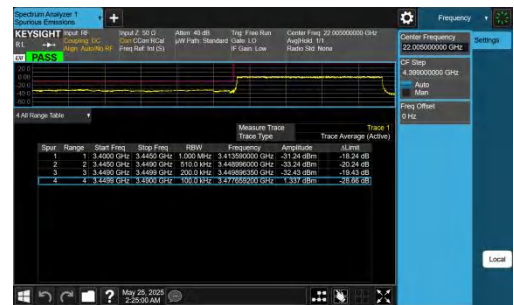
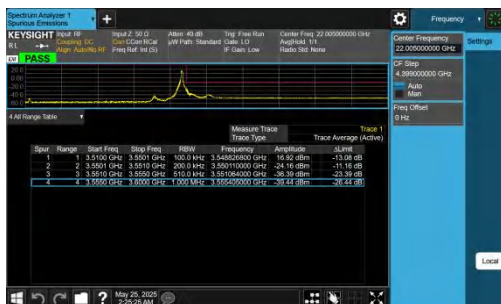
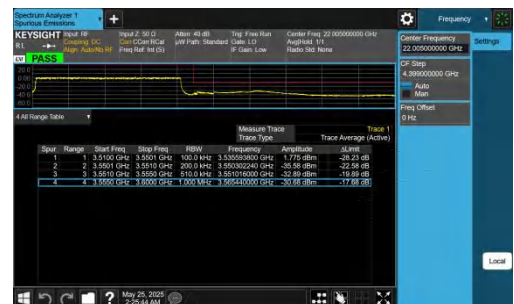
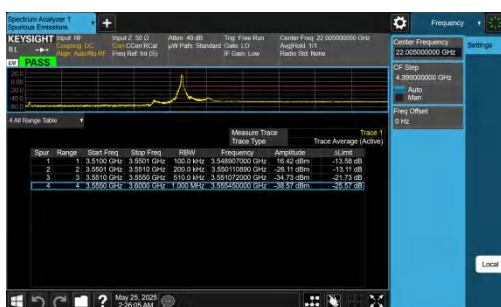
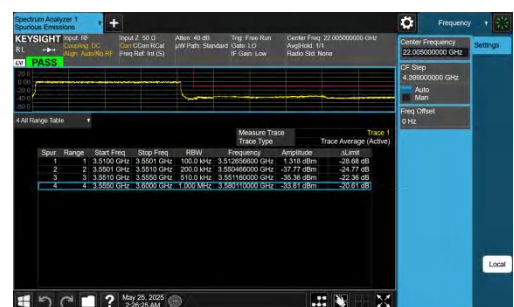
NTNV-DC_5A_n78A-PC2-30-5+10-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

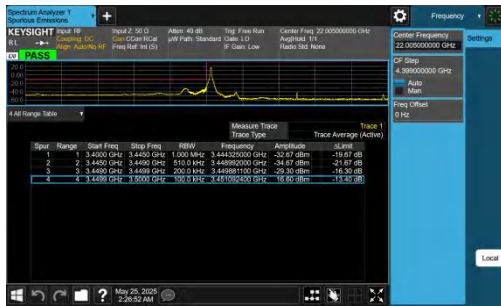
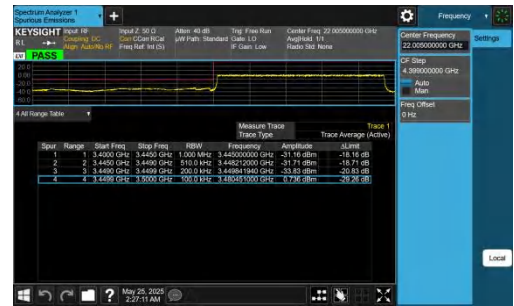
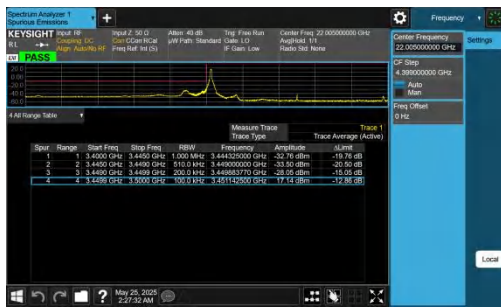
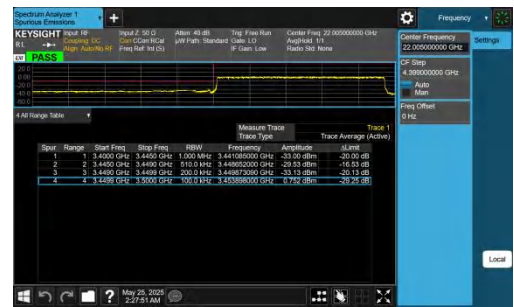
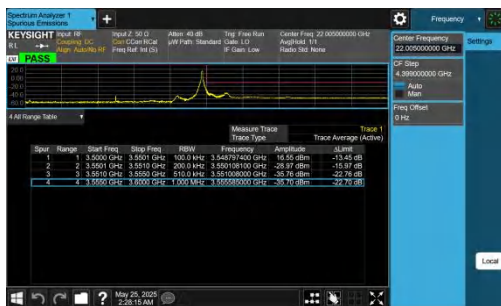
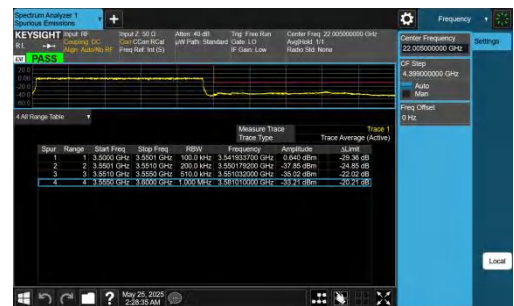
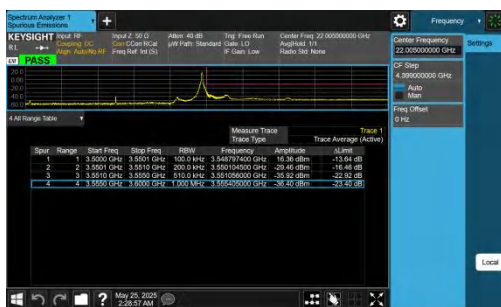
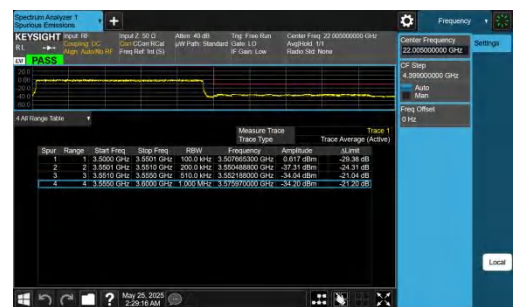

NTNV-DC_5A_n78A-PC2-30-5+15-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+15-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

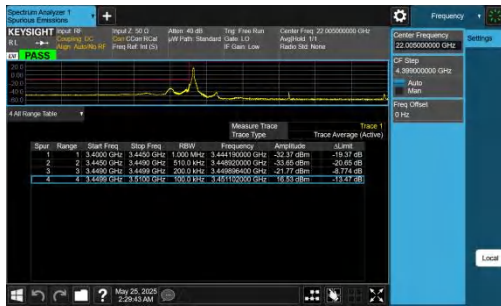
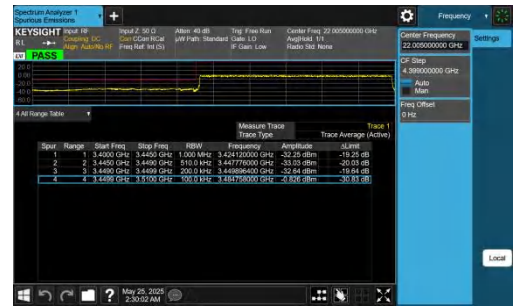
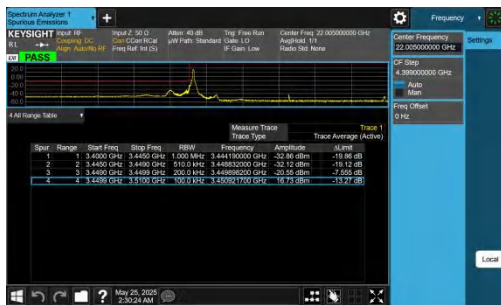
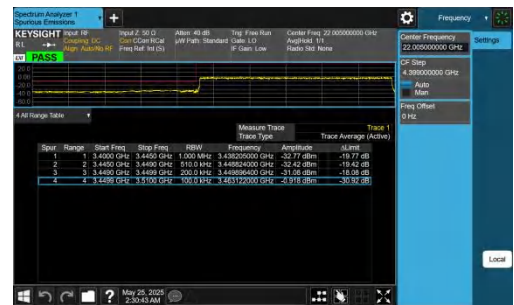
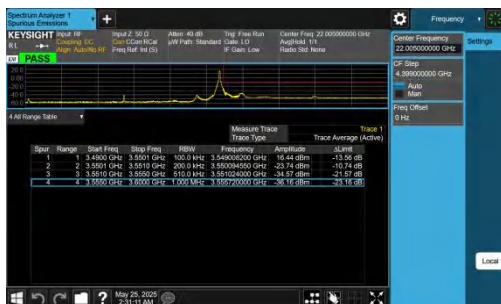
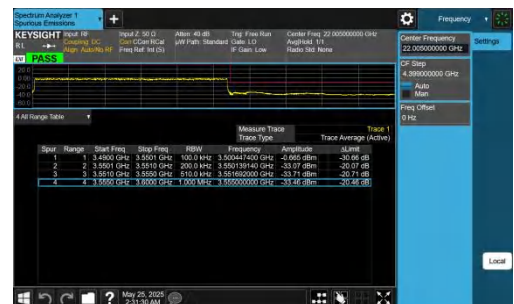
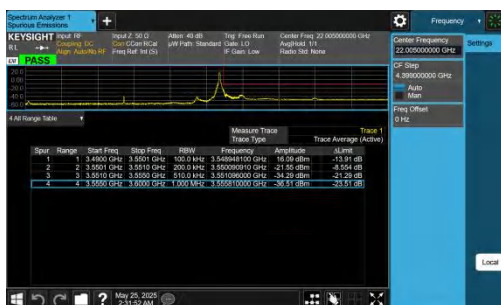
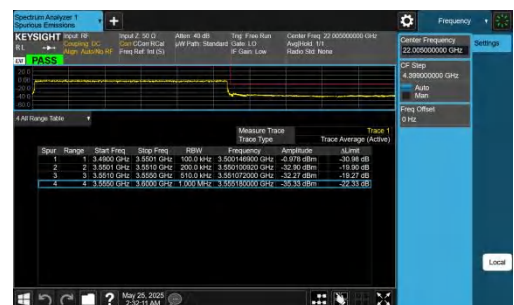

NTNV-DC_5A_n78A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

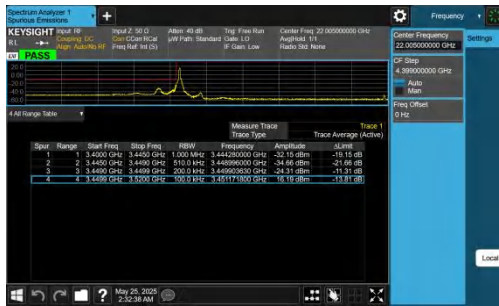
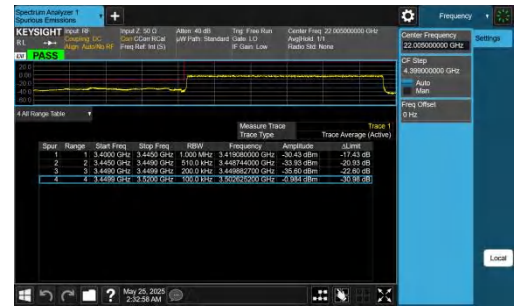
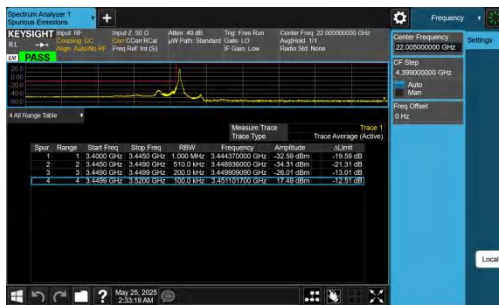
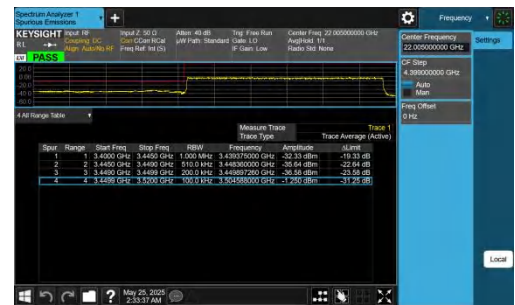
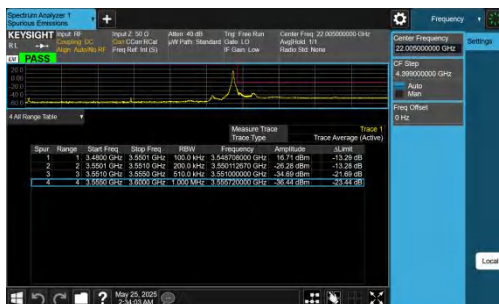
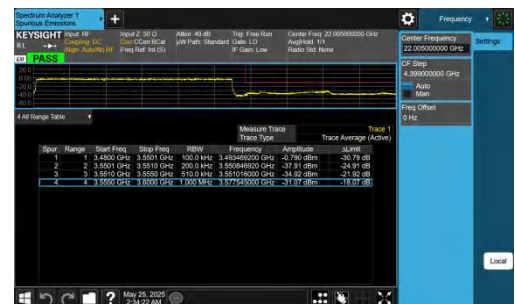
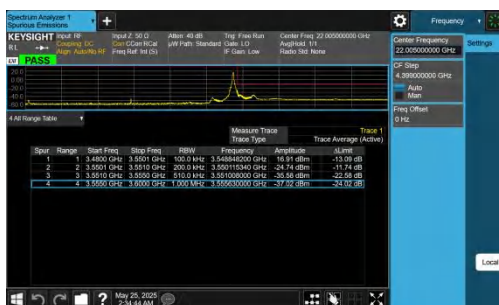
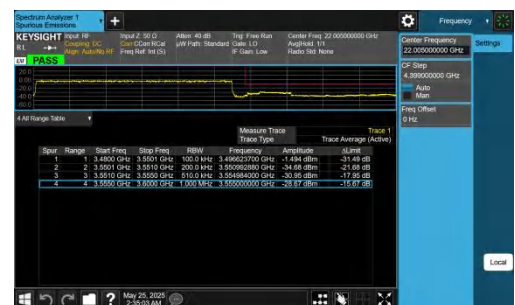

NTNV-DC_5A_n78A-PC2-30-5+25-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

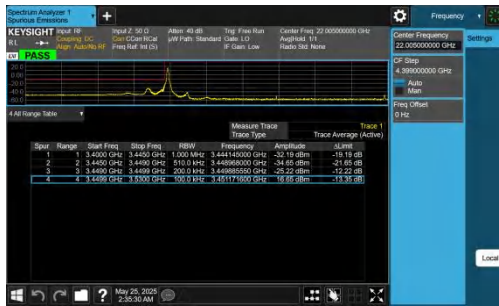
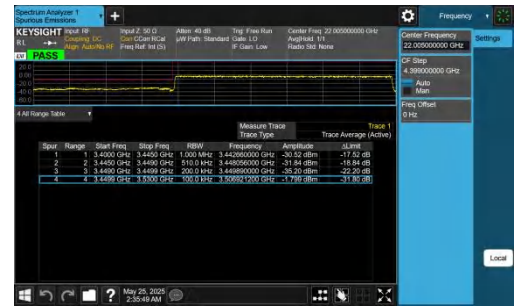
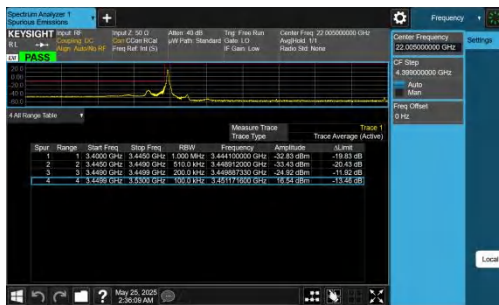
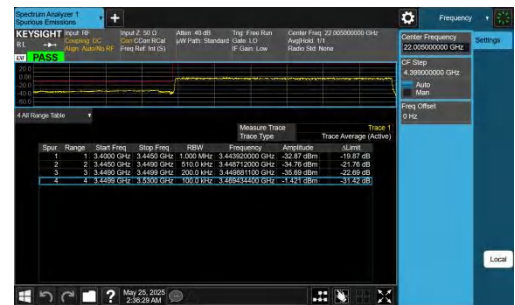
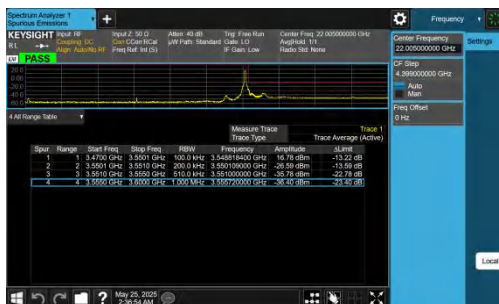
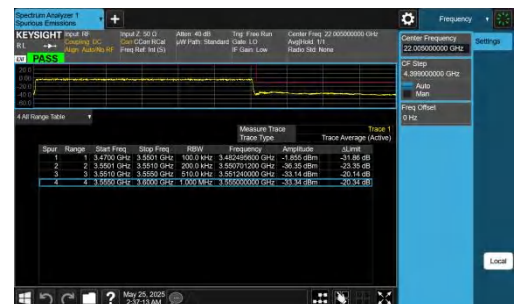
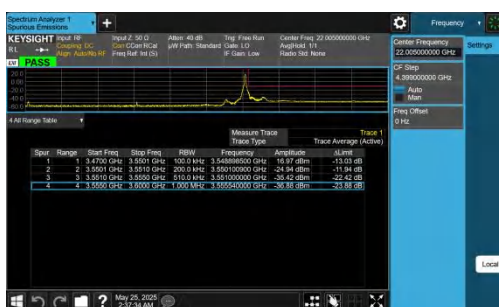
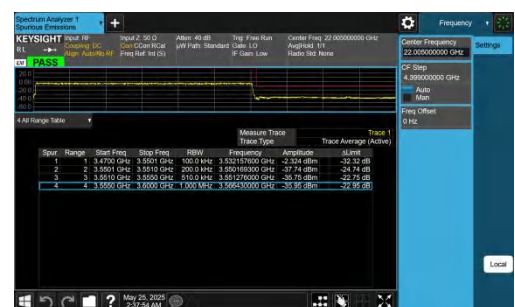

NTNV-DC_5A_n78A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

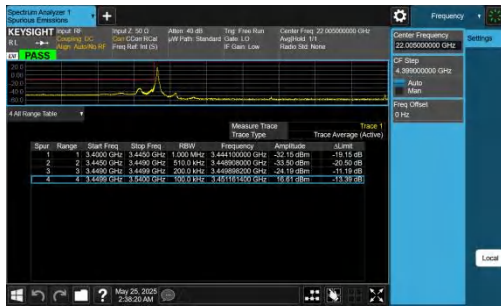
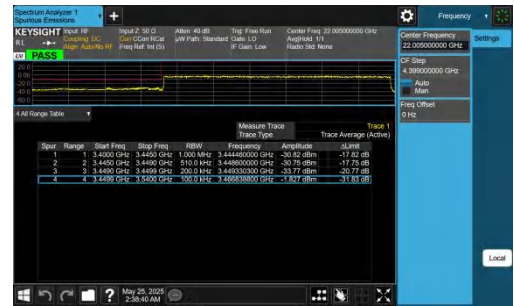
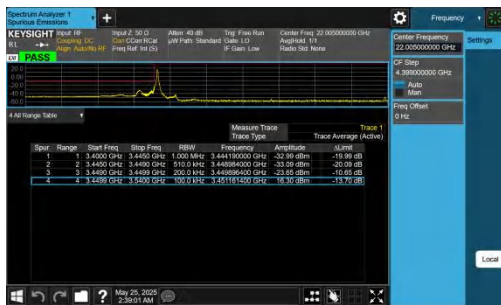
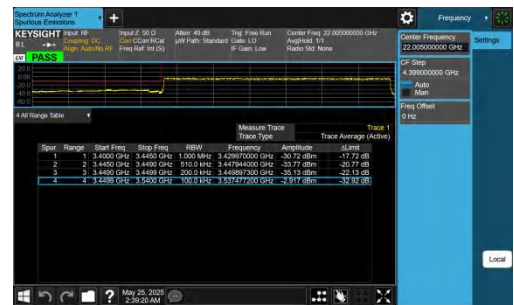
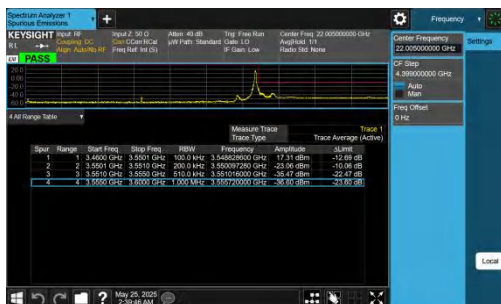
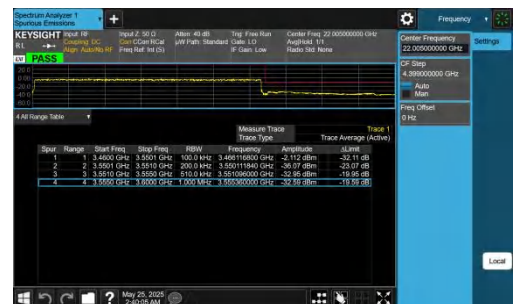
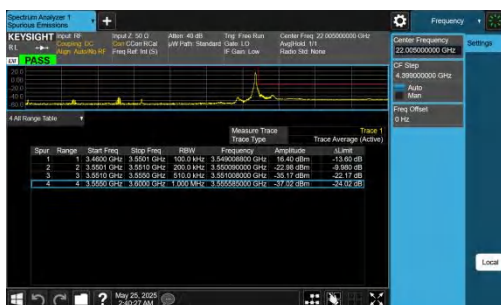
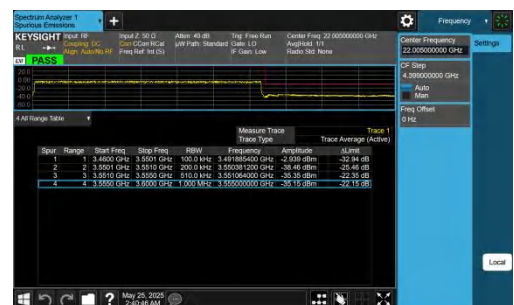

NTNV-DC_5A_n78A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

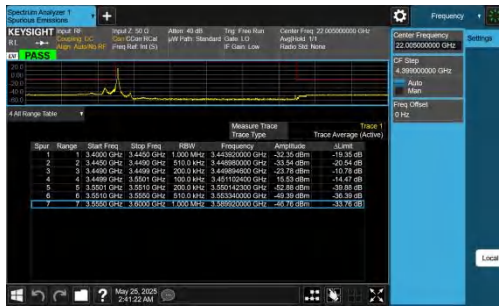

NTNV-DC_5A_n78A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

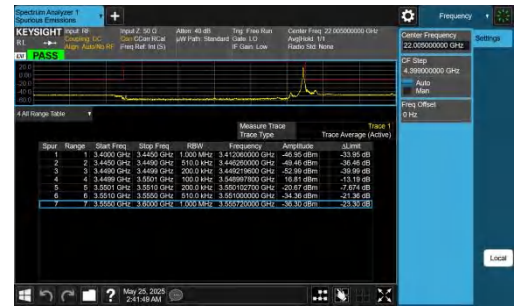

NTNV-DC_5A_n78A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS


NTNV-DC_5A_n78A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

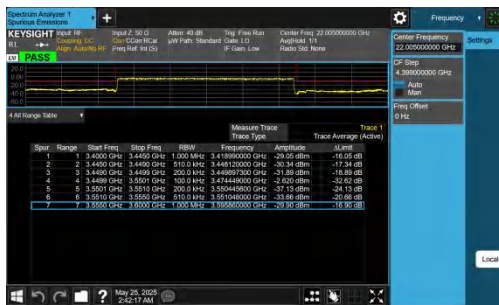

NTNV-DC_5A_n78A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_5A_n78A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS



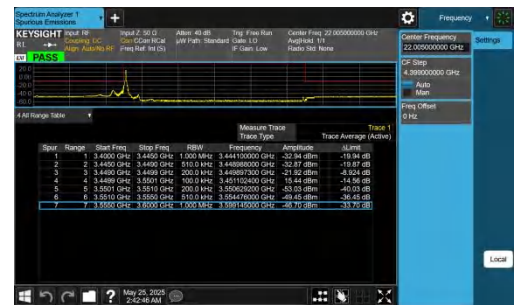
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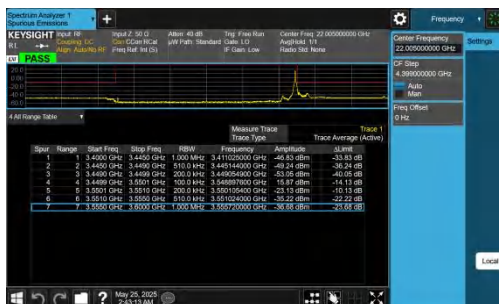
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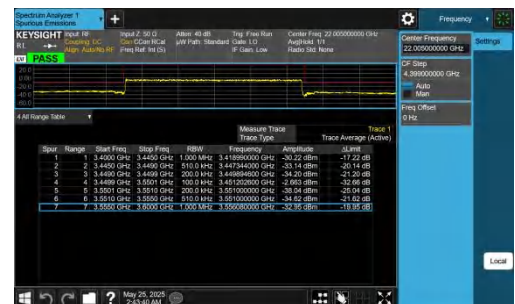
NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



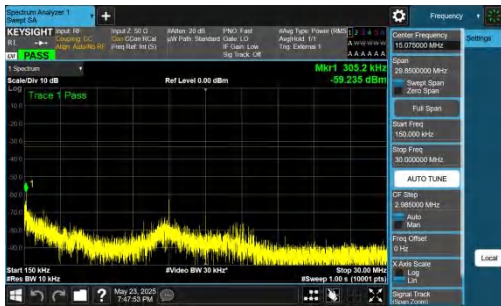
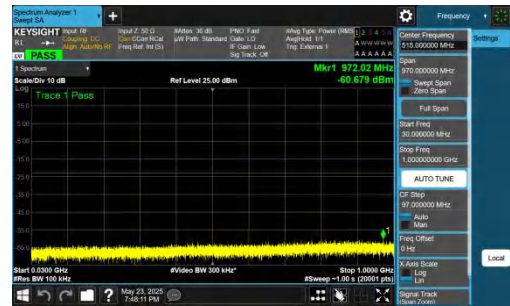
NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-QPSK-Edge_1RB_Left-PASS



NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-QPSK-Edge_1RB_Right-PASS



NTNV-DC_5A_n78A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS

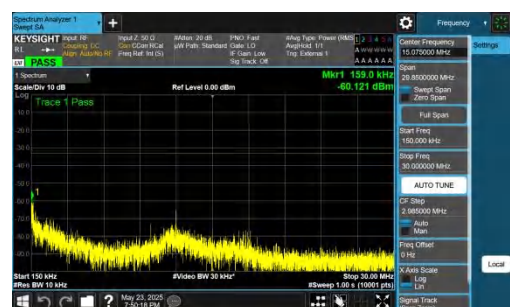

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-0.15-30-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-30-1000-PASS

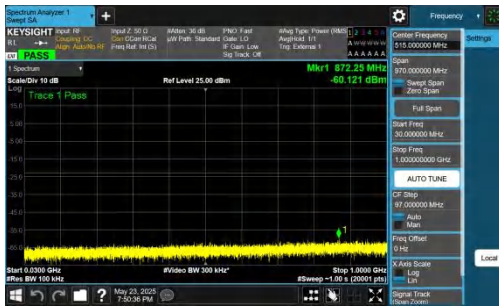
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NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-3000-6000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-6000-26000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-26000-40000-PASS

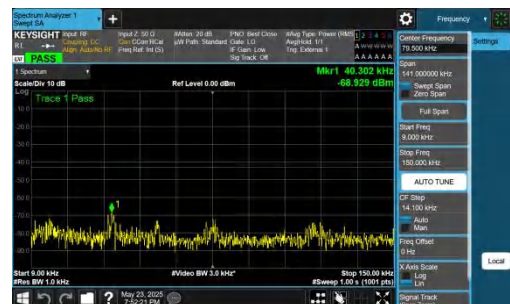
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ner_1RB_Left-0.009-0.15-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-0.15-30-PASS


NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-30-1000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-1000-3000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-3000-6000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-6000-26000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-26000-40000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-0.009-0.15-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-0.15-30-PASS

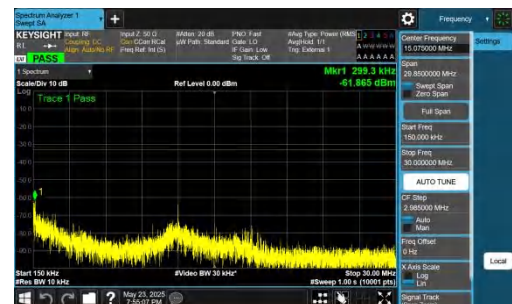
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NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-1000-3000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-3000-6000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-6000-26000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-26000-40000-PASS

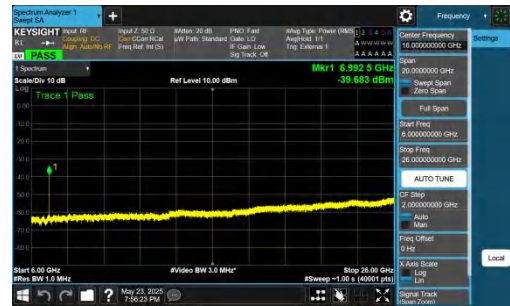
NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-I
nner_1RB_Left-0.009-0.15-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-0.15-30-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-I
nner_1RB_Left-30-1000-PASS

NTNV-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-1000-3000-PASS



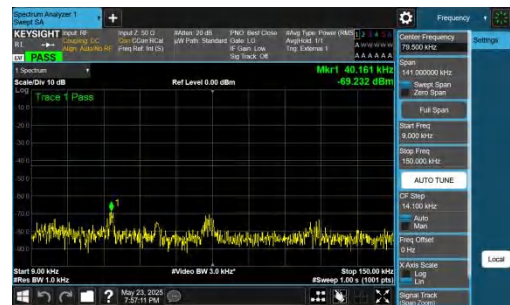
NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-3000-6000-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-6000-26000-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-26000-40000-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.009-0.15-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.15-30-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-30-1000-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-1000-3000-PASS



NTVN-DC_5A_n78A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-3000-6000-PASS