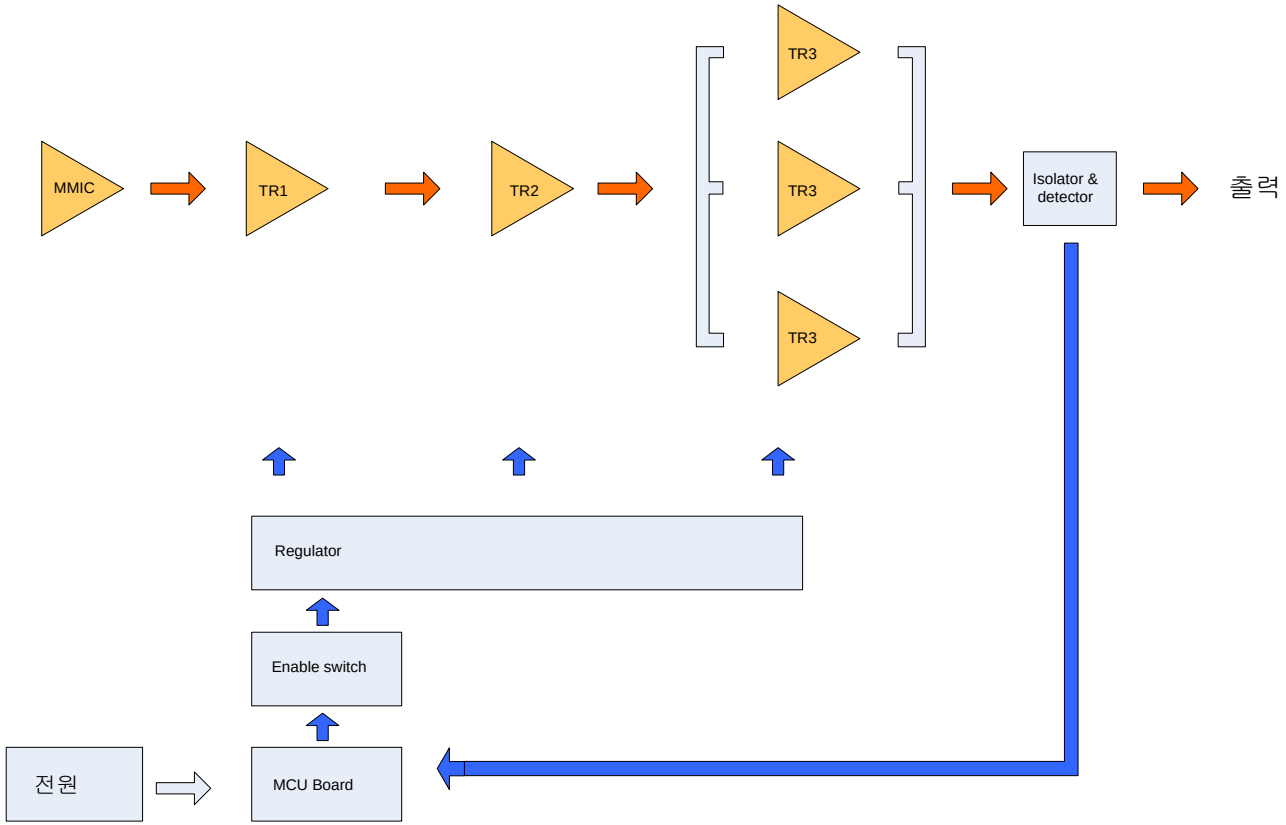
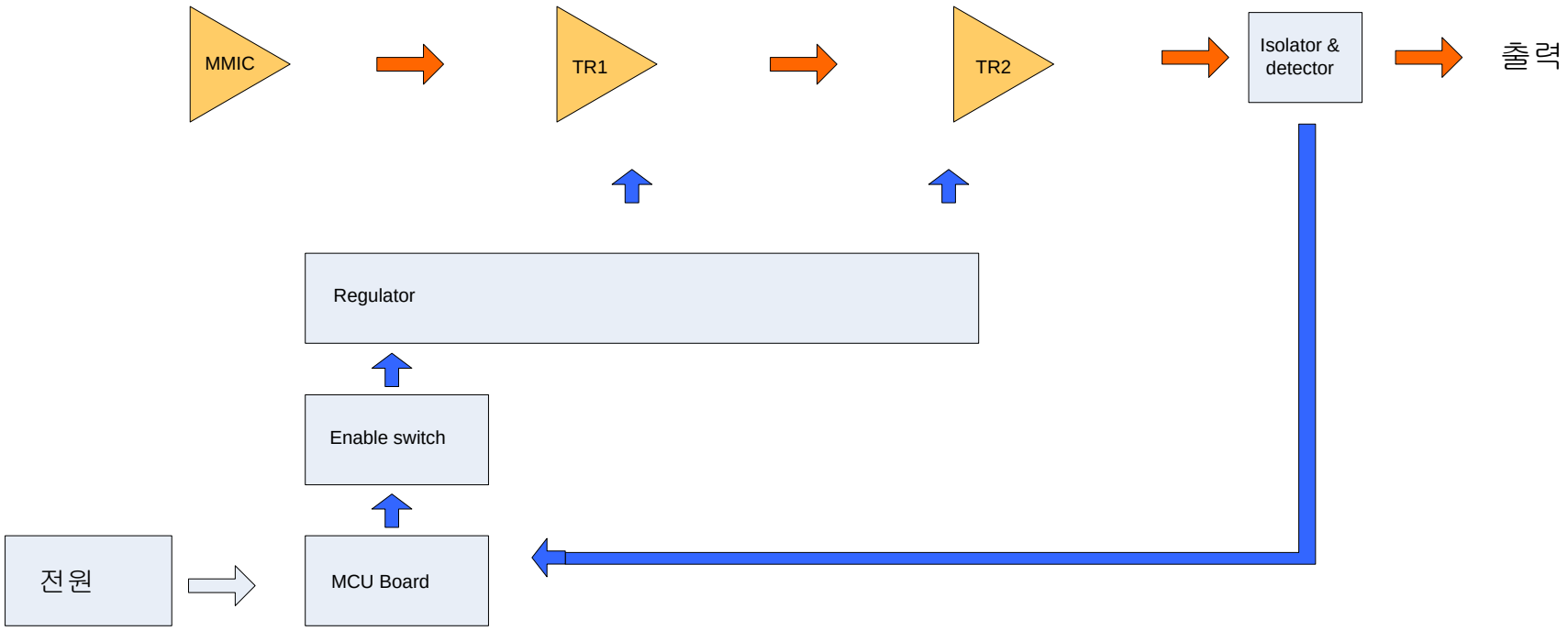
HPA

AP1960H30F11
Block Diagram



MMIC : RF 입력 신호를 적절히 증폭하고 입력 Impedance Matching
TR1 : GaAs FET를 사용하였으며 증폭 및 ACPR 특성 유지
TR2 : LDMOS 사용하였으며 TR1의 증폭 및 ACPR 특성 유지
TR3 : LDMOS 사용하였으며 TR1과 연계하여 predistortion 에 의한 선형성 확보
Isolator & detector : Amp이후의 VSWR이 나쁠 경우 main TR의 보호 및 power 측정
Control : enable, power detector에 의한 amp 운용, VSWR alarm제어

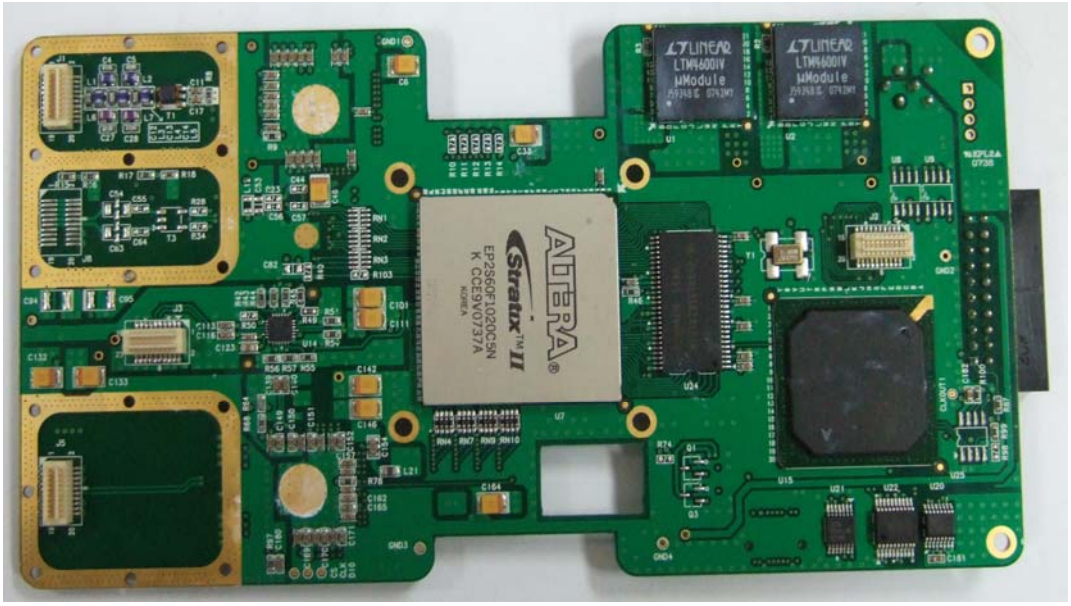
AP1875H1F11
Block Diagram



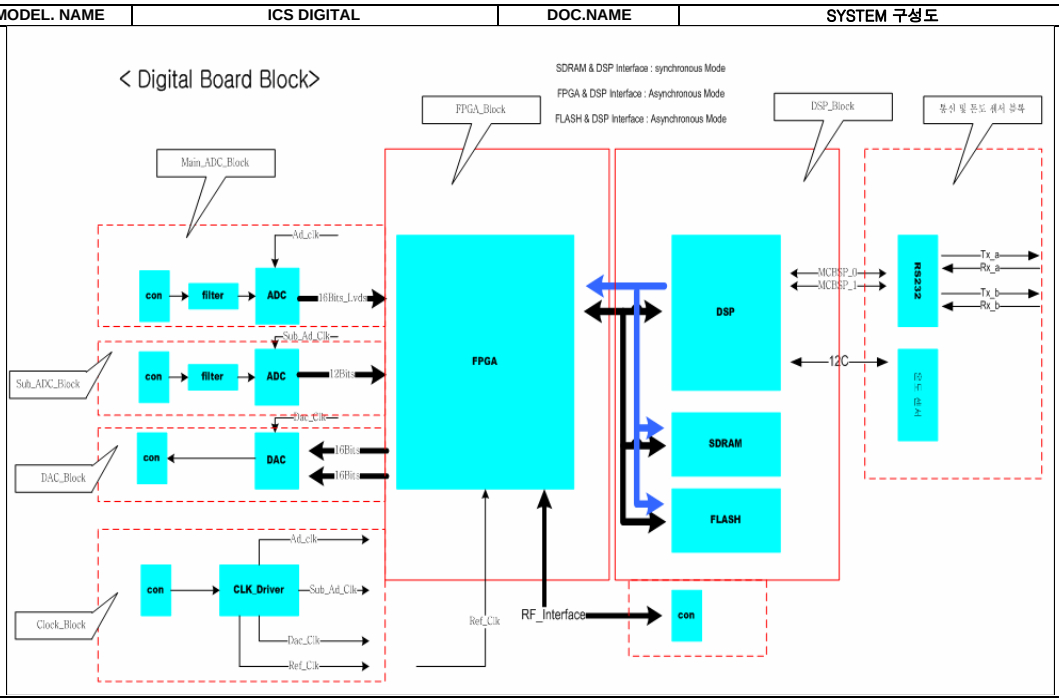
MMIC : RF 입력 신호를 적절히 증폭하고 입력 Impedance Matching
TR1 : LDMOS 사용하였으며 증폭 및 ACPR 특성 유지
TR2 : LDMOS 사용하였으며 TR1과 연계하여 predistortion 에 의한 선형성 확보
Isolator & detector : Amp이후의 VSWR이 나쁠 경우 main TR의 보호 및 power 측정
Control : enable, power detector에 의한 amp 운용, VSWR alarm제어

ICS

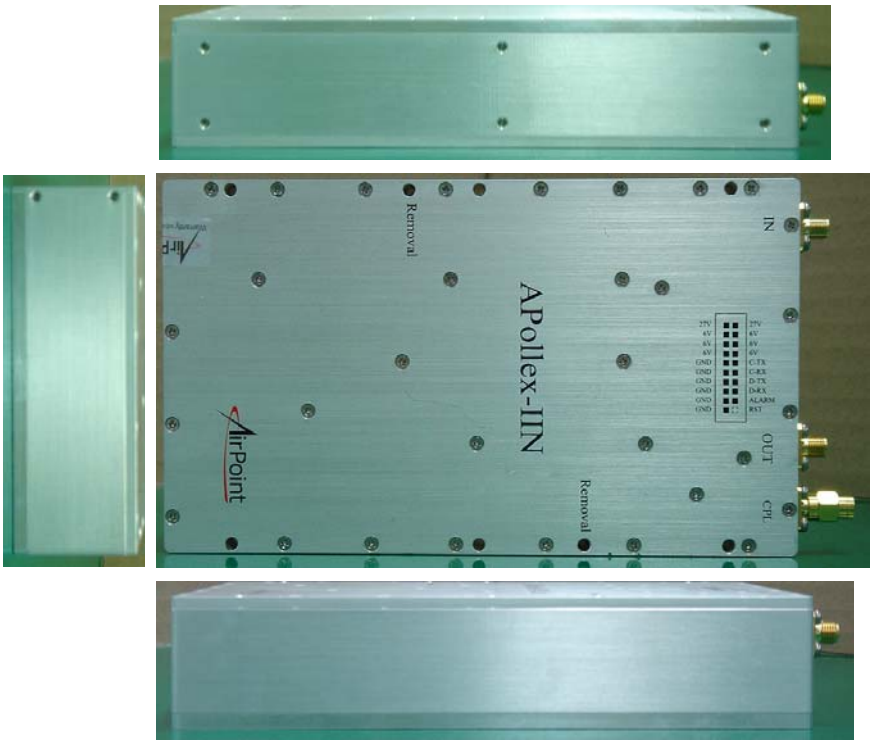
MODEL. NAME	ICS DIGITAL	DOC.NAME	PCB 사진(BOTTOM)	SHEET	15
				DATE	2008.10.09
				CHANGED	



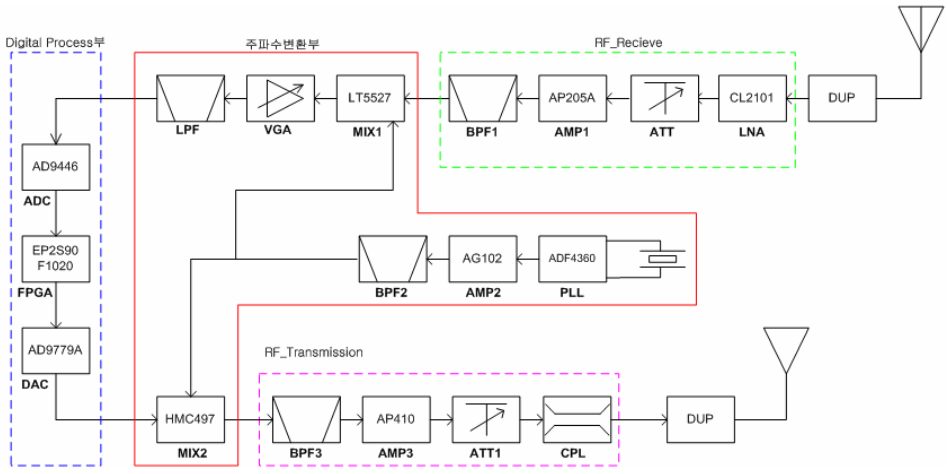
MODEL. NAME	ICS DIGITAL	DOC.NAME	SYSTEM 구성도	SHEET	16
				DATE	2008.10.09
				CHANGED	



MODEL. NAME	Apollex 2N Sprint CDMA FWD	DOC.NAME	하우징 장착 후	SHEET	9
				DATE	2008.10.1
				CHANGED	

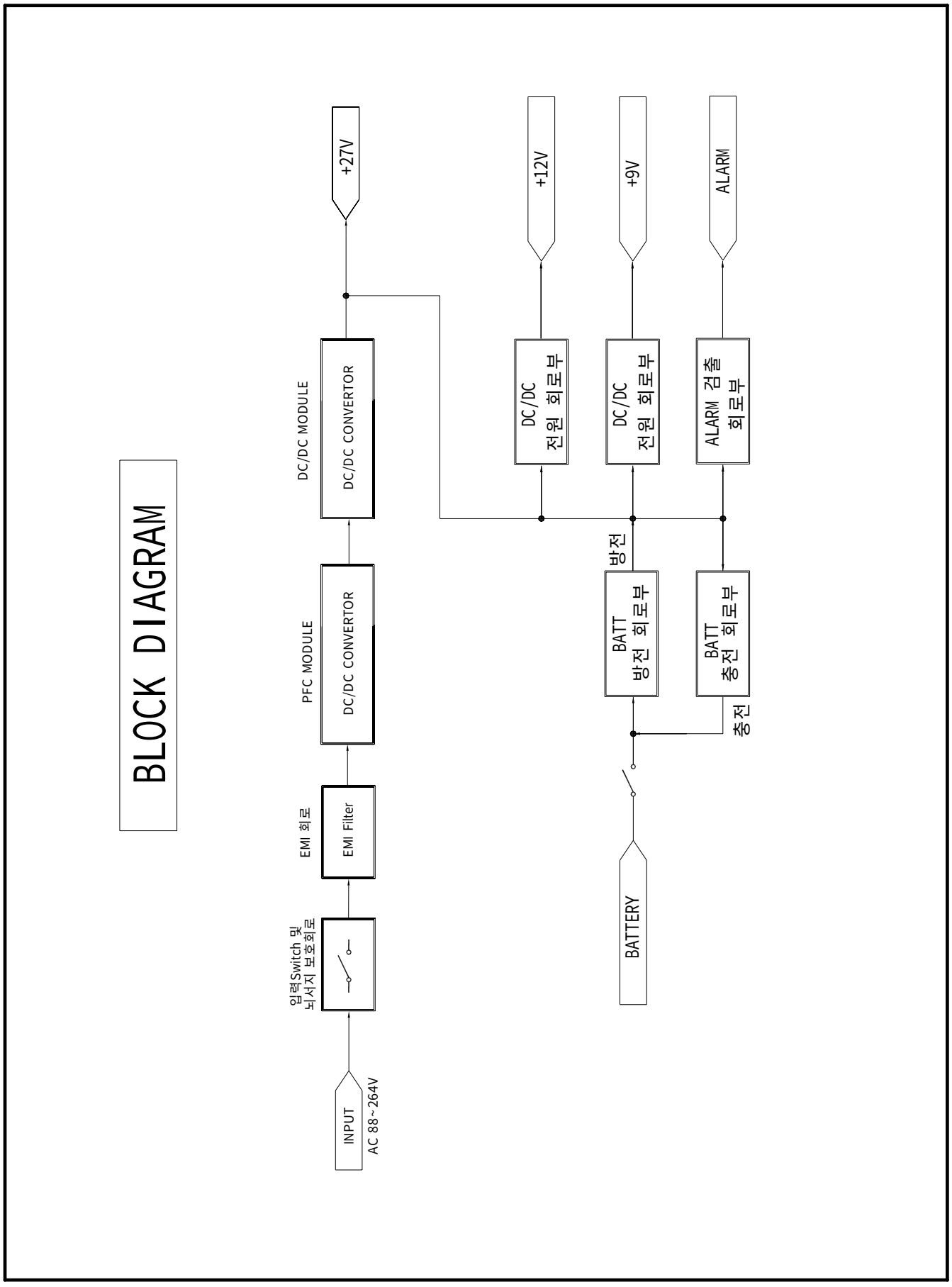


MODEL. NAME		DOC.NAME	블록도	SHEET	10
				DATE	2008.10.1
				CHANGED	



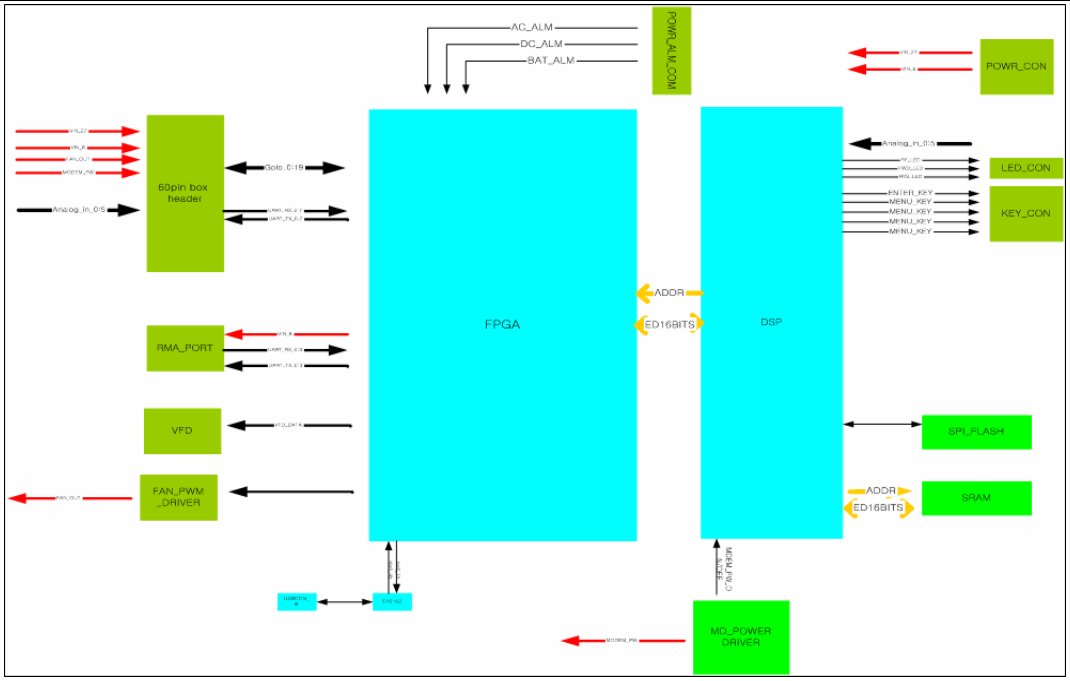
PSU

BLOCK DIAGRAM



RCU

MODEL. NAME	RCU	DOC.NAME	시스템 구성도	SHEET	19
				CHANGED	



SNMP

MODEL. NAME	SYSTEM 구성도	DOC.NAME	Part list	SHEET	1
				DATE	2008.10.09
				CHANGED