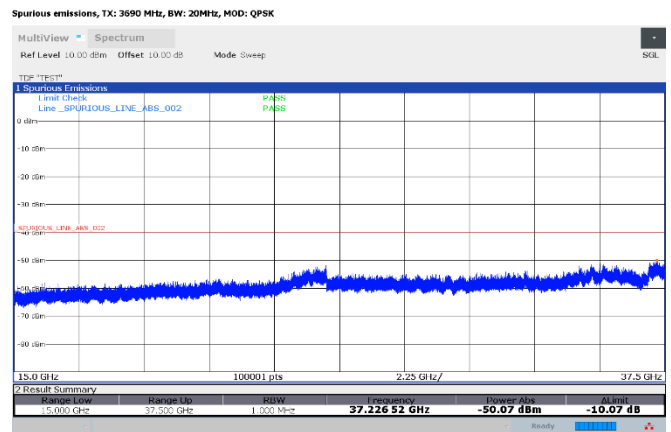
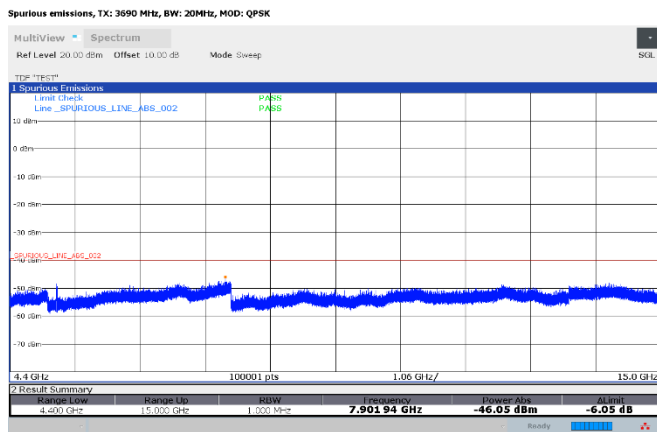
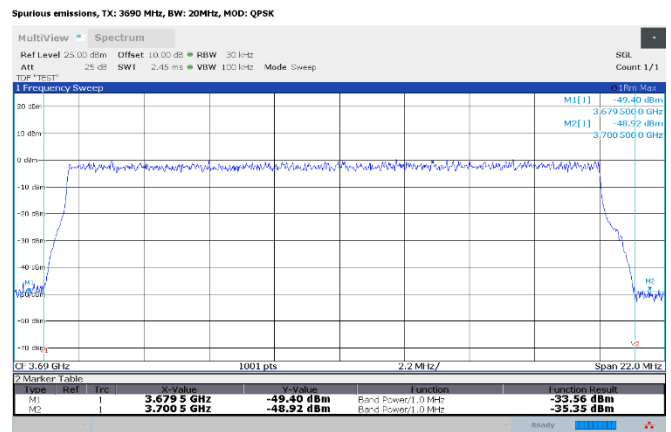
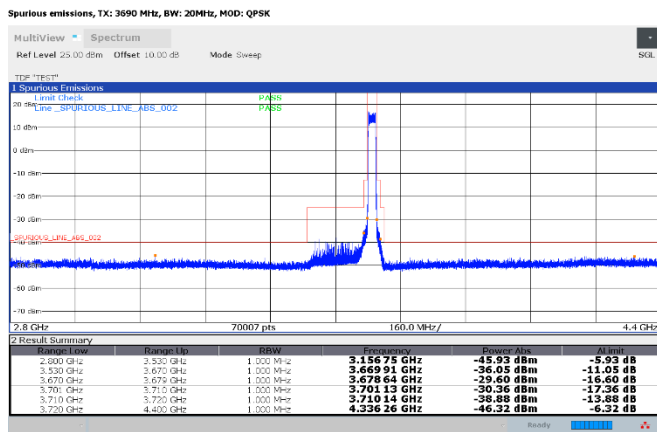
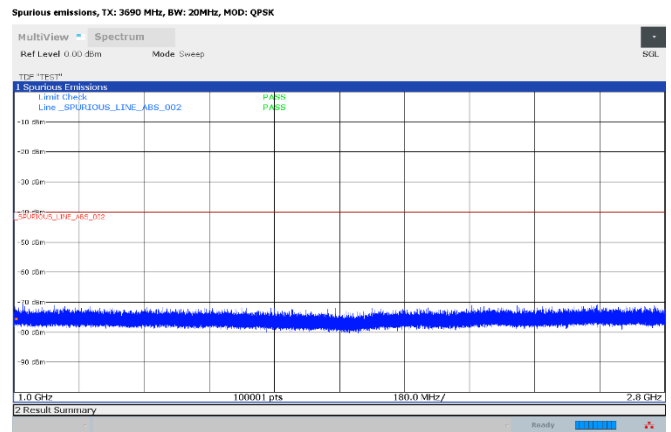
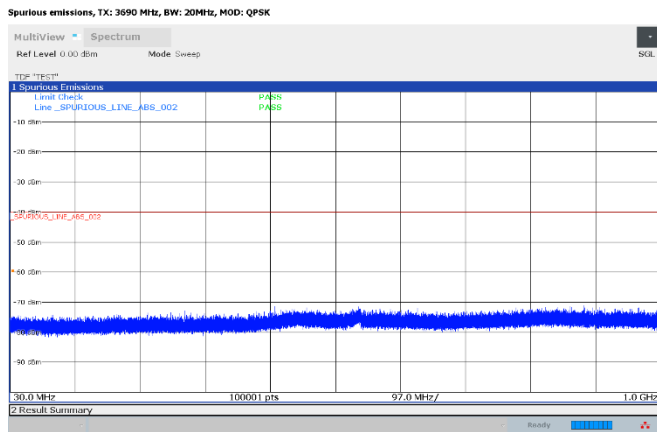
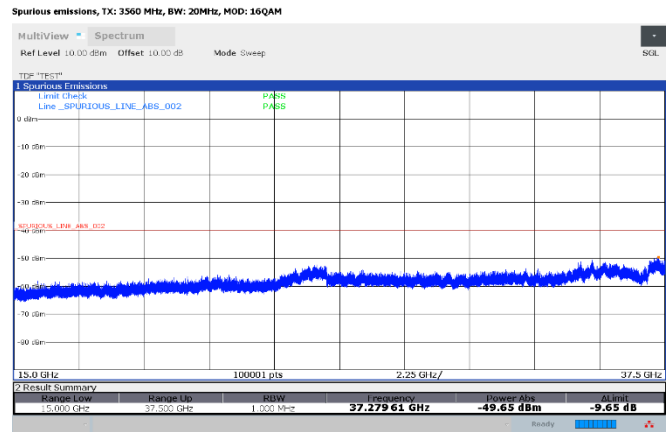
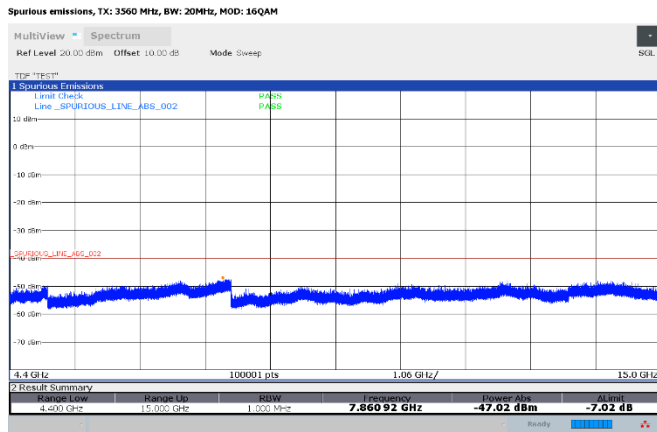
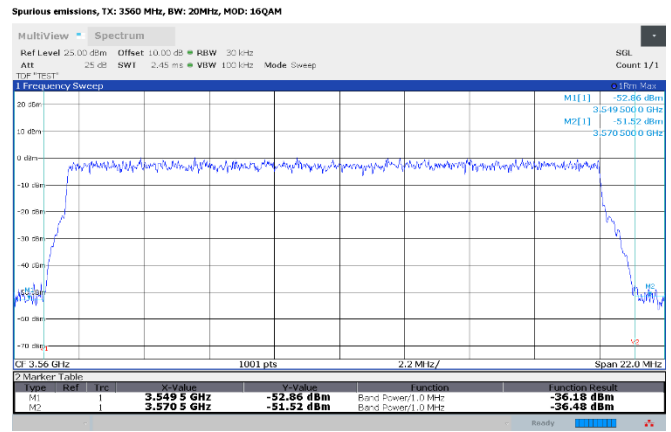
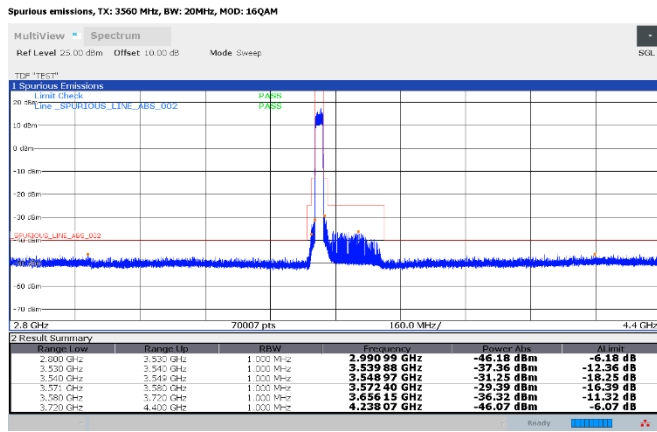
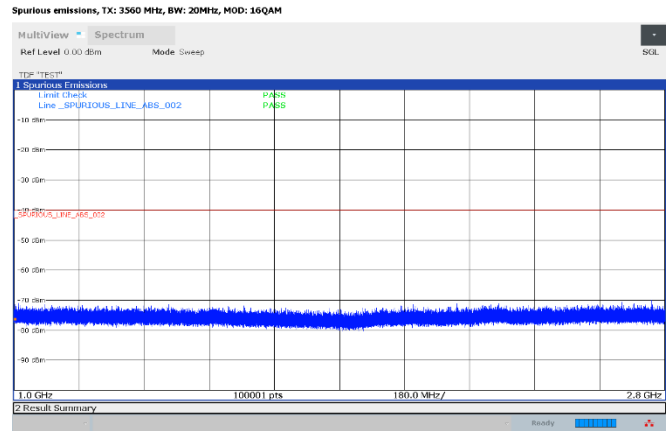
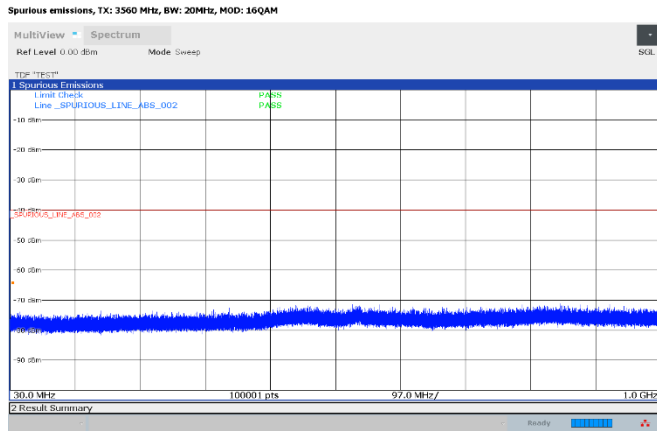


**TX: 3690 MHz, 20 MHz BW, QPSK modulation:**

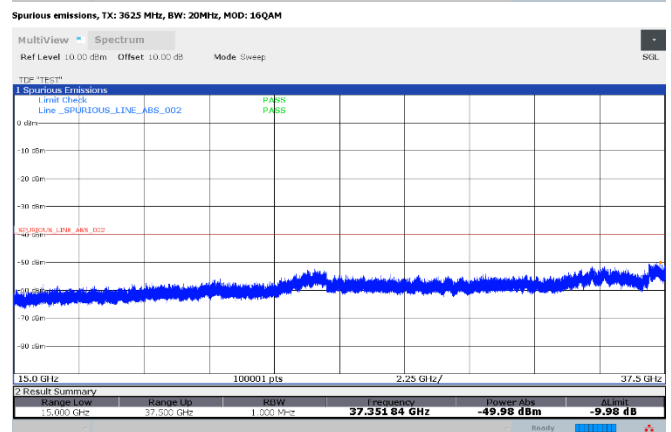
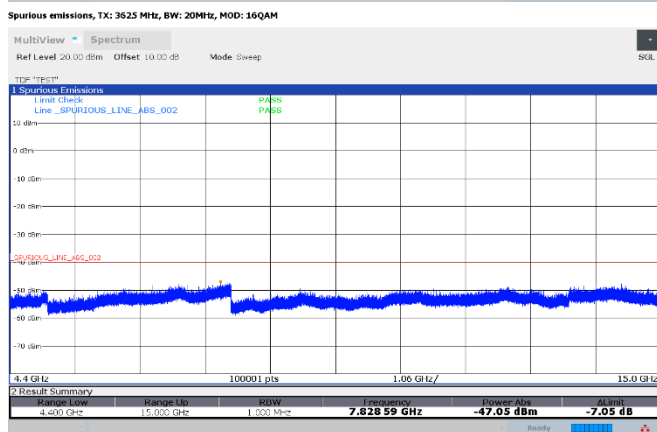
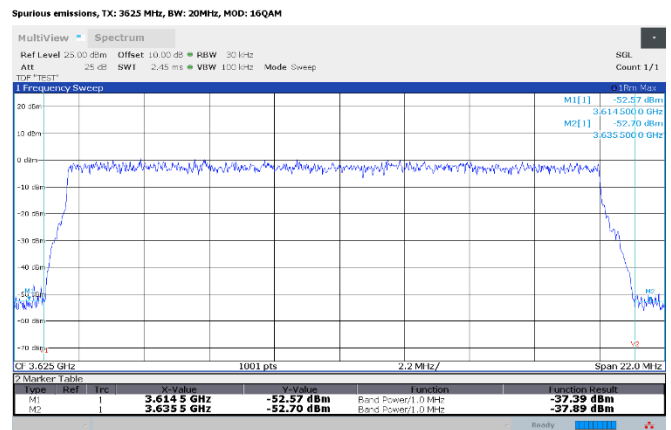
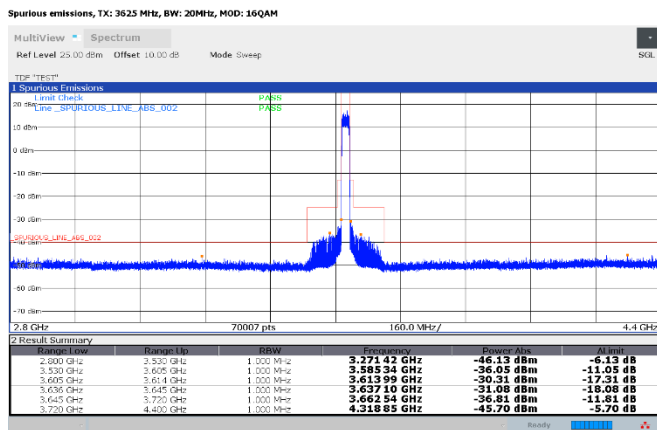
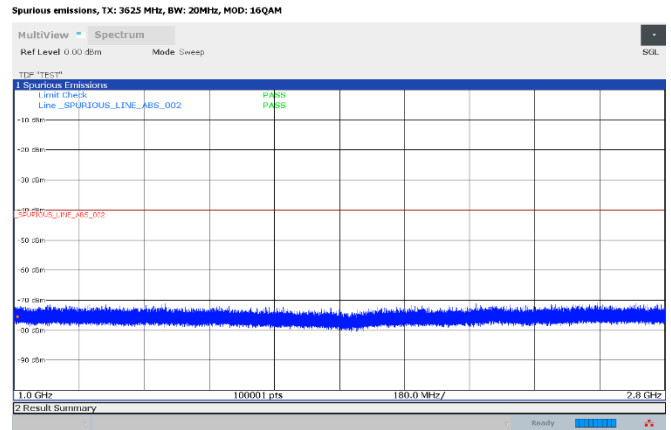
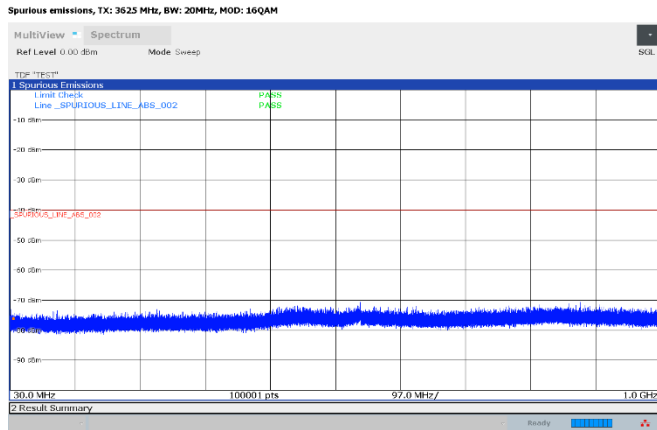




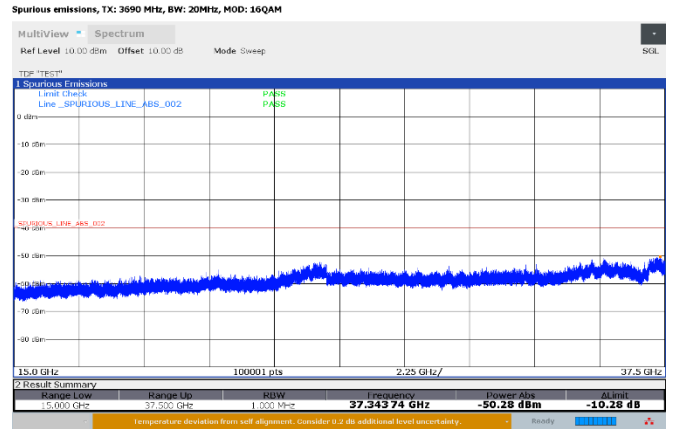
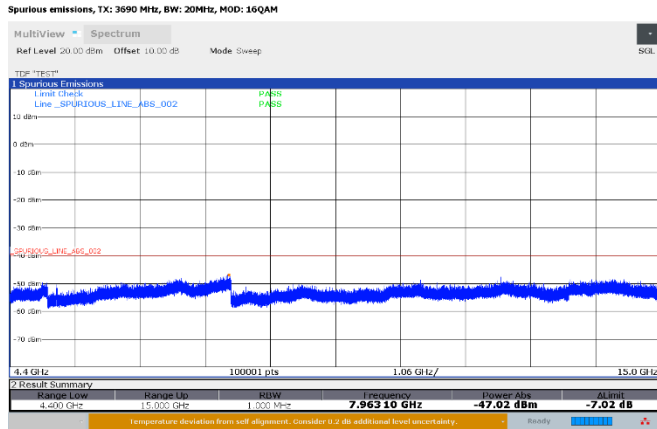
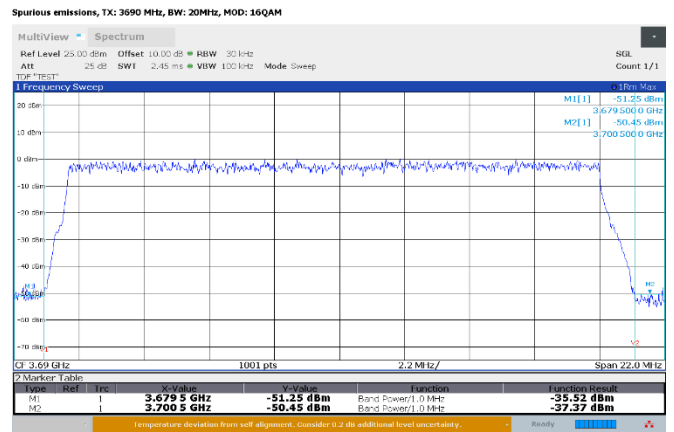
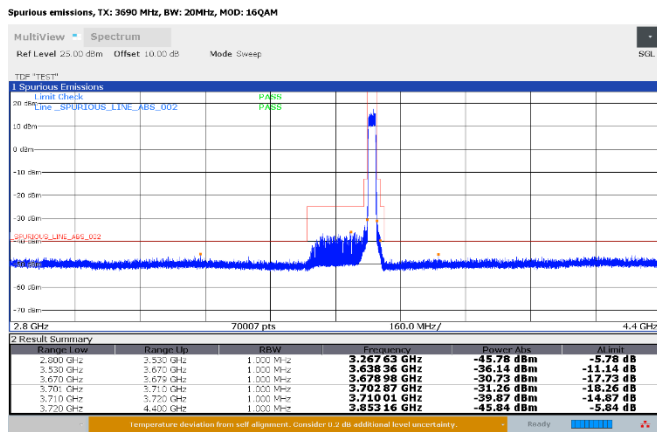
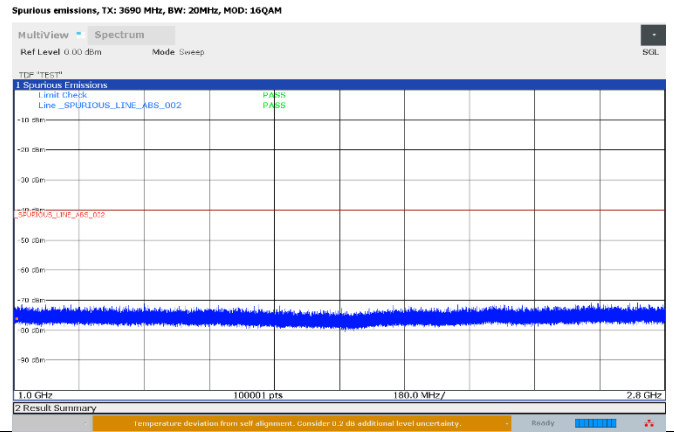
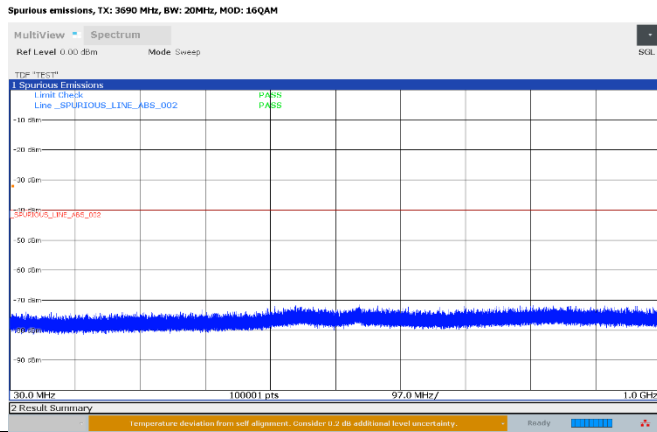
TX: 3560 MHz, 20 MHz BW, 16QAM modulation:



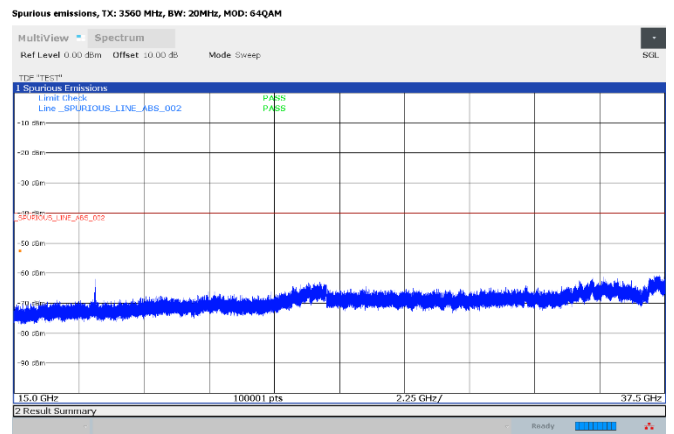
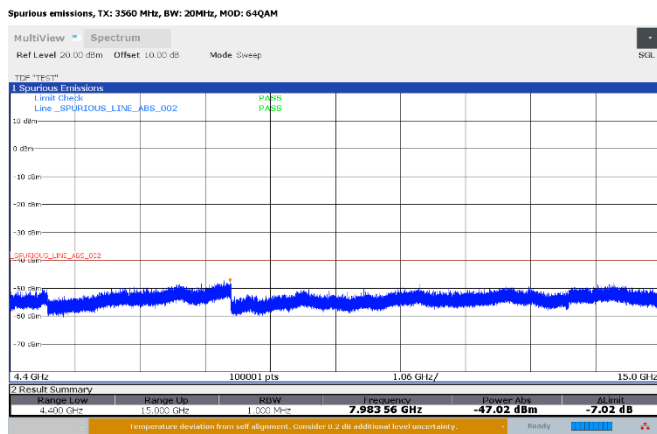
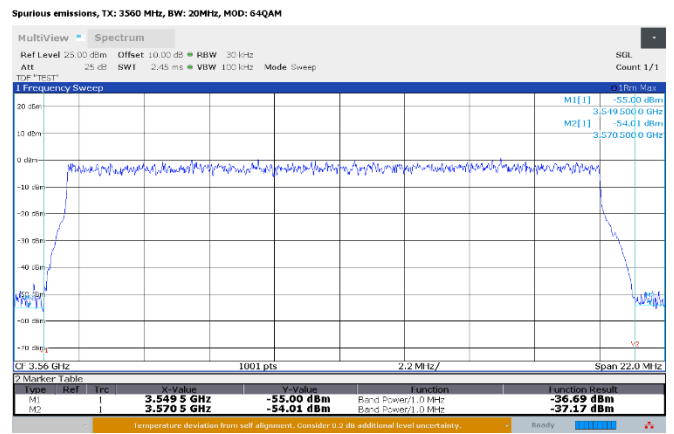
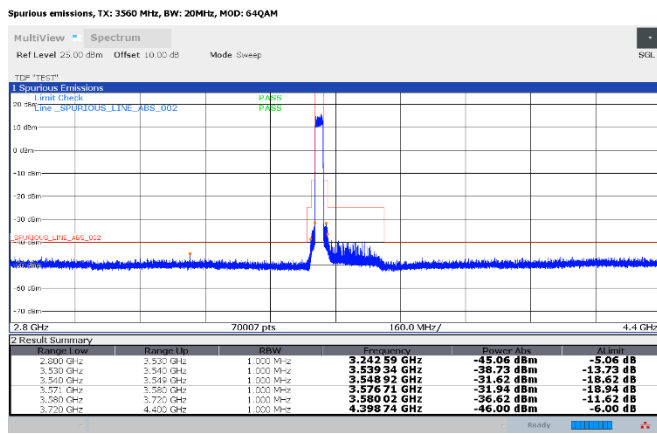
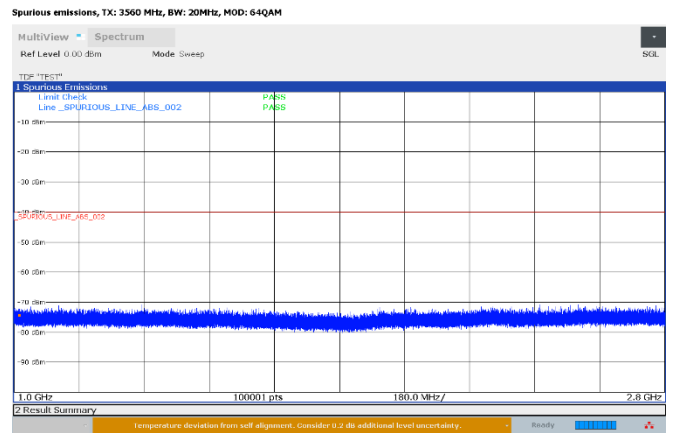
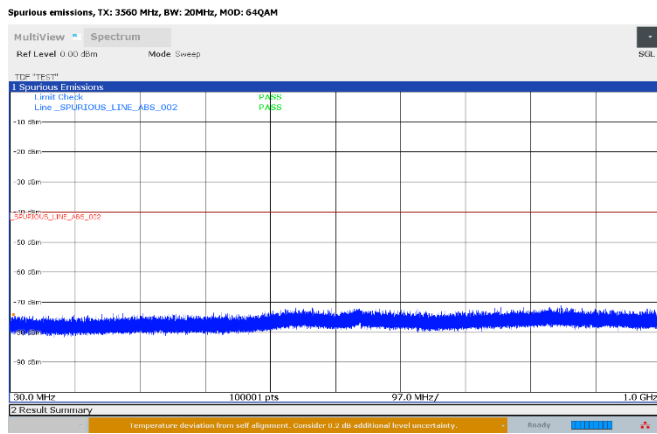
**TX: 3625 MHz, 20 MHz BW, 16QAM modulation:**



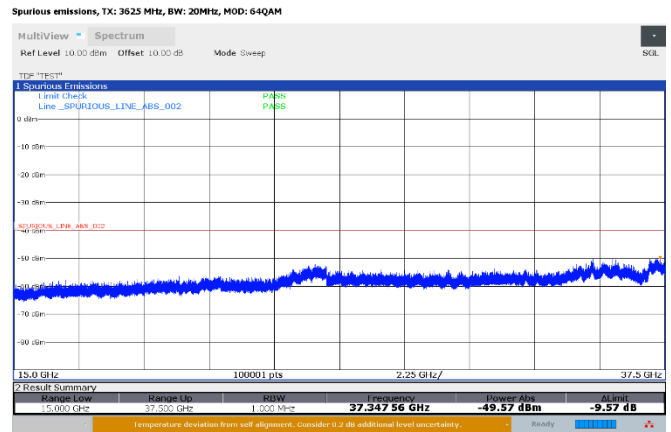
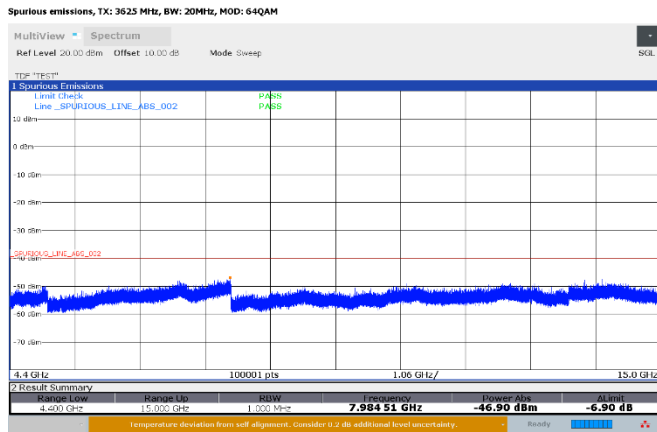
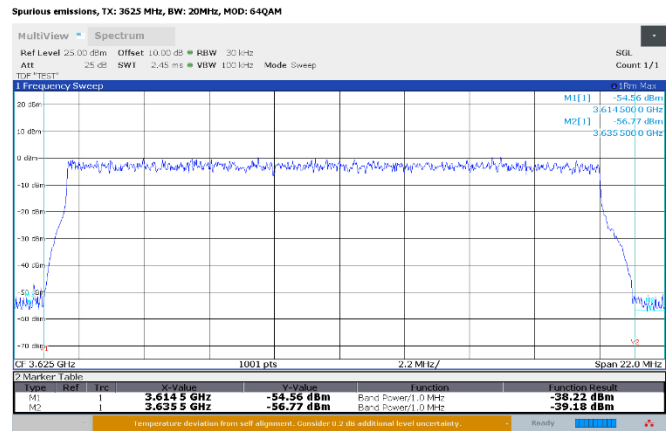
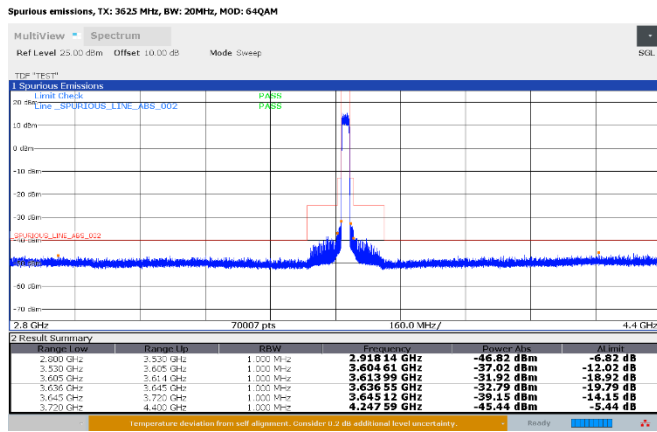
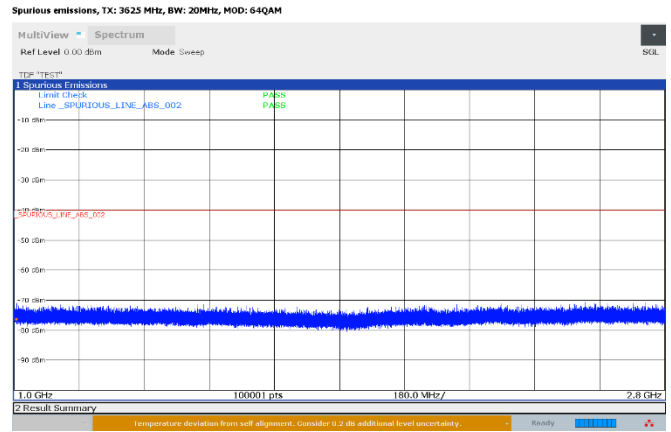
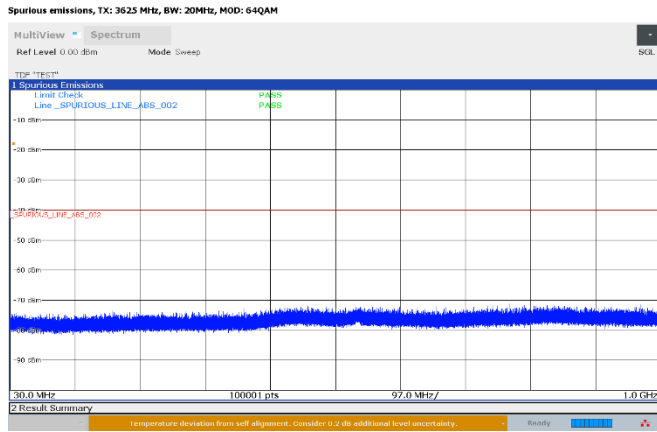
**TX: 3690 MHz, 20 MHz BW, 16QAM modulation:**



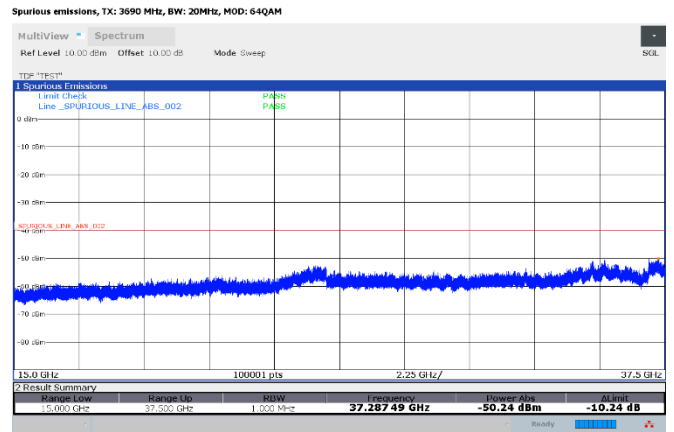
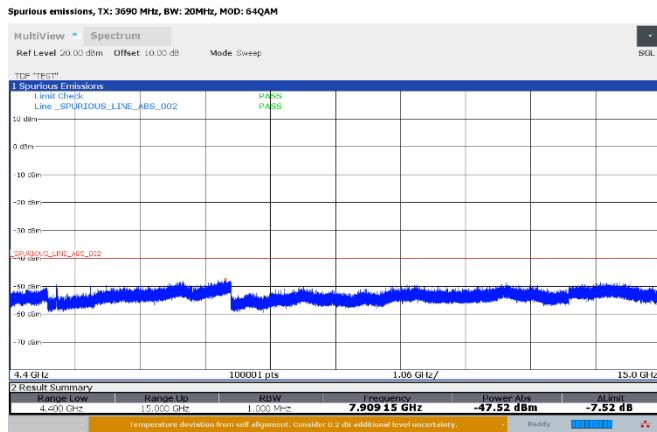
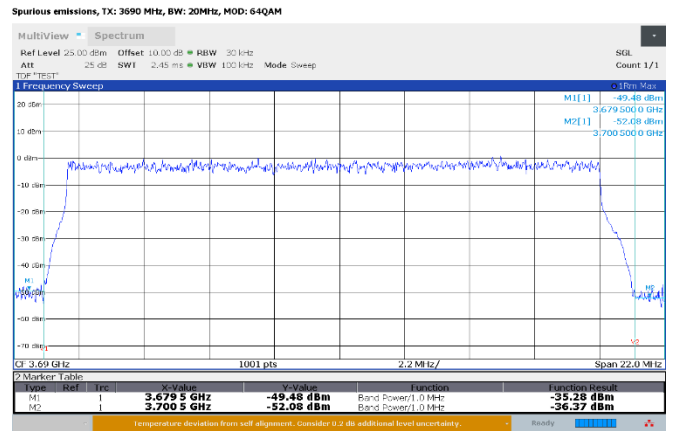
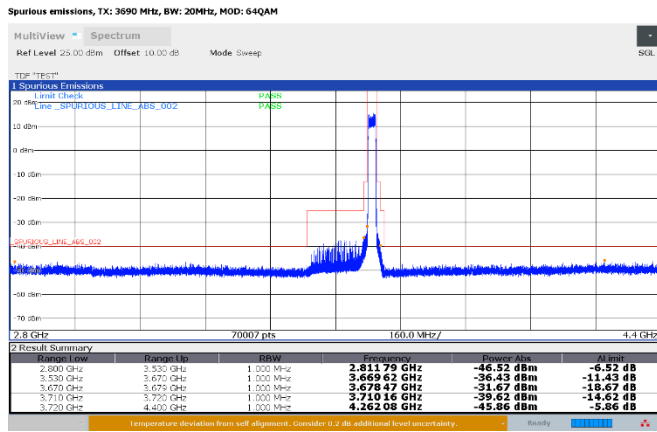
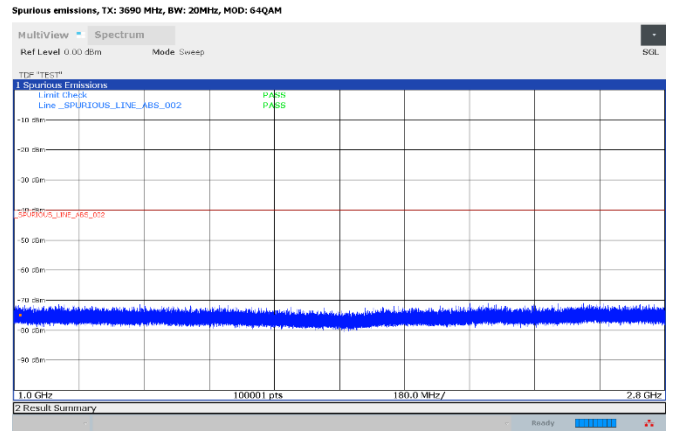
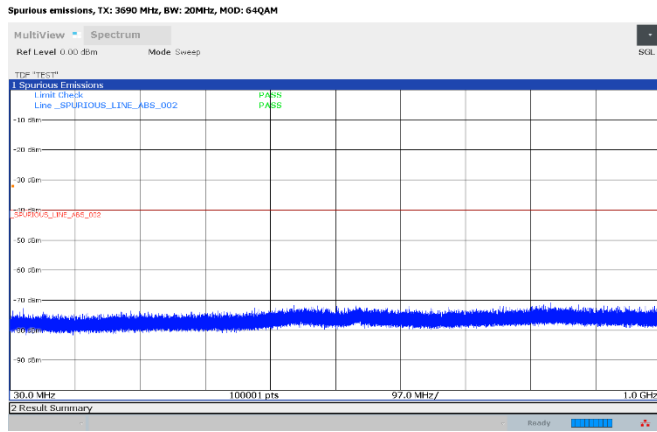
**TX: 3560 MHz, 20 MHz BW, 64QAM modulation:**



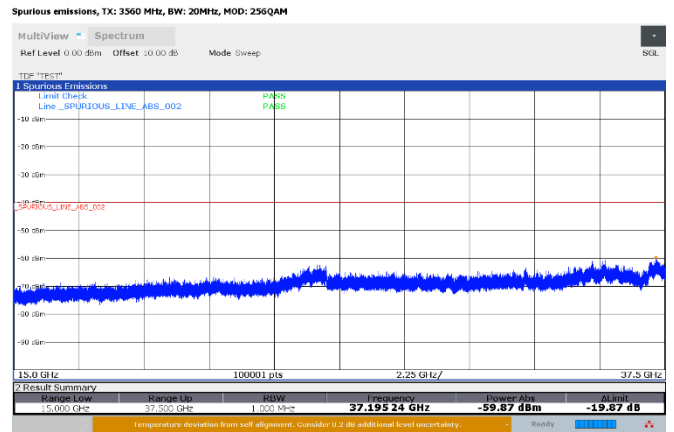
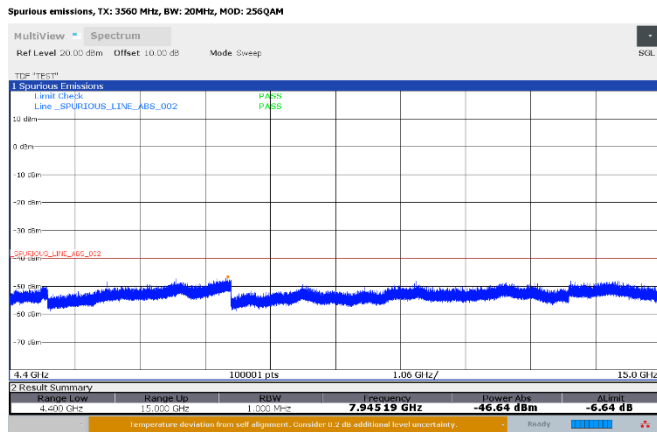
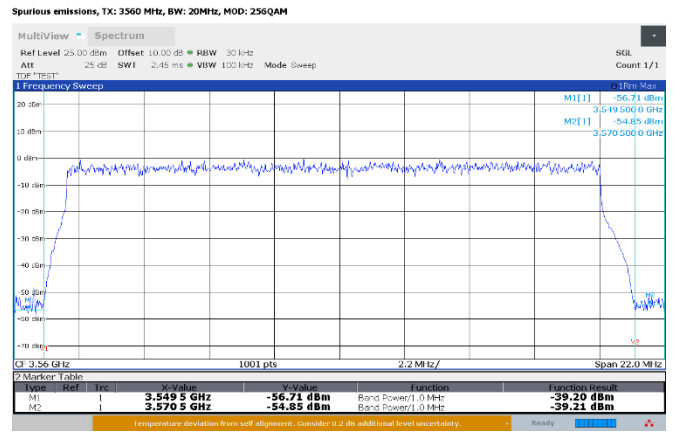
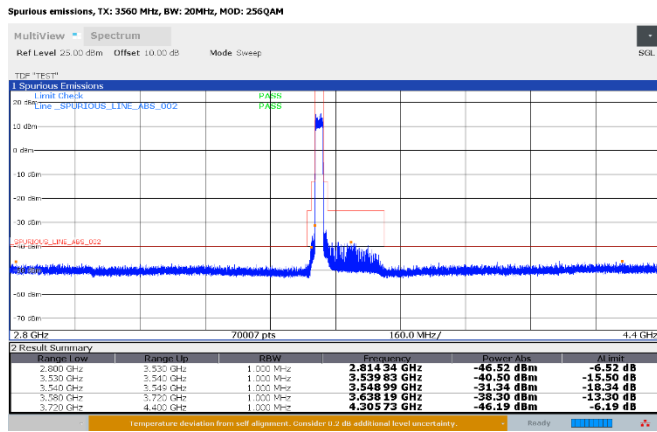
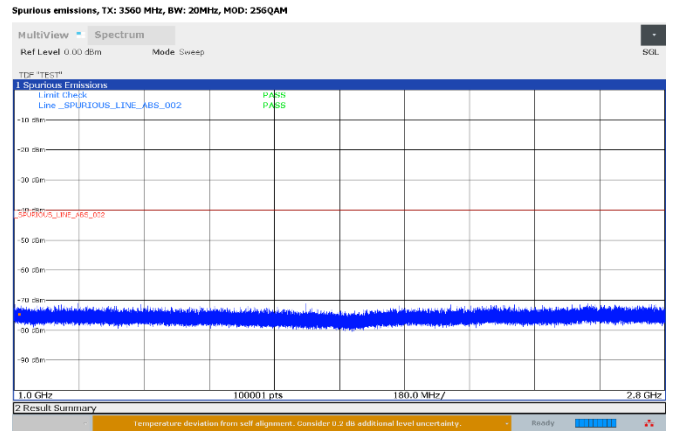
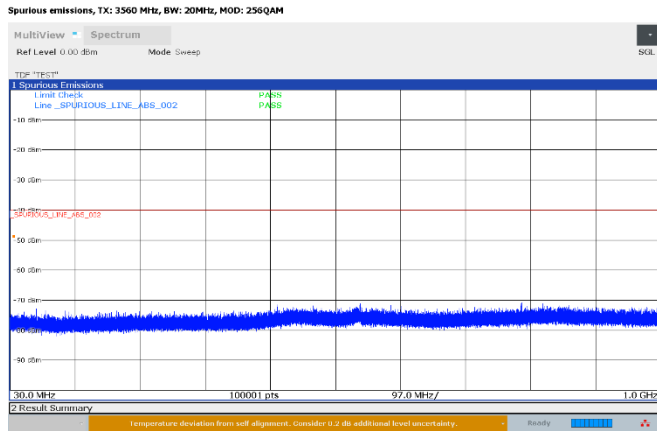
**TX: 3625 MHz, 20 MHz BW, 64QAM modulation:**



TX: 3690 MHz, 20 MHz BW, 64QAM modulation:

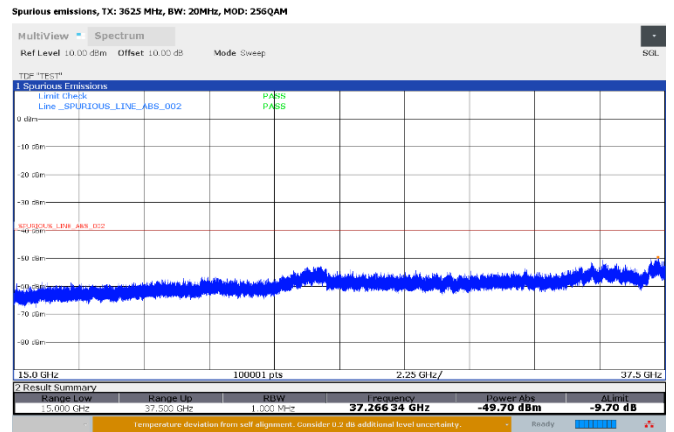
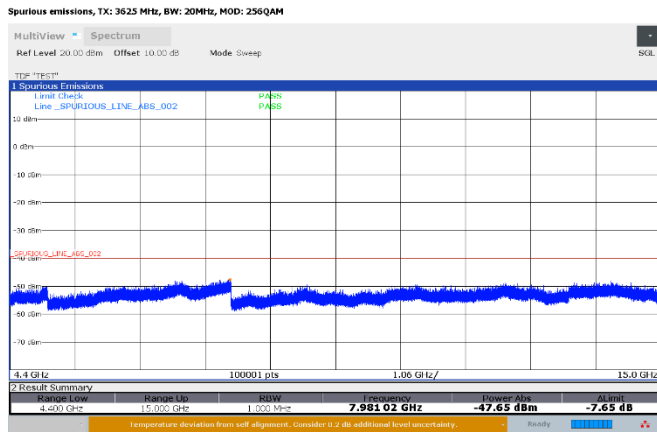
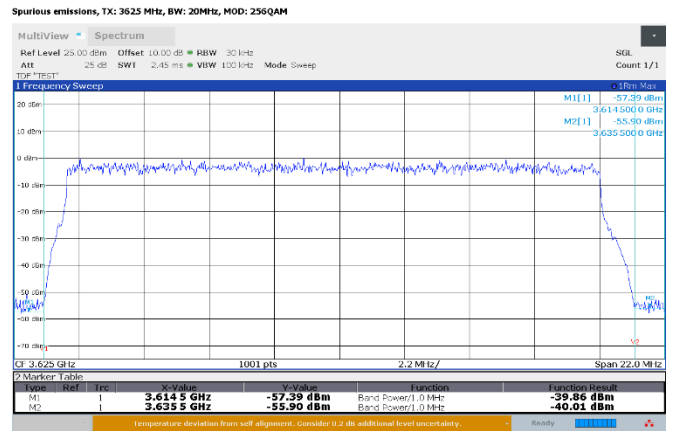
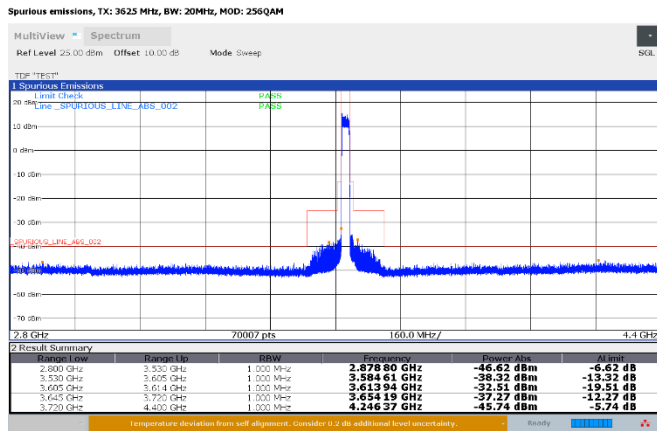
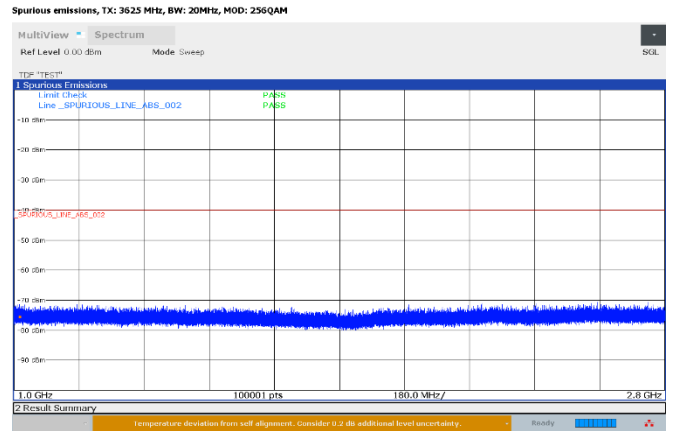
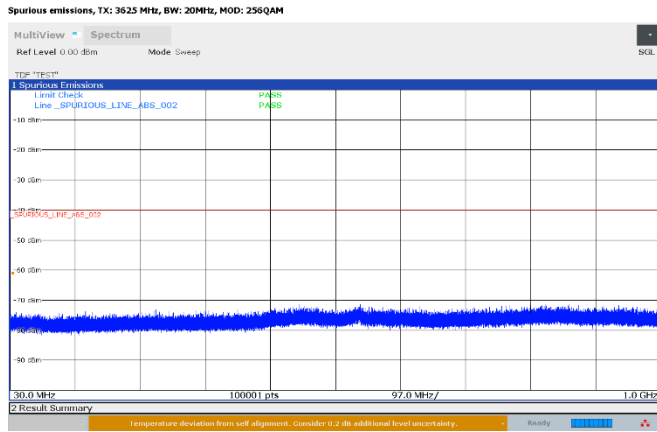


TX: 3560 MHz, 20 MHz BW, 256QAM modulation:

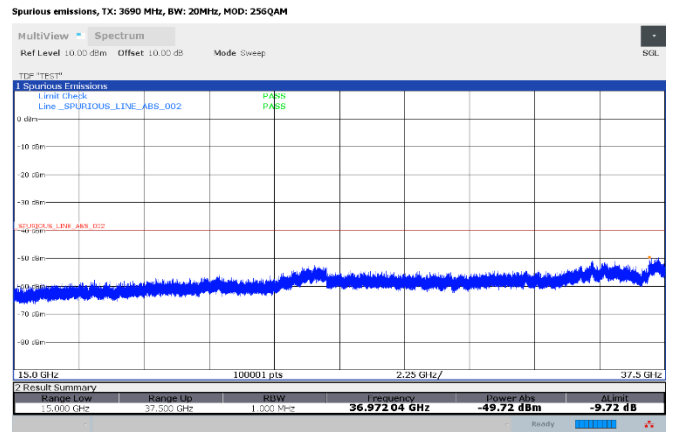
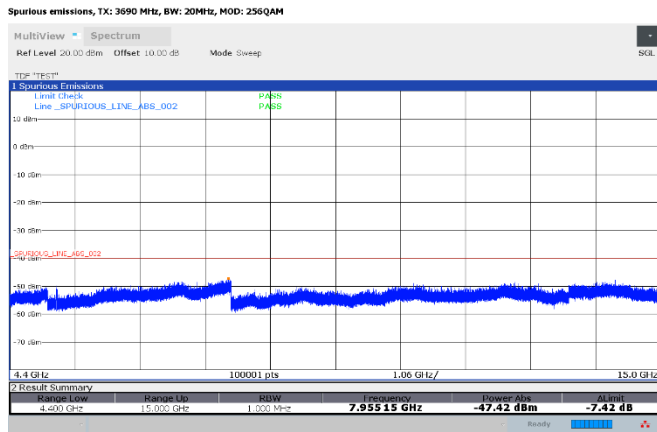
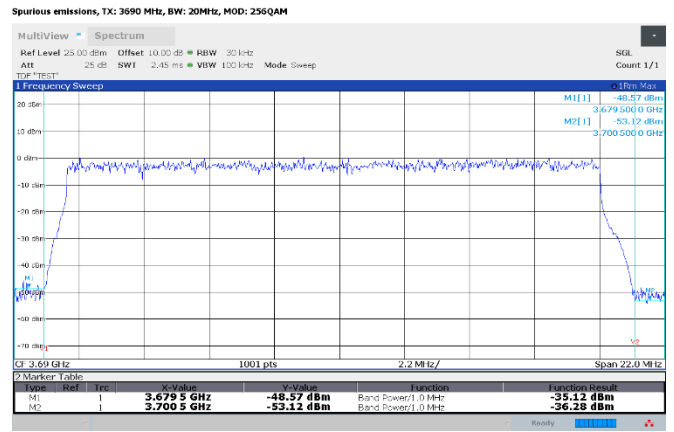
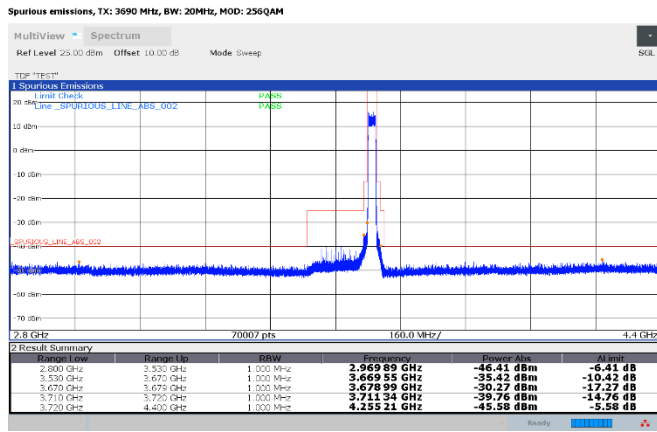
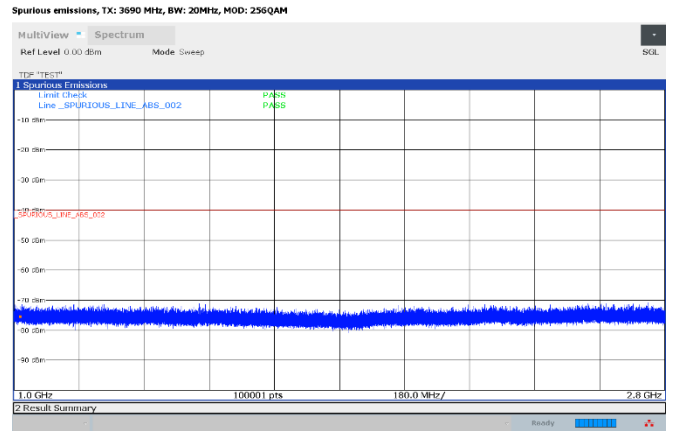
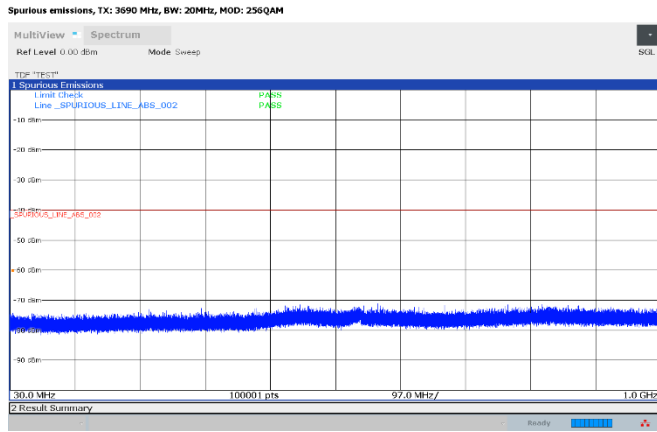




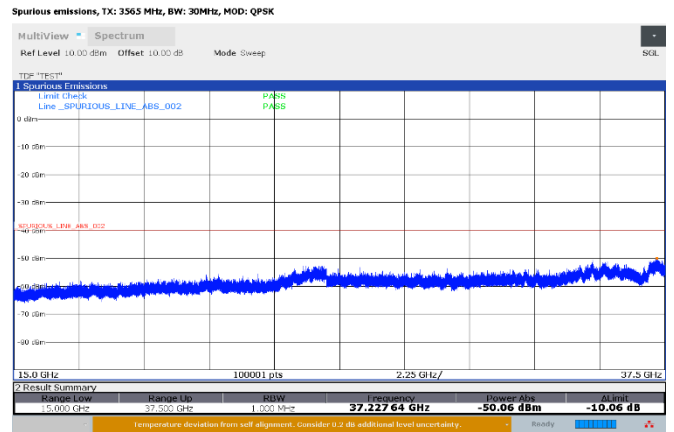
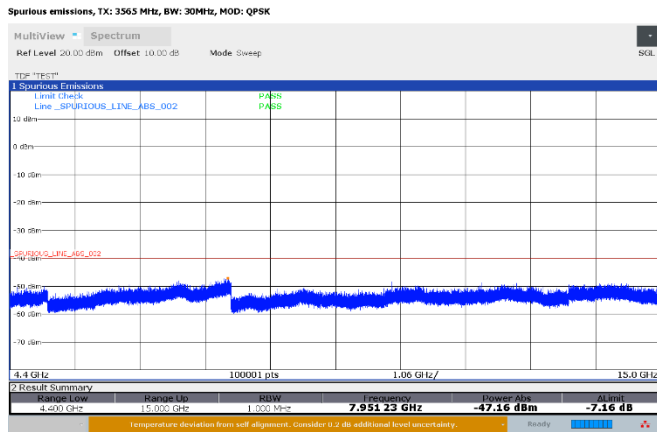
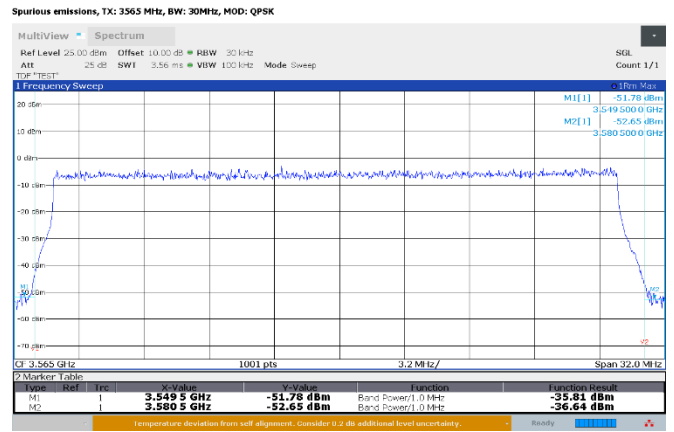
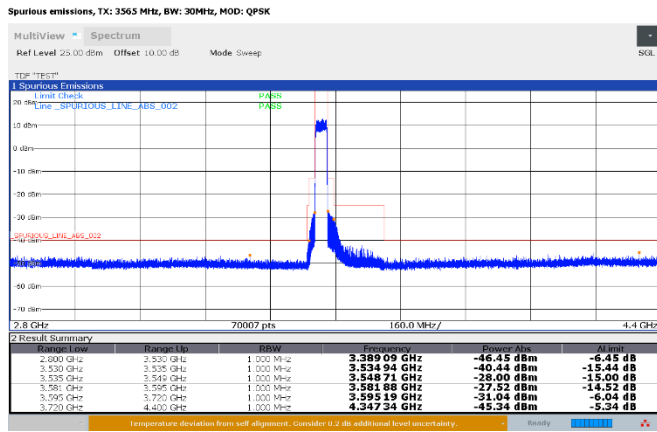
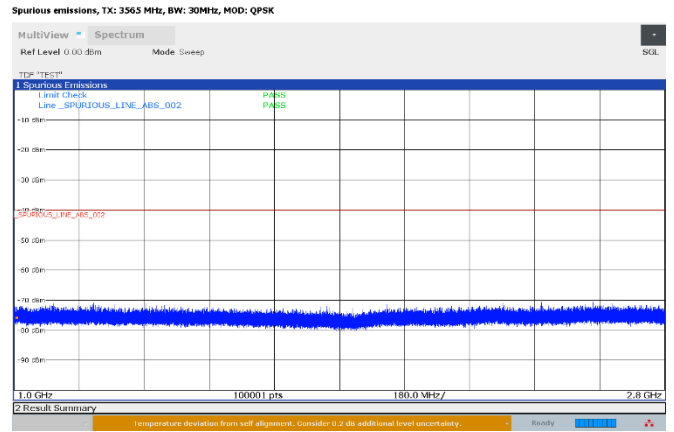
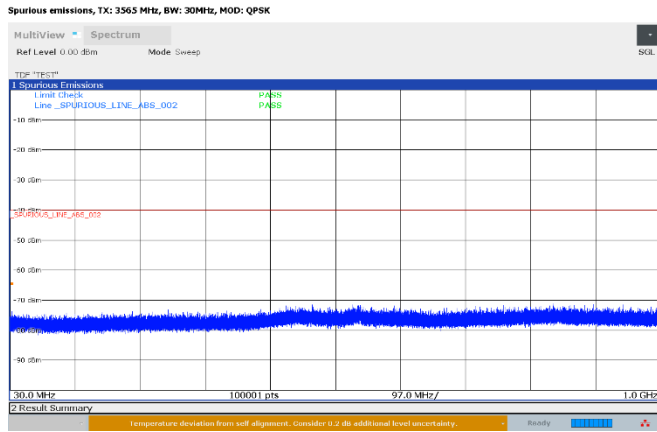
**TX: 3625 MHz, 20 MHz BW, 256QAM modulation:**



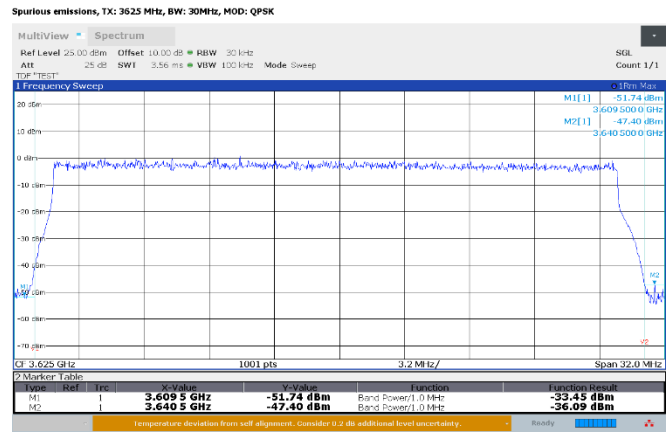
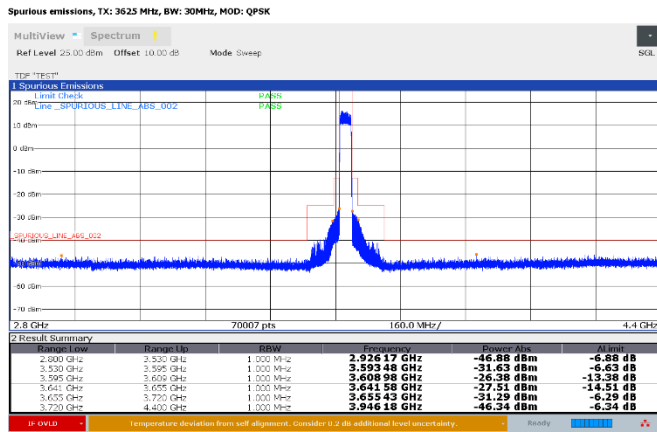
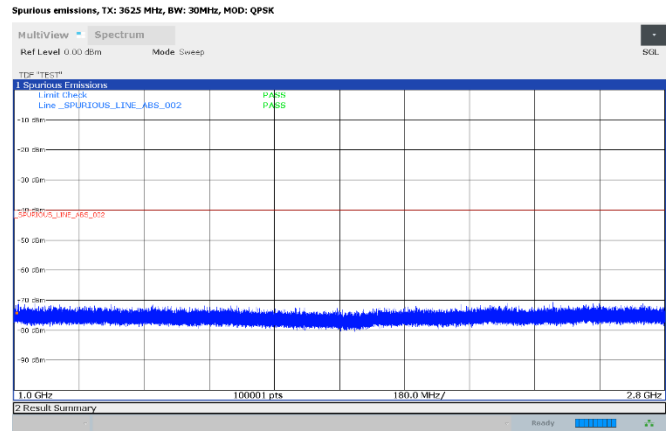
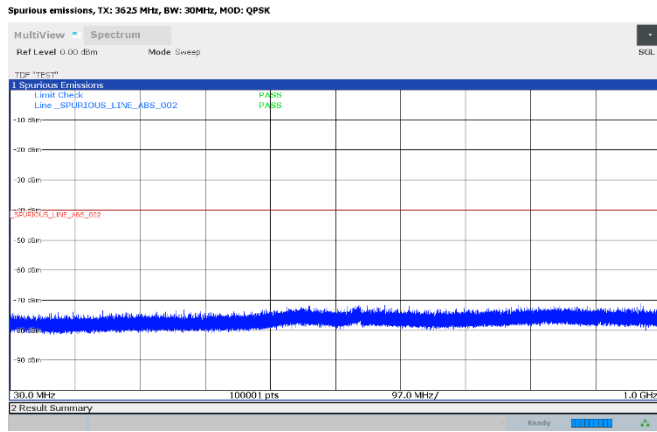
**TX: 3690 MHz, 20 MHz BW, 256QAM modulation:**



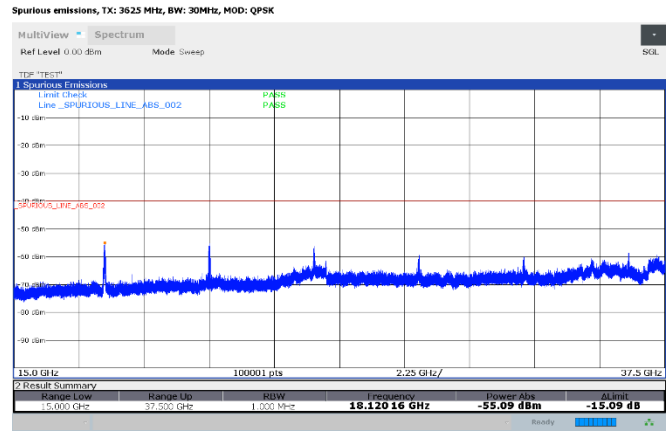
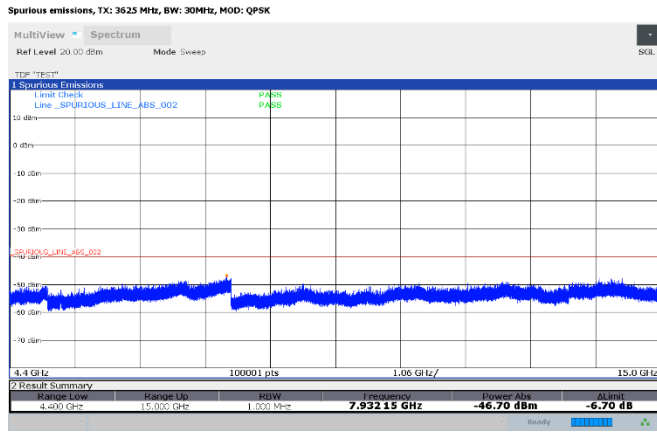
**TX: 3565 MHz, 30 MHz BW, QPSK modulation:**



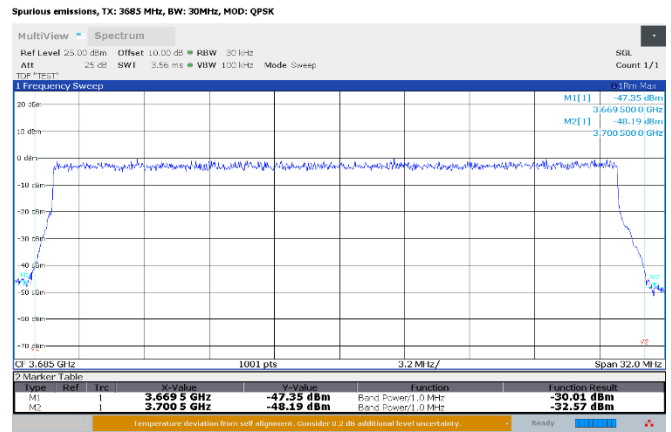
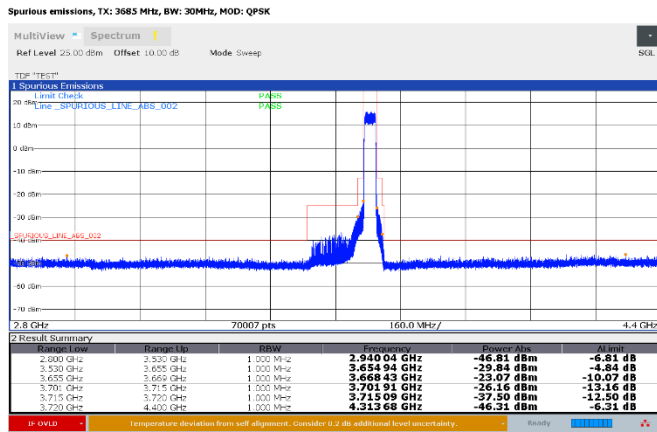
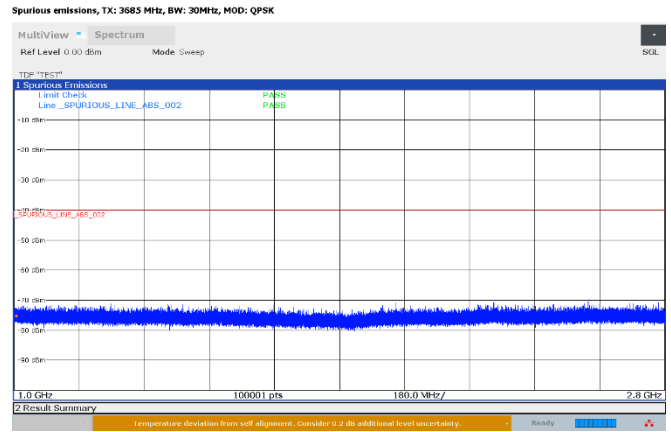
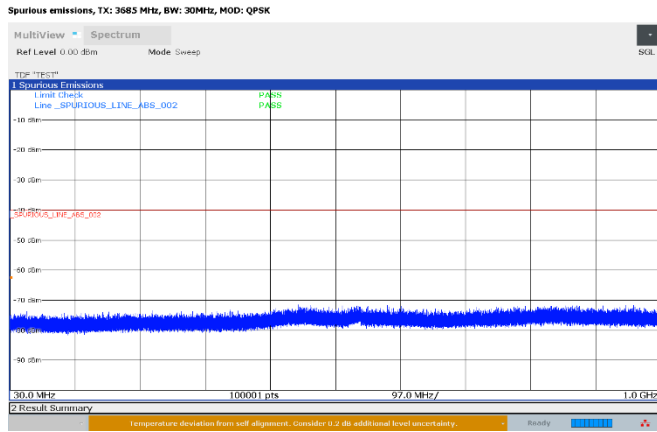
**TX: 3625 MHz, 30 MHz BW, QPSK modulation:**



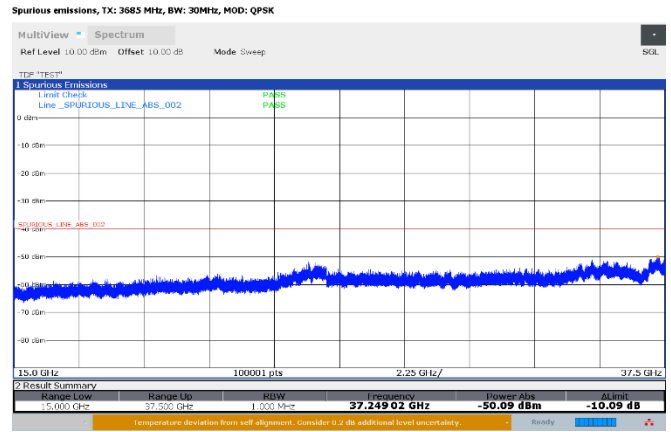
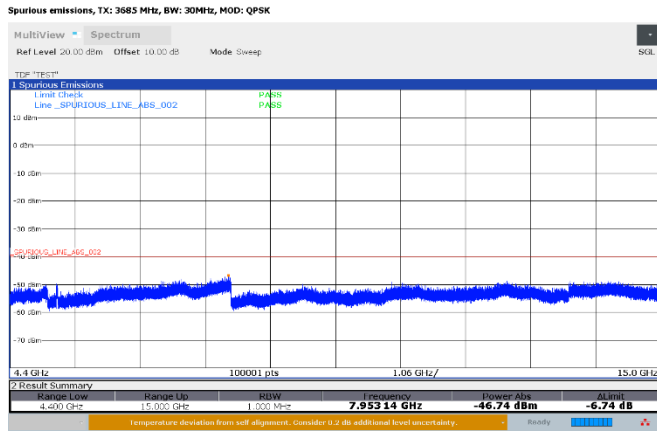
Note: Analyzer was marginally driven to IF overload due to the fundamental. Result kept so as to keep sufficient headroom between noise floor and limit.



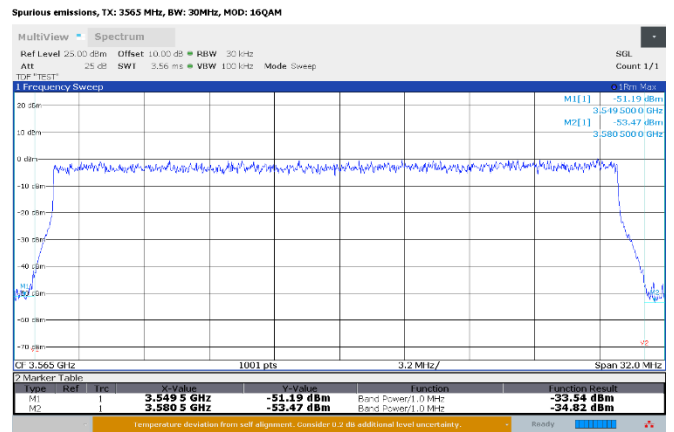
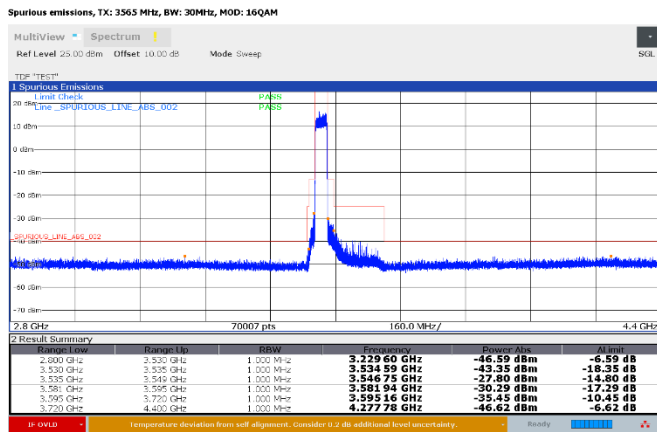
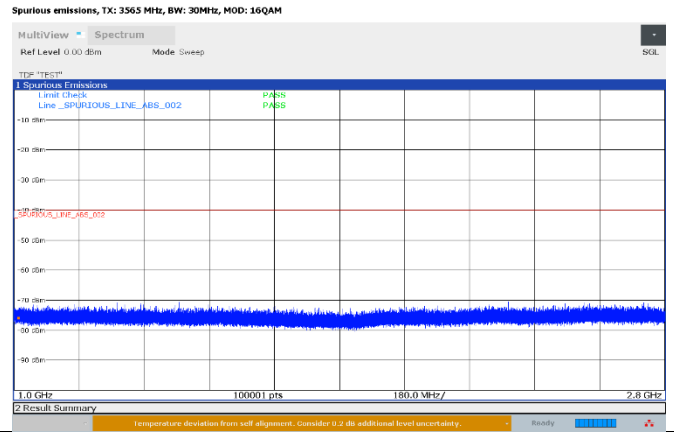
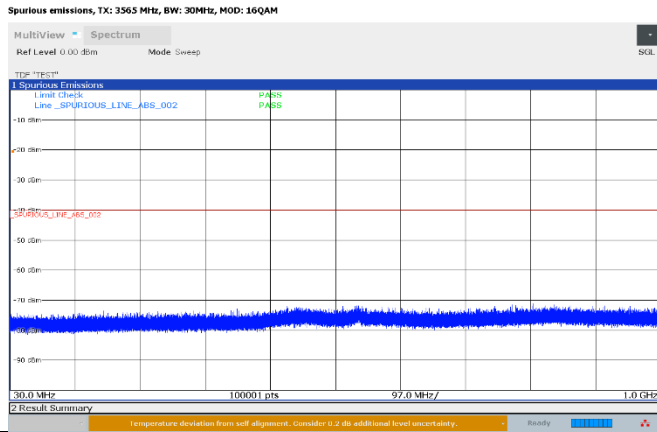
**TX: 3685 MHz, 30 MHz BW, QPSK modulation:**



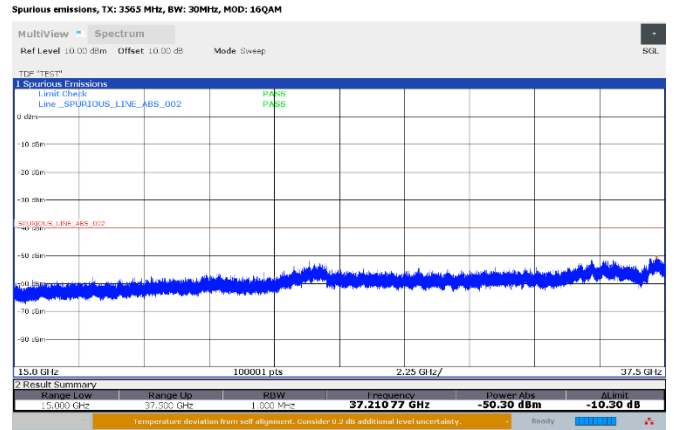
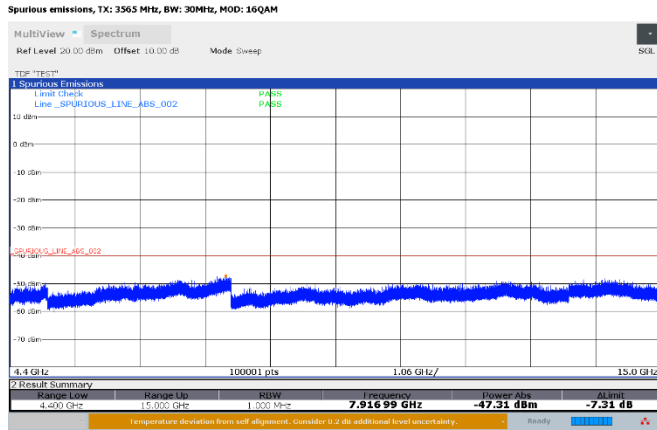
Note: Analyzer was marginally driven to IF overload due to the fundamental. Result kept so as to keep sufficient headroom between noise floor and limit.



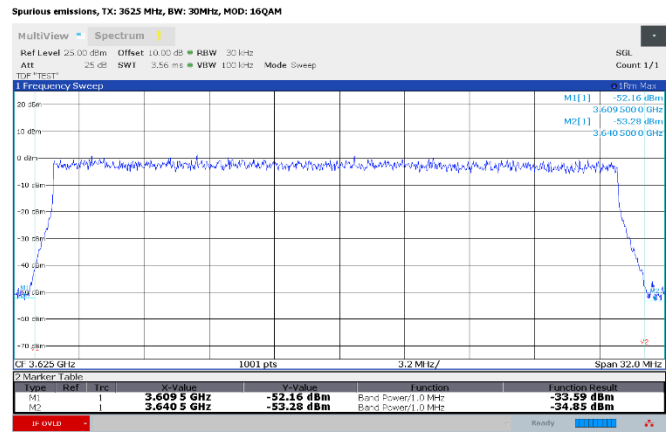
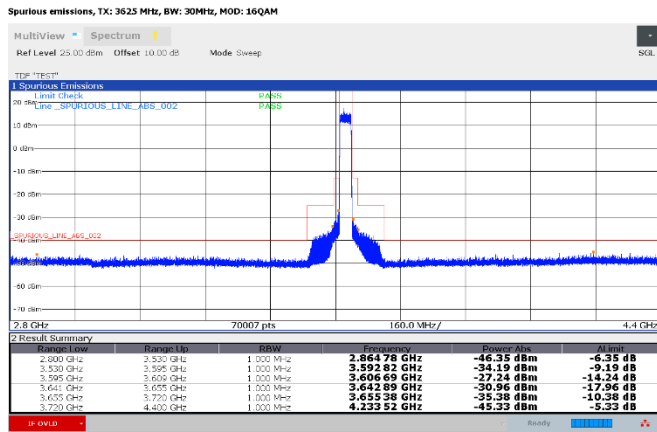
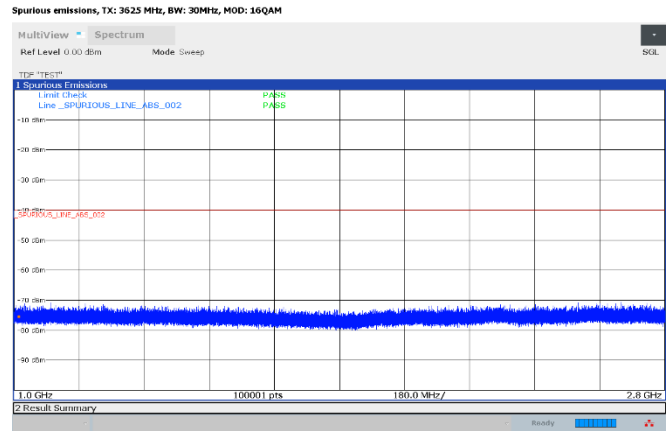
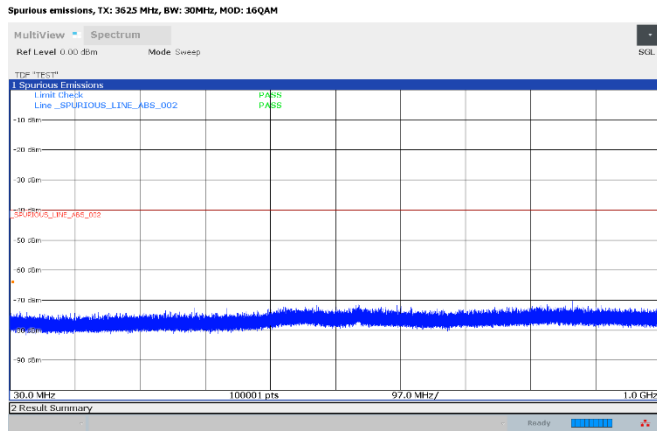
**TX: 3565 MHz, 30 MHz BW, 16QAM modulation:**



Note: Analyzer was marginally driven to IF overload due to the fundamental. Result kept so as to keep sufficient headroom between noise floor and limit.

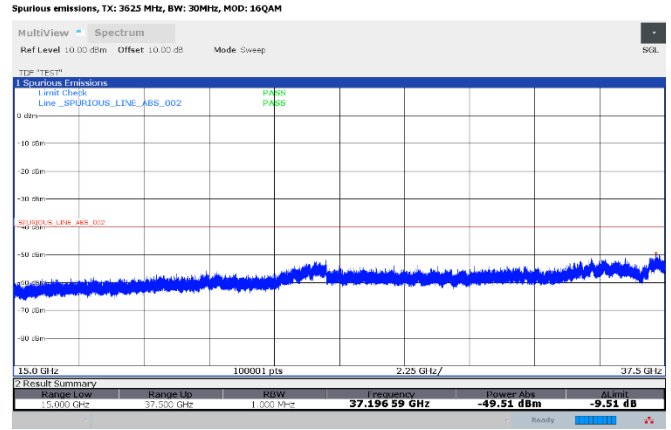
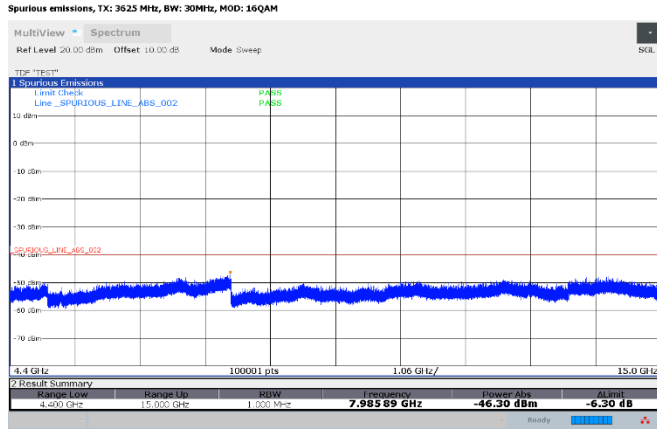


**TX: 3625 MHz, 30 MHz BW, 16QAM modulation:**

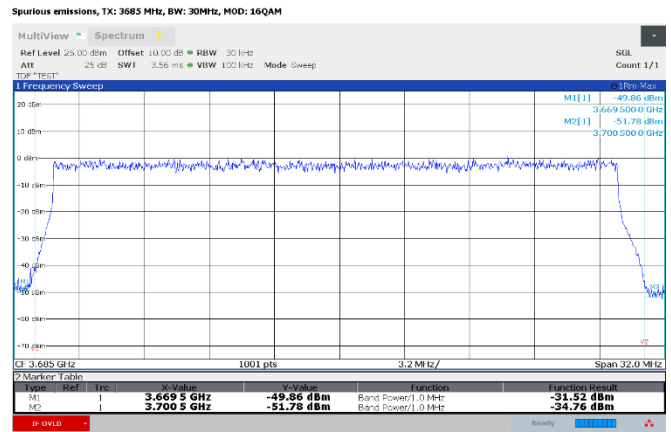
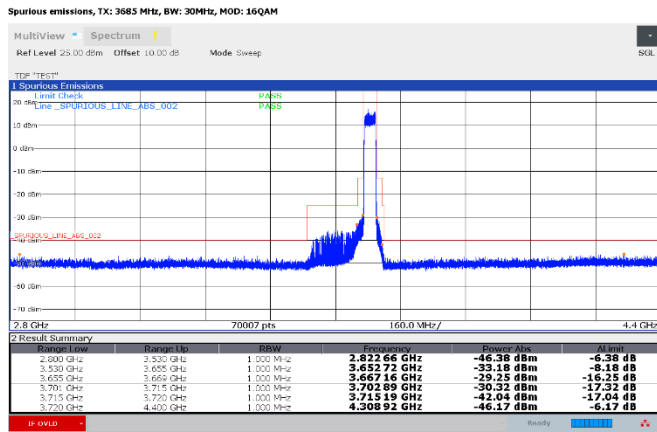
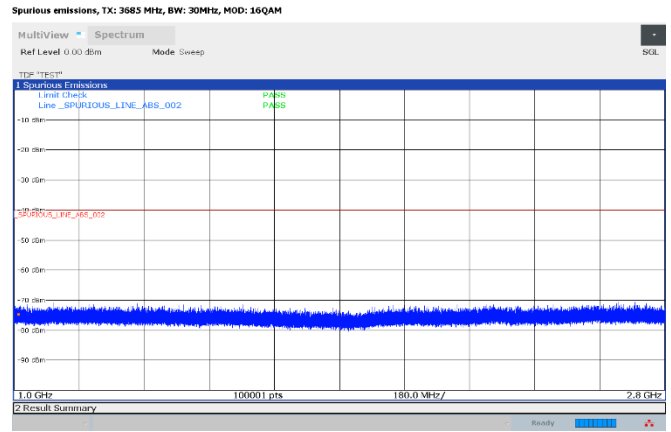
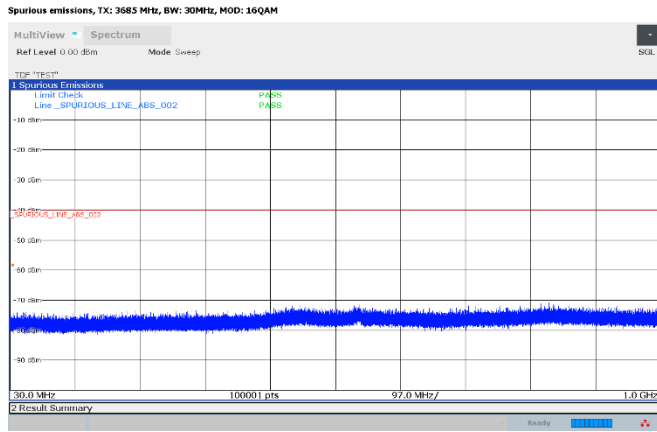


Note: Analyzer was marginally driven to IF overload due to the fundamental. Result kept so as to keep sufficient headroom between noise floor and limit.

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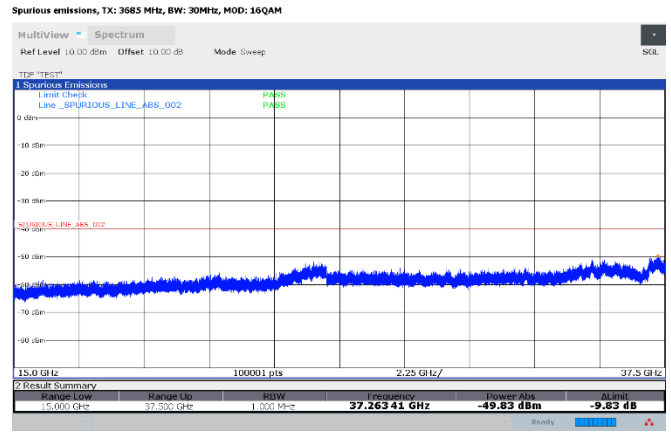
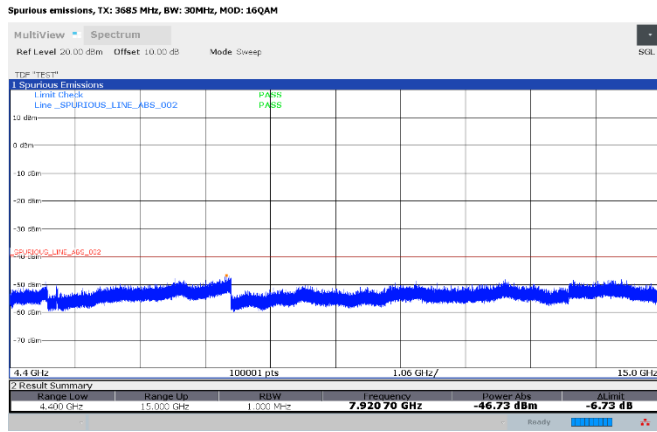


**TX: 3685 MHz, 30 MHz BW, 16QAM modulation:**



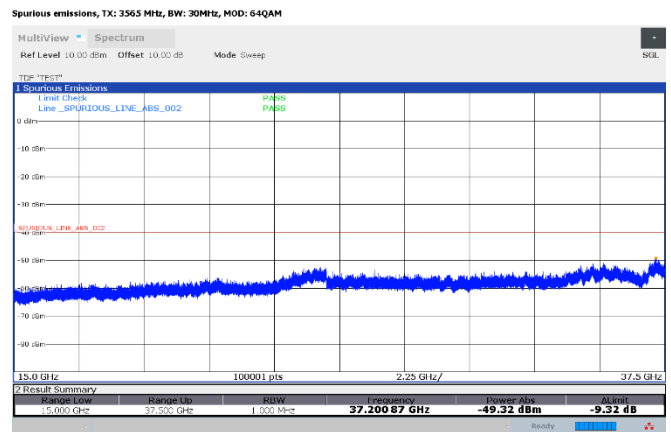
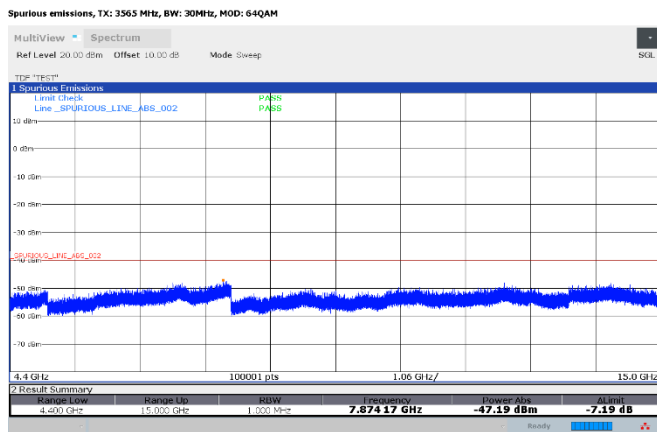
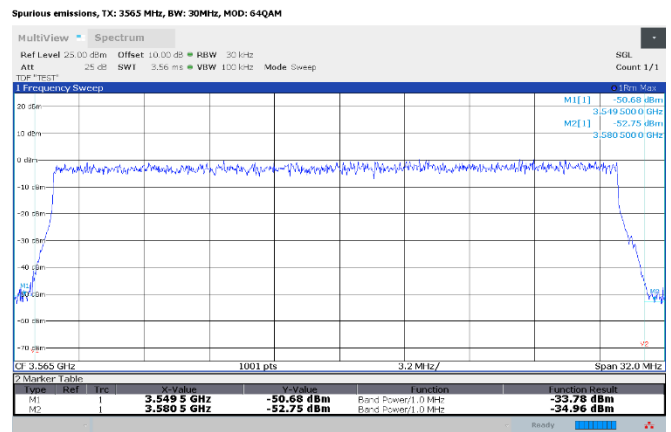
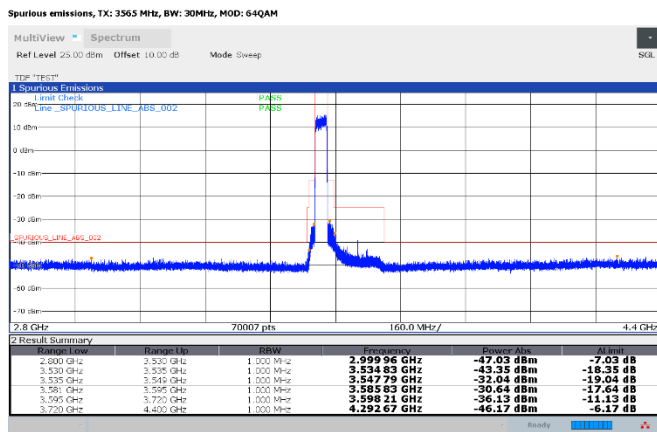
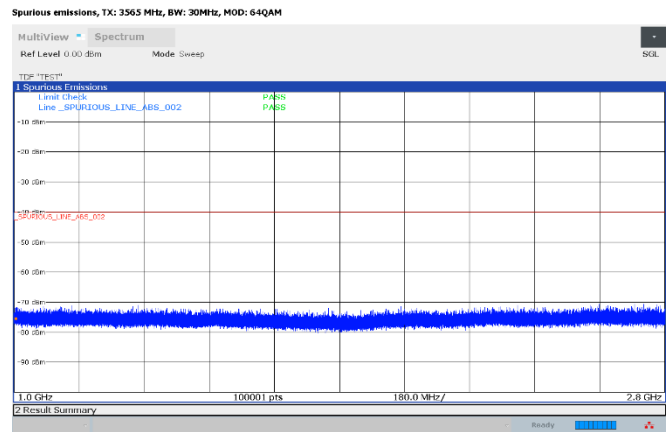
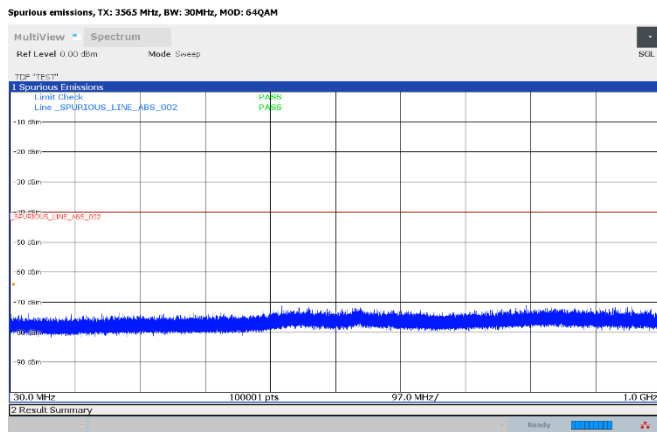
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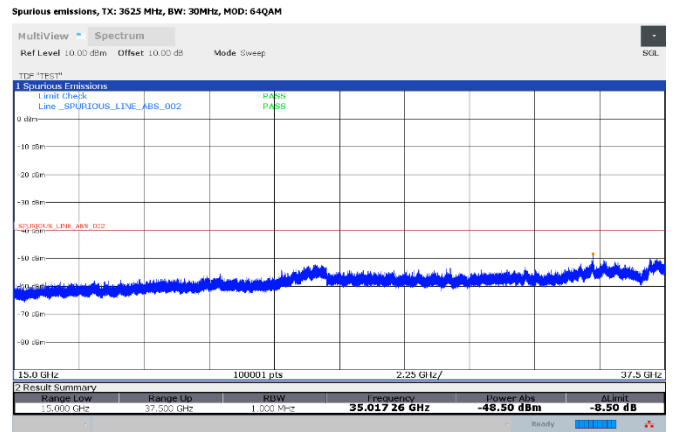
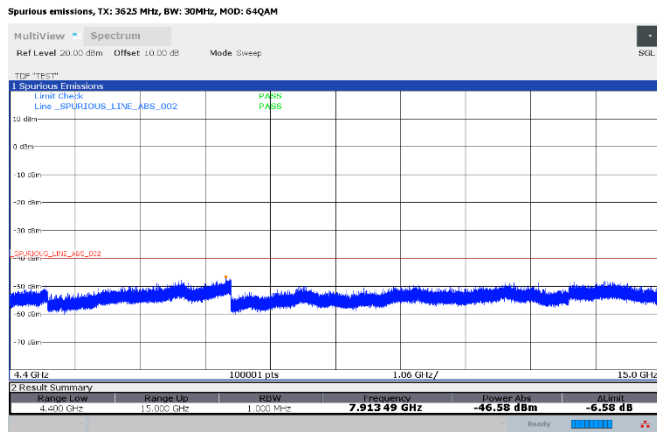
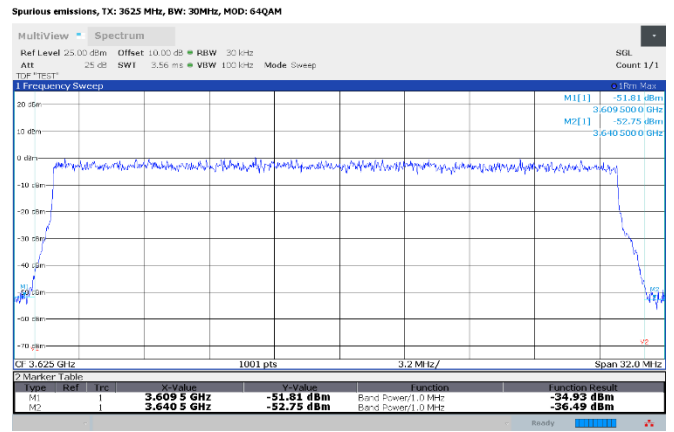
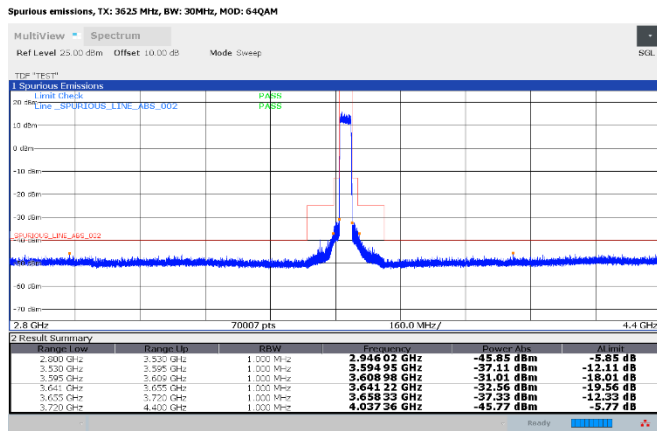
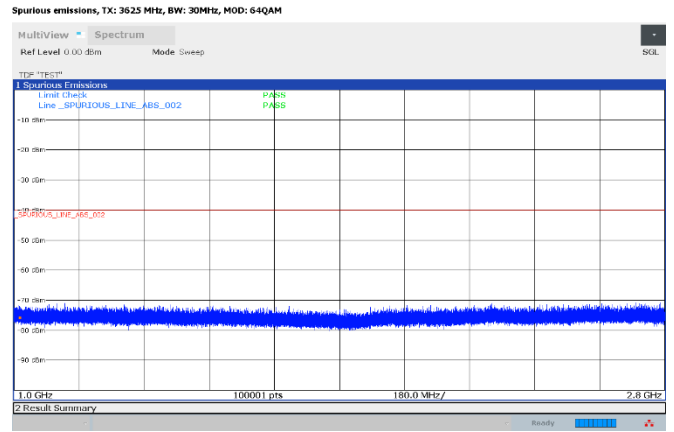
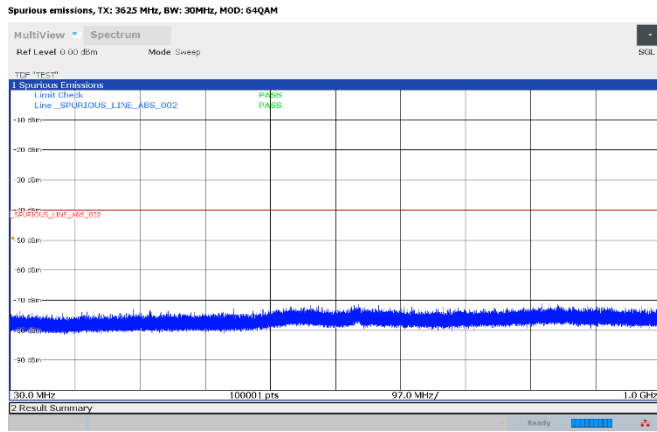


**TX: 3565 MHz, 30 MHz BW, 64QAM modulation:**

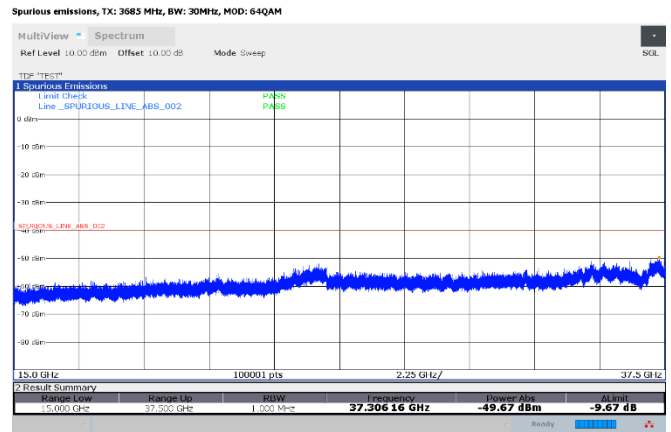
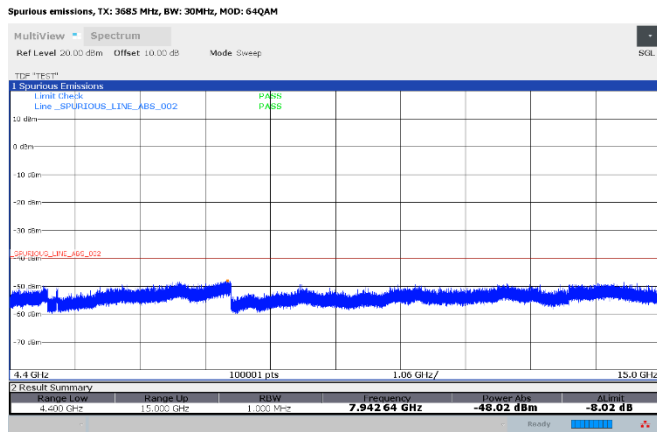
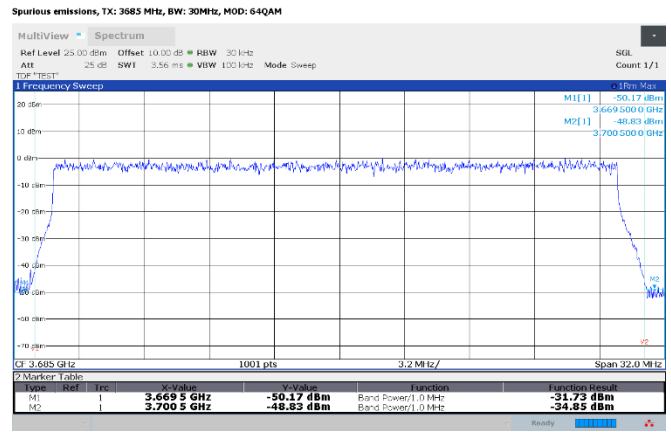
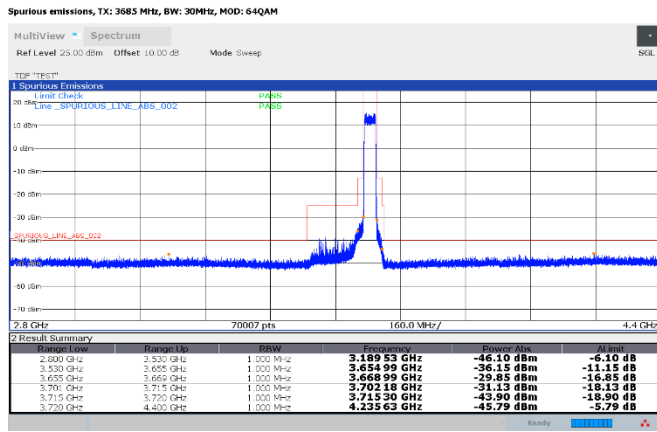
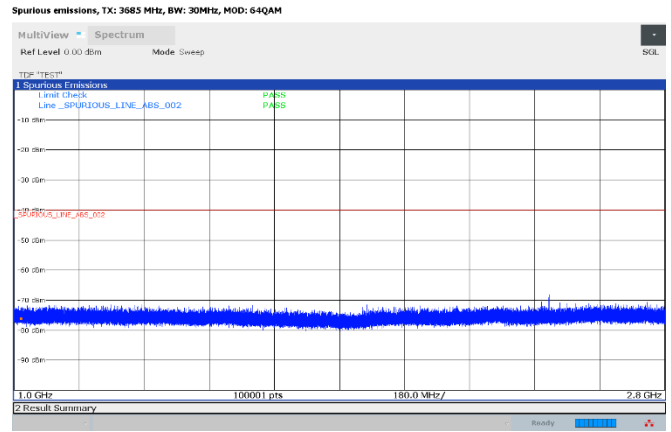
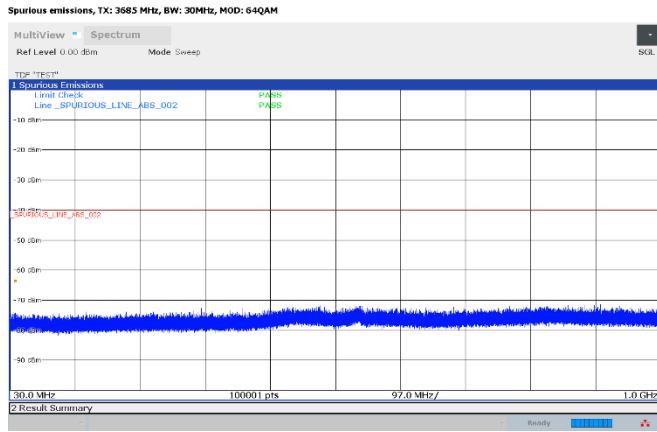




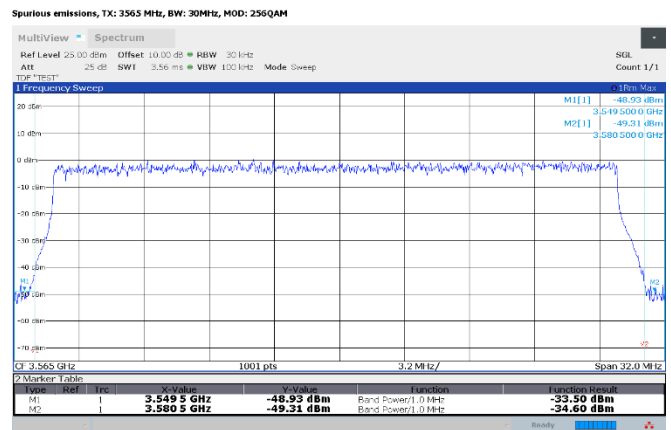
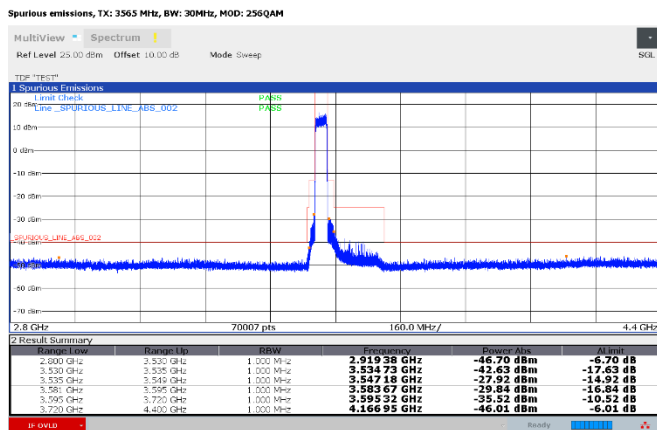
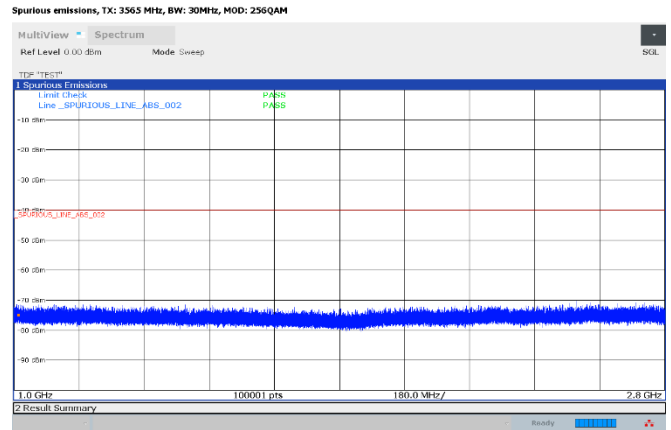
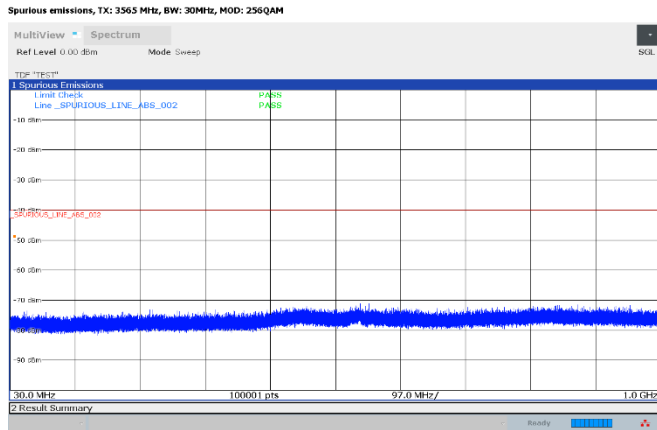
**TX: 3625 MHz, 30 MHz BW, 64QAM modulation:**



TX: 3685 MHz, 30 MHz BW, 64QAM modulation:



**TX: 3565 MHz, 30 MHz BW, 256QAM modulation:**



Note: Analyzer was marginally driven to IF overload due to the fundamental. Result kept so as to keep sufficient headroom between noise floor and limit.

