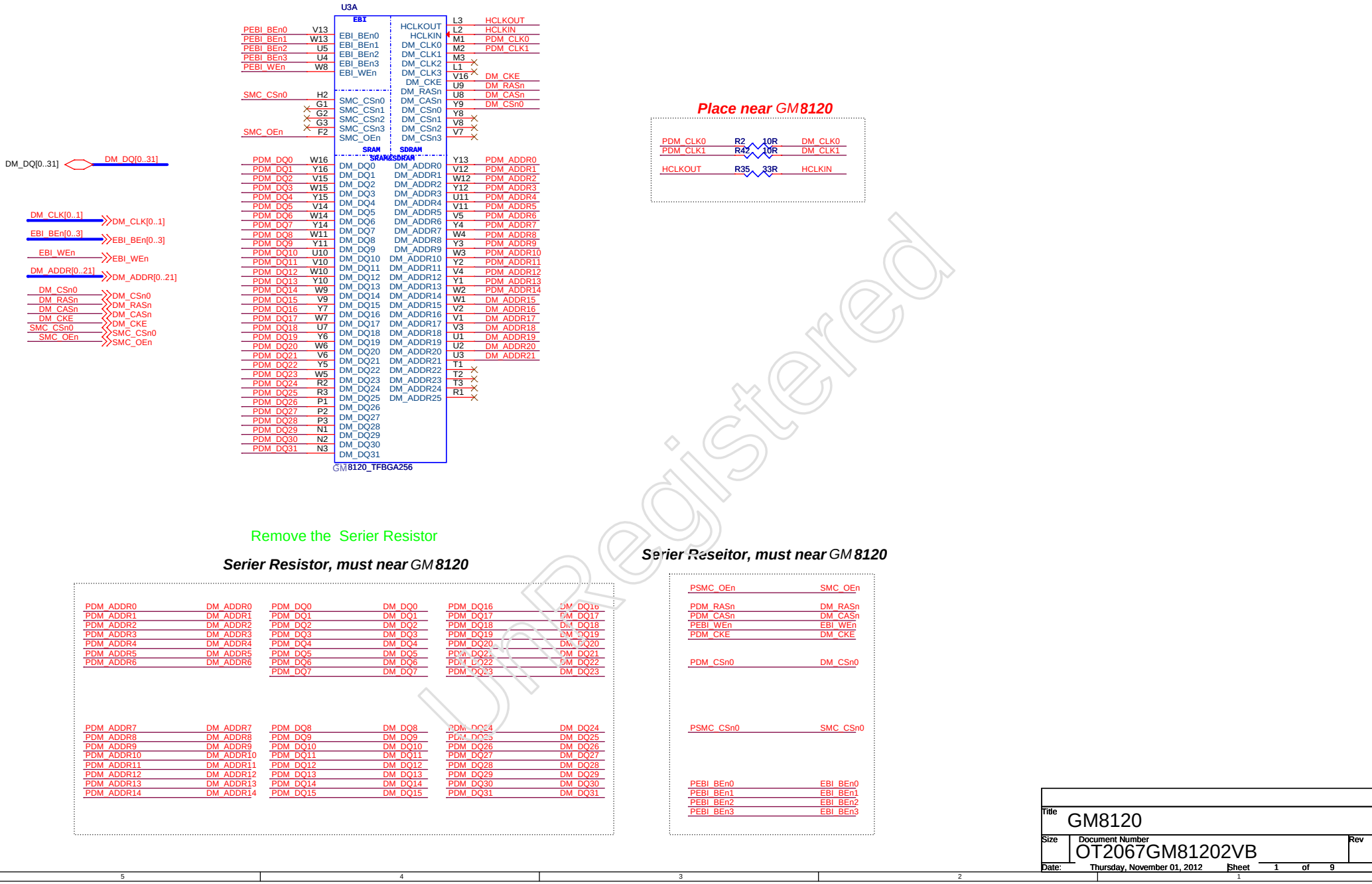
Data code/ Version	Modification
1.0	-First release

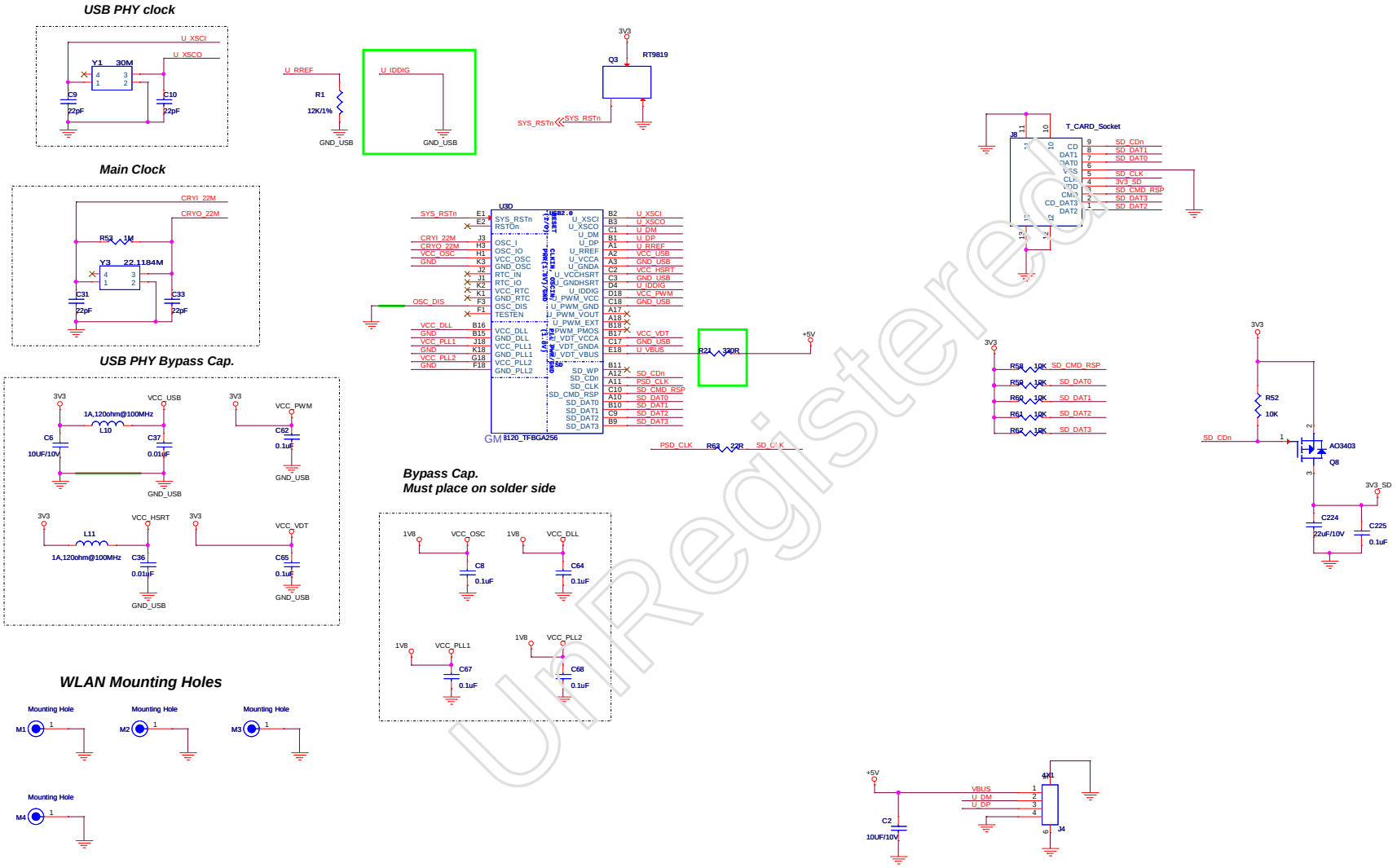
Change log:

1. USB Connector

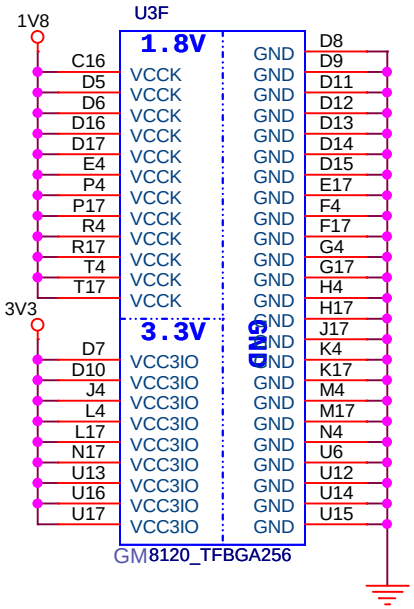
2. IRLED Board Connector

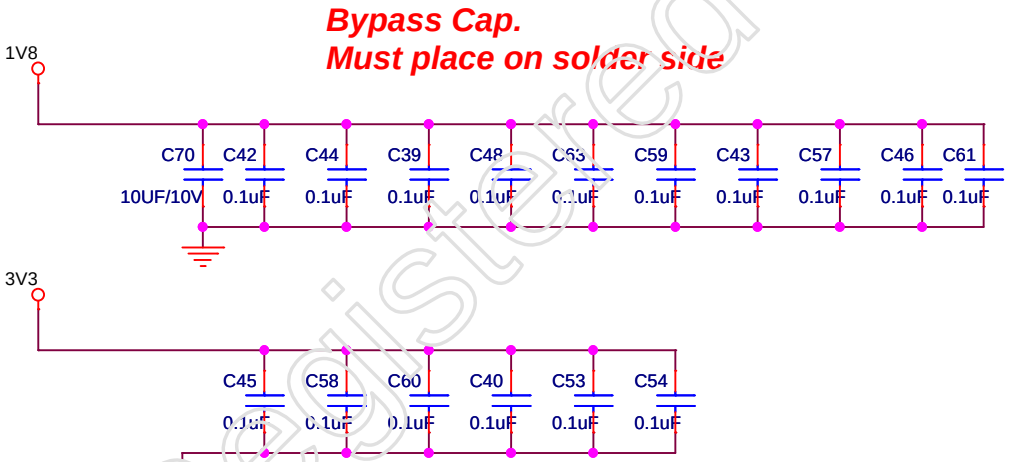
Title	Note
Size	Document Number OT2067GM81202VB Rev
Date:	Thursday, November 01, 2012 Sheet 1 of 9



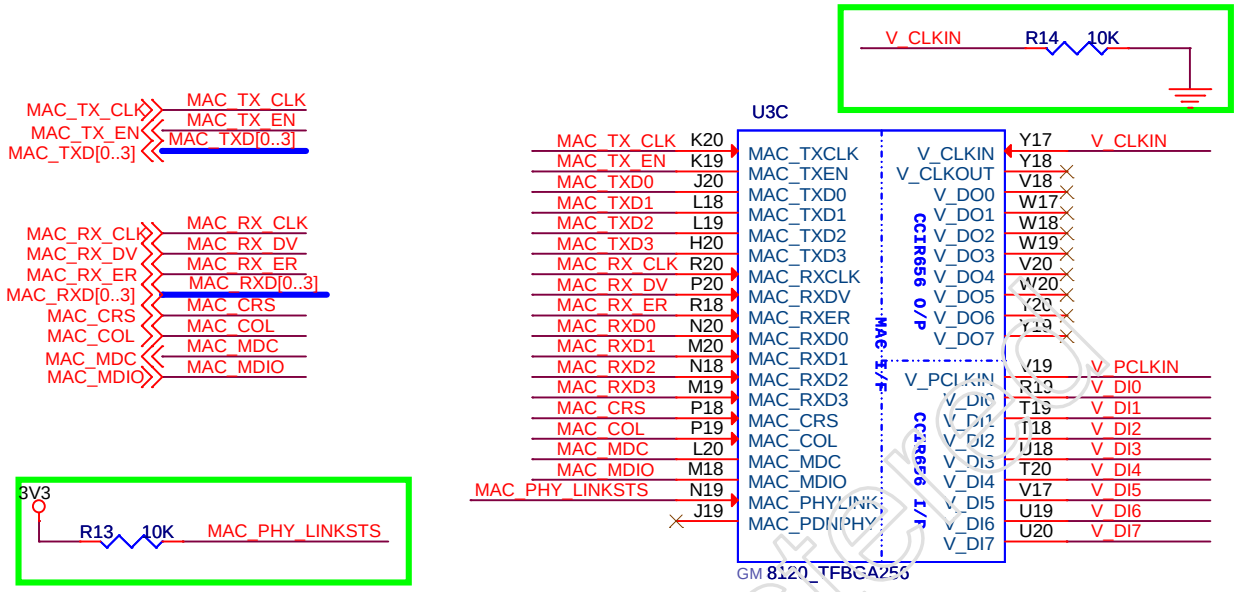


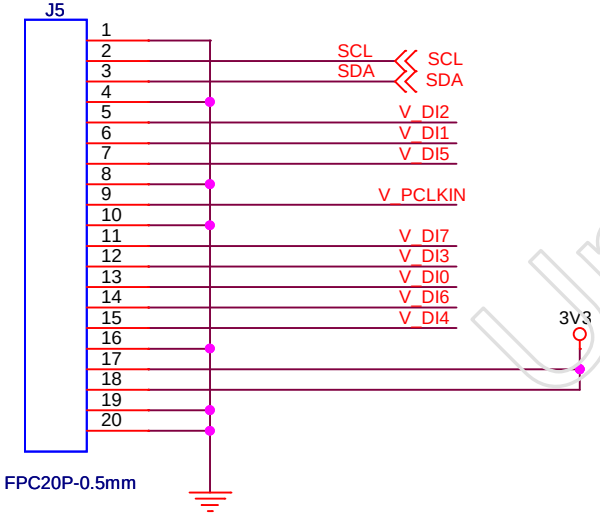
Title	GM8120
Size	Document Number: OT2067GM81202VB — Rev
Date: Thursday, November 01, 2012	Sheet 2 of 9



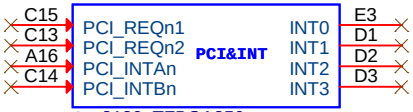


Title	GM8120	
Size	Document Number OT2067GM81202VB	Rev
Date: Thursday, November 01, 2012	Sheet 2 of 9	



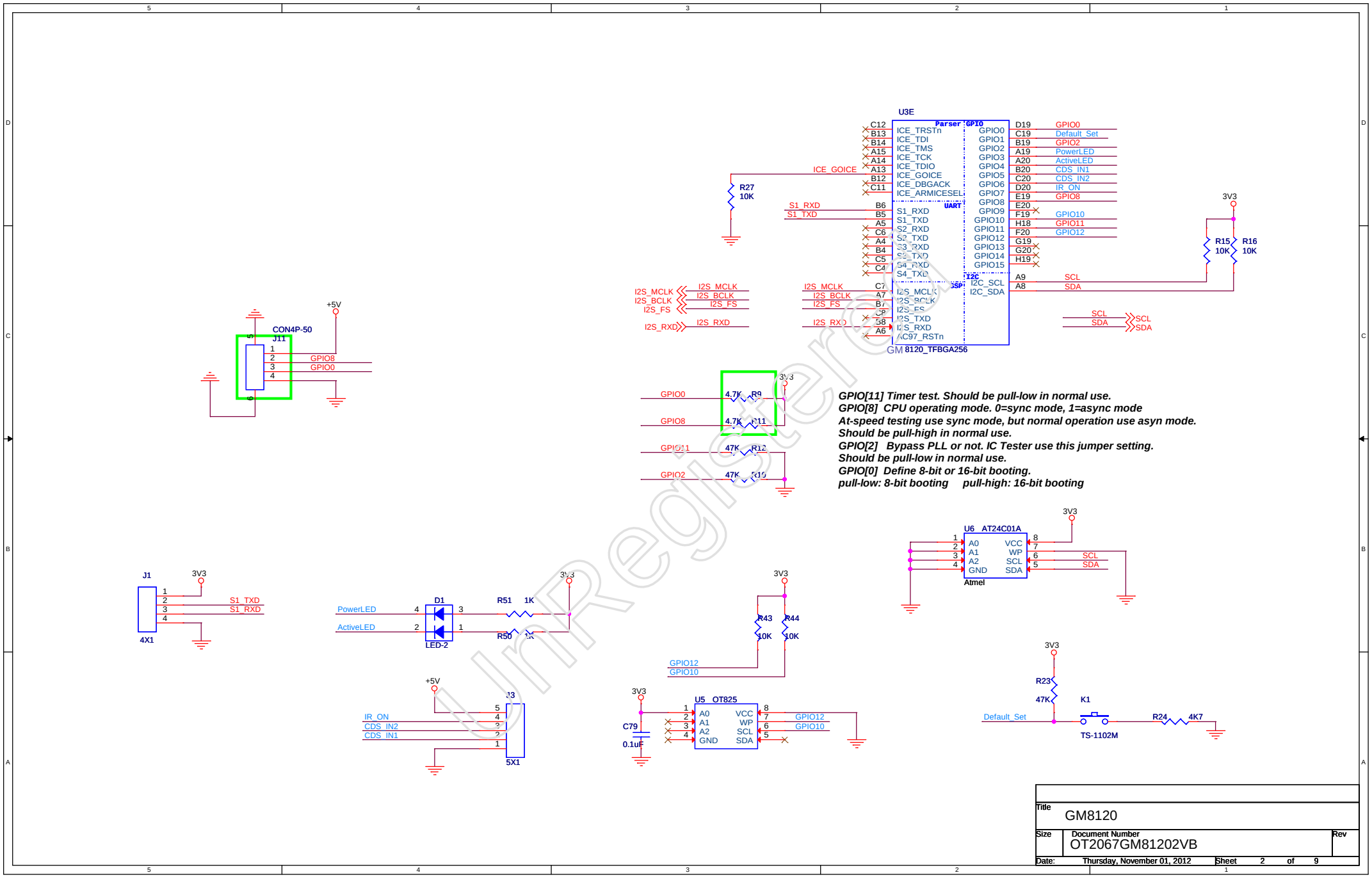


External devices interrupt input. Active high.
(internal pull-down)



Master requests to use PCI bus. (active low)

Title: GM8120		
Size	Document Number: OT2067GM81202VB	Rev
Date: Thursday, November 01, 2012	Sheet 2 of 9	

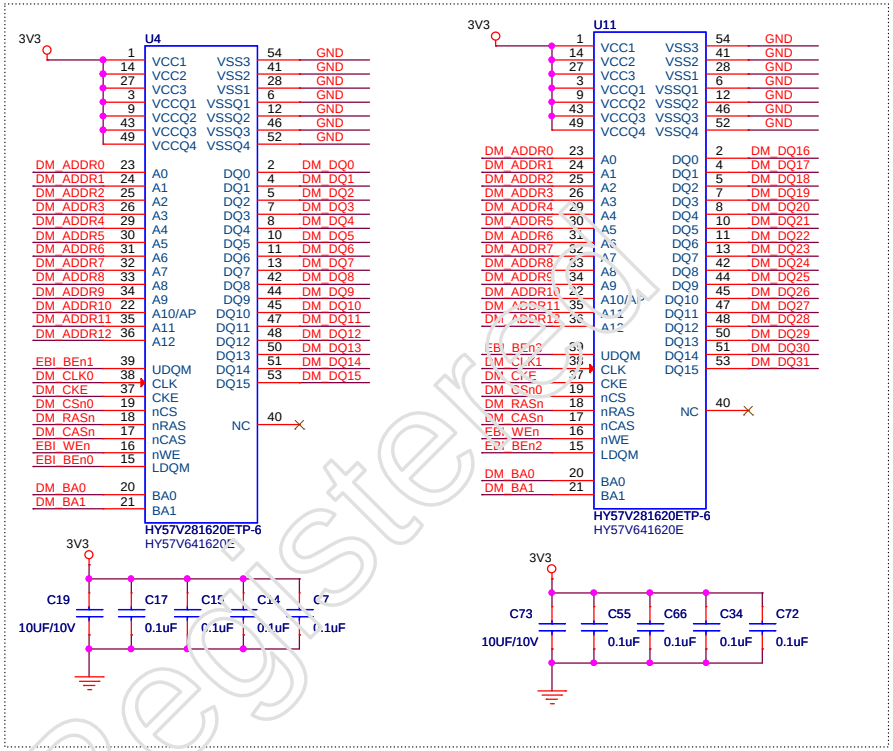


GPIO[11] Timer test. Should be pull-low in normal use.
GPIO[8] CPU operating mode. 0=sync mode, 1=async mode
At-speed testing use sync mode, but normal operation use asyn mode.
Should be pull-high in normal use.
GPIO[2] Bypass PLL or not. IC Tester use this jumper setting.
Should be pull-low in normal use.
GPIO[0] Define 8-bit or 16-bit booting.
pull-low: 8-bit booting pull-high: 16-bit booting

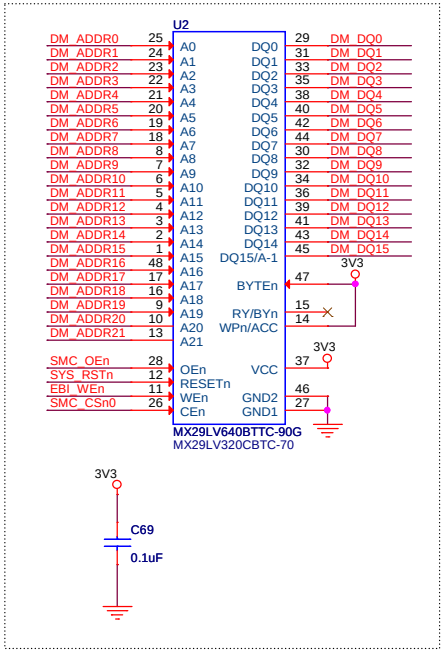
DM_ADDR[0..21] DM_ADDR[0..21]
DM_DQ[0..31] DM_DQ[0..31]
EBI_BEn[0..3] EBI_BEn[0..3]
EBI_WEn EBI_WEn
DM_CSn0 DM_CSn0
DM_RASn DM_RASn
DM_CASn DM_CASn
DM_CLK[0..1] DM_CLK[0..1]
DM_CKE DM_CKE
SMC_CSn0 SMC_CSn0
SMC_OEn SMC_OEn
SYS_RSTn SYS_RSTn

DM_ADDR13 DM_BA0
DM_ADDR14 DM_BA1

On Board SDRAM(8Mx16)x2



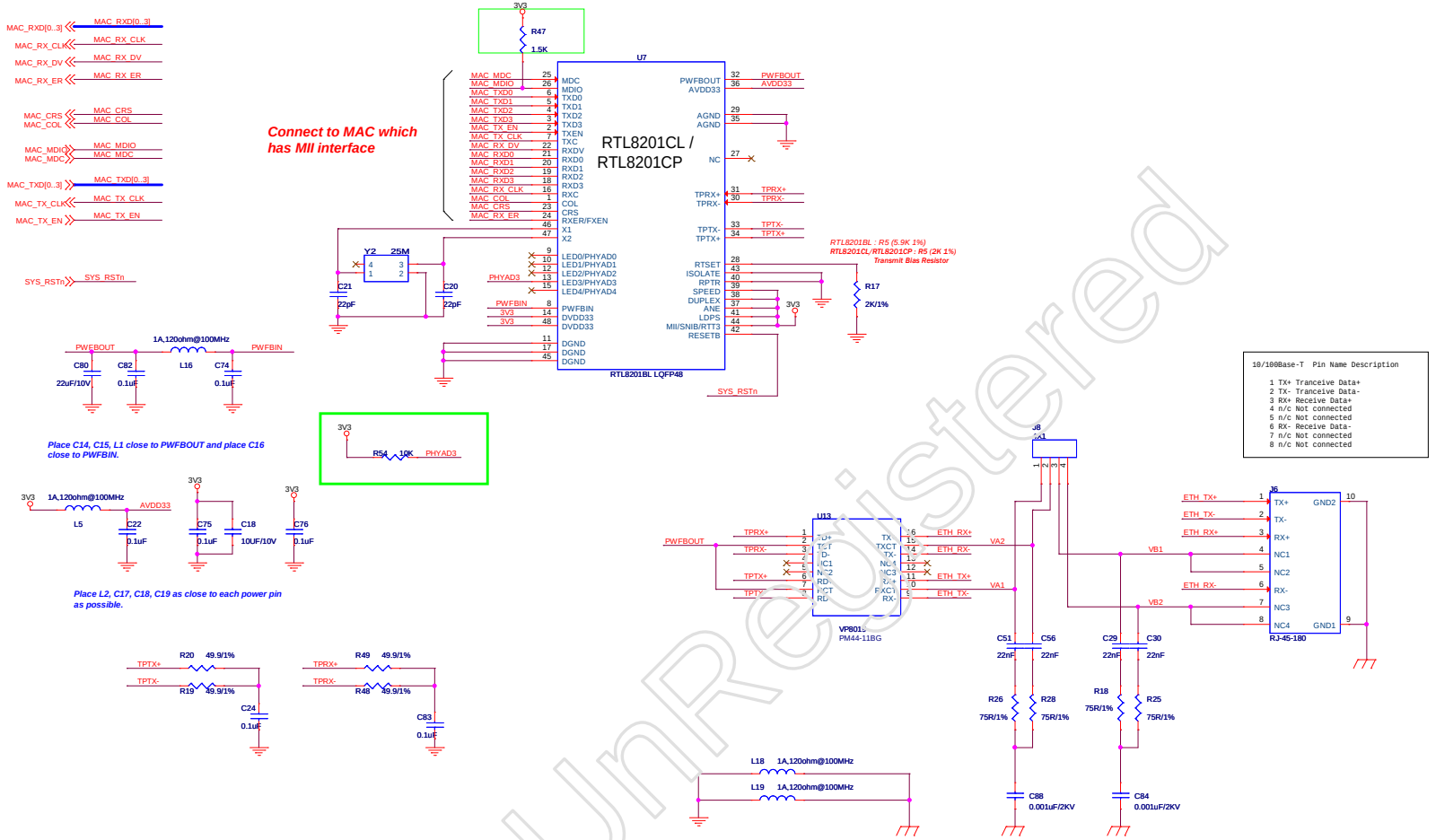
On Board Flash (4Mx16 or 2Mx16)



HY57V281620ETP-6

HY57V641620E

Title: Memory		
Size	Document Number: OT2067GM81202VB	Rev
Date: Thursday, November 01, 2012	Sheet 4 of 9	



Title	Network
Size	OT2067GM81202VB
Rev	
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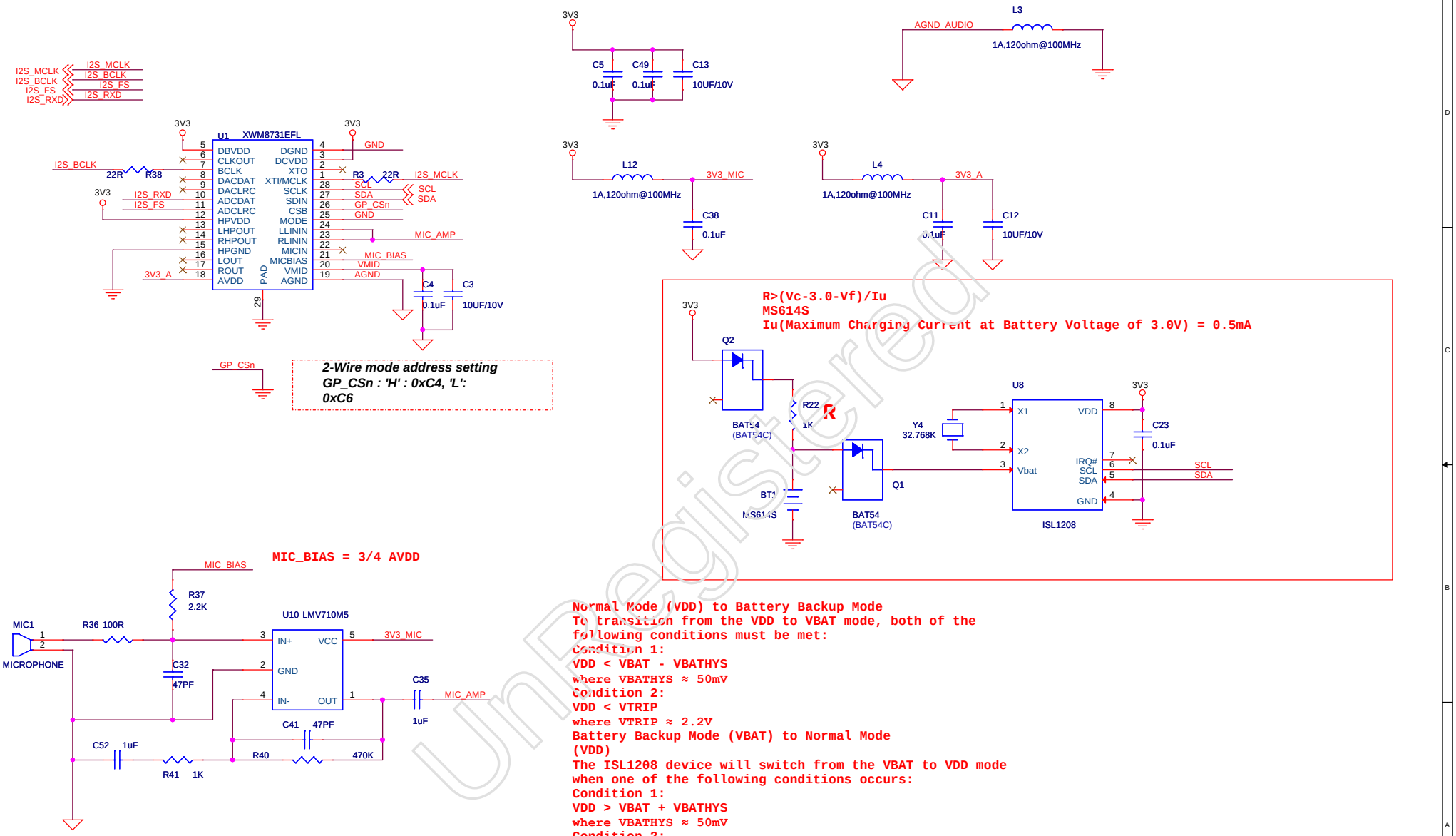
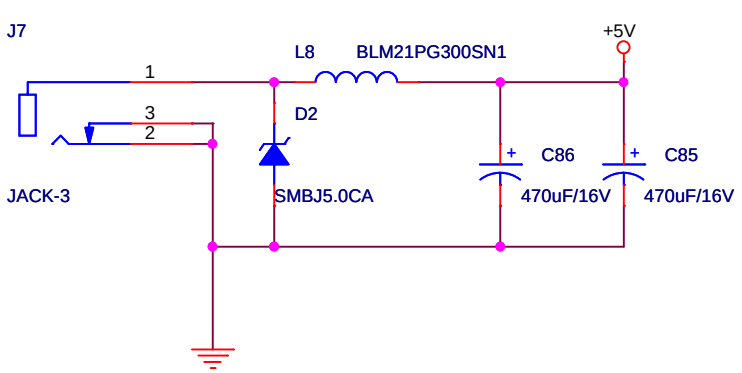
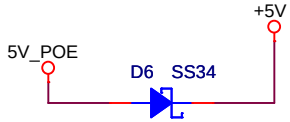
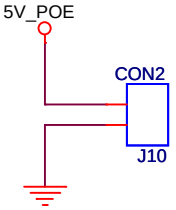






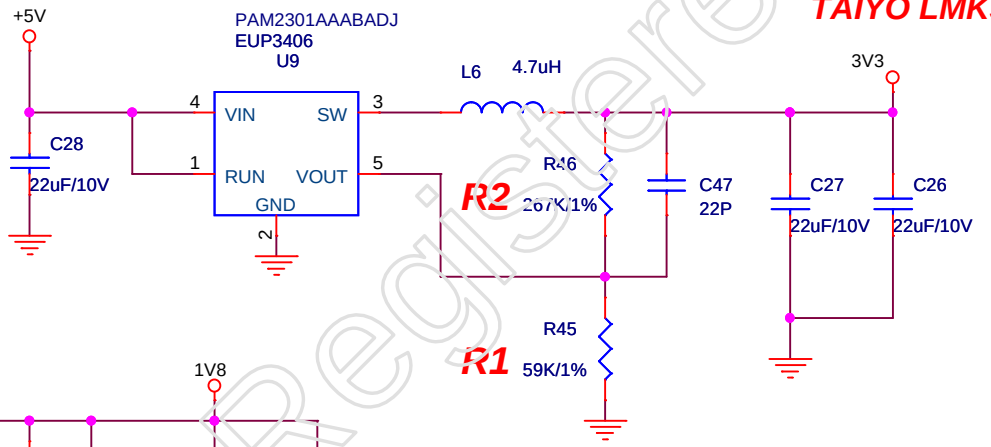


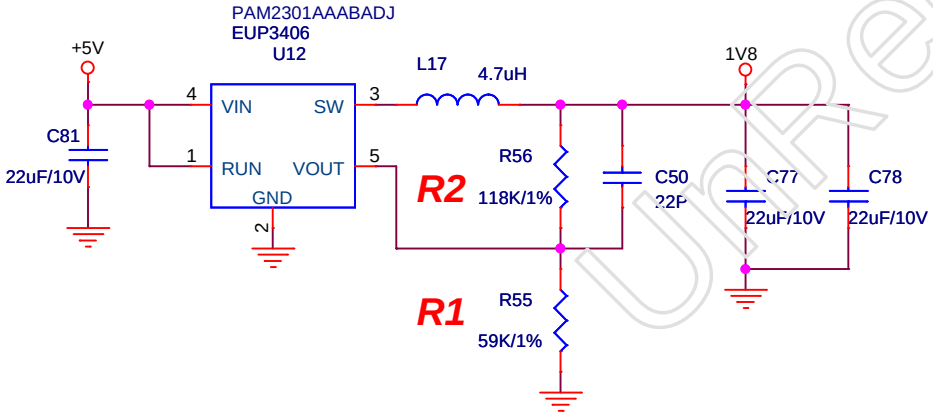
22uF/10V Capacitor use

C4-K2.5L MITSUMI
Sumida CDRH4D18

TDK C3216Y5V1C226MT

TAIYO LMK316BJ226ML-T





Vout = 0.6V × (1 + R2/R1)
3.3V = 0.6V × (1 + 267K/59K)
1.8V = 0.6V × (1 + 118K/59K)

Title	Power	
Size	Document Number OT2067GM81202VB	Rev
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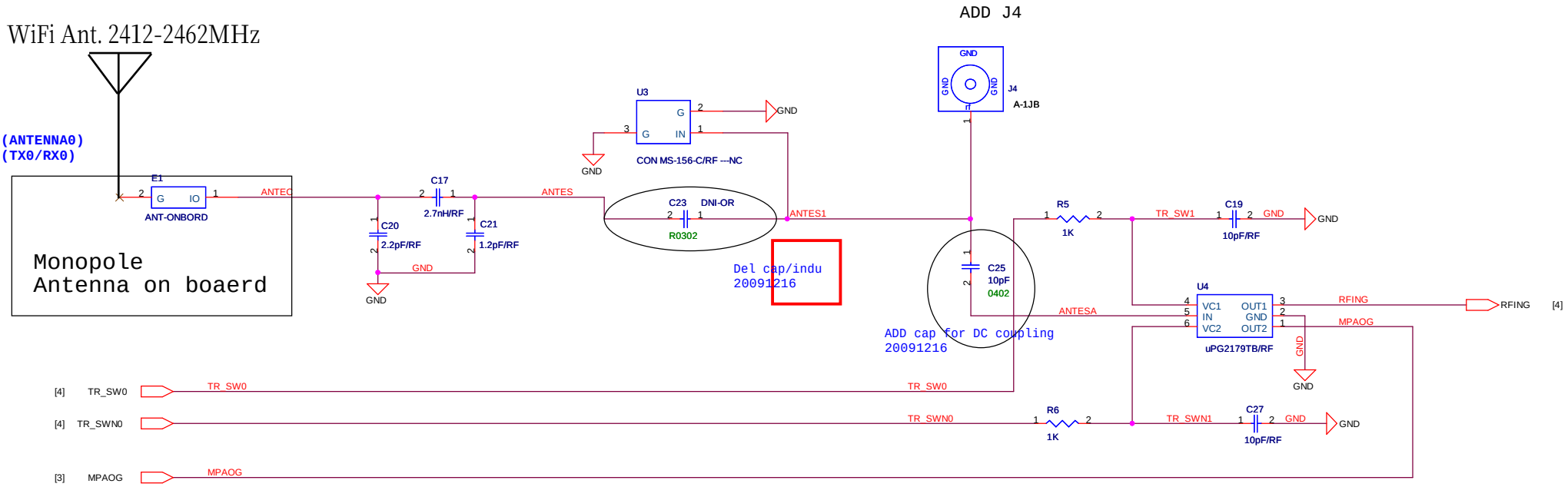
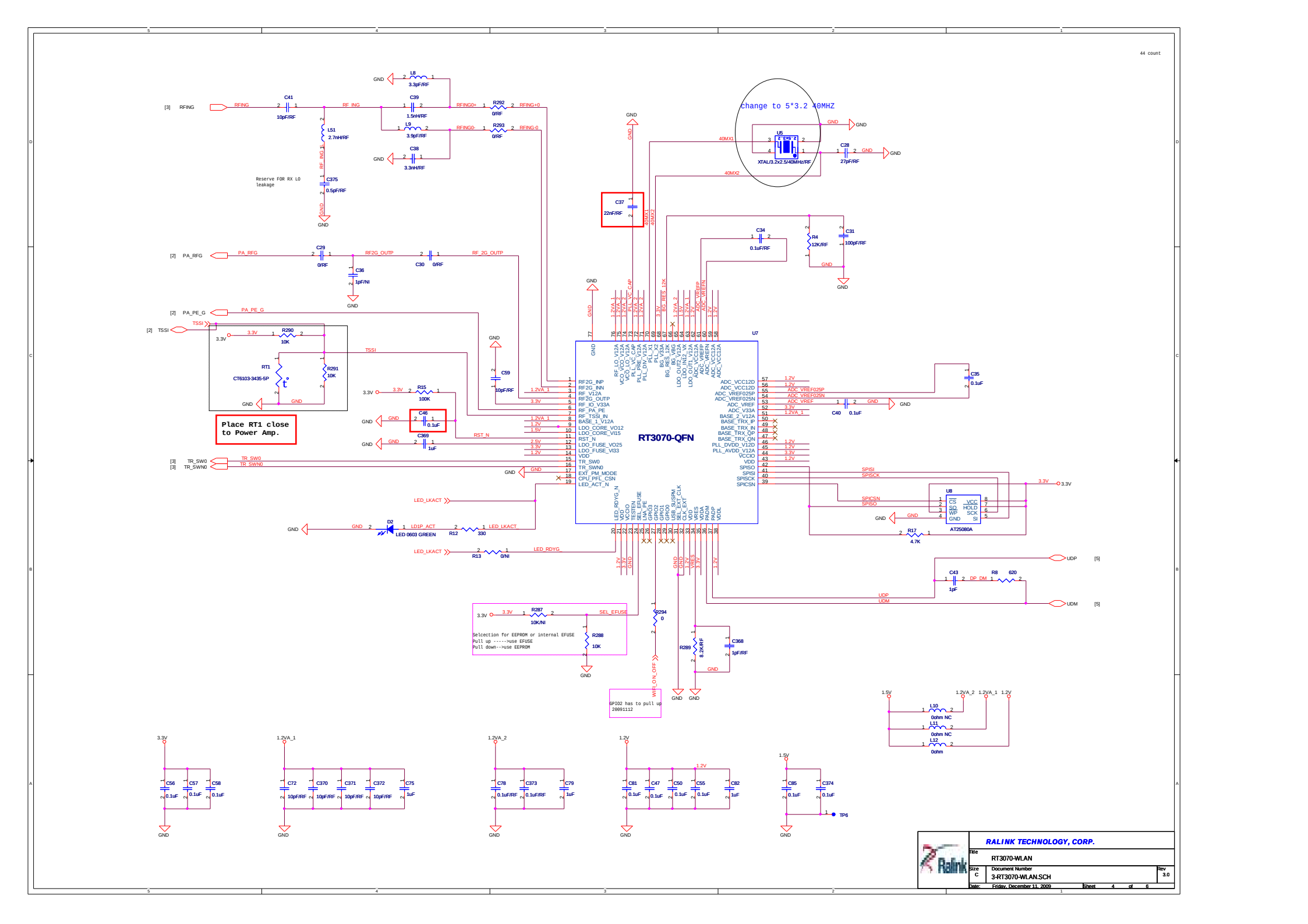
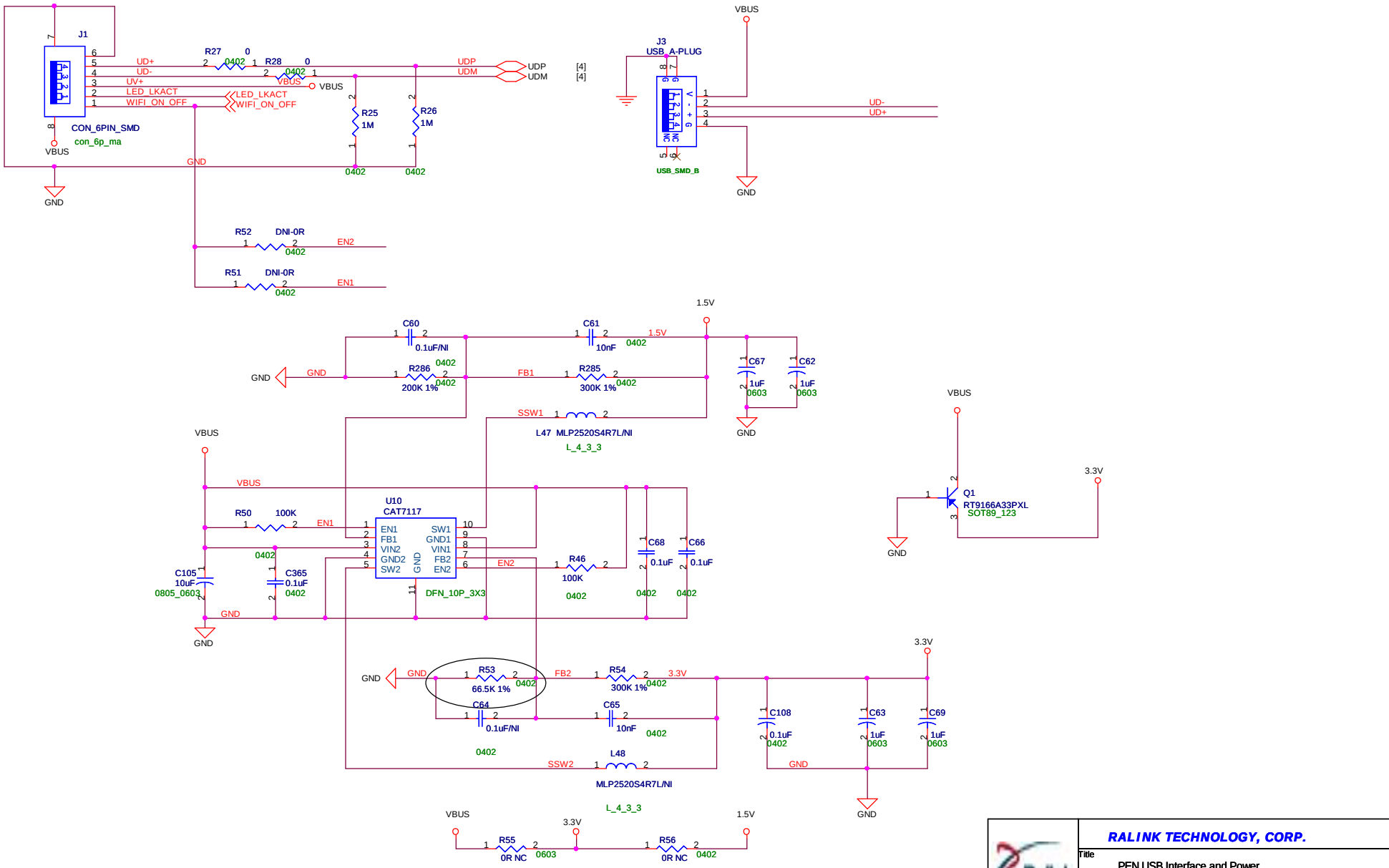


RALINK TECHNOLOGY, CORP.		
Title: Front-End		
Size: B	Document Number: 2-FrontEnd.SCH	Rev: 3.0
Date: Wednesday, December 16, 2009	Sheet 3 of 6	


change to 5*3.2 40MHZ
RT3070-QFN
Place RT1 close to Power Amp.
Selcection for EEPROM or internal EFUSE
Pull up ----->use EFUSE
Pull down-->use EEPROM
GPIO2 has to pull up 20091112
Reserve FOR RX LO leakage
LED 0603 GREEN
AT25080A
XTAL/3.2x2.5/40MHz/RF
RALINK TECHNOLOGY, CORP.
RT3070-WLAN
3-RT3070-WLAN.SCH
Friday, December 11, 2009



RALINK TECHNOLOGY, CORP.

Title	PEN USB Interface and Power	
Size B	Document Number 4-USB-A.SCH	Rev 3.0
Date: Thursday, September 22, 2011	Sheet 5 of 6	