

**Hunt Electronic Co., Ltd.**

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Date: SEP, 27, 2007

Dear Madam / Sir.

We declare that all of EMI suppression parts added on the EUT, FCC ID: UTBHLT86FW will be put on mass production product.

(A)

1. Sticking copper foil inside the plastic case of EUT, it is including the upper case. Plastic case suggests using conductive lacquer with higher density and lower impedance or changing it to the Iron case.
2. Adding a core to the side of module of antenna cable. The model of core is KING CORE K5B RH9\*16\*5.
3. Changing the antenna cable to shielding cable.

**Debugged countermeasures that is in the part of main IC board are as follows,**

4. Adding conductive shielding gasket,
  - a. Adding conductive shielding gasket on the iron case of LAN port.
  - b. Adding conductive shielding gasket on the iron case of J1 (Video) port to touch iron slice.
5. Pulling wire with jumper from side of C11 GND to side of R23 GND.
6. Changing RS14 to 68-OHM resistance and connecting 120-ohm bead in series. The model of bead is KING CORE FBMA-11-160808-121.
7. Shorting R10, R11, R12, R13, and R17 of circuit board.
8. Changing the RS2 to RS9 of circuit board to 33-ohm resistances.
9. Changing the RS13 to 100-ohm resistance.
10. Connecting 150-ohm bead to 25 pin of J2 IC of circuit board in series. The model of bead is KING CORE FBM-10-100505-151.
11. Connecting an 82nH differential-mode inductance to VCC of POWER JACK in series. The model of differential-mode inductance is EROCORE MCS1008C-82NJ.

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**12. Capacitances in parallels.**

- a. Respective connecting the 0.1uF capacitance in parallels from 47 and 49 pin of J2 to GND.
- b. Respective connecting the 10pF capacitance to C5 to C8 in parallels.
- c. Changing C11 and C12 of circuit board to 10pF capacitance.
- d. Respective connecting the 0.1uF capacitance in parallels from No.9 and 11 pins of U11 (LAN port) to GND.
- e. Respective connecting the 15pF capacitance in parallels from No.1, 3, 4 and 6 pins of U11 (LAN port) to GND.
- f. Respective connecting the 100pF capacitance in parallels from No.2 and 5 pin of A1 and No.2 and 3 pin of A2 to GND.
- g. Respective connecting 0.1uF capacitances in parallels from side of No.1, 3 and 14 pins of U2 IC (SDRAM) to GND.
- h. Respective connecting 0.1uF capacitances in parallels from side of No.1, 3, 43 and 49 pins of U5 IC (SDRAM) to GND.

**In the part of Card Read Board**

13. Connecting 120-ohm bead to No.25 pin of J1 in series. The model of bead is KING CORE FBMA-11-160808-121.
14. Connecting 0.1uF capacitance in parallels from No.49 pin of J1 to GND.
15. Connecting 33pF capacitance in parallels from No.5 pin of SD port to GND.

**(B)** Mount one suppression core on adaptor power cord.

Sincerely,

Assistant Vice President  
R&D Dept.

Shu-Shing Lin