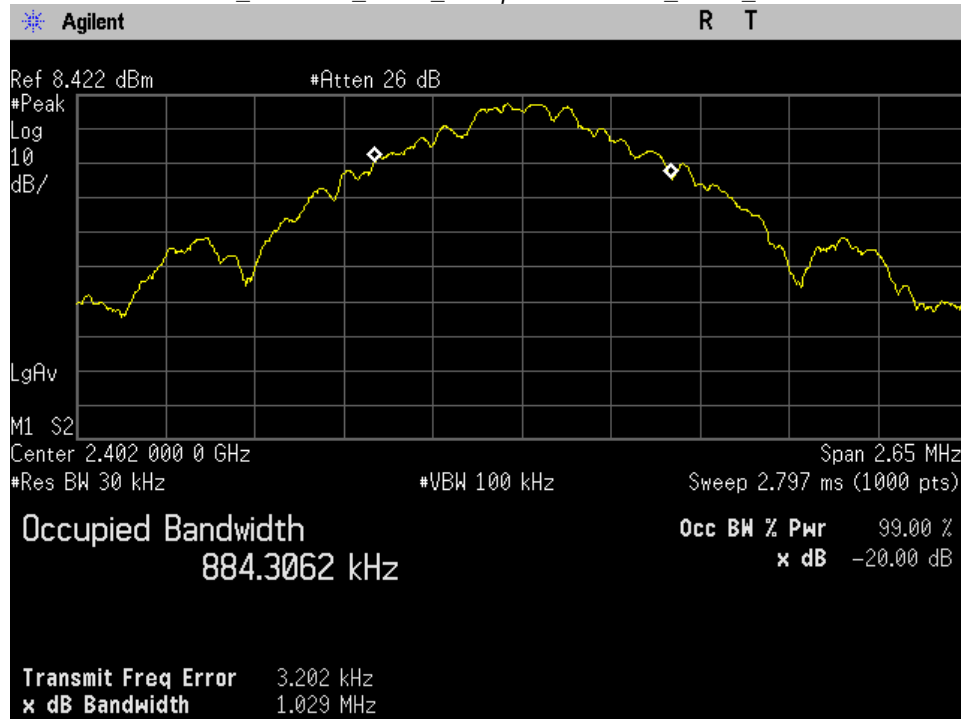


Annex A: Emission Bandwidth

GFSK

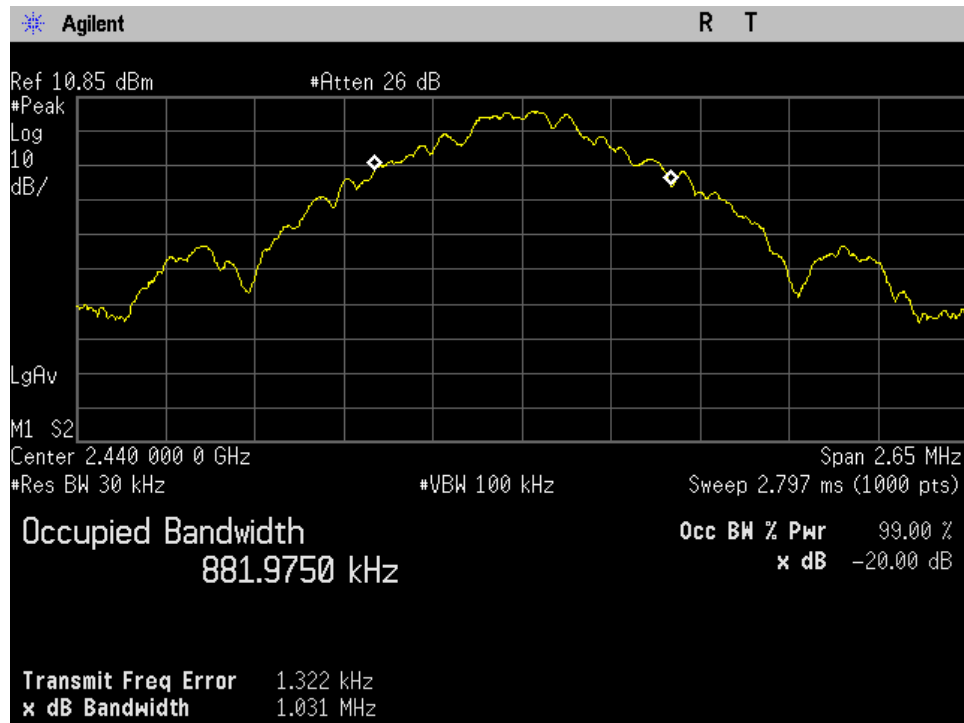
Low Ch_2402MHz_GFSK_Occupied Bandwidth_-20dB_Port 1



Low Ch_2402MHz_GFSK_Occupied Bandwidth_99 percent_Port 1



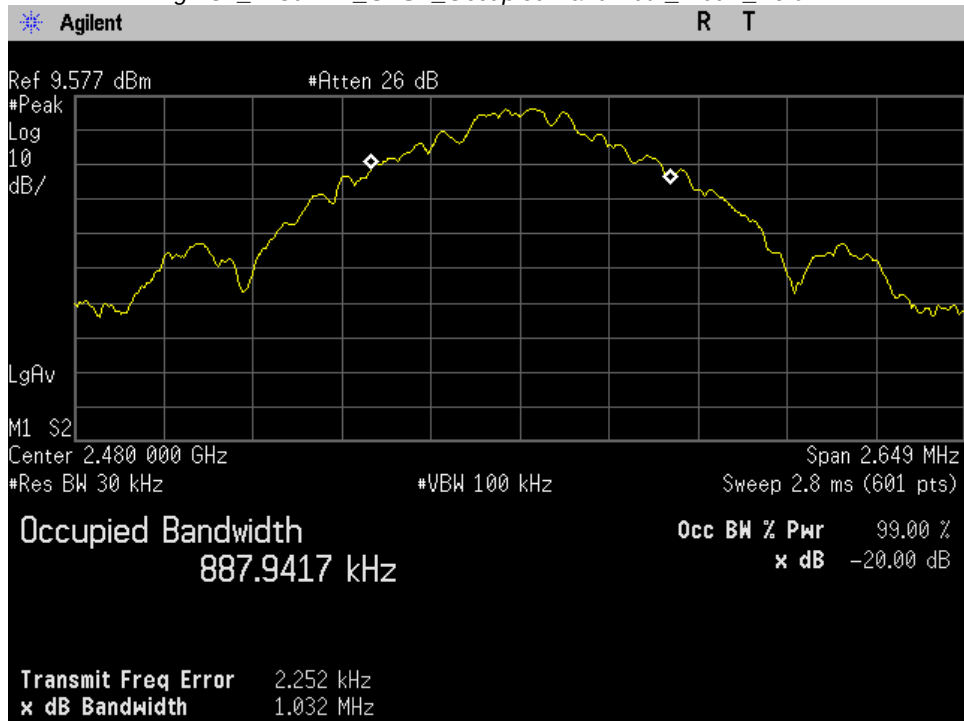
Mid Ch_2440MHz_GFSK_Occupied Bandwidth_-20dB_Port 1



Mid Ch_2440MHz_GFSK_Occupied Bandwidth_99 percent_Port 1



High Ch_2480MHz_GFSK_Occupied Bandwidth_-20dB_Port 1

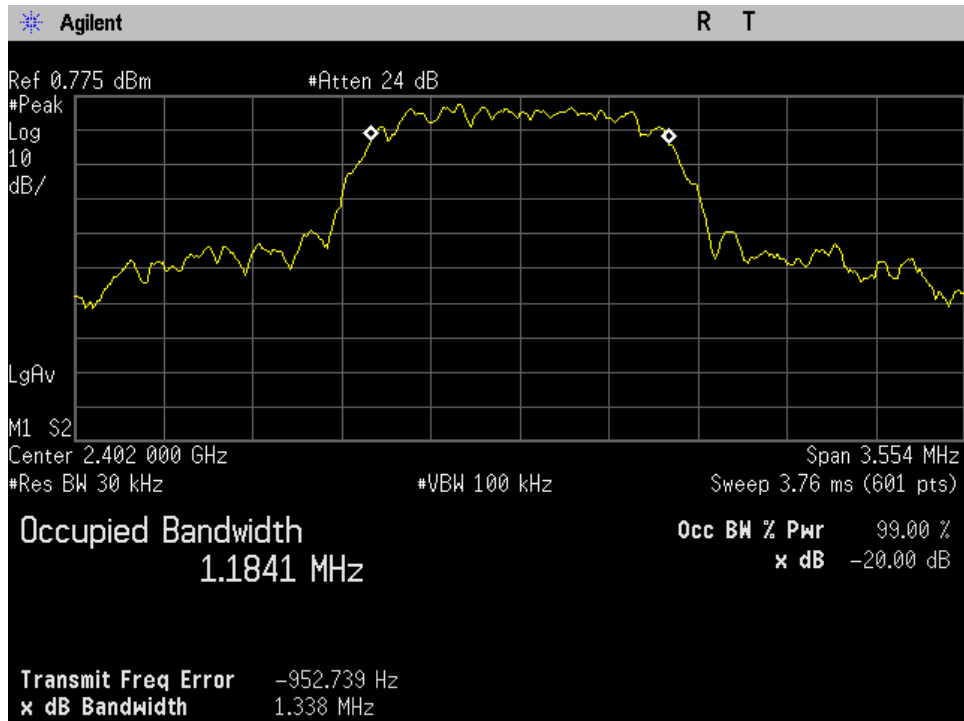


High Ch_2480MHz_GFSK_Occupied Bandwidth_99 percent_Port 1

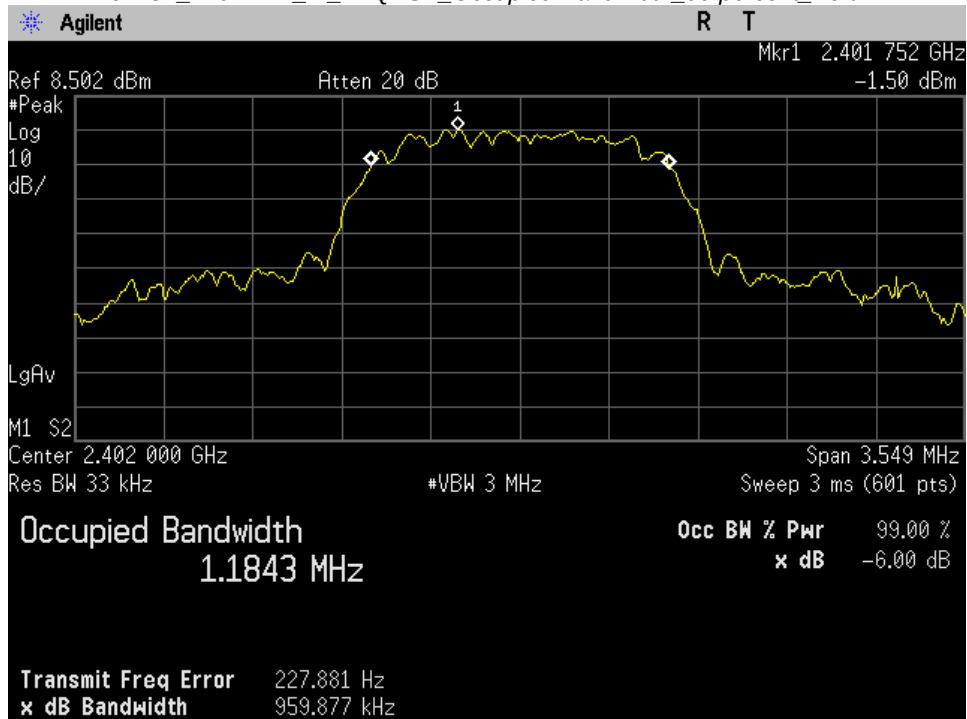


$\Pi/4$ DPSK

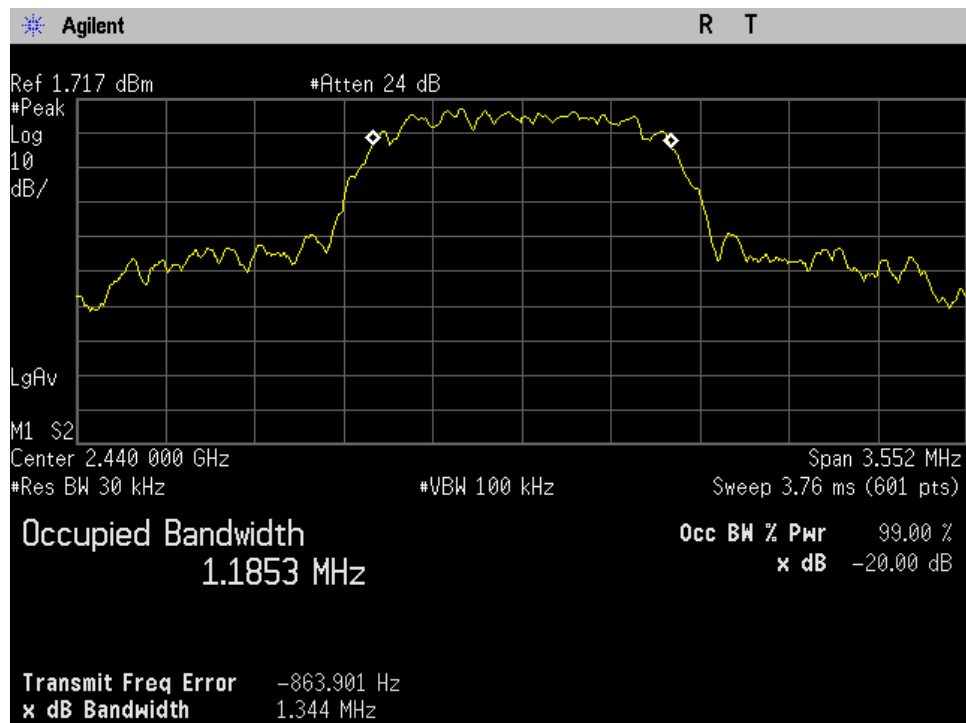
Low Ch_2402MHz_Pi_4DQPSK_Occupied Bandwidth_-20dB_Port 1



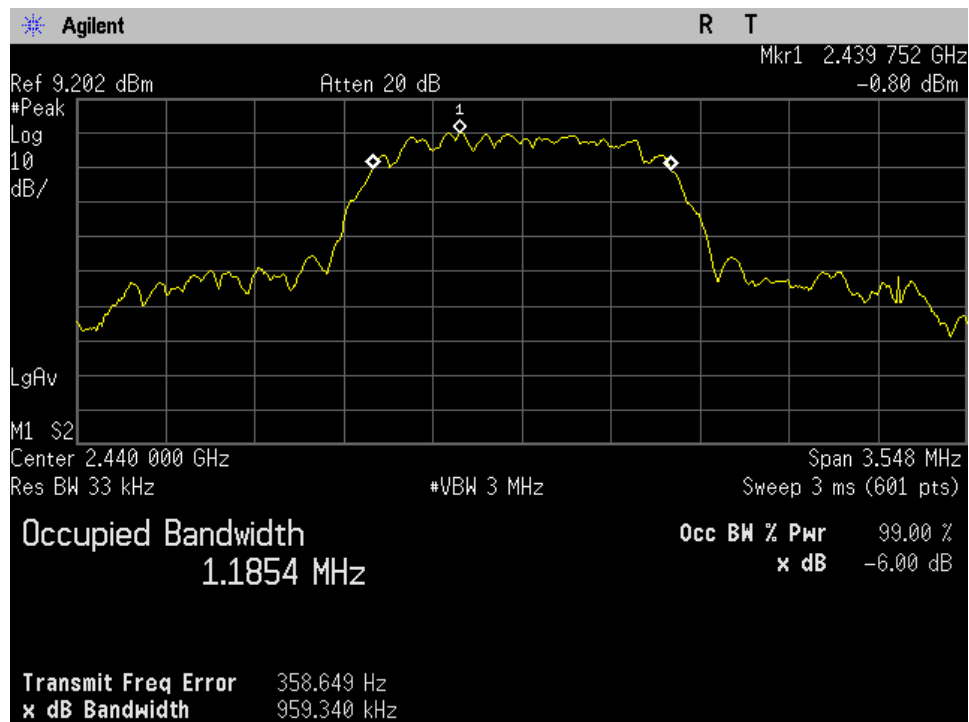
Low Ch_2402MHz_Pi_4DQPSK_Occupied Bandwidth_99 percent_Port 1



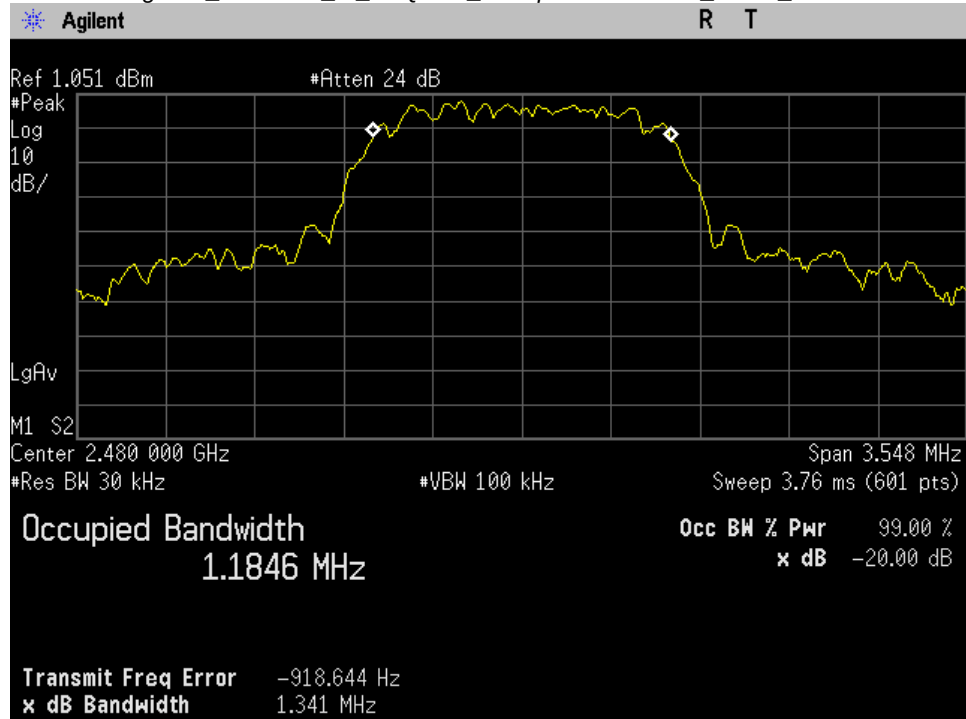
Mid Ch_2440MHz_Pi_4DQPSK_Occupied Bandwidth_-20dB_Port 1



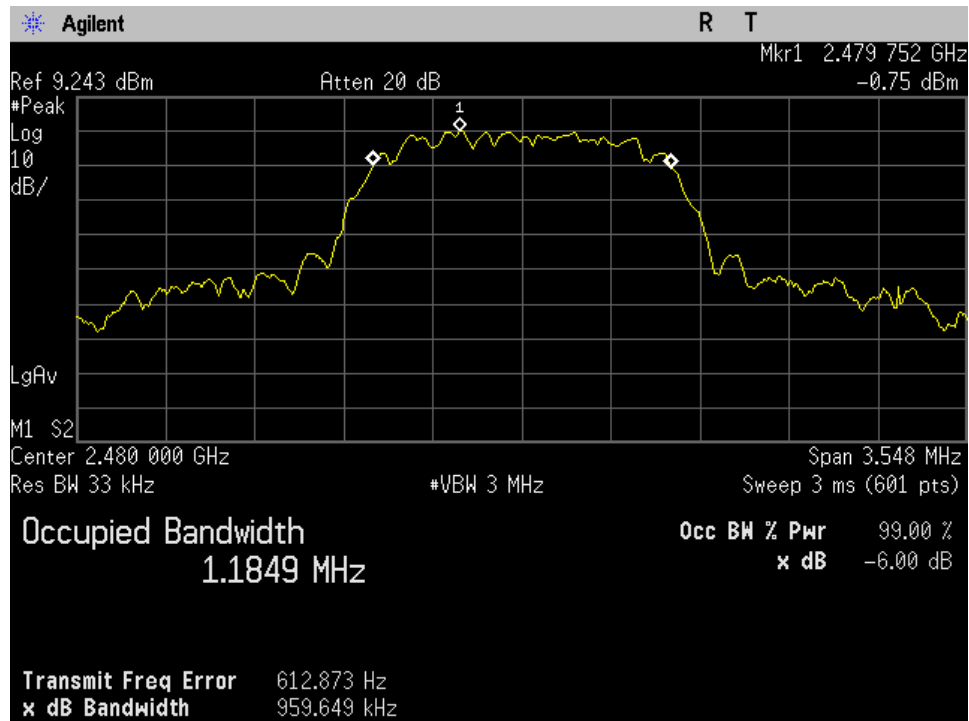
Mid Ch_2440MHz_Pi_4DQPSK_Occupied Bandwidth_99 percent_Port 1



High Ch_2480MHz_Pi_4DQPSK_Occupied Bandwidth_-20dB_Port 1

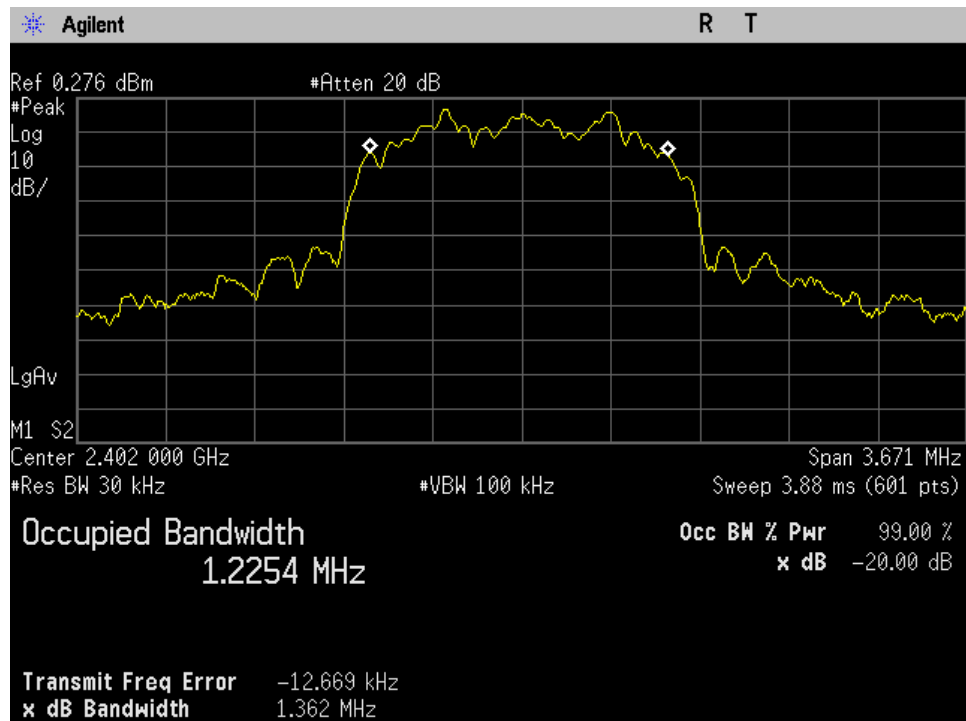


High Ch_2480MHz_Pi_4DQPSK_Occupied Bandwidth_99 percent_Port 1

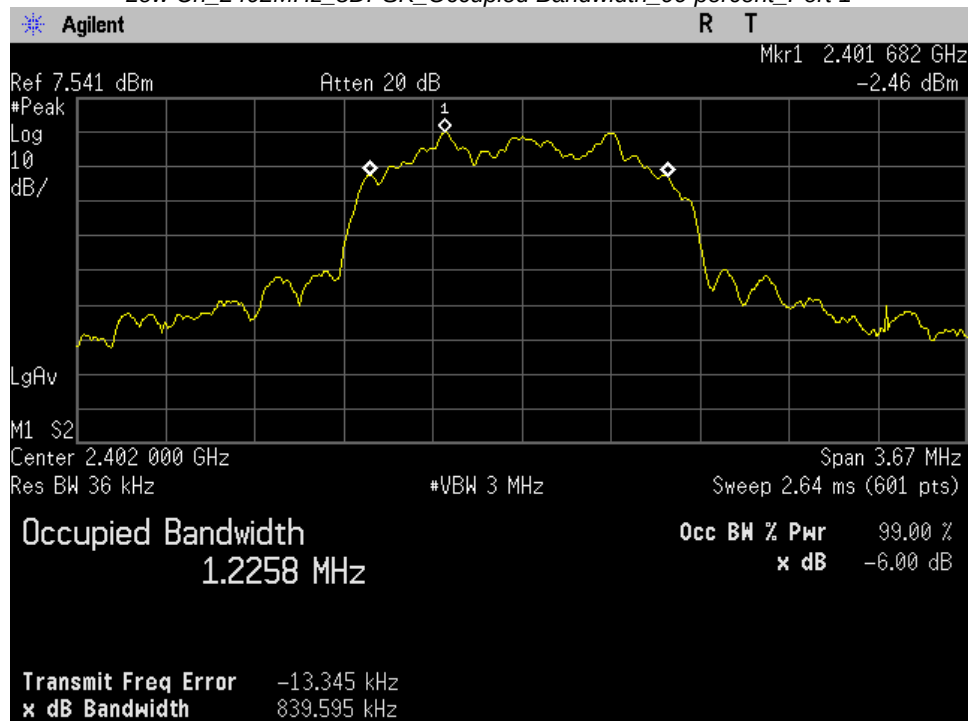


8DPSK

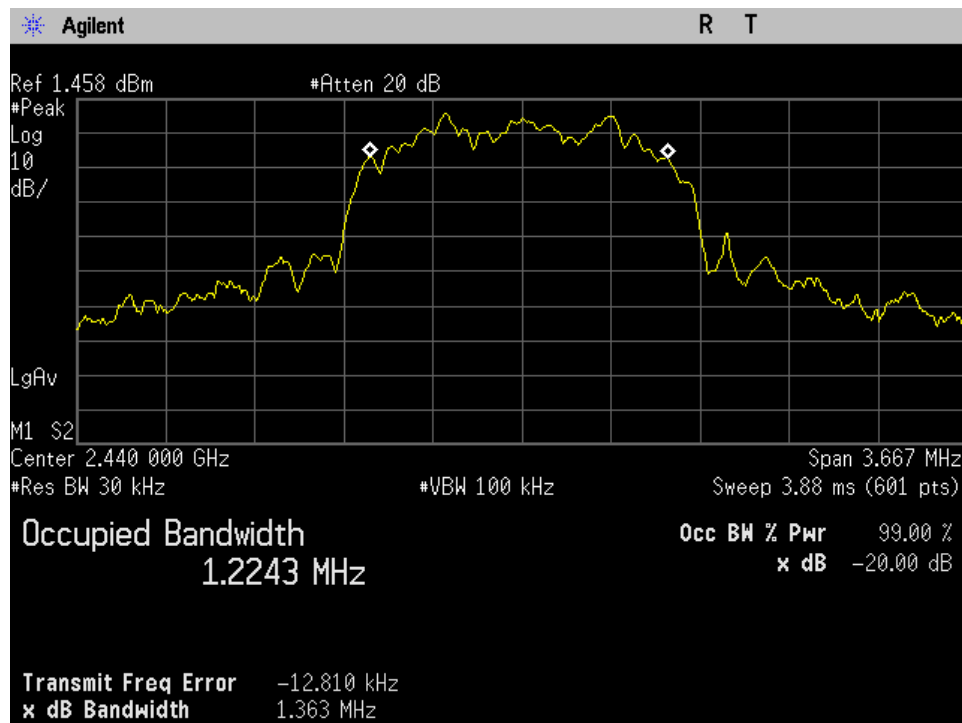
Low Ch_2402MHz_8DPSK_Occupied Bandwidth_-20dB_Port 1



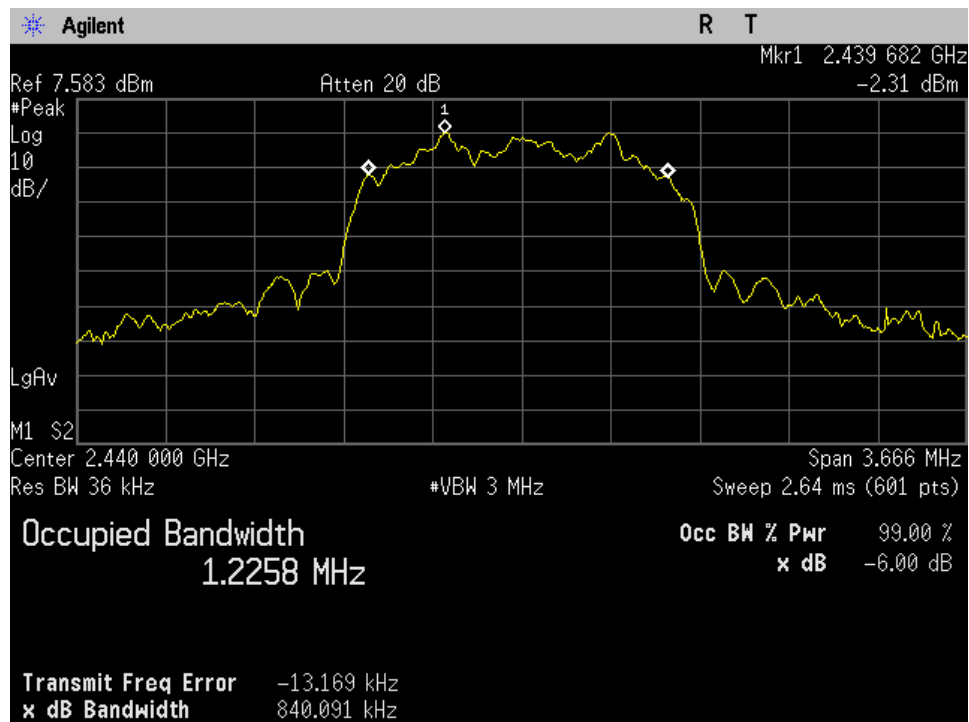
Low Ch_2402MHz_8DPSK_Occupied Bandwidth_99 percent_Port 1



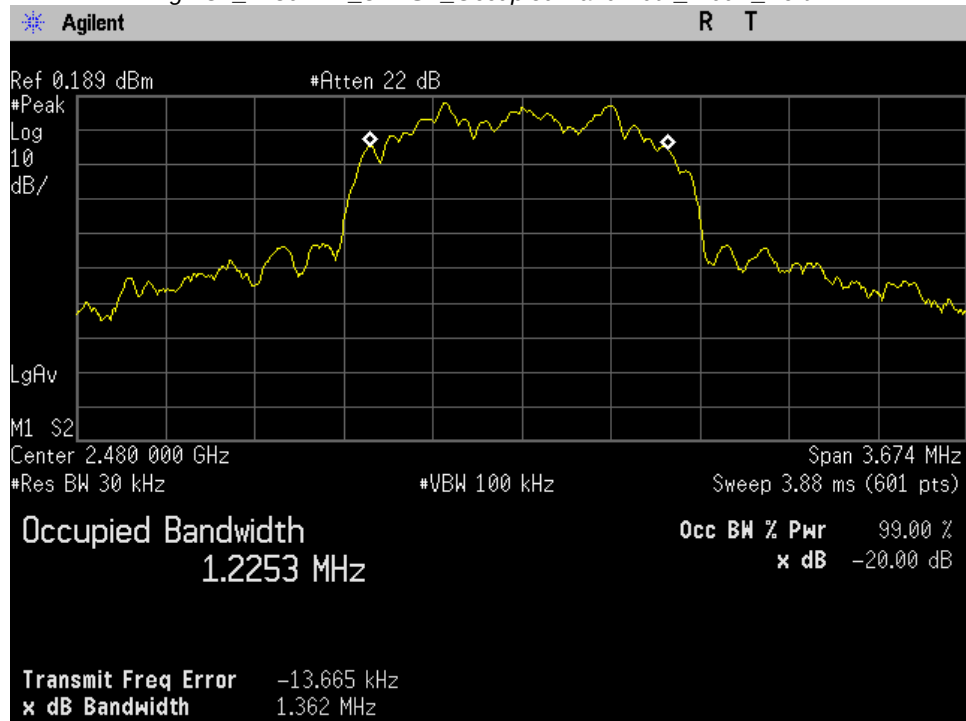
Mid Ch_2440MHz_8DPSK_Occupied Bandwidth_-20dB_Port 1



Mid Ch_2440MHz_8DPSK_Occupied Bandwidth_99 percent_Port 1



High Ch_2480MHz_8DPSK_Occupied Bandwidth_-20dB_Port 1



High Ch_2480MHz_8DPSK_Occupied Bandwidth_99 percent_Port 1

