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Appendix for 2A_N41A

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	21.59	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	22.53	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.44	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.78	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	20.51	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Inner_Full	21.46	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.53	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.23	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	22.15	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	23.18	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.04	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.45	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	21.21	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Inner_Full	22.22	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.91	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	21.32	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	22.86	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	23.95	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.95	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.84	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	21.95	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Inner_Full	22.85	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.23	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.16	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	21.44	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	22.36	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.26	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.12	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	20.41	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Inner_Full	21.42	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.08	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.21	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	22.19	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	23.09	PC2	PASS

DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.04	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.64	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	21.21	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Inner_Full	22.23	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.19	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	21.44	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	22.94	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	23.96	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.07	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.94	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	22.01	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Inner_Full	22.86	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.07	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.01	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	21.22	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	22.37	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.32	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.45	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	20.34	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Inner_Full	21.32	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.05	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.31	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	22.16	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	23.16	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.05	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.65	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	21.22	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Inner_Full	22.30	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	21.24	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	21.76	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	22.97	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	23.94	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.02	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.99	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	21.97	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Inner_Full	21.98	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.39	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	21.92	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	21.64	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Inner_Full	22.62	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	21.42	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	21.65	PC2	PASS

DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	20.57	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Inner_Full	21.41	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	20.32	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	20.62	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	21.90	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Inner_Full	21.90	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	21.36	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	21.15	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	20.96	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Inner_Full	21.98	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.13	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.89	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	22.75	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Inner_Full	23.81	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.64	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.58	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	21.77	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Inner_Full	22.86	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.88	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	21.64	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	21.12	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	22.31	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	20.71	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	20.97	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	20.31	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Inner_Full	21.30	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.49	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.92	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	21.91	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	23.06	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.14	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	20.87	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	20.91	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	22.01	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.22	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.71	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	22.61	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	23.75	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.47	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.27	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	21.68	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	22.75	PC2	PASS

DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.87	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.65	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	21.44	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Inner_Full	22.22	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	21.29	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	21.41	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	20.33	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Inner_Full	21.30	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	20.08	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	20.29	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	22.06	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Inner_Full	22.99	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	21.50	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.32	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	21.11	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Inner_Full	22.08	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	20.55	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	21.25	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	22.66	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Inner_Full	22.45	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	22.53	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.63	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	22.35	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Inner_Full	22.67	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	21.85	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	21.57	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	21.42	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Inner_Full	22.42	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	21.15	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.68	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	20.29	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Inner_Full	21.23	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	19.91	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.88	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	21.97	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Inner_Full	23.05	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.63	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.51	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	21.01	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Inner_Full	22.03	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.33	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.48	PC2	PASS

DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	22.79	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Inner_Full	23.77	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.52	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.81	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	21.73	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Inner_Full	22.77	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.67	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.66	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	21.55	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Inner_Full	22.47	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	21.30	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.93	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	20.48	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Inner_Full	21.38	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	20.11	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.83	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	22.16	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Inner_Full	23.17	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.47	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.71	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	21.02	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Inner_Full	22.15	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.58	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.05	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	22.80	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Inner_Full	23.68	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.37	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.69	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	21.79	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Inner_Full	22.73	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	21.37	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.70	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	21.57	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	22.62	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.01	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.76	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	20.06	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	21.46	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	20.31	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.49	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	22.10	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	21.86	PC2	PASS

DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.23	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.46	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	20.65	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	22.16	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.61	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.66	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	22.55	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	23.63	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.08	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.63	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	21.63	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	22.68	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	20.55	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.64	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

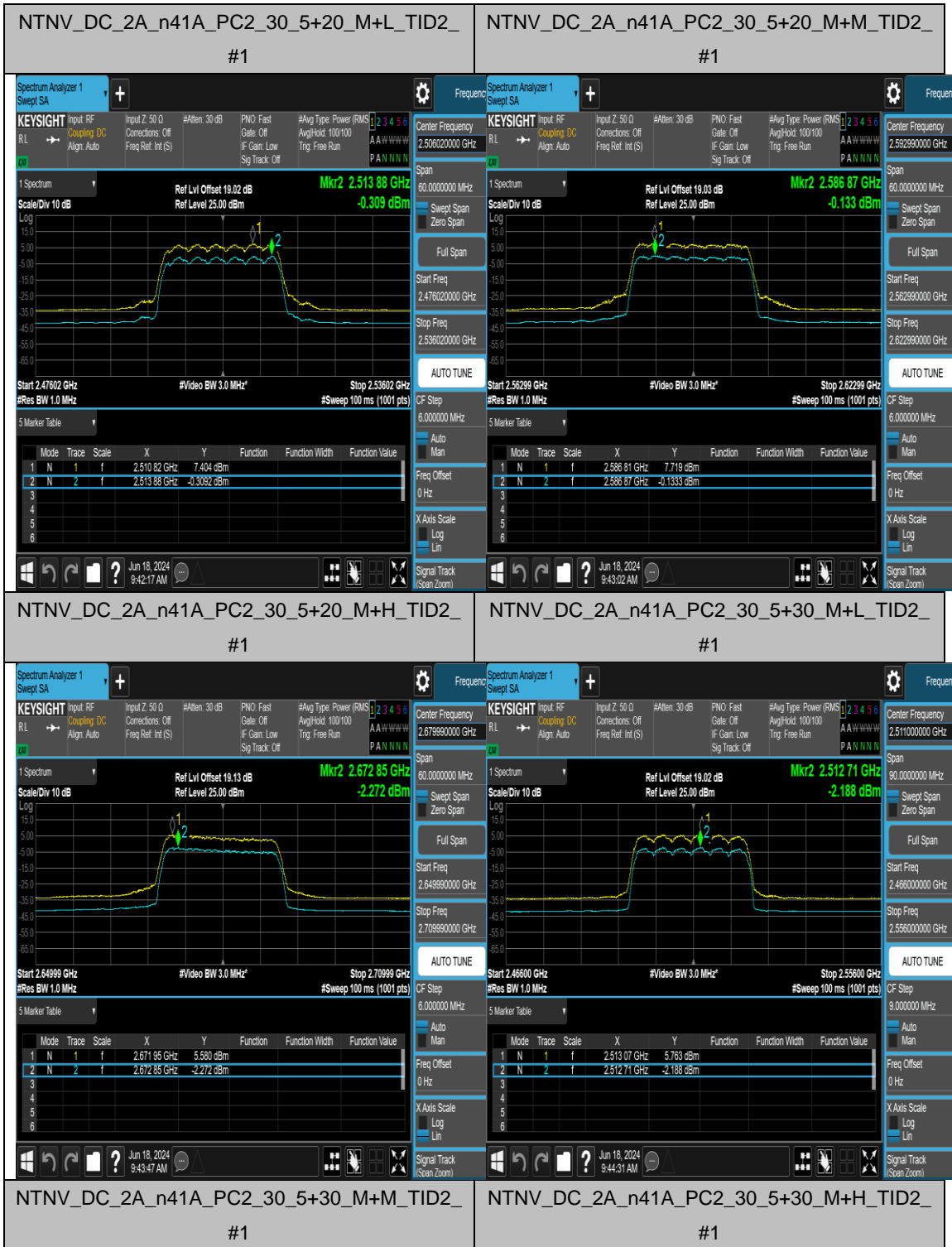
Peak-to-Average Ratio(PAPR)

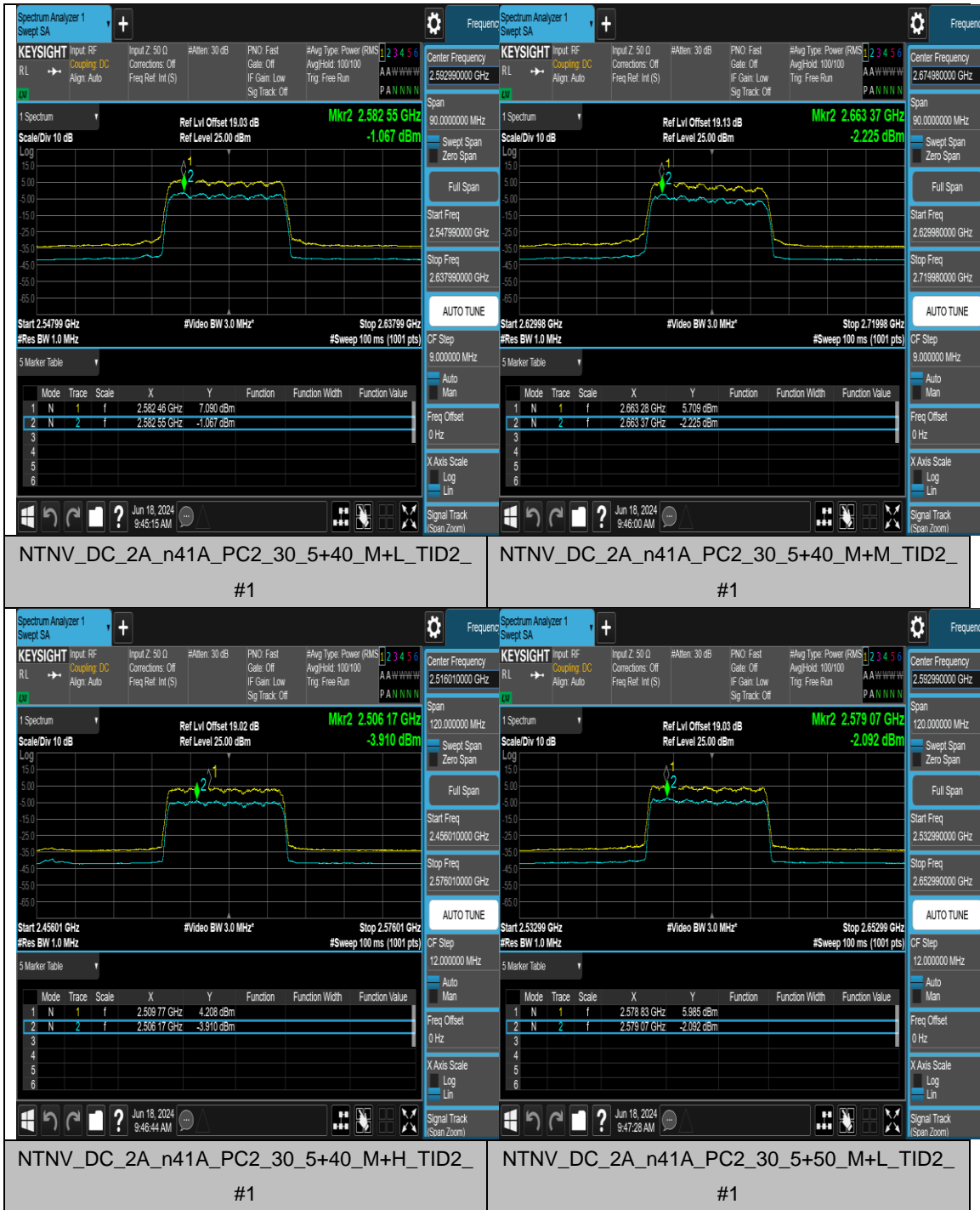
Test Result

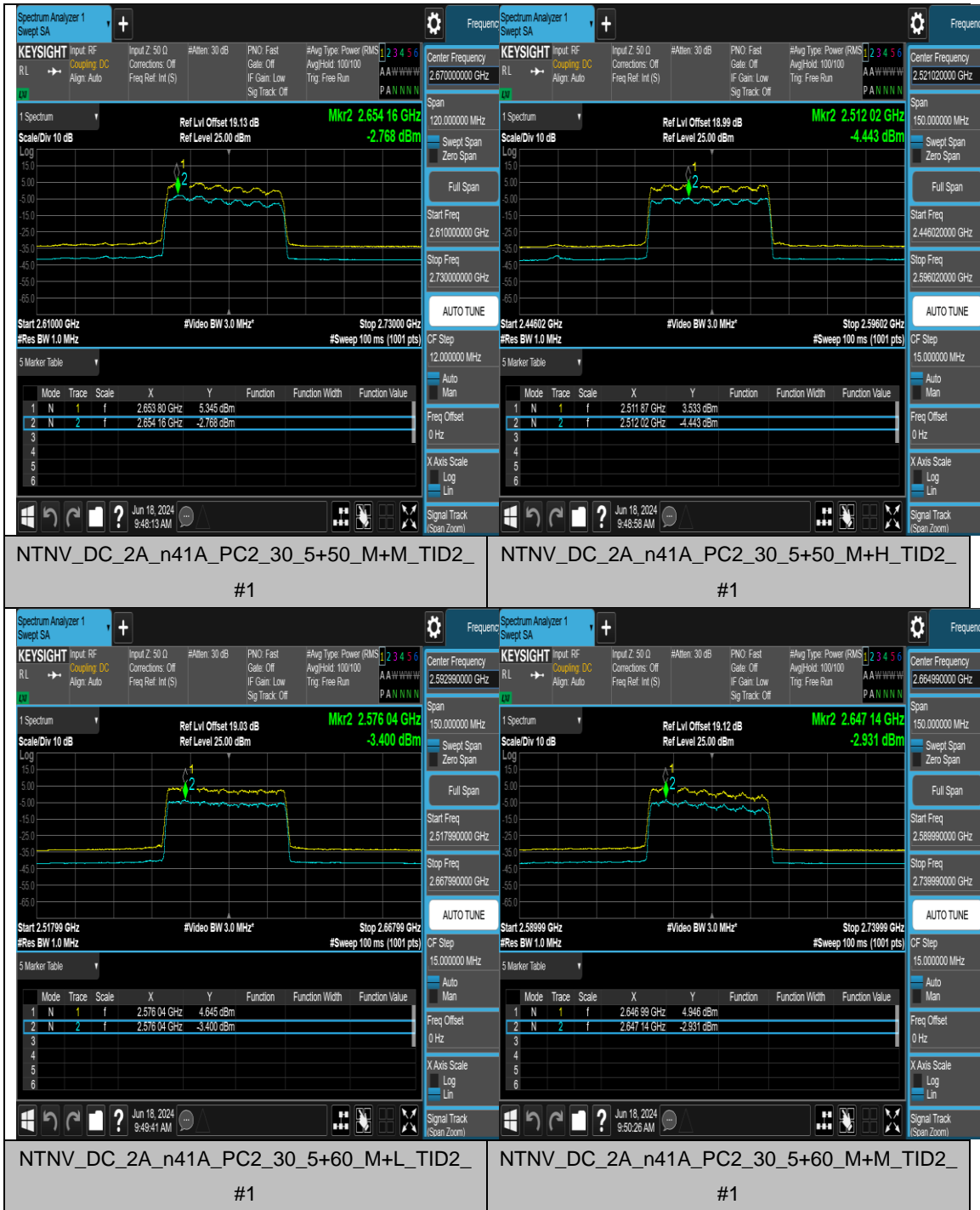
Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.63	≤13	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.87	≤13	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.08	≤13	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.86	≤13	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.00	≤13	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.89	≤13	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.97	≤13	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.80	≤13	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.94	≤13	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.08	≤13	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.10	≤13	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.88	≤13	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.95	≤13	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	0.91	≤13	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.16	≤13	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.37	≤13	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.62	≤13	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.92	≤13	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.96	≤13	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.20	≤13	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.86	≤13	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.08	≤13	PASS

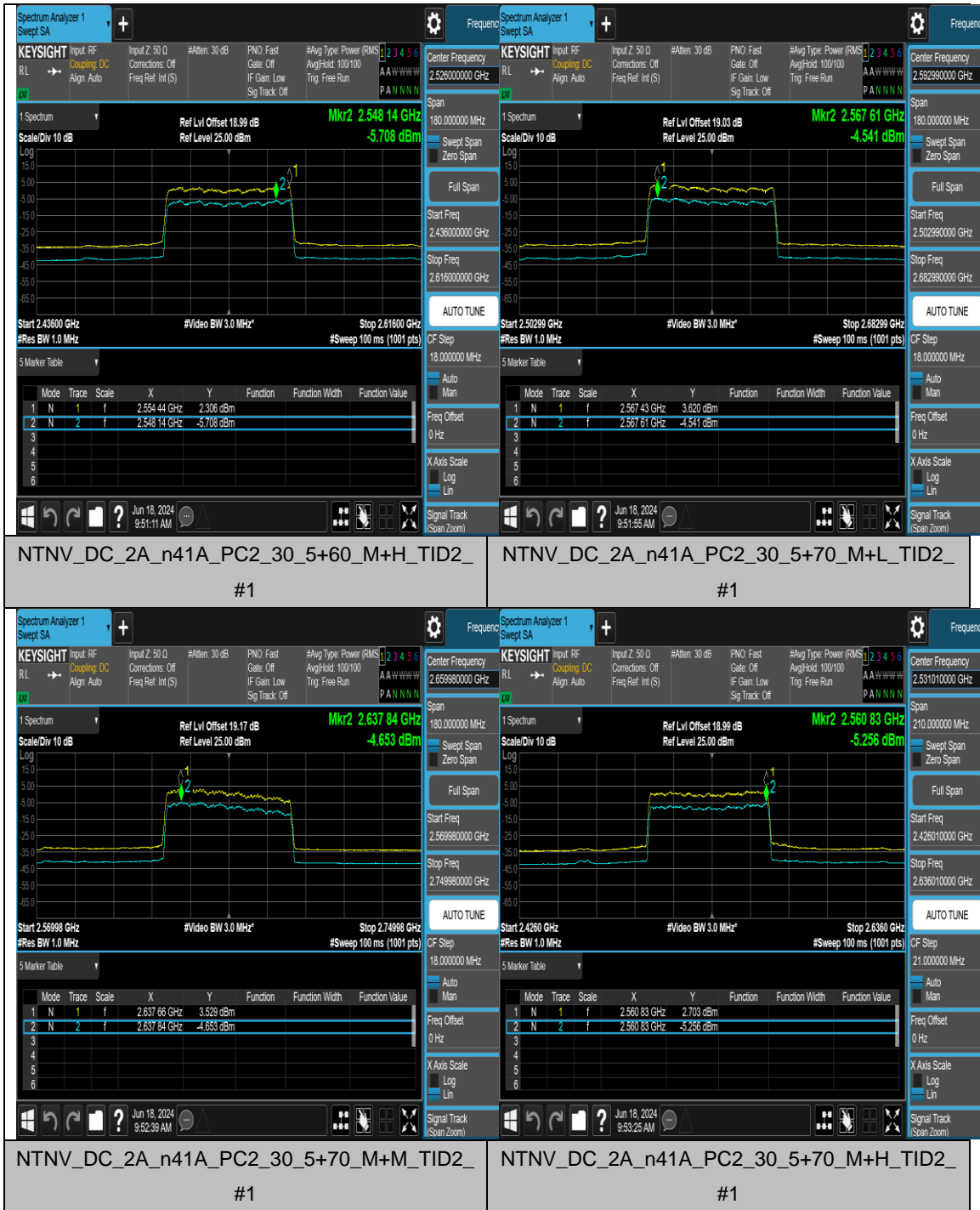
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.04	≤13	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.51	≤13	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.93	≤13	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.13	≤13	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.89	≤13	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.27	≤13	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	0.77	≤13	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	0.92	≤13	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	0.96	≤13	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.11	≤13	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.30	≤13	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.30	≤13	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.10	≤13	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.41	≤13	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.17	≤13	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.14	≤13	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.07	≤13	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	19.6%	7.08	1.01	≤13	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	19.6%	7.08	1.10	≤13	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	0.92	≤13	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.23	≤13	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	0.87	≤13	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.26	≤13	PASS

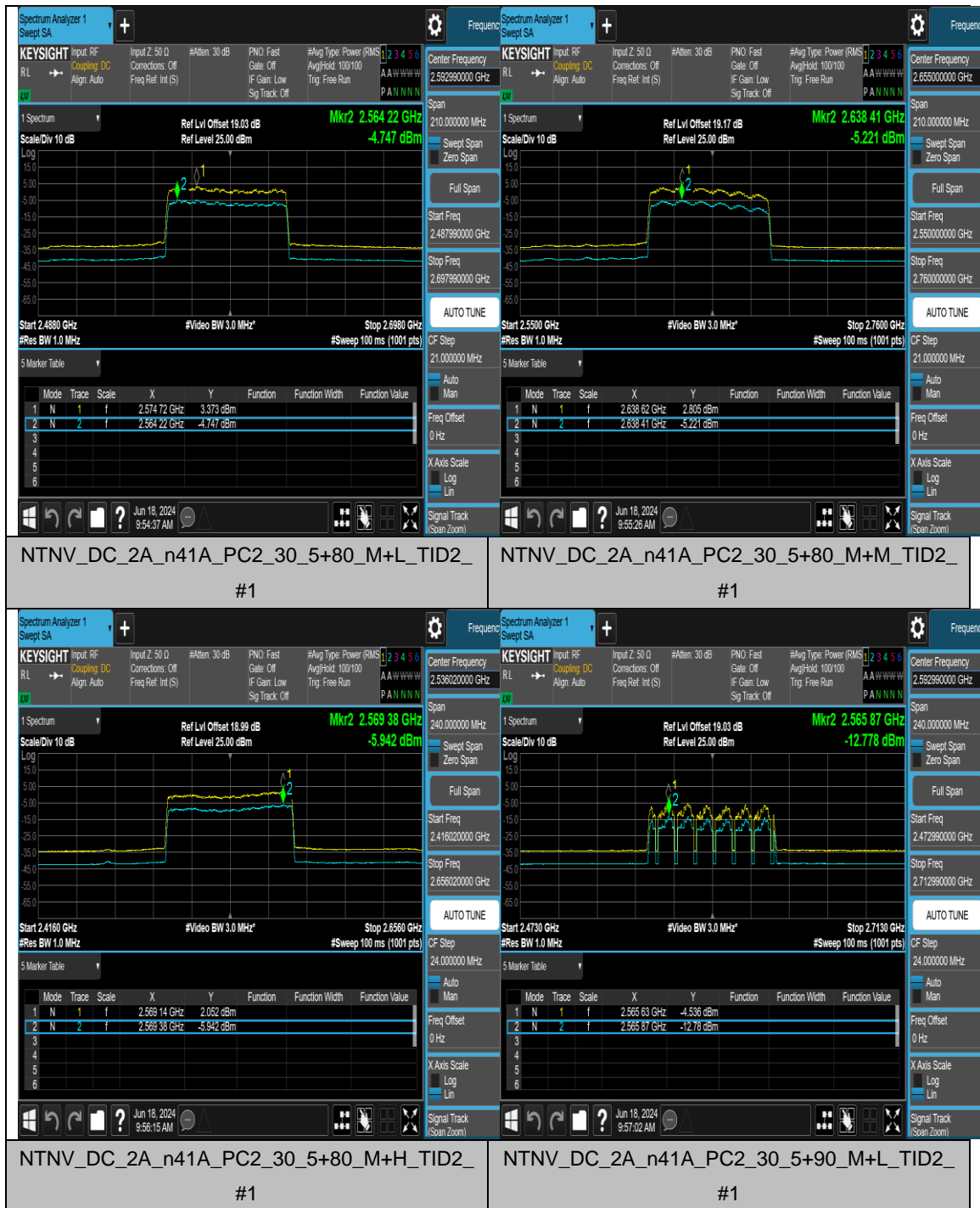
Test Graphs

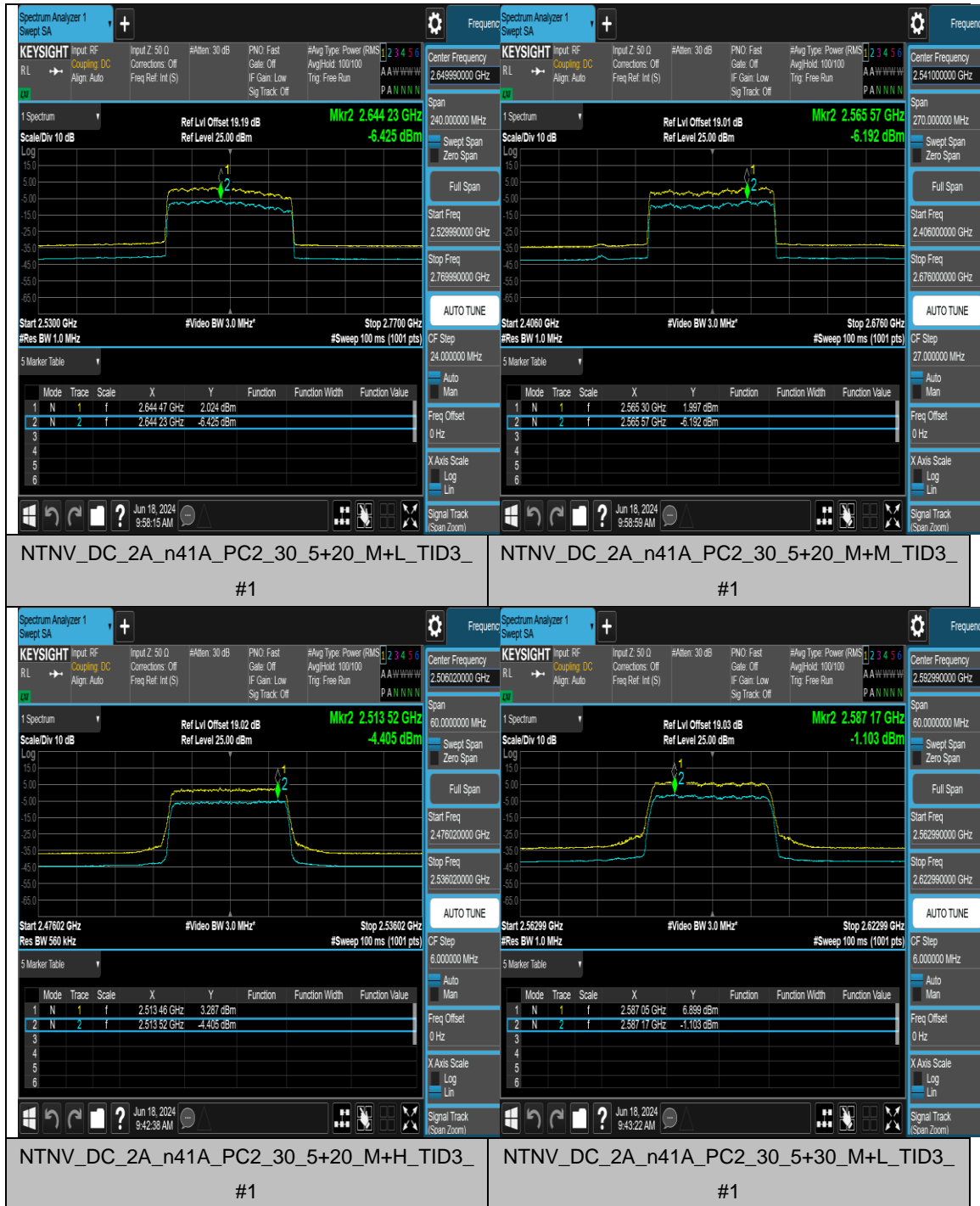


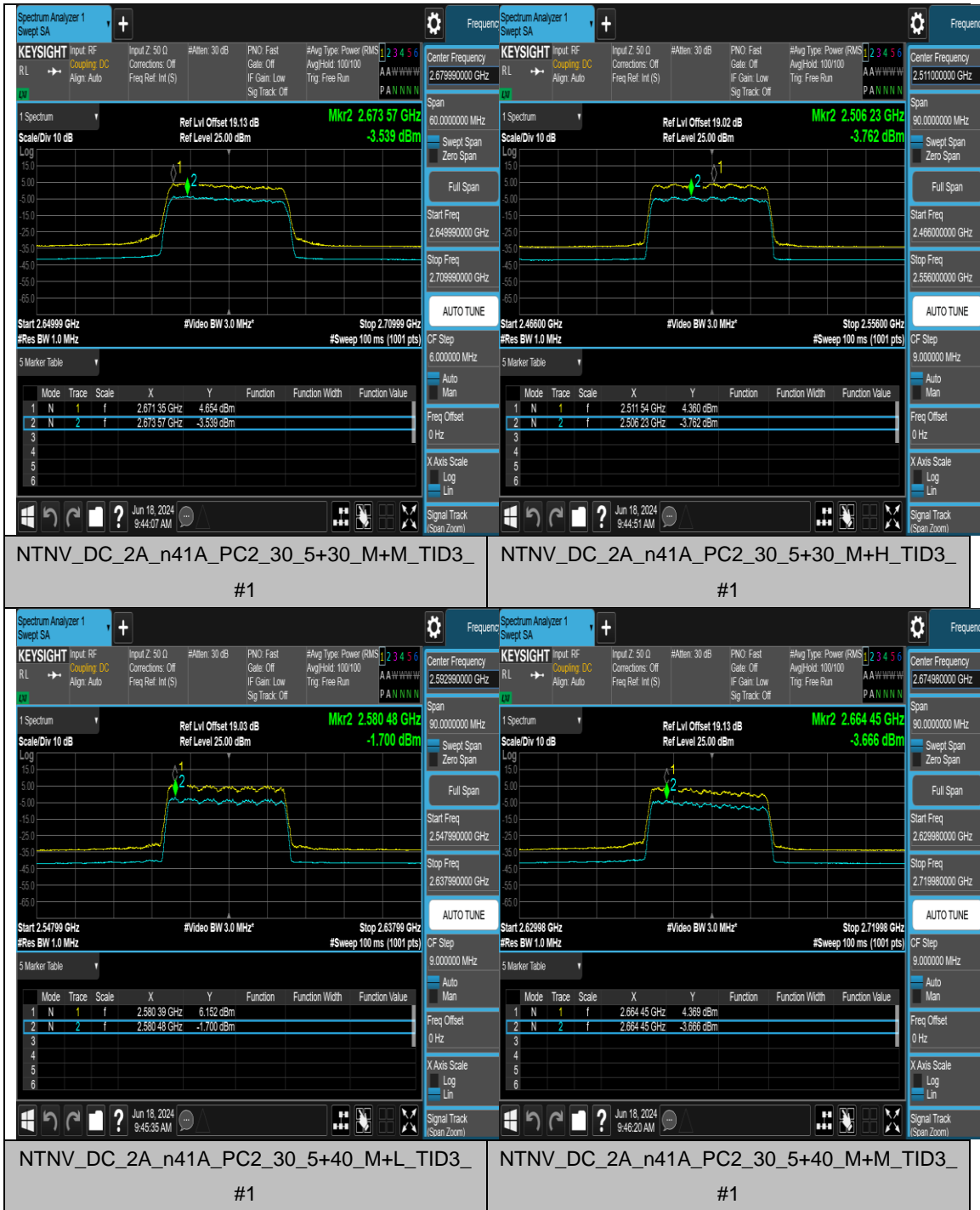


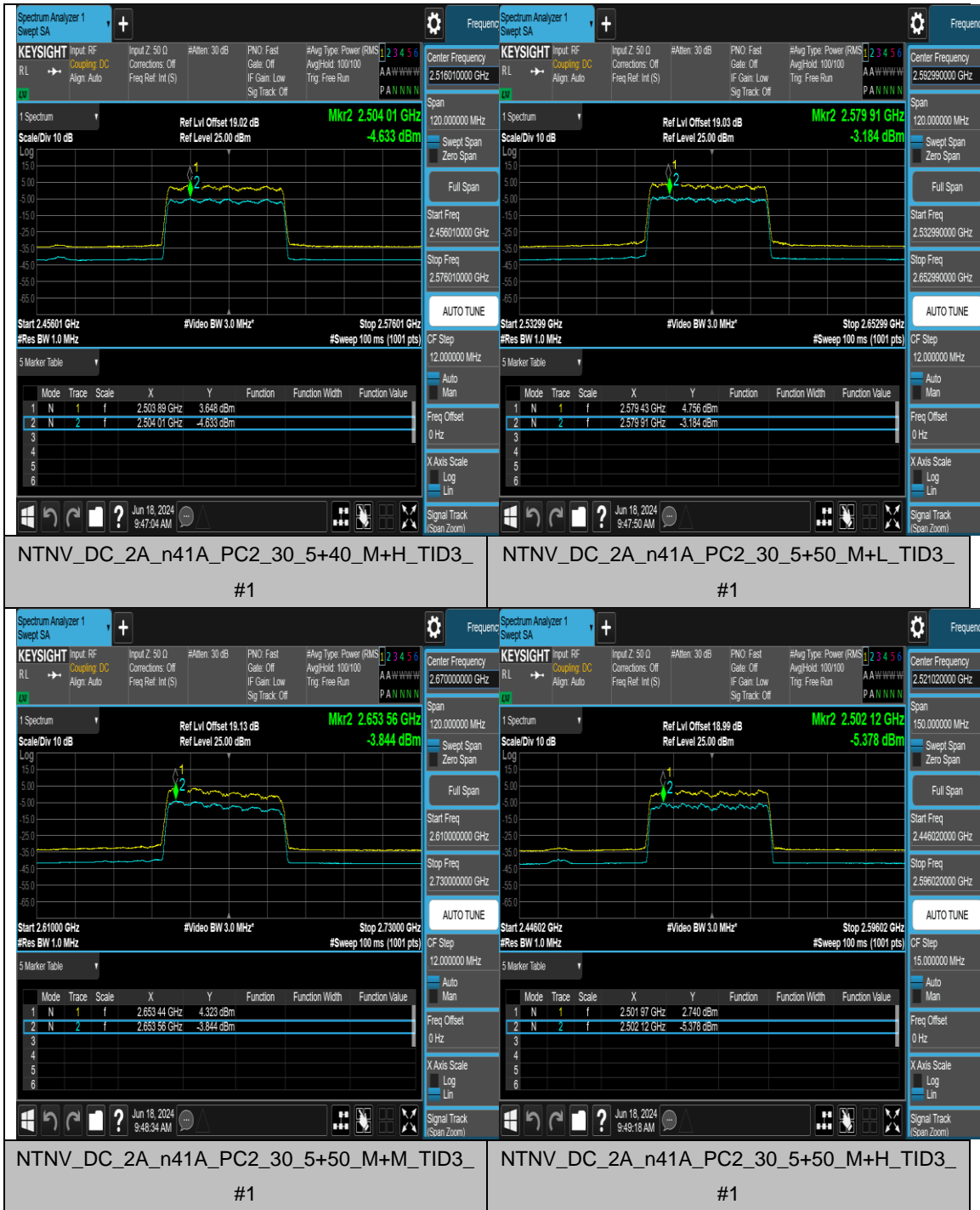


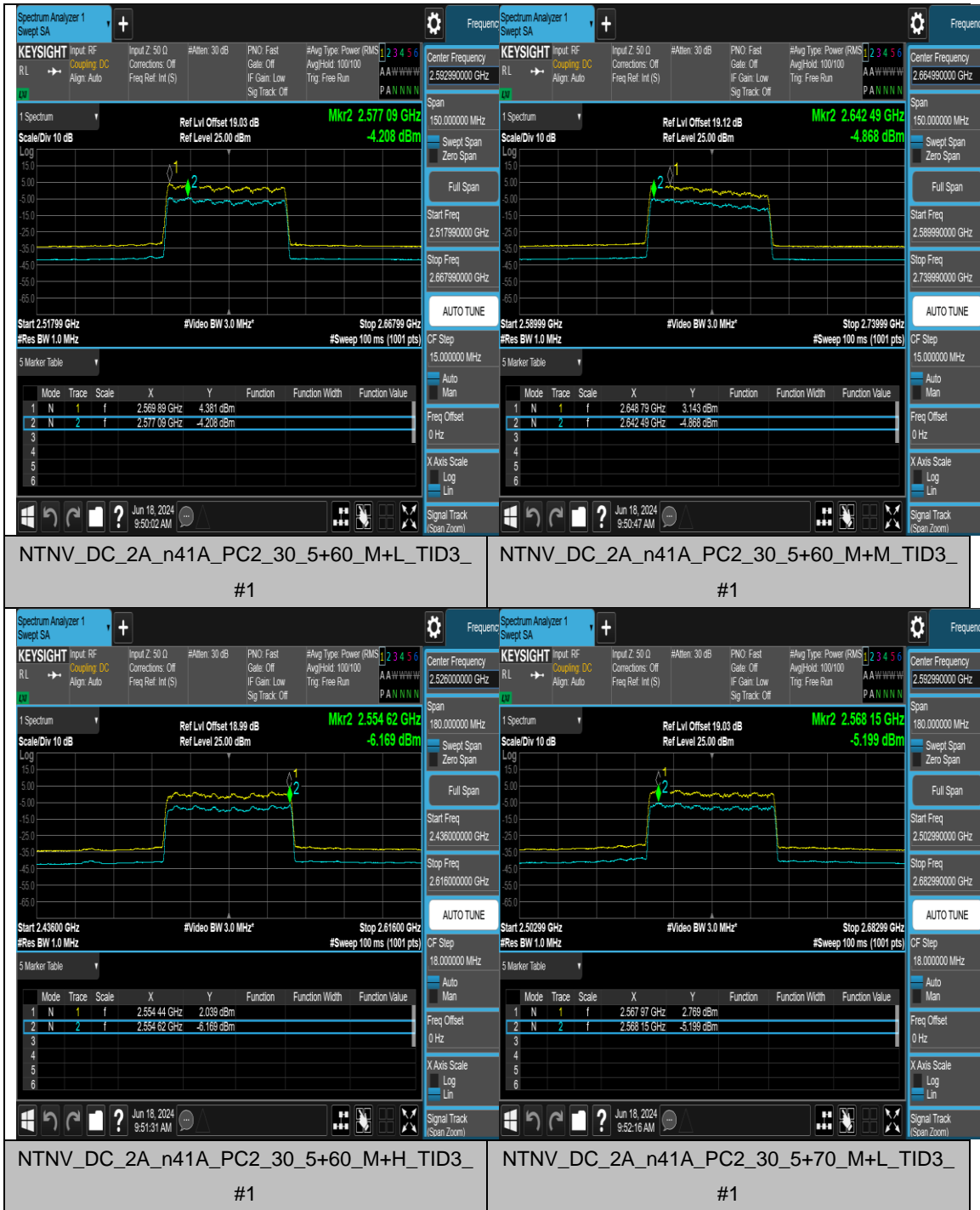


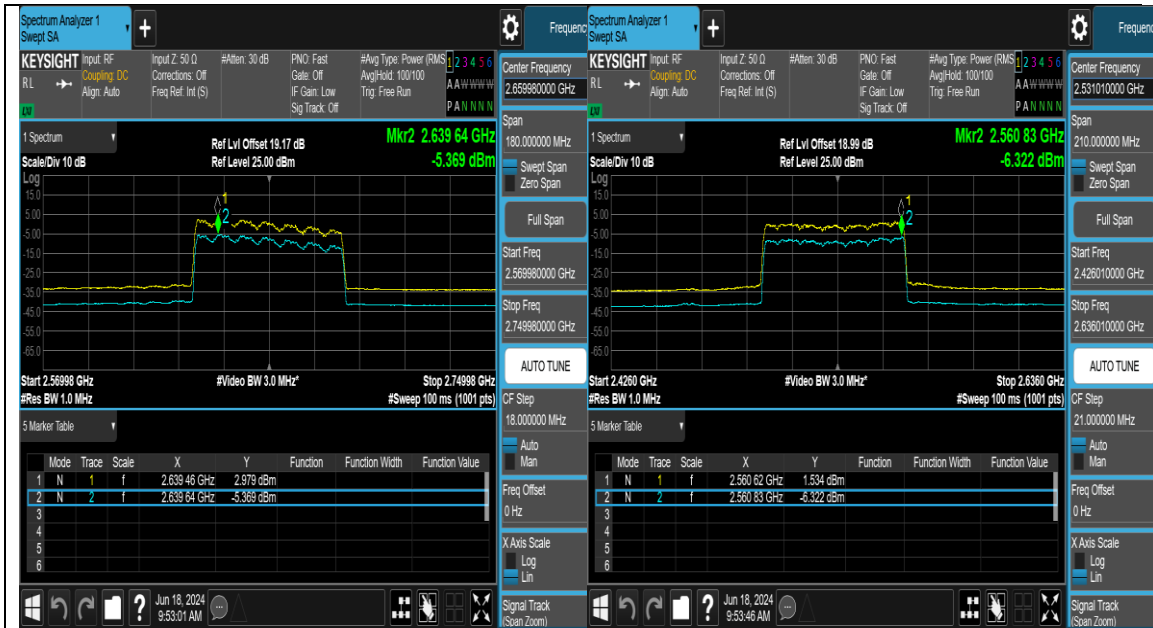










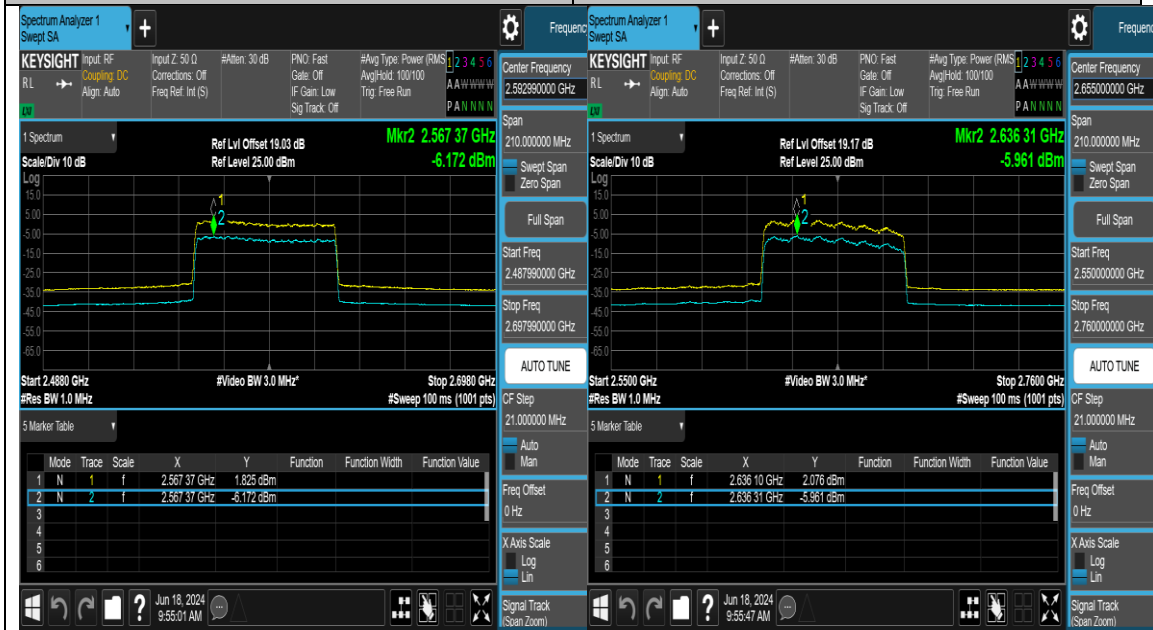


NTNV_DC_2A_n41A_PC2_30_5+70_M+M_TID3_

NTNV_DC_2A_n41A_PC2_30_5+70_M+H_TID3_

#1

#1

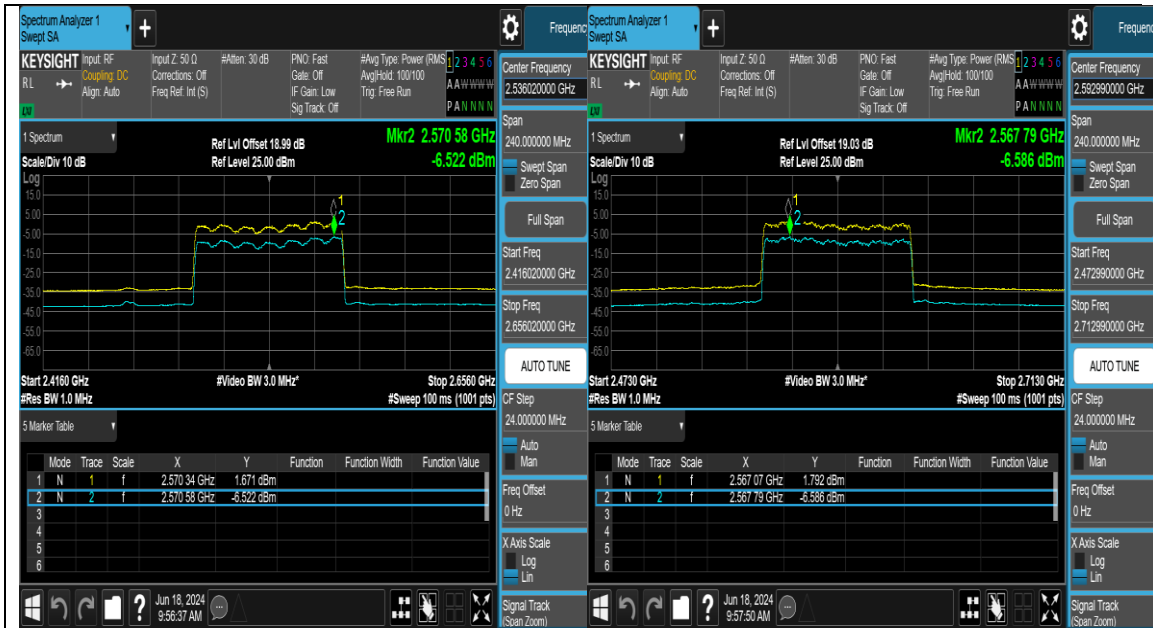


NTNV_DC_2A_n41A_PC2_30_5+80_M+L_TID3_

NTNV_DC_2A_n41A_PC2_30_5+80_M+M_TID3_

#1

#1

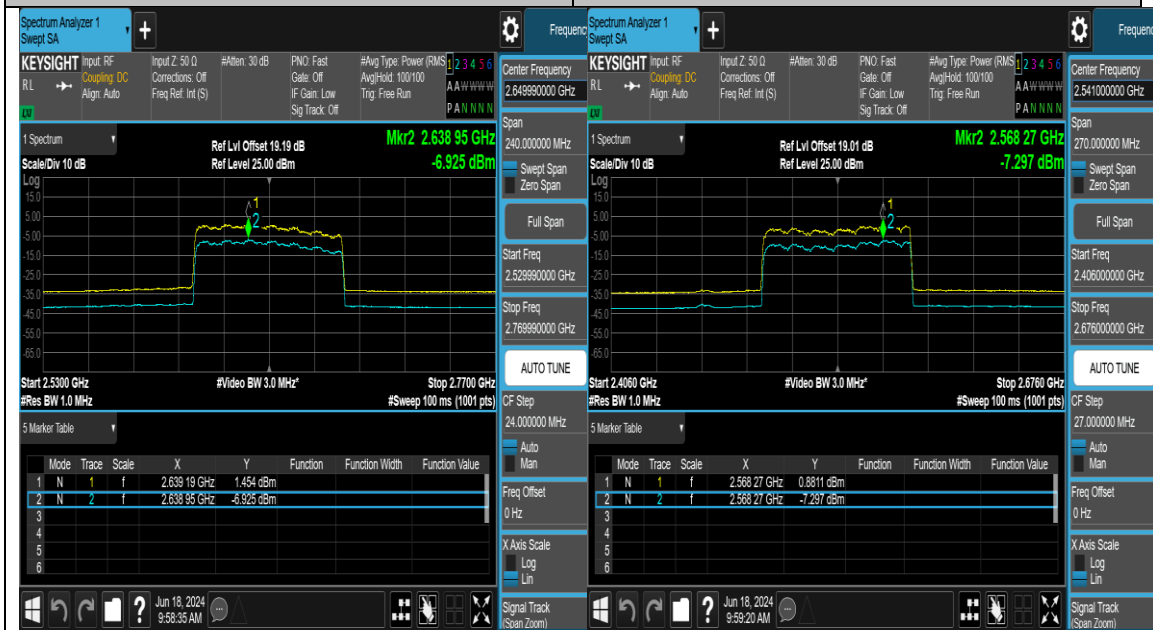


NTNV_DC_2A_n41A_PC2_30_5+80_M+H_TID3_

NTNV_DC_2A_n41A_PC2_30_5+90_M+L_TID3_

#1

#1



NTNV_DC_2A_n41A_PC2_30_5+90_M+M_TID2_

NTNV_DC_2A_n41A_PC2_30_5+90_M+M_TID3_

#1

#1

