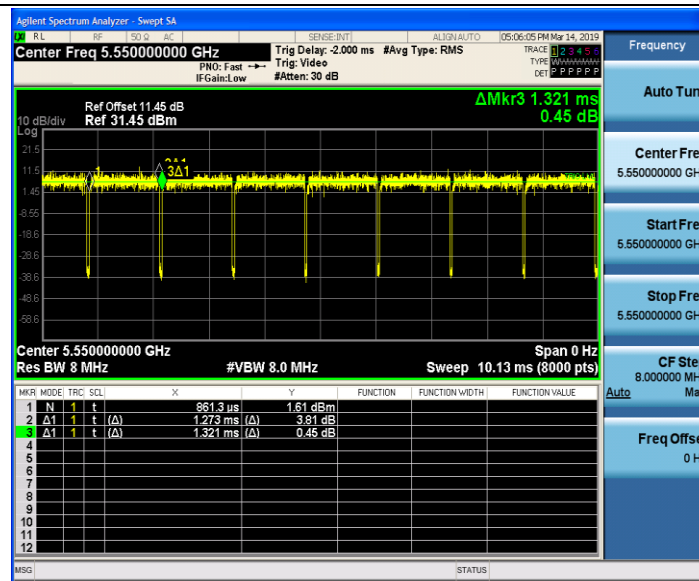
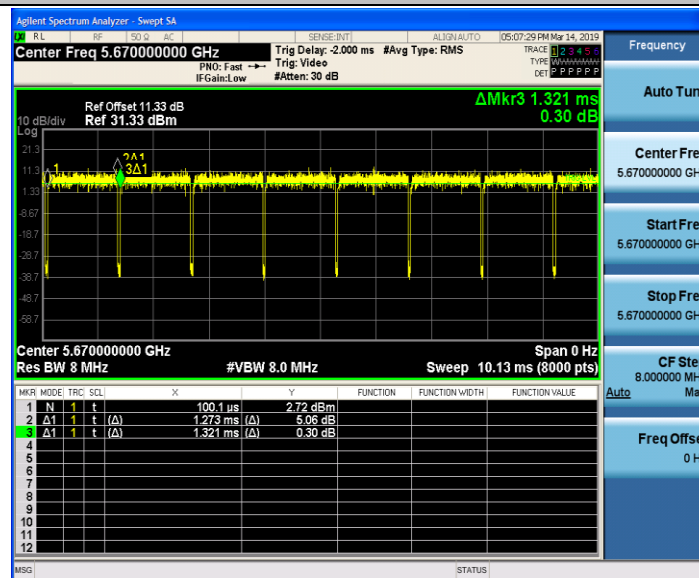
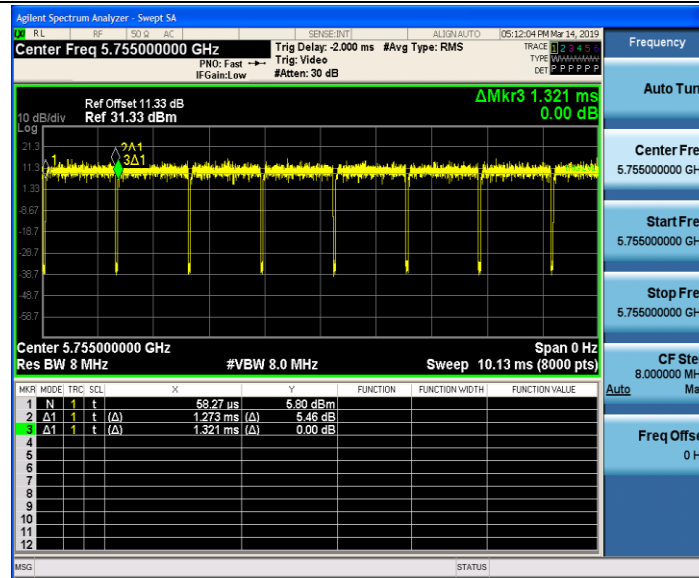




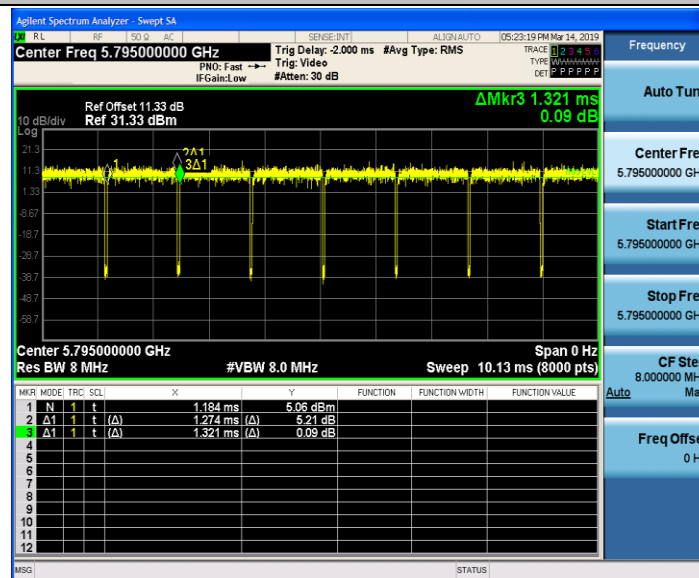
11AC40SISO_Ant1_5670



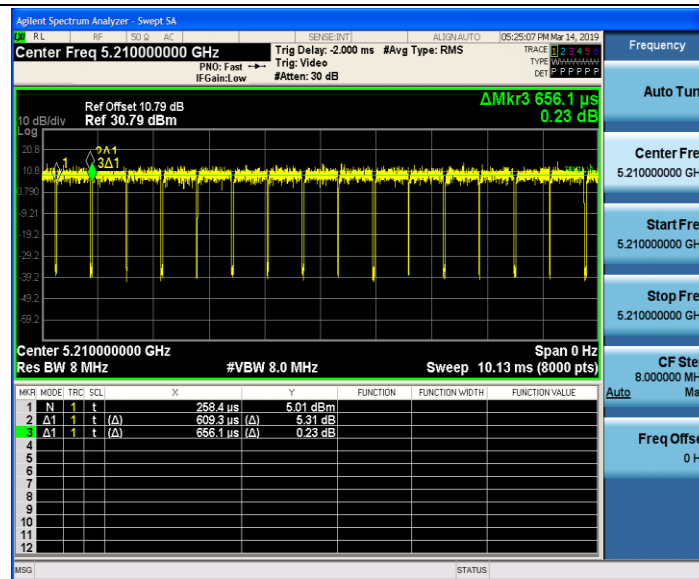
11AC40SISO_Ant1_5755



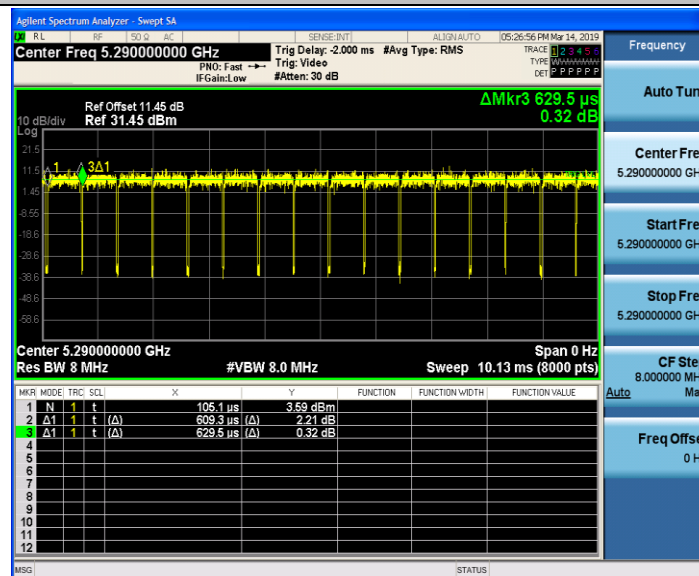
11AC40SISO_Ant1_5795



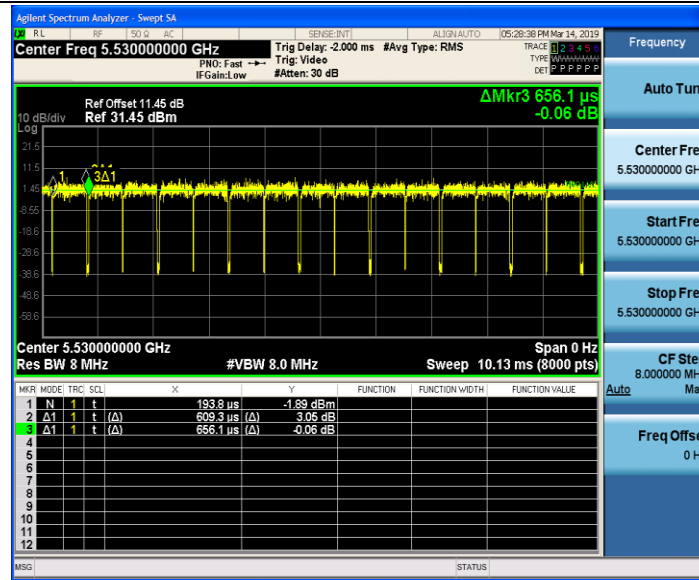
11AC80SISO_Ant1_5210



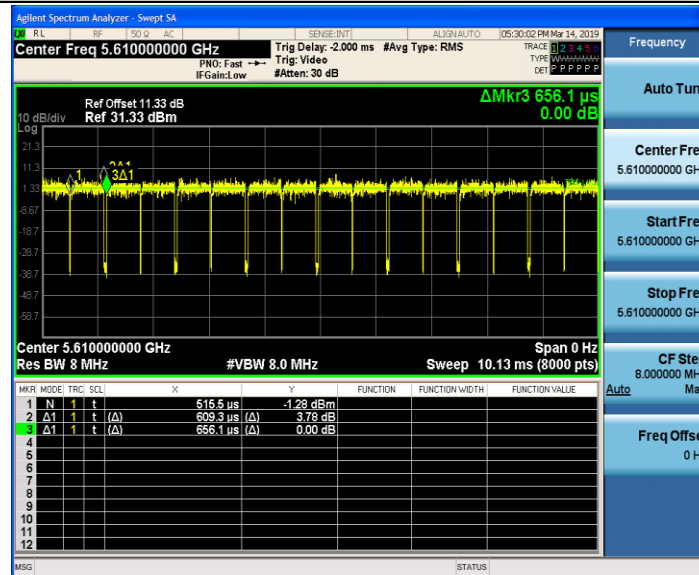
11AC80SISO_Ant1_5290



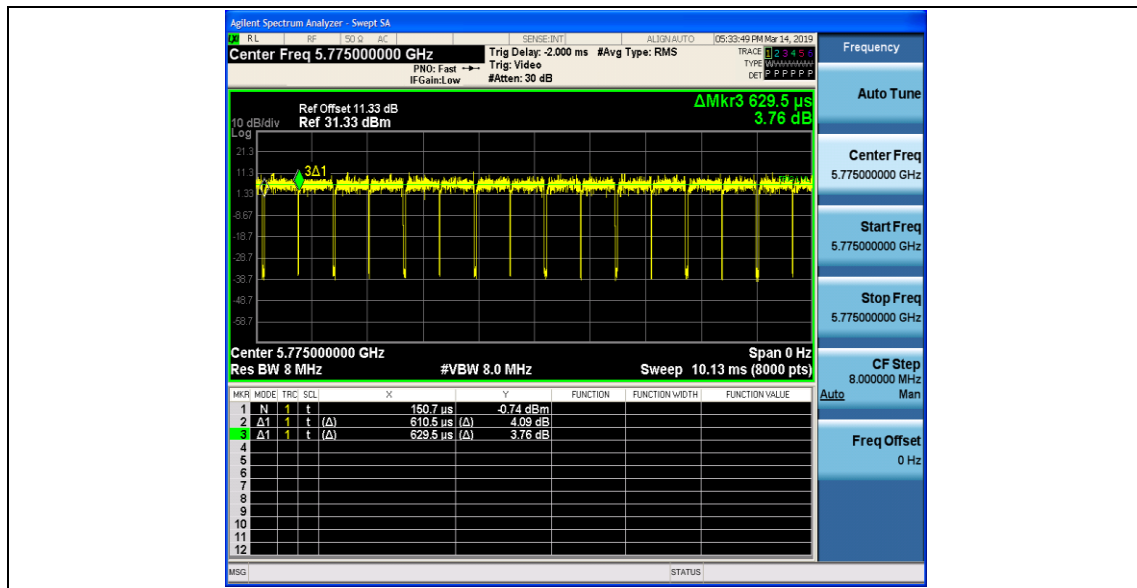
11AC80SISO_Ant1_5530



11AC80SISO_Ant1_5610



11AC80SISO_Ant1_5775





Appendix D: Maximum Average conducted output power

Test Result

TestMode	Antenna	Channel	Conducted Result[dBm]	Conducted Limit[dBm]	Verdict
11A	Ant1	5180	17.32	24	PASS
		5200	17.19	24	PASS
		5240	16.76	24	PASS
		5260	16.36	23.90	PASS
		5280	16.33	23.88	PASS
		5320	16.65	23.86	PASS
		5500	10.66	23.90	PASS
		5580	11.47	23.89	PASS
		5700	11.55	23.89	PASS
		5745	15.65	30	PASS
		5785	15.49	30	PASS
		5825	15.10	30	PASS
11N20SISO	Ant1	5180	16.71	24	PASS
		5200	16.74	24	PASS
		5240	16.35	24	PASS
		5260	15.73	24	PASS
		5280	15.82	24	PASS
		5320	16.18	24	PASS
		5500	11.30	24	PASS
		5580	12.06	24	PASS
		5700	12.38	24	PASS
		5745	15.32	30	PASS
		5785	15.23	30	PASS
		5825	14.85	30	PASS
11N40SISO	Ant1	5190	14.76	24	PASS
		5230	14.52	24	PASS
		5270	12.58	24	PASS
		5310	13.13	24	PASS
		5510	8.15	24	PASS
		5550	8.58	24	PASS
		5670	9.03	24	PASS
		5755	12.00	30	PASS
		5795	11.97	30	PASS



11AC20SISO	Ant1	5180	16.69	24	PASS
		5200	16.69	24	PASS
		5240	16.36	24	PASS
		5260	15.71	23.97	PASS
		5280	15.79	23.98	PASS
		5320	16.17	23.98	PASS
		5500	11.33	23.98	PASS
		5580	12.08	24	PASS
		5700	12.42	24	PASS
		5745	15.32	30	PASS
		5785	15.23	30	PASS
		5825	14.86	30	PASS
11AC40SISO	Ant1	5190	14.75	24	PASS
		5230	14.53	24	PASS
		5270	12.57	24	PASS
		5310	13.13	24	PASS
		5510	8.18	24	PASS
		5550	8.58	24	PASS
		5670	9.05	24	PASS
		5755	12.01	30	PASS
		5795	11.92	30	PASS
11AC80SISO	Ant1	5210	14.60	24	PASS
		5290	12.86	24	PASS
		5530	7.02	24	PASS
		5610	7.70	24	PASS
		5775	11.59	30	PASS

Note: The Duty Cycle Factor is compensated in the graph.



Appendix E: Peak Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	7.11	11	PASS
		5200	6.8	11	PASS
		5240	5.97	11	PASS
		5260	5.41	11	PASS
		5280	5.19	11	PASS
		5320	5.79	11	PASS
		5500	0.03	11	PASS
		5580	0.5	11	PASS
		5700	0.65	11	PASS
		5745	2.63	30	PASS
		5785	2.96	30	PASS
		5825	2.16	30	PASS
11N20SISO	Ant1	5180	6.13	11	PASS
		5200	6.16	11	PASS
		5240	5.65	11	PASS
		5260	4.75	11	PASS
		5280	4.63	11	PASS
		5320	5.07	11	PASS
		5500	0.42	11	PASS
		5580	0.86	11	PASS
		5700	1.38	11	PASS
		5745	2.55	30	PASS
		5785	2.4	30	PASS
		5825	2.17	30	PASS
11N40SISO	Ant1	5190	1.21	11	PASS
		5230	1.1	11	PASS
		5270	-1.41	11	PASS
		5310	-0.63	11	PASS
		5510	-5.24	11	PASS
		5550	-5	11	PASS
		5670	-4.84	11	PASS

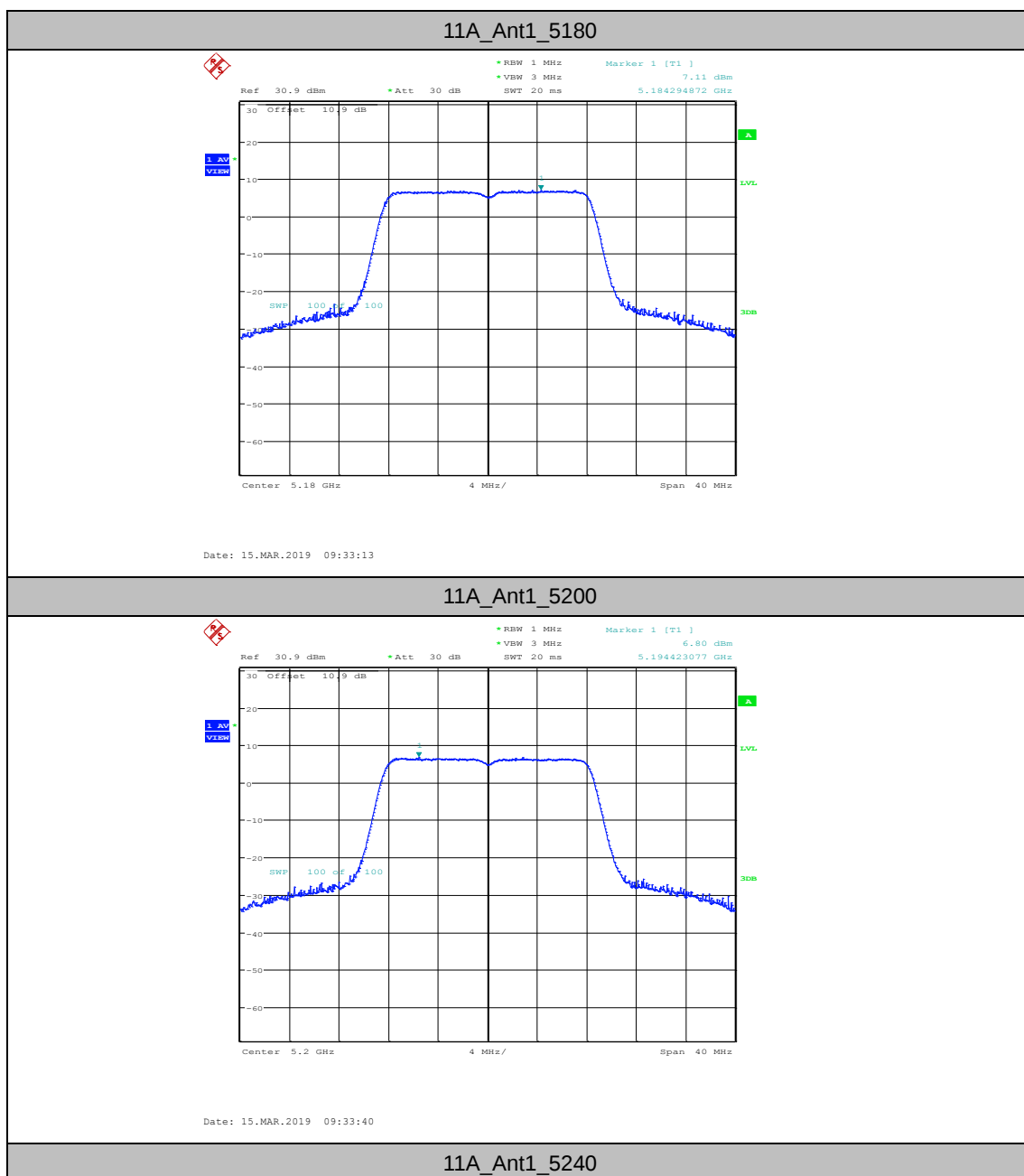


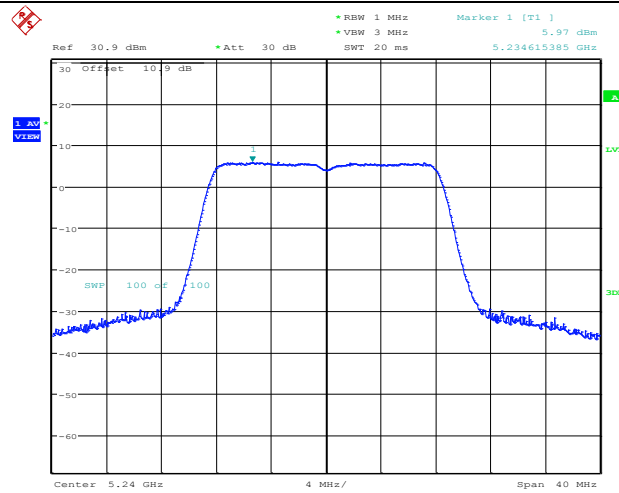
		5755	-3.54	30	PASS
		5795	-3.37	30	PASS
11AC20SISO	Ant1	5180	5.99	11	PASS
		5200	6.08	11	PASS
		5240	5.64	11	PASS
		5260	4.71	11	PASS
		5280	4.63	11	PASS
		5320	4.9	11	PASS
		5500	0.48	11	PASS
		5580	0.74	11	PASS
		5700	1.38	11	PASS
		5745	2.57	30	PASS
		5785	2.28	30	PASS
		5825	1.78	30	PASS
11AC40SISO	Ant1	5190	1.36	11	PASS
		5230	0.99	11	PASS
		5270	-1.34	11	PASS
		5310	-0.77	11	PASS
		5510	-5.35	11	PASS
		5550	-5.22	11	PASS
		5670	-4.72	11	PASS
		5755	-3.63	30	PASS
		5795	-3.61	30	PASS
11AC80SISO	Ant1	5210	-1.87	11	PASS
		5290	-3.98	11	PASS
		5530	-9.53	11	PASS
		5610	-9.28	11	PASS
		5775	-7.12	30	PASS

Note: The Duty Cycle Factor and RBW Factor (5725-5850) is compensated in the graph.



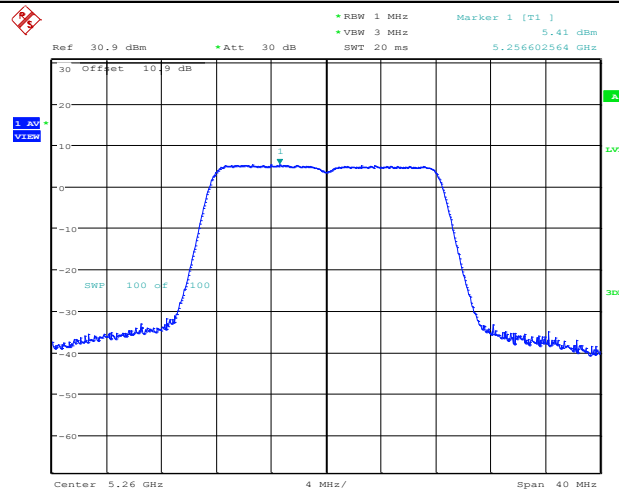
Test Graphs





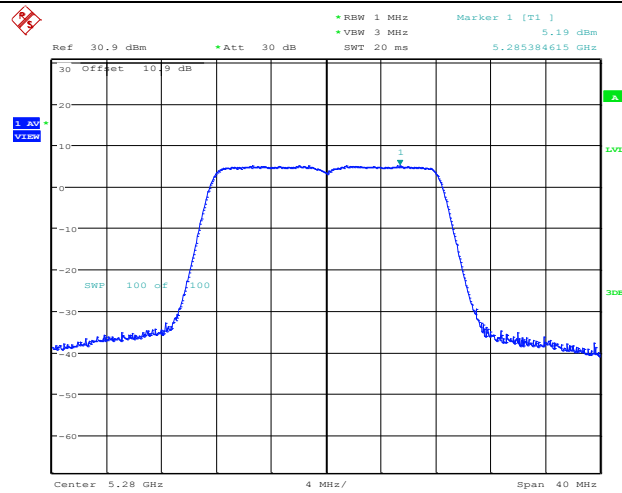
Date: 15.MAR.2019 09:34:37

11A_Ant1_5260



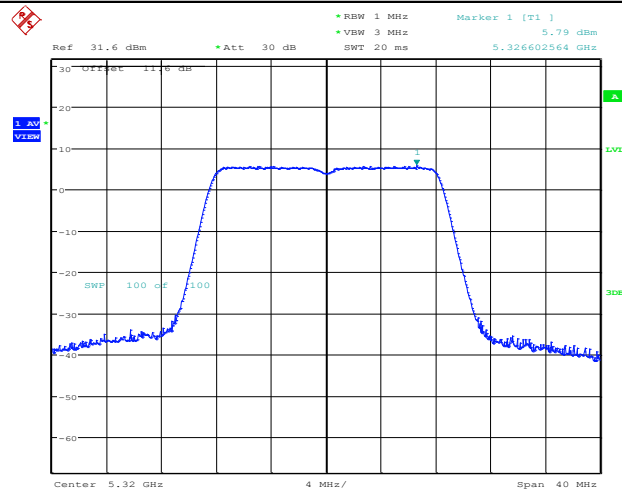
Date: 15.MAR.2019 09:35:06

11A_Ant1_5280



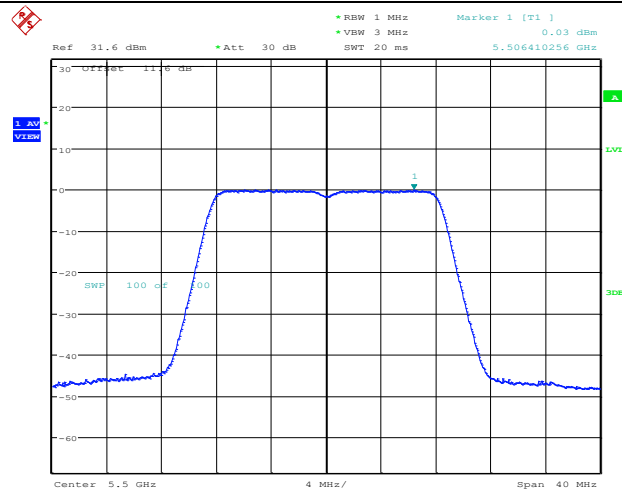
Date: 15.MAR.2019 09:35:36

11A_Ant1_5320



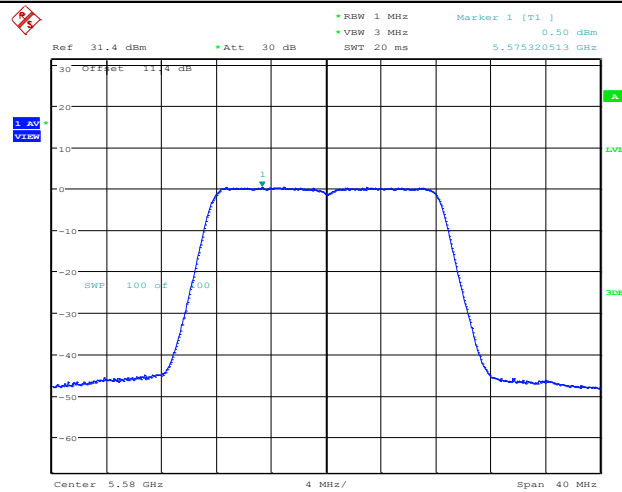
Date: 15.MAR.2019 09:35:55

11A_Ant1_5500



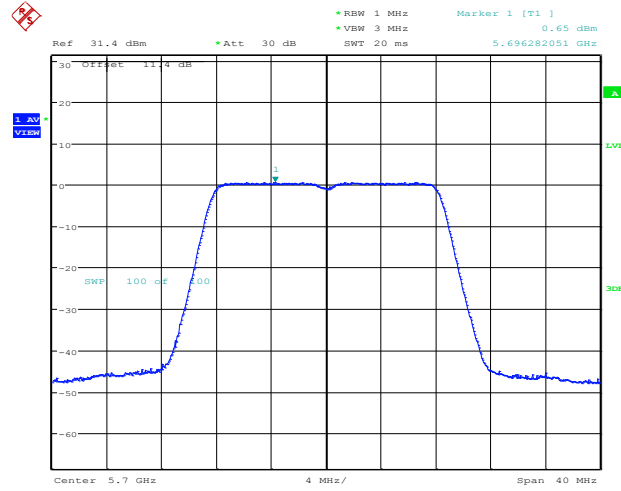
Date: 15.MAR.2019 09:36:16

11A_Ant1_5580



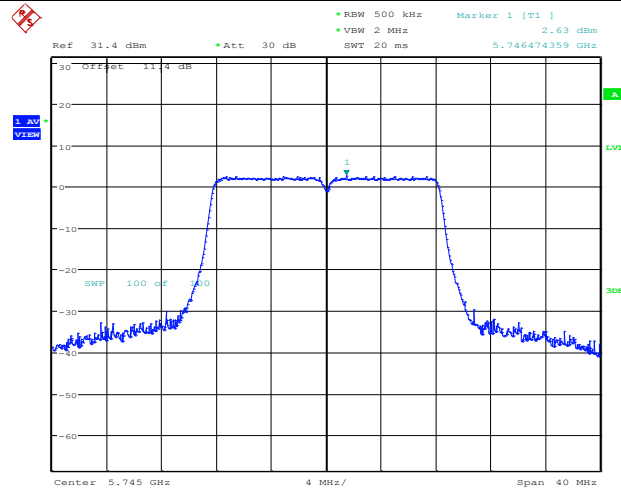
Date: 15.MAR.2019 09:36:39

11A_Ant1_5700



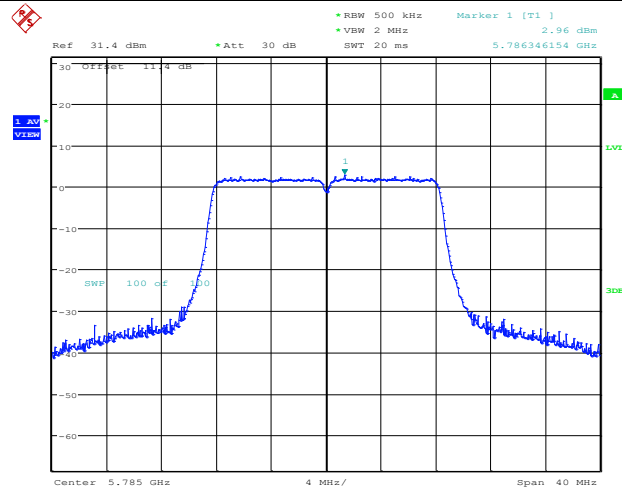
Date: 15.MAR.2019 09:37:03

11A_Ant1_5745



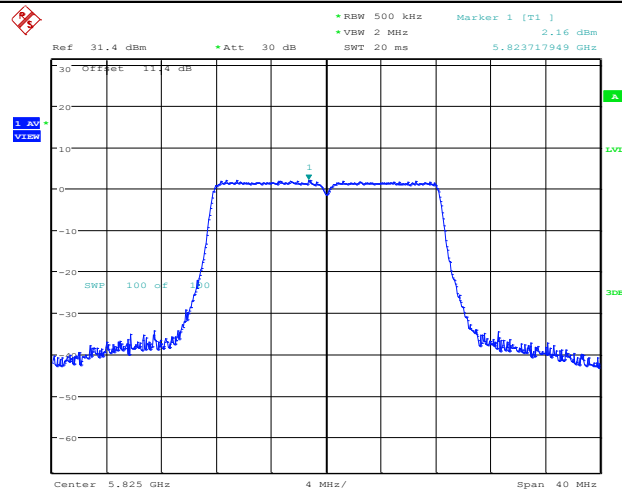
Date: 15.MAR.2019 09:37:28

11A_Ant1_5785



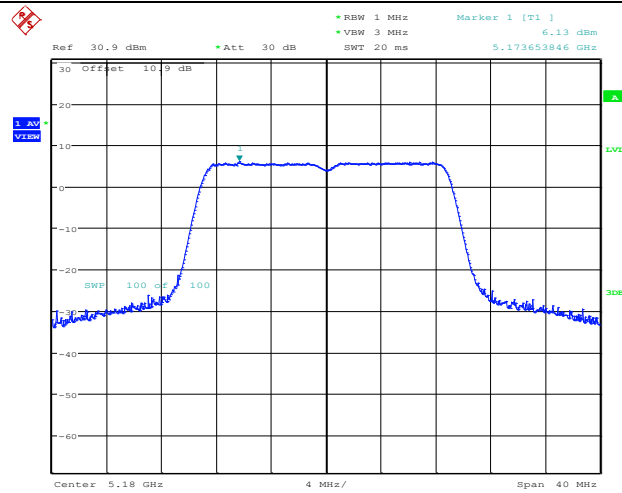
Date: 15.MAR.2019 09:37:55

11A_Ant1_5825



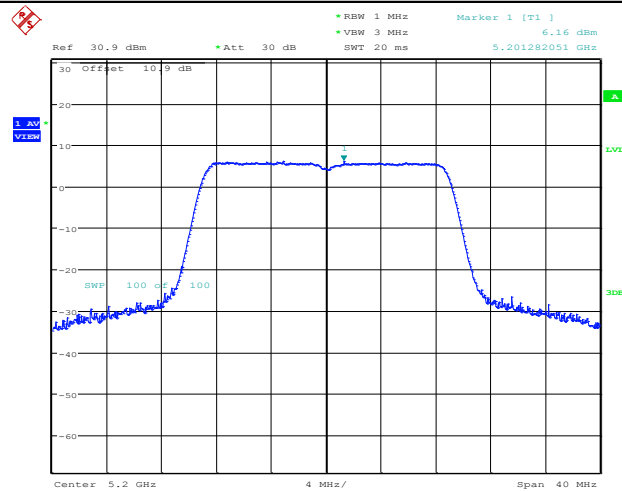
Date: 15.MAR.2019 09:38:11

11N20SISO_Ant1_5180



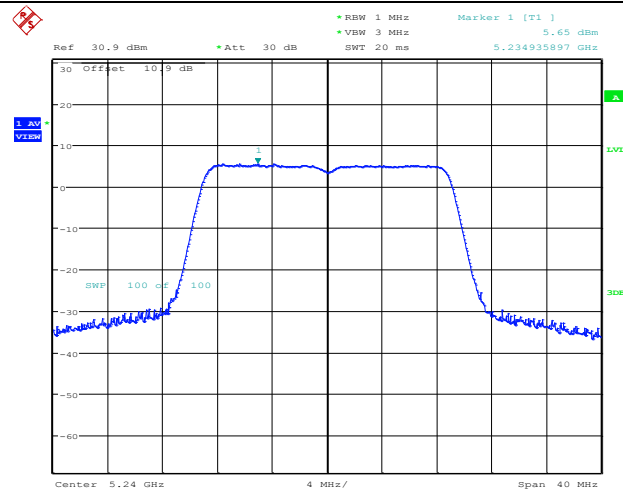
Date: 15.MAR.2019 09:38:55

11N20SISO_Ant1_5200



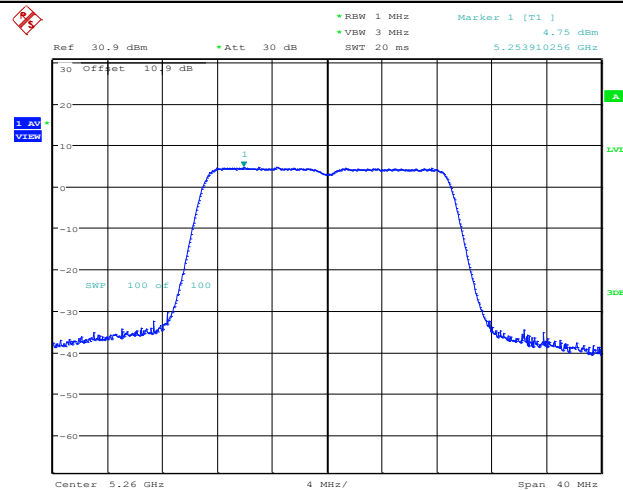
Date: 15.MAR.2019 09:39:18

11N20SISO_Ant1_5240



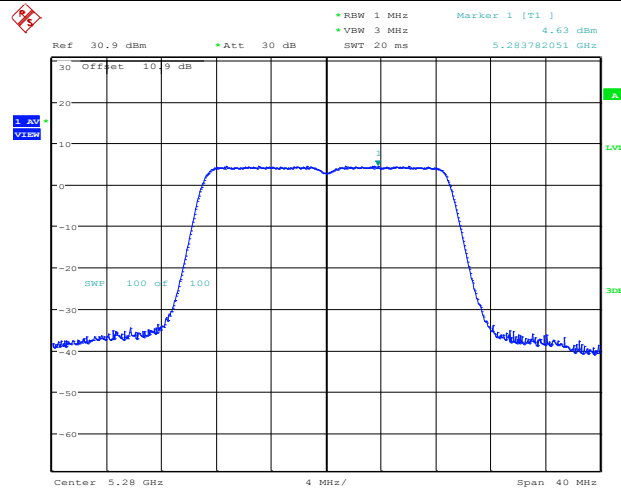
Date: 15.MAR.2019 09:41:02

11N20SISO_Ant1_5260



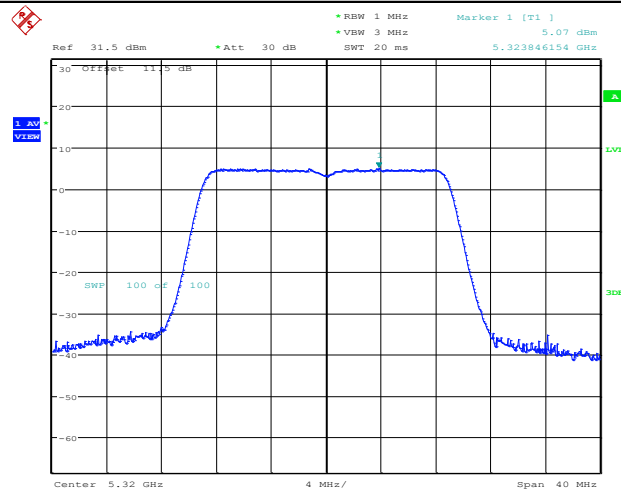
Date: 15.MAR.2019 09:41:29

11N20SISO_Ant1_5280



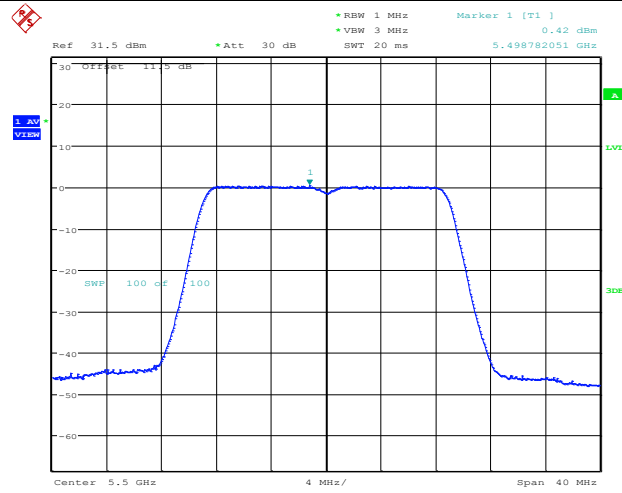
Date: 15.MAR.2019 09:41:55

11N20SISO_Ant1_5320



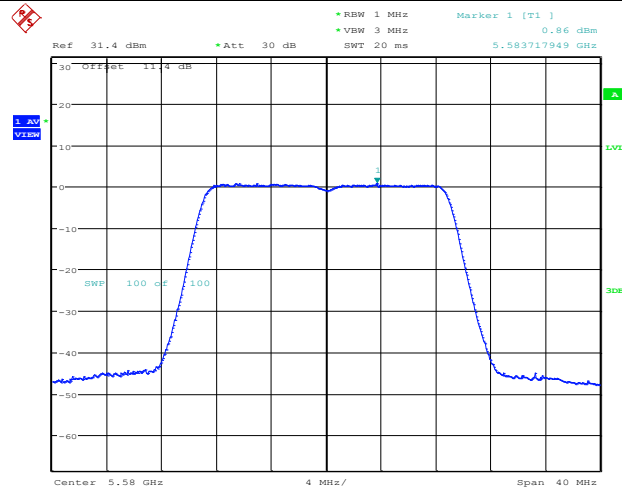
Date: 15.MAR.2019 09:42:13

11N20SISO_Ant1_5500



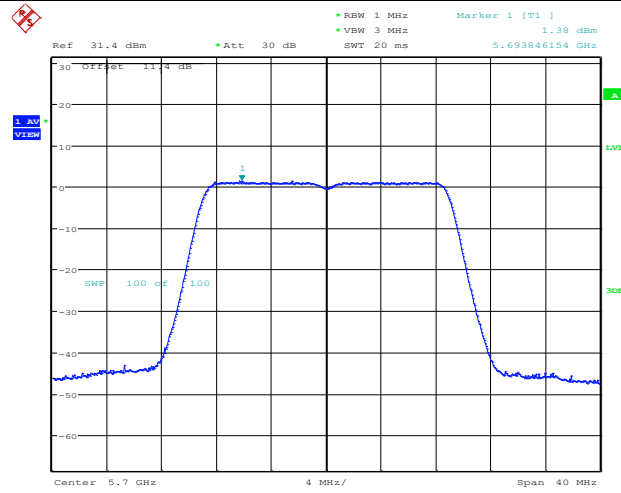
Date: 15.MAR.2019 09:43:01

11N20SISO_Ant1_5580



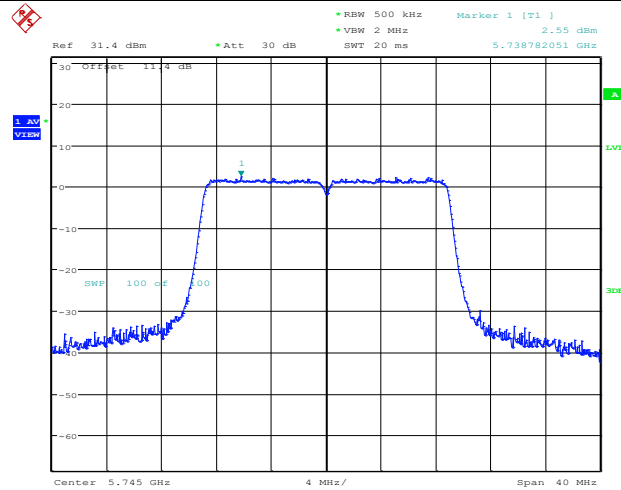
Date: 15.MAR.2019 09:43:36

11N20SISO_Ant1_5700



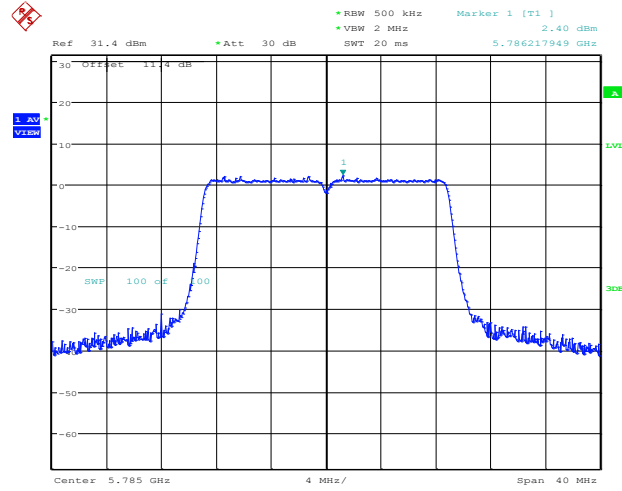
Date: 15.MAR.2019 09:43:54

11N20SISO_Ant1_5745



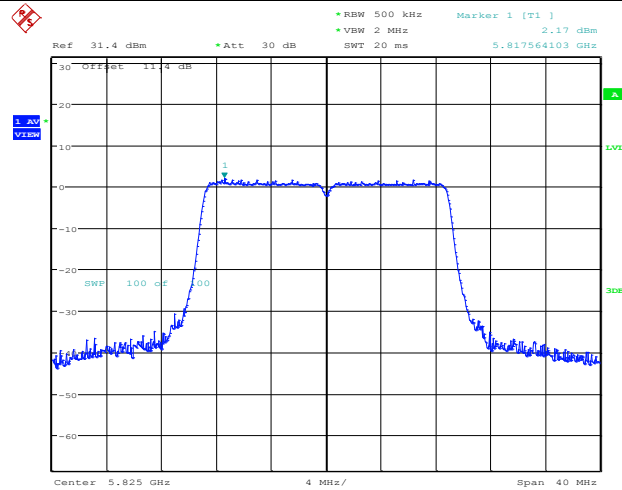
Date: 15.MAR.2019 09:44:15

11N20SISO_Ant1_5785



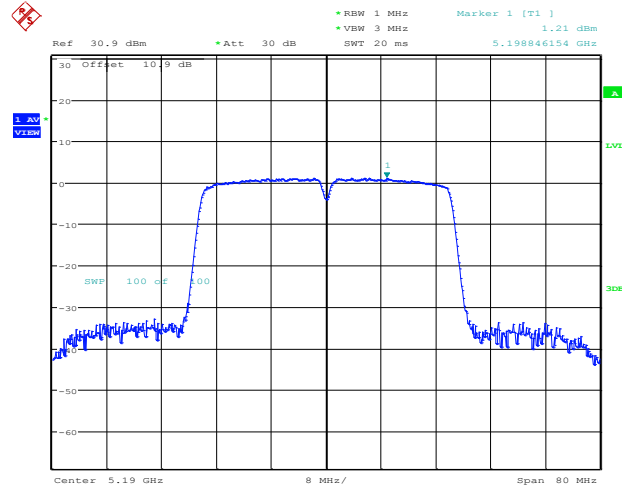
Date: 15.MAR.2019 09:44:39

11N20SISO_Ant1_5825



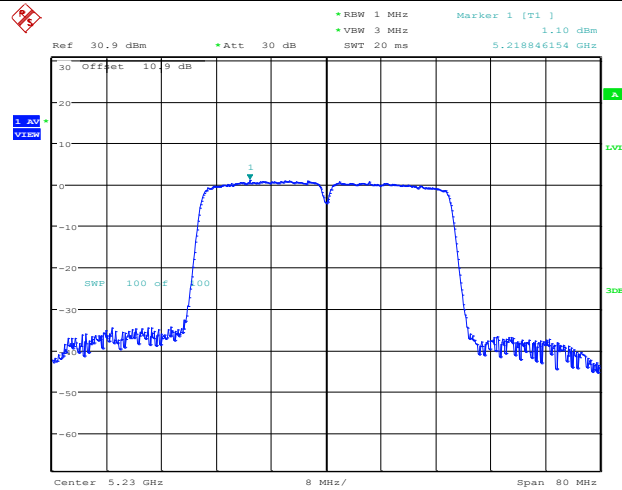
Date: 15.MAR.2019 09:45:04

11N40SISO_Ant1_5190



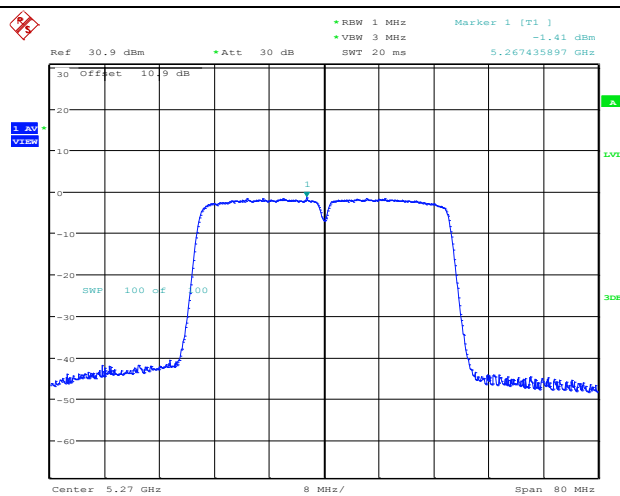
Date: 15.MAR.2019 09:46:19

11N40SISO_Ant1_5230



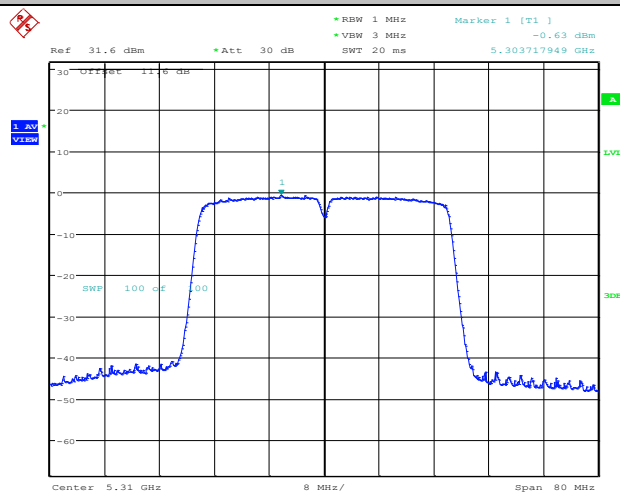
Date: 15.MAR.2019 09:48:40

11N40SISO_Ant1_5270



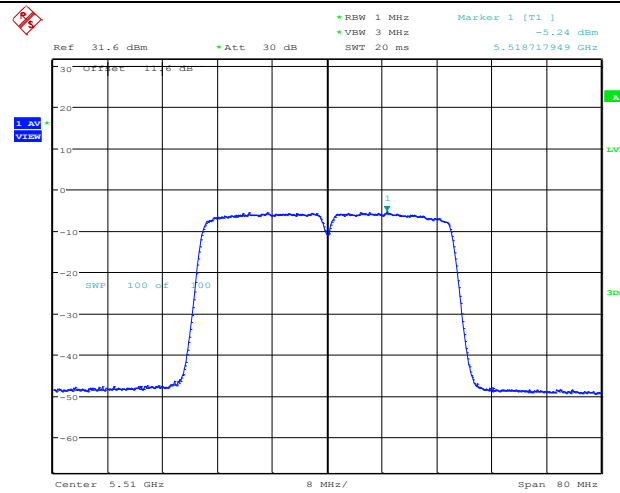
Date: 15.MAR.2019 09:49:50

11N40SISO_Ant1_5310



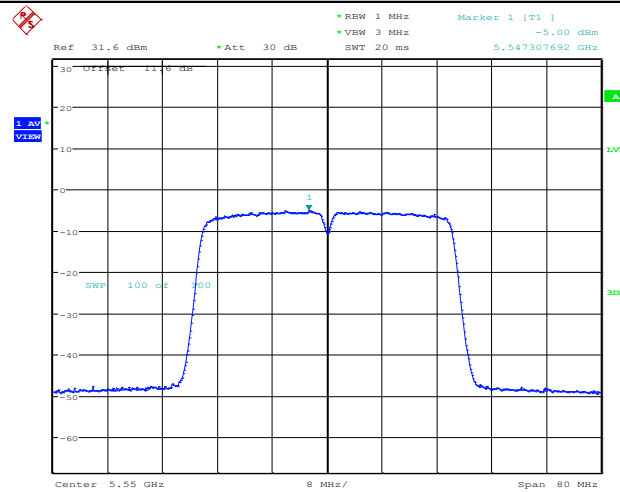
Date: 15.MAR.2019 09:50:48

11N40SISO_Ant1_5510



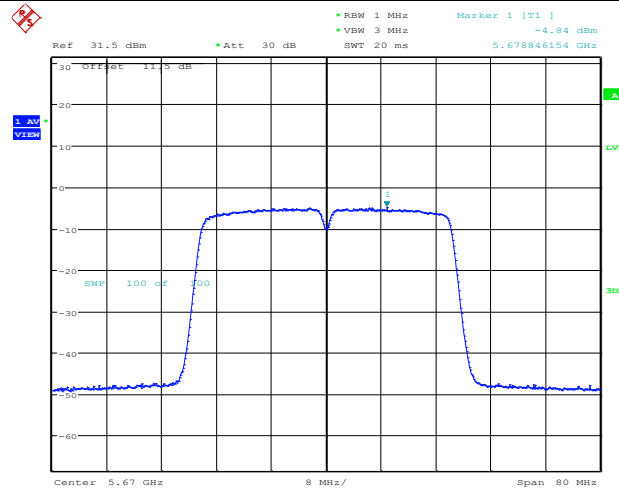
Date: 15.MAR.2019 09:51:43

11N40SISO_Ant1_5550



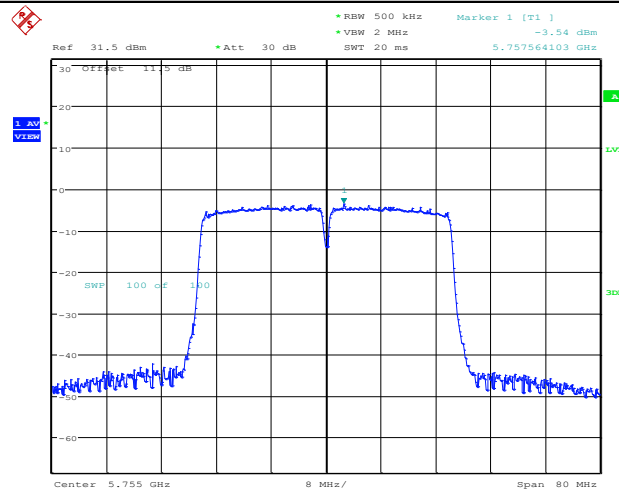
Date: 15.MAR.2019 09:52:02

11N40SISO_Ant1_5670



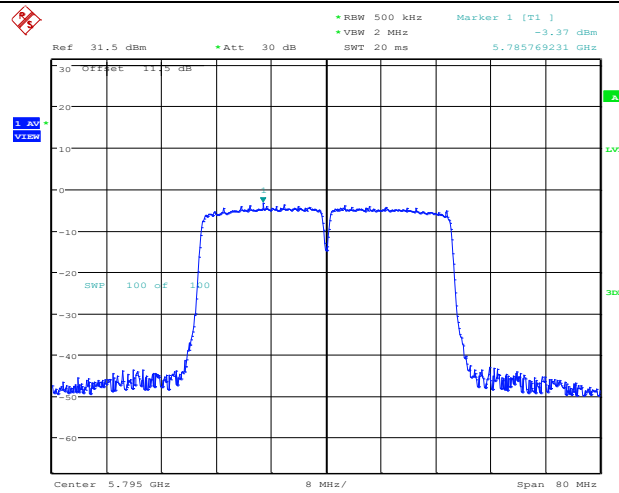
Date: 15.MAR.2019 09:53:58

11N40SISO_Ant1_5755



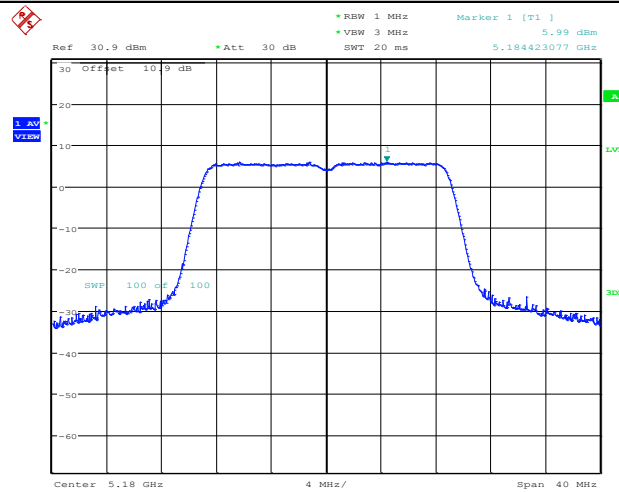
Date: 15.MAR.2019 09:55:32

11N40SISO_Ant1_5795



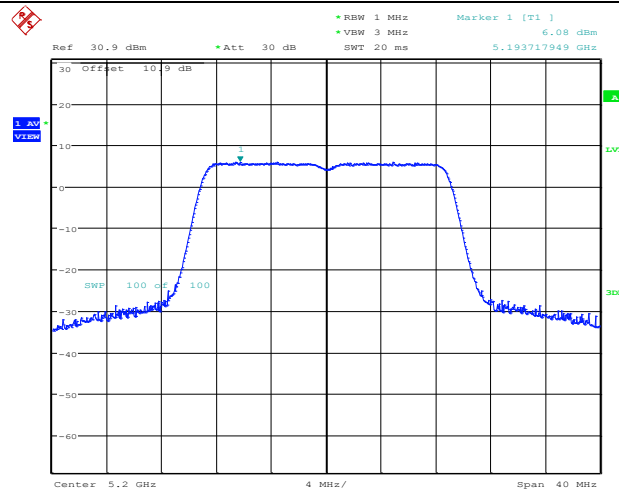
Date: 15.MAR.2019 09:55:50

11AC20SISO_Ant1_5180



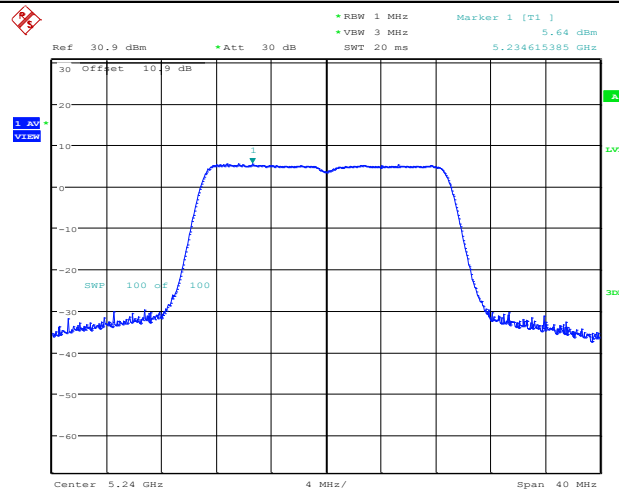
Date: 15.MAR.2019 09:56:28

11AC20SISO_Ant1_5200



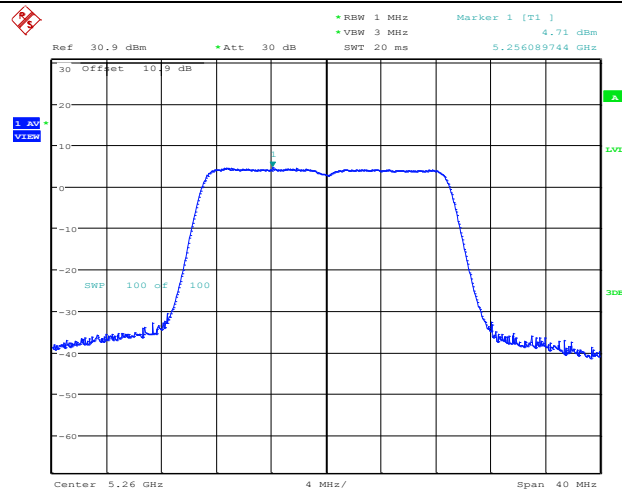
Date: 15.MAR.2019 09:57:13

11AC20SISO_Ant1_5240



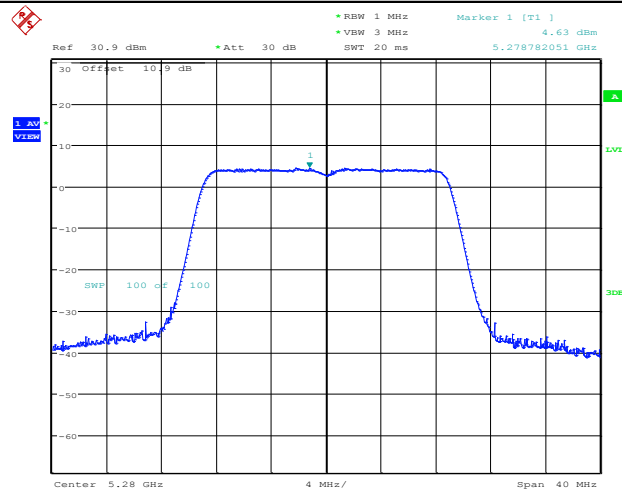
Date: 15.MAR.2019 09:57:58

11AC20SISO_Ant1_5260



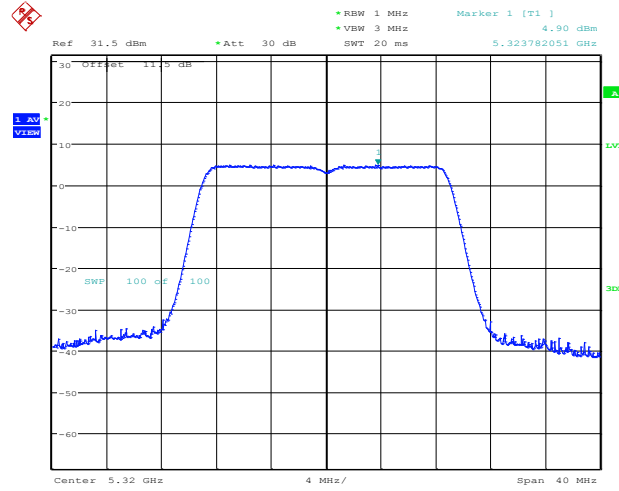
Date: 15.MAR.2019 09:58:21

11AC20SISO_Ant1_5280



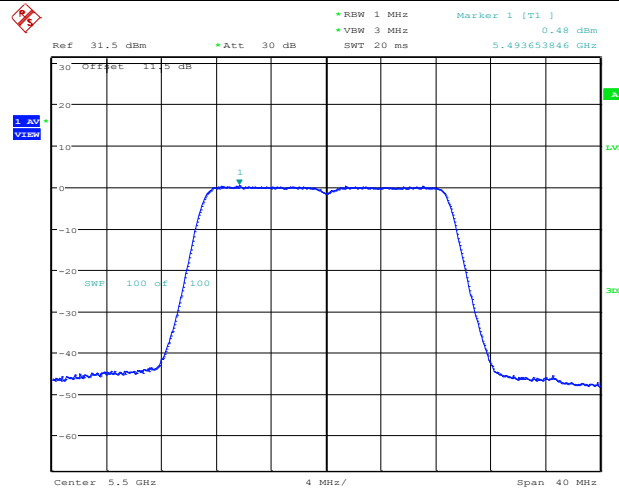
Date: 15.MAR.2019 09:59:09

11AC20SISO_Ant1_5320



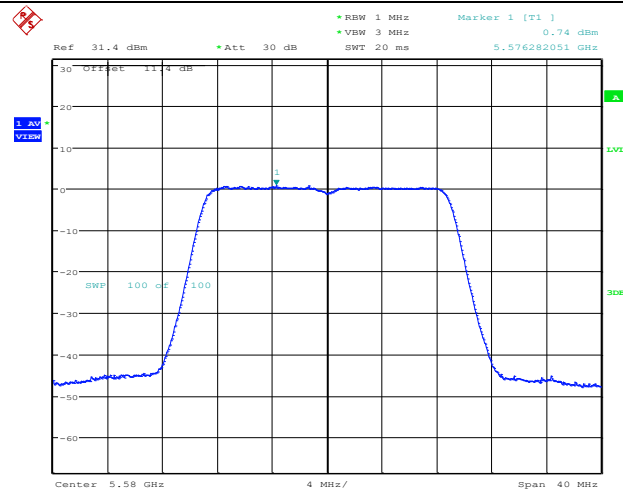
Date: 15.MAR.2019 09:59:27

11AC20SISO_Ant1_5500



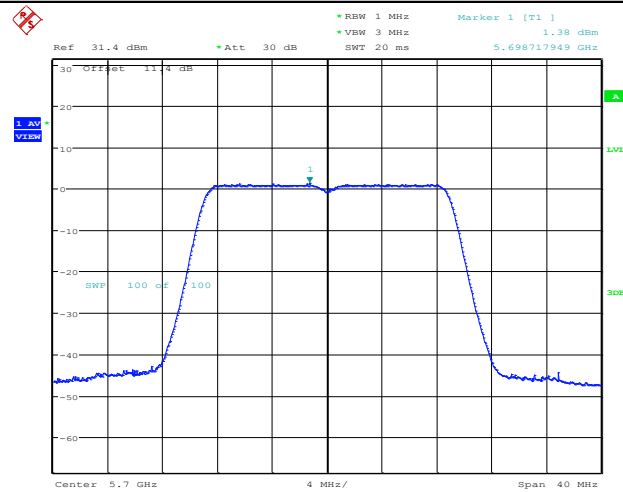
Date: 15.MAR.2019 09:59:52

11AC20SISO_Ant1_5580



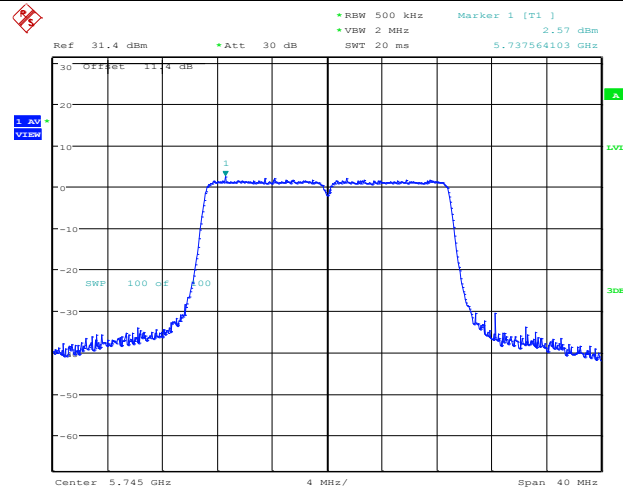
Date: 15.MAR.2019 10:00:47

11AC20SISO_Ant1_5700



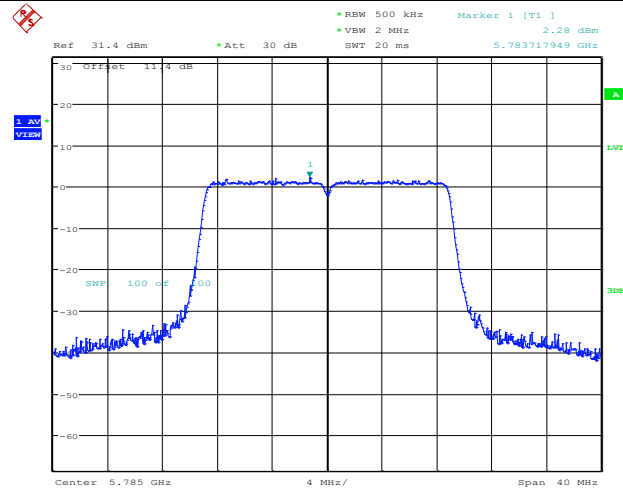
Date: 15.MAR.2019 10:01:04

11AC20SISO_Ant1_5745



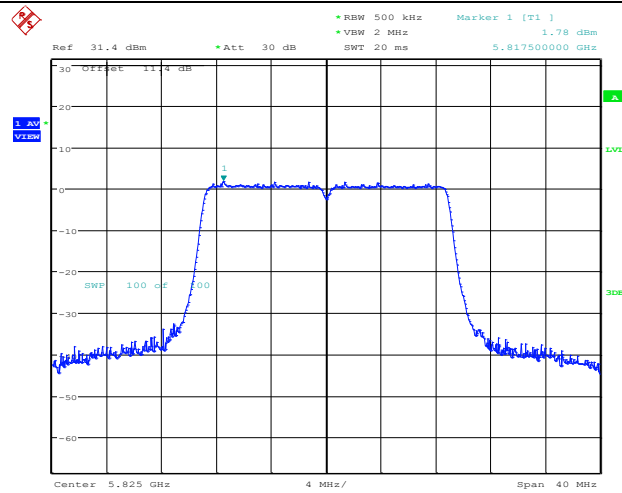
Date: 15.MAR.2019 10:01:26

11AC20SISO_Ant1_5785



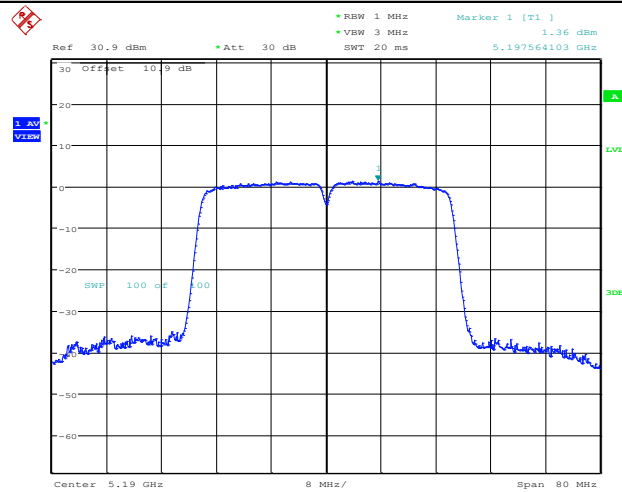
Date: 15.MAR.2019 10:01:45

11AC20SISO_Ant1_5825



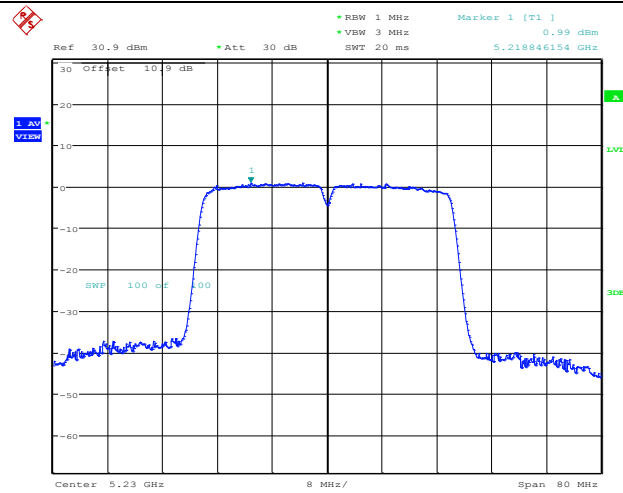
Date: 15.MAR.2019 10:02:02

11AC40SISO_Ant1_5190



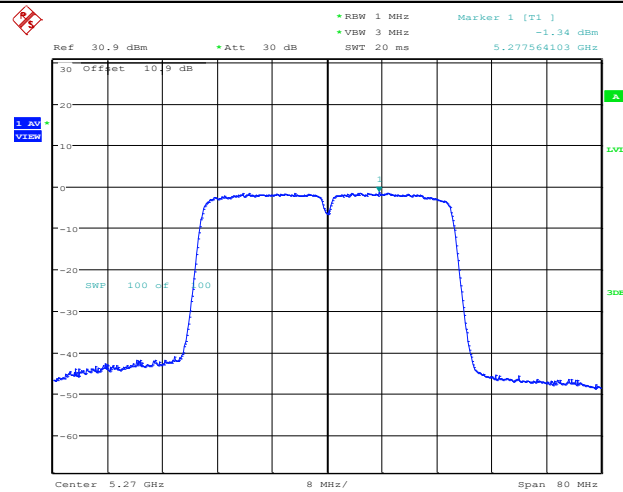
Date: 15.MAR.2019 10:03:30

11AC40SISO_Ant1_5230



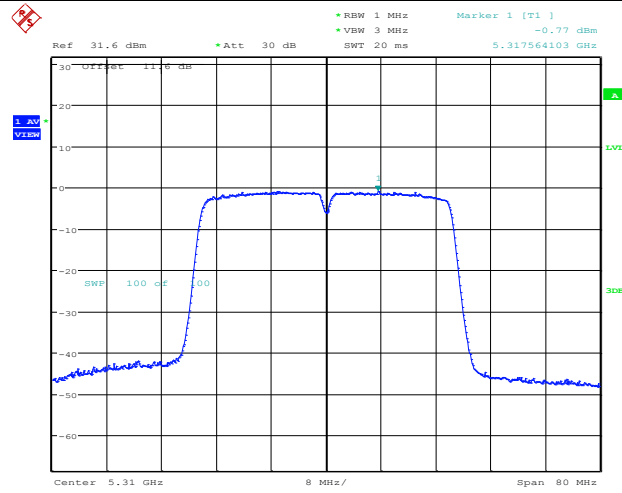
Date: 15.MAR.2019 10:03:51

11AC40SISO_Ant1_5270



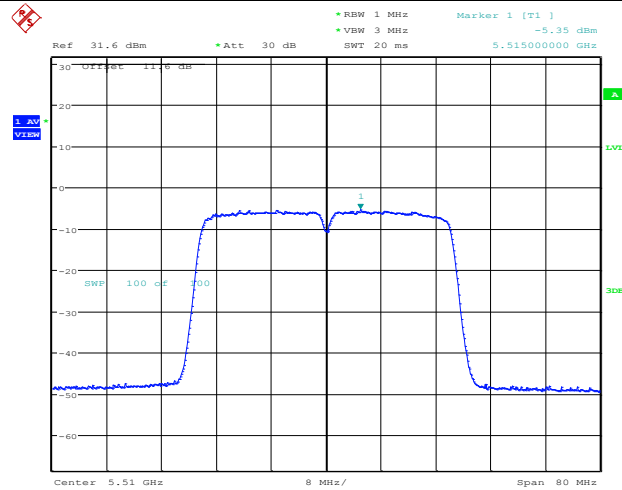
Date: 15.MAR.2019 10:04:19

11AC40SISO_Ant1_5310



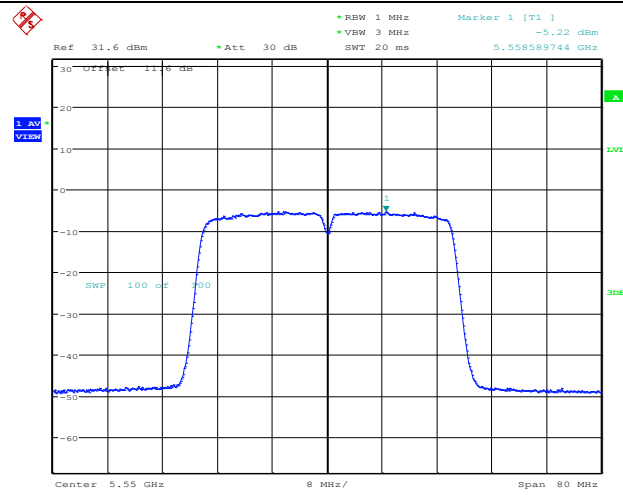
Date: 15.MAR.2019 10:04:38

11AC40SISO_Ant1_5510



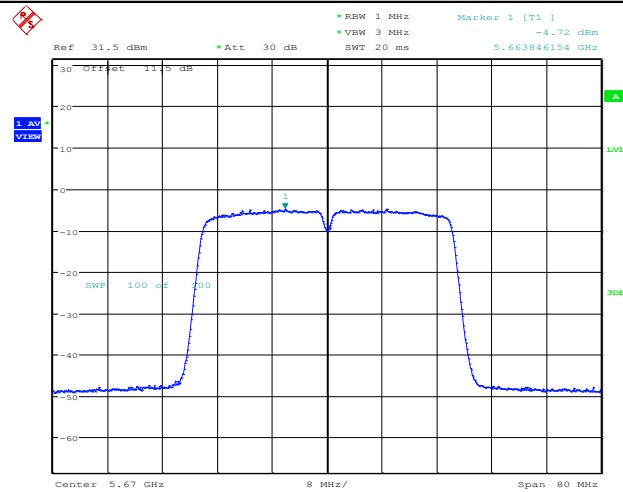
Date: 15.MAR.2019 10:05:00

11AC40SISO_Ant1_5550



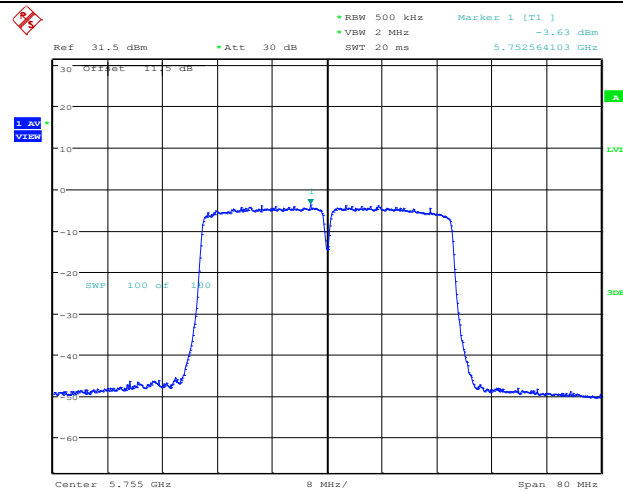
Date: 15.MAR.2019 10:05:18

11AC40SISO_Ant1_5670



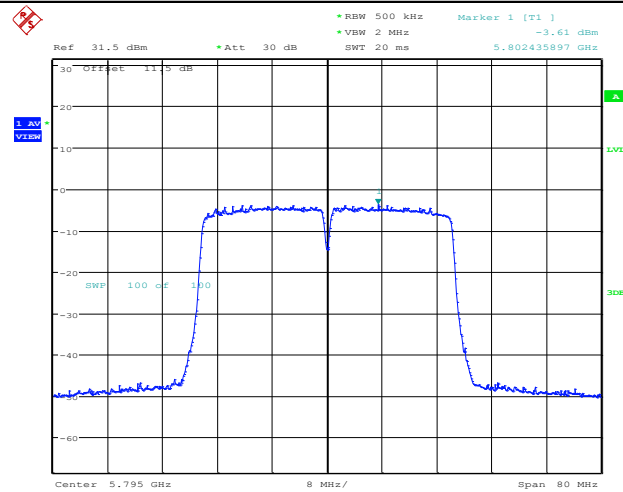
Date: 15.MAR.2019 10:05:43

11AC40SISO_Ant1_5755



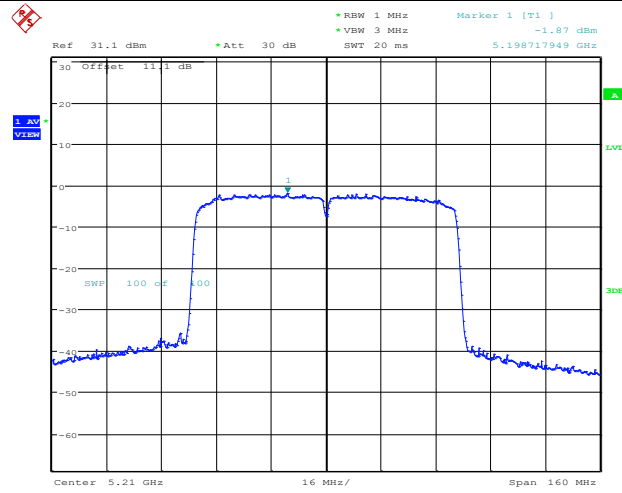
Date: 15.MAR.2019 10:08:08

11AC40SISO_Ant1_5795



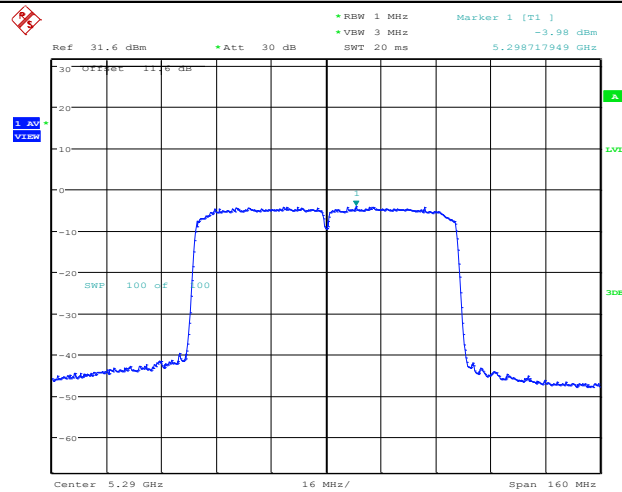
Date: 15.MAR.2019 10:08:27

11AC80SISO_Ant1_5210



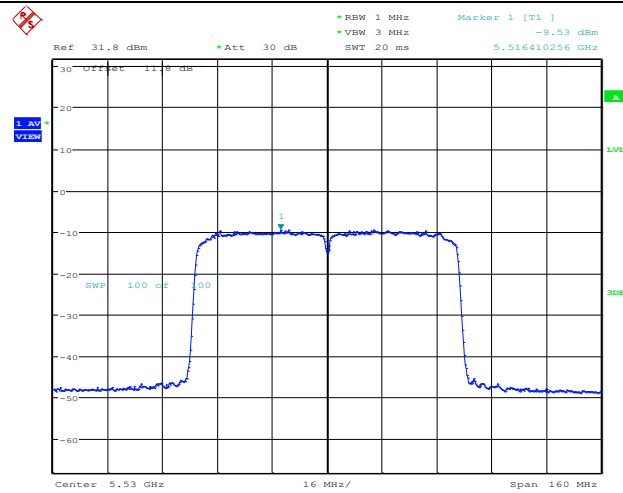
Date: 15.MAR.2019 10:09:08

11AC80SISO_Ant1_5290



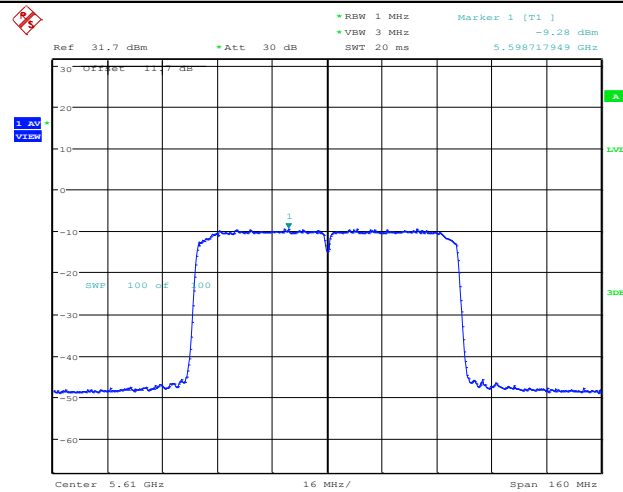
Date: 15.MAR.2019 10:09:32

11AC80SISO_Ant1_5530



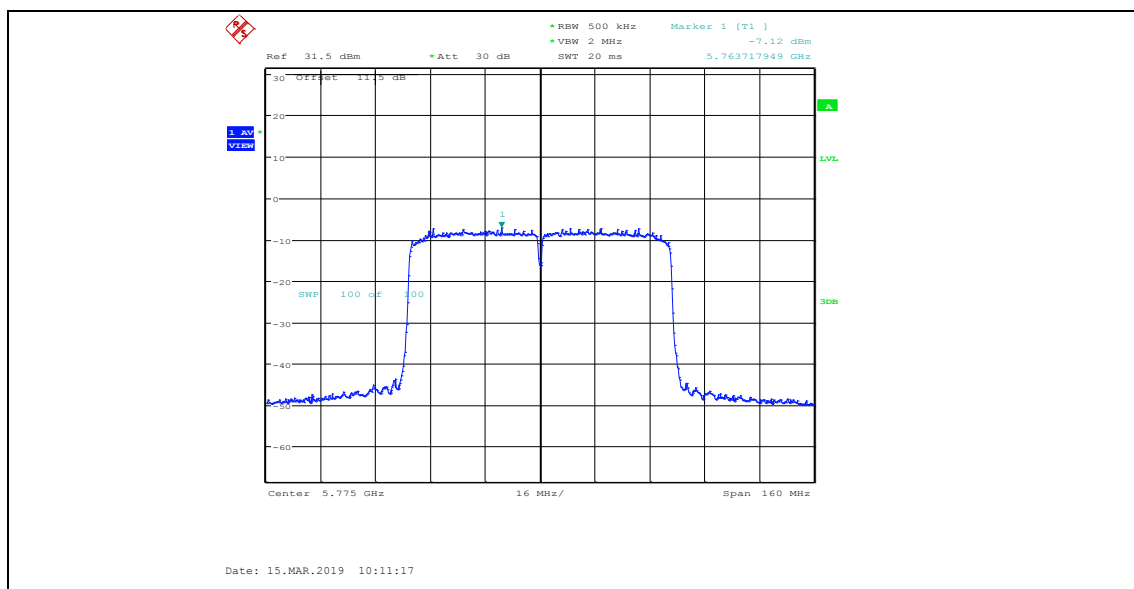
Date: 15.MAR.2019 10:09:56

11AC80SISO_Ant1_5610



Date: 15.MAR.2019 10:10:25

11AC80SISO_Ant1_5775





Appendix F: Frequency Stability

Test Result

Voltage								
TestMode	Antenna	Channel	Voltage [Vdc]	Temperature (°C)	Deviation (Hz)	Deviation (ppm)	Limit (ppm)	Verdict
11A	Ant1	5180	NV	NT	0	0	20	PASS
			LV	NT	0	0	20	PASS
			HV	NT	0	0	20	PASS
		5825	NV	NT	0	0	20	PASS
			LV	NT	0	0	20	PASS
			HV	NT	0	0	20	PASS

Temperature								
TestMode	Antenna	Channel	Voltage [Vdc]	Temperature (°C)	Deviation (Hz)	Deviation (ppm)	Limit (ppm)	Verdict
11A	Ant1	5180	NV	-30	0	0	20	PASS
			NV	-20	0	0	20	PASS
			NV	-10	0	0	20	PASS
			NV	0	-20000	-3.861004	20	PASS
			NV	10	0	0	20	PASS
			NV	20	0	0	20	PASS
			NV	30	0	0	20	PASS
			NV	40	20000	3.861004	20	PASS
			NV	50	0	0	20	PASS
		5825	NV	-30	0	0	20	PASS
			NV	-20	0	0	20	PASS
			NV	-10	-20000	-3.433476	20	PASS
			NV	0	0	0	20	PASS
			NV	10	0	0	20	PASS
			NV	20	0	0	20	PASS
			NV	30	0	0	20	PASS
			NV	40	0	0	20	PASS
			NV	50	0	0	20	PASS

END