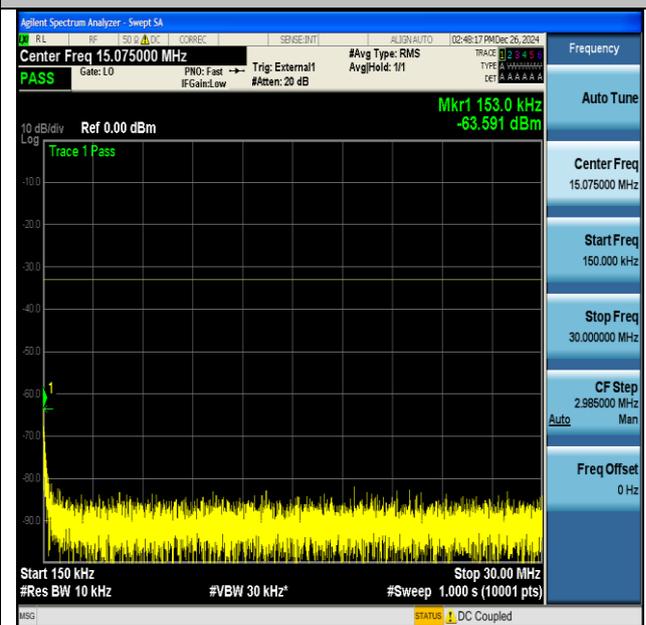
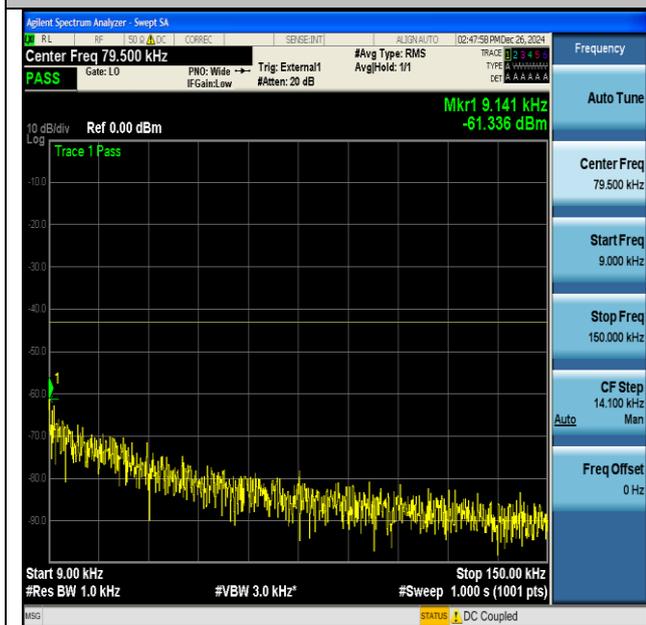


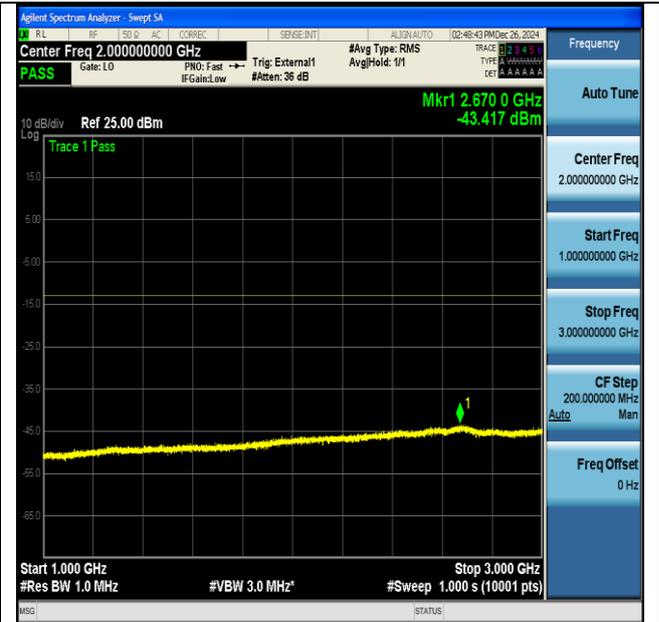
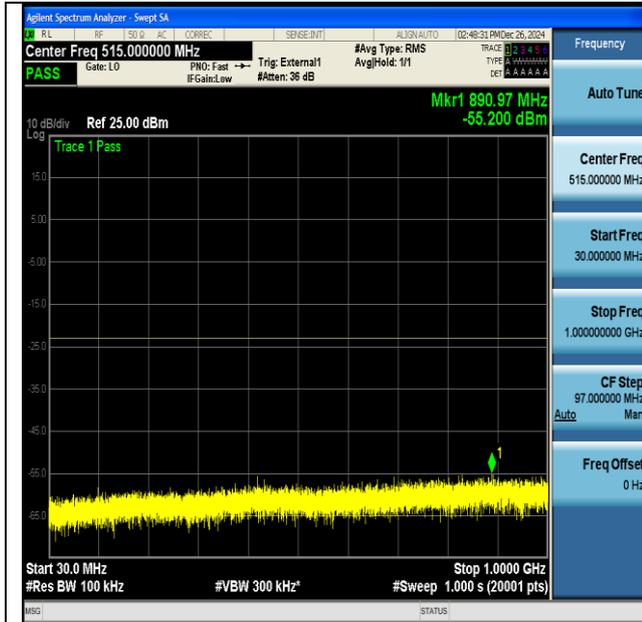
NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+L-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-6000-26000-PASS

NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+L-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-26000-40000-PASS



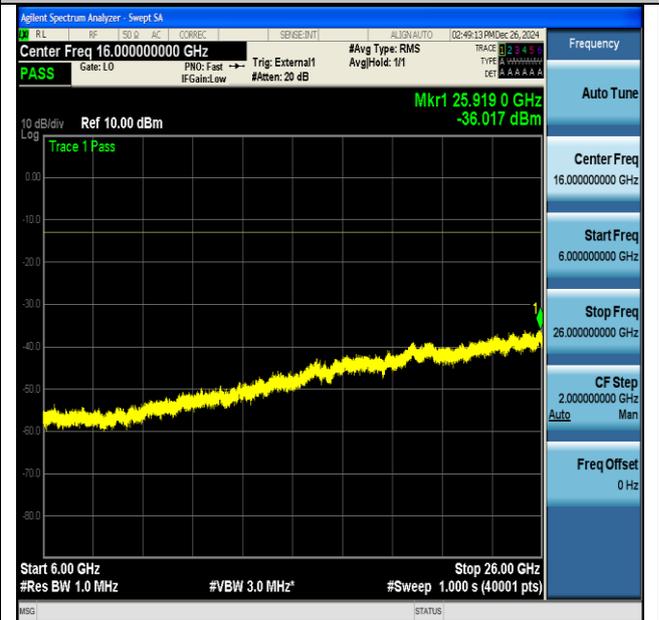
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-0.15-30-PASS



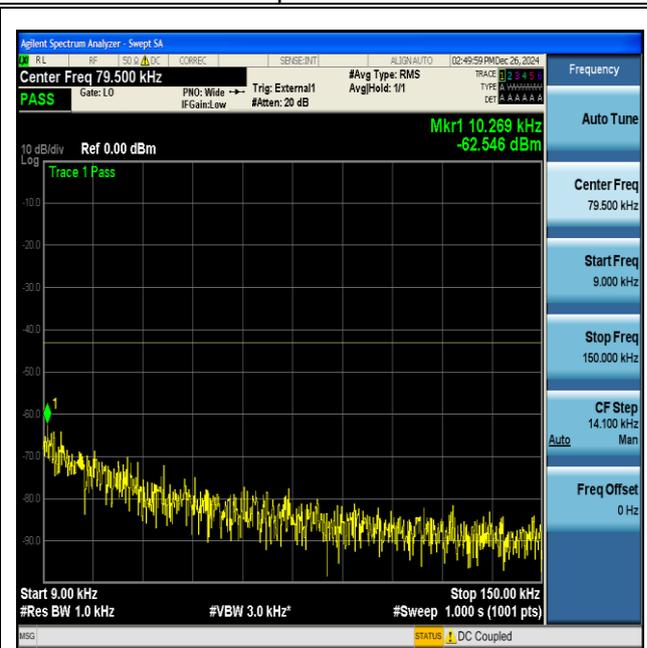
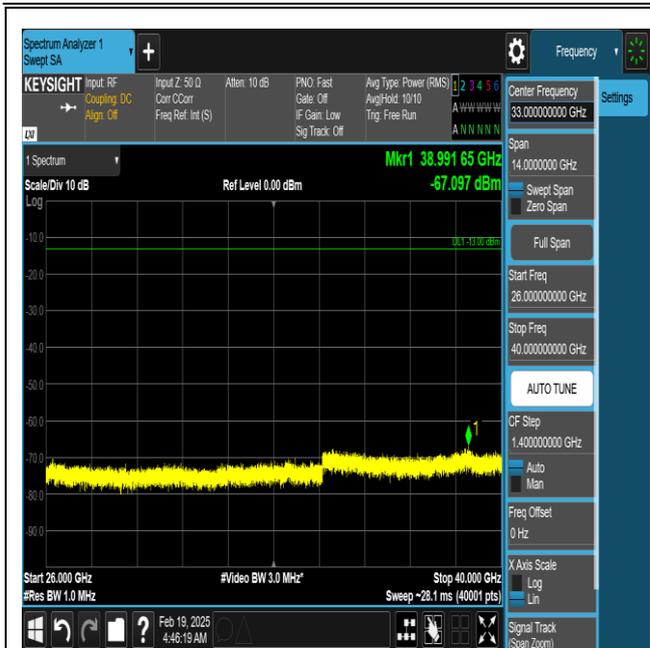
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-1000-3000-PASS



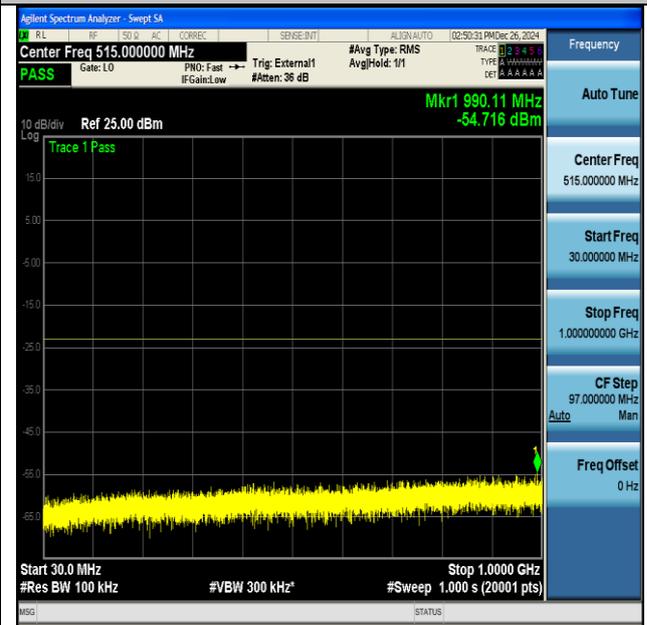
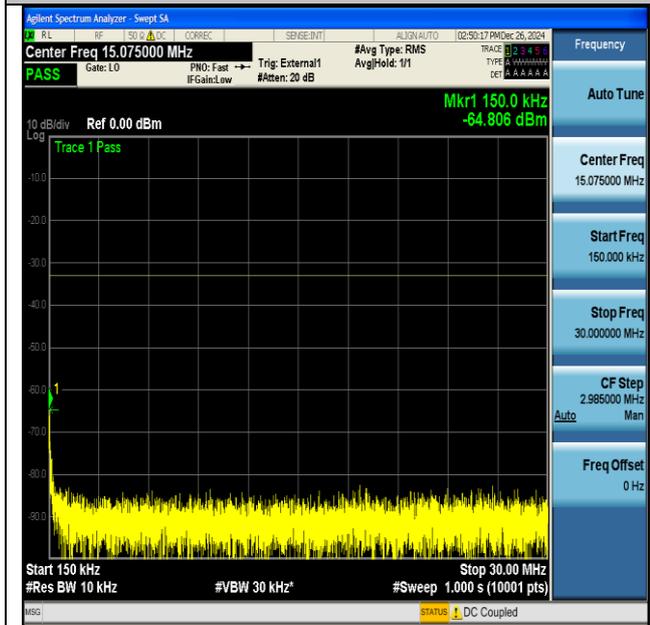
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-6000-26000-PASS



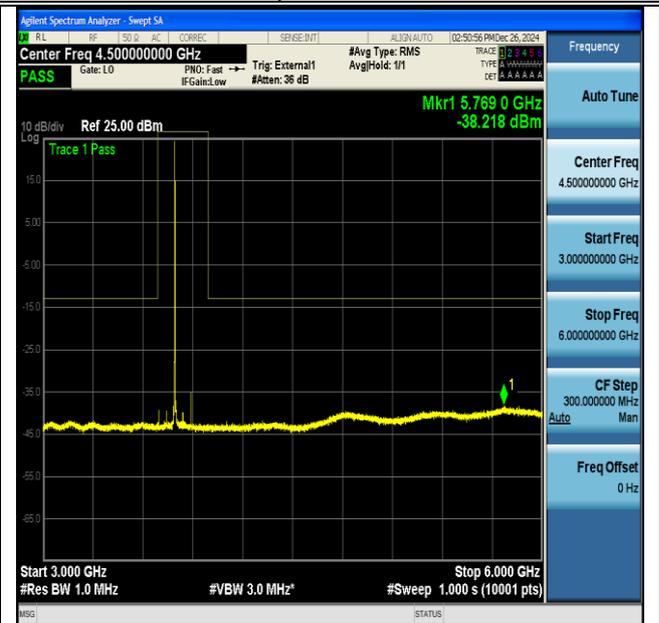
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-0.009-0.15-PASS



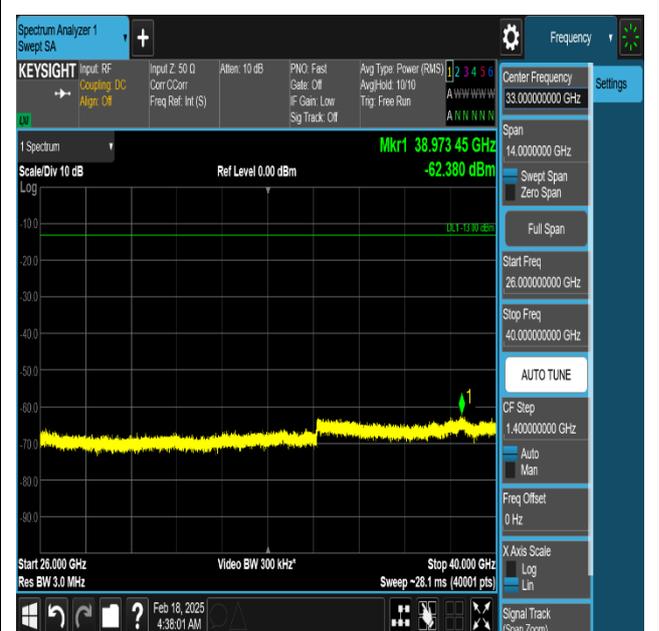
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-30-1000-PASS



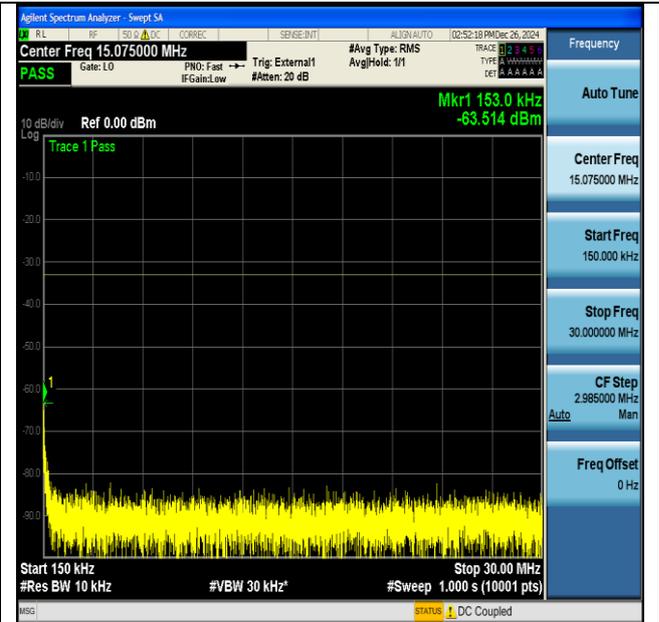
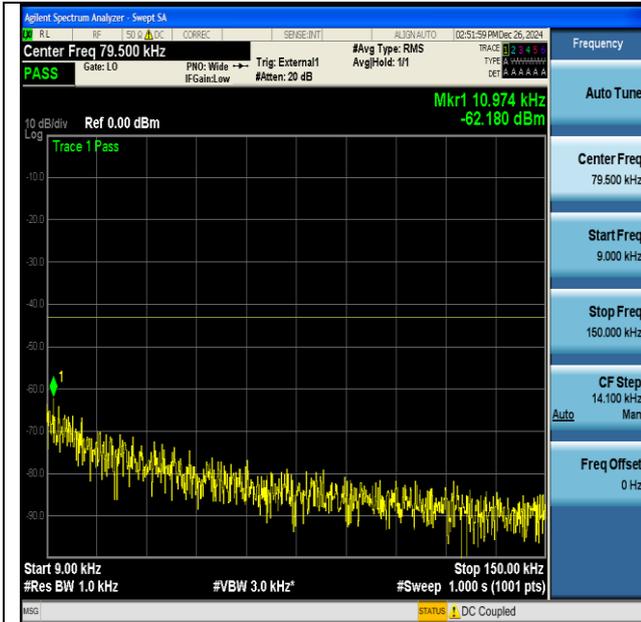
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-3000-6000-PASS



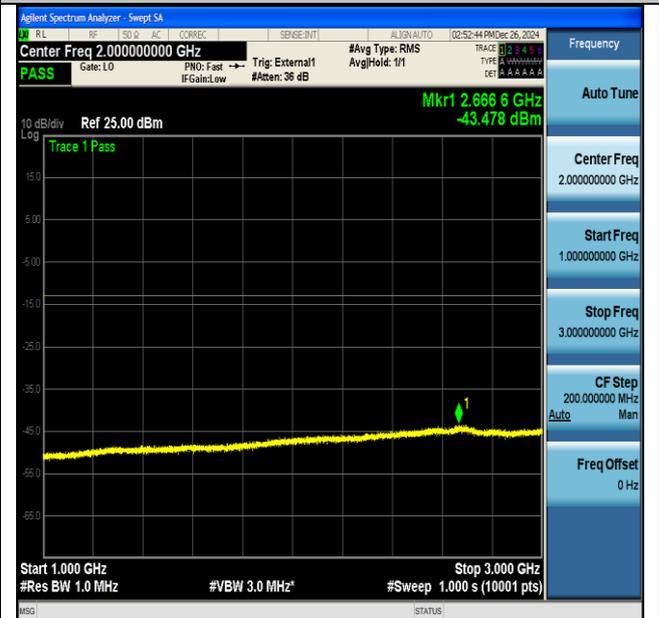
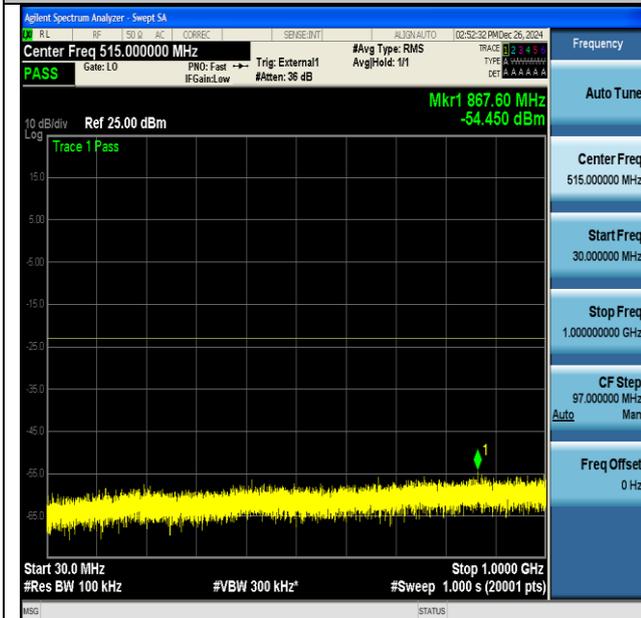
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+M-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-26000-40000-PASS



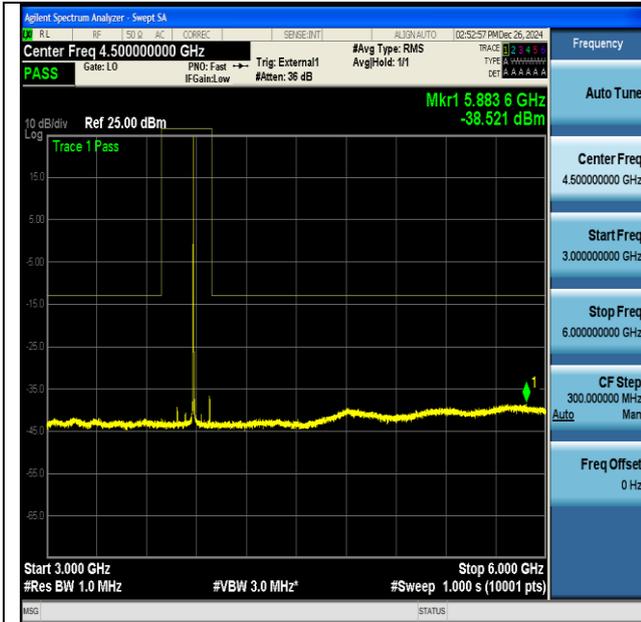
NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-0.009-0.15-PASS

NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-0.15-30-PASS

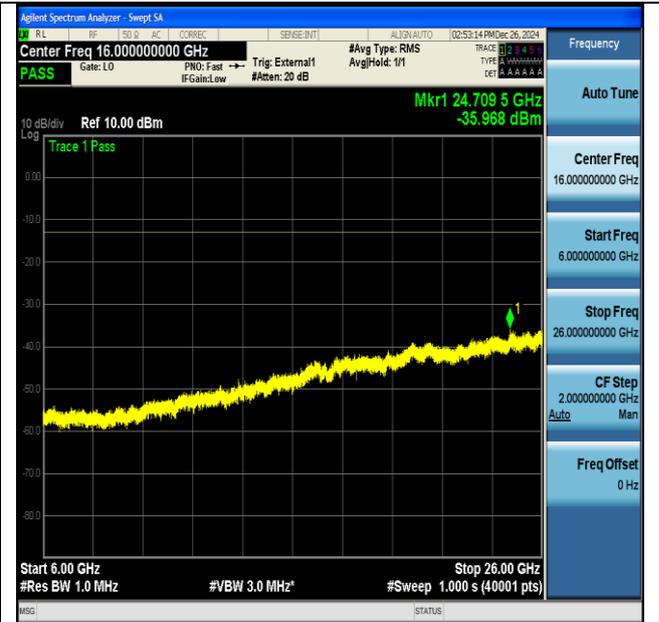


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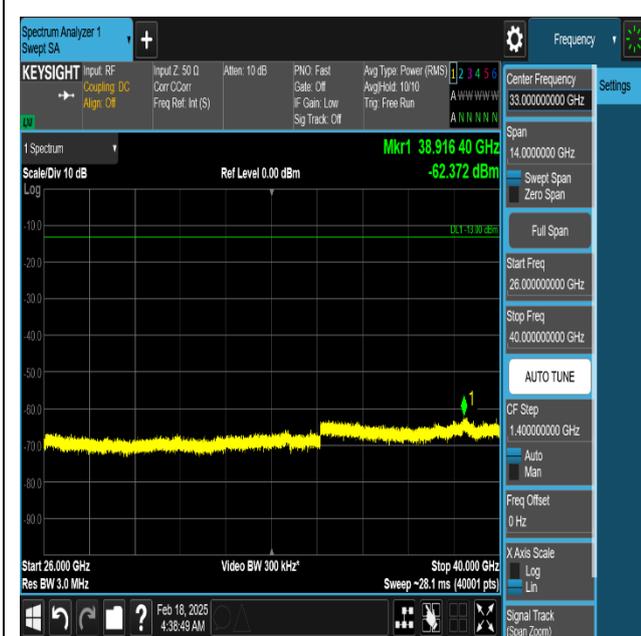
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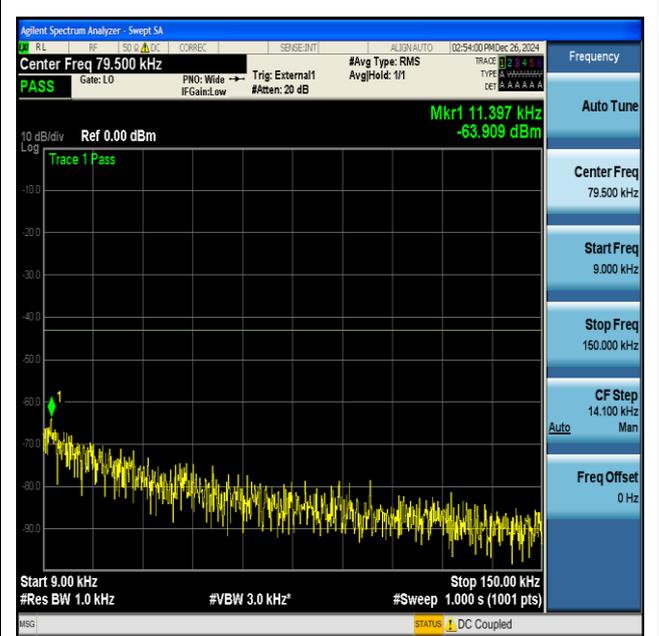
NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-3000-6000-PASS



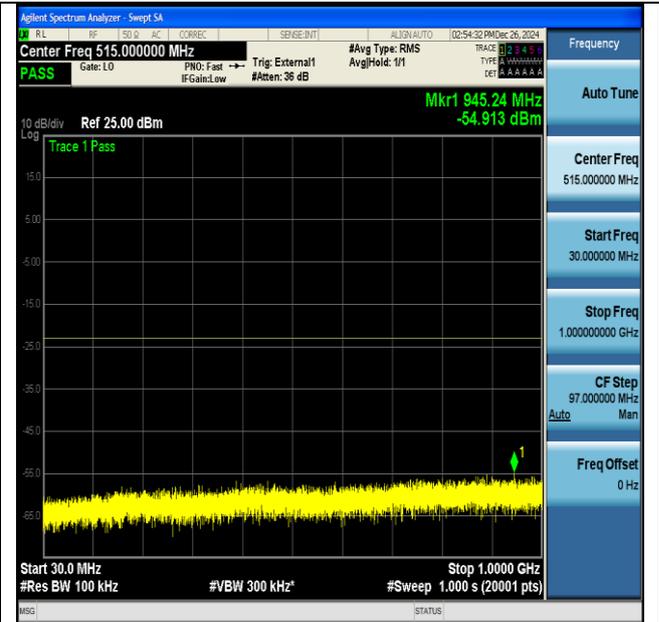
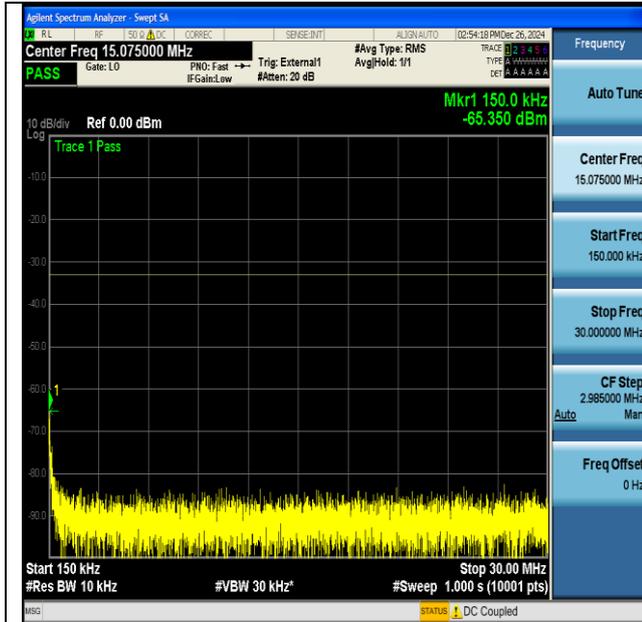
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NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-26000-40000-PASS

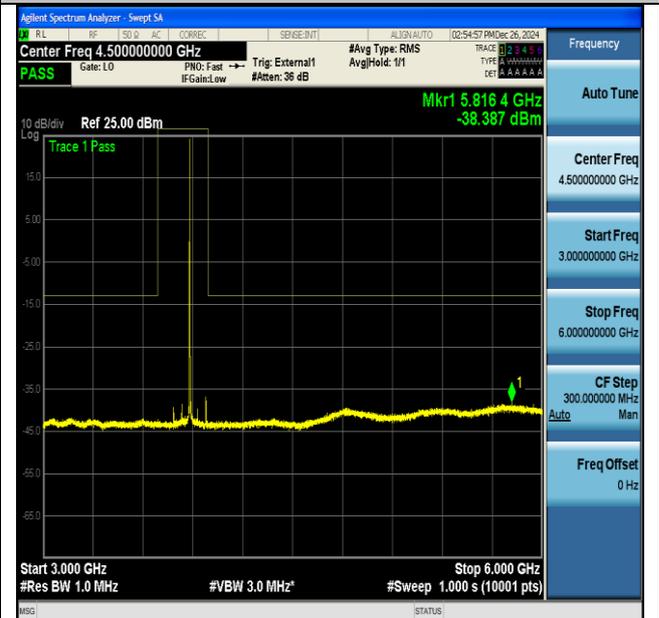


NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-0.009-0.15-PASS



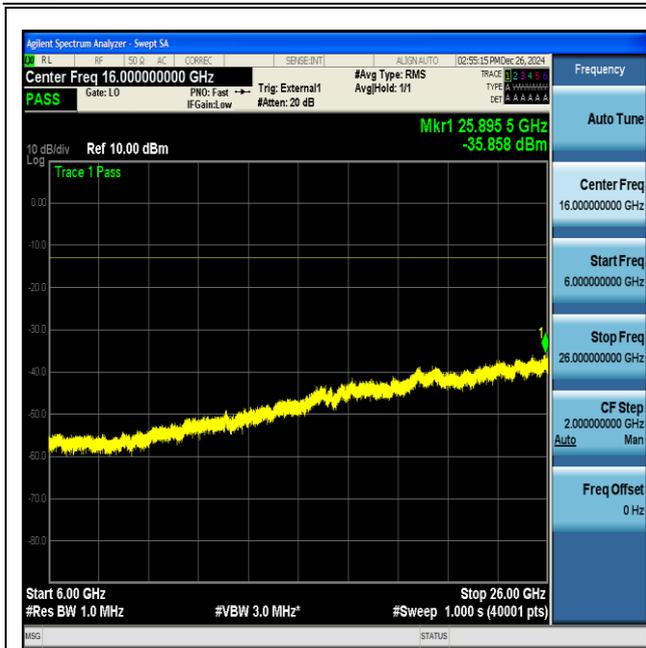
NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-0.15-30-PASS

NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-30-1000-PASS



NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-1000-3000-PASS

NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-3000-6000-PASS



NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-6000-26000-PASS



NTNV-DC_41A_n77A(3700-3980)-PC3-30-5+100-M+H-QPSK-IDLE-CP-QPSK-Inner_1RB_Left-26000-40000-PASS

Frequency Stability for NR

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temp	Deviation (Hz)	Deviation (ppm)	Verdict
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	VL	NT	-6.4	-0.001667	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	VN	NT	-2.3	-0.000599	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	VH	NT	7.5	0.001953	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	VL	NT	-6.5	-0.001693	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	VN	NT	-15.4	-0.00401	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	VH	NT	-7.3	-0.001901	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temp	Deviation (Hz)	Deviation (ppm)	Verdict
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	-30	-6.7	-0.001745	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	-20	-1.6	-0.000417	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	-10	8.7	0.002266	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	0	-1.6	-0.000417	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	10	2.8	0.000729	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	20	3.1	0.000807	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	30	0.2	5.2E-05	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	40	1.1	0.000286	PASS
DC_41A_n77A(3700-3980)	30	5+100	DFT-BPSK	M+M	Outer_Full	NV	50	0	0	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	-30	-8.9	-0.002318	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	-20	-12.6	-0.003281	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	-10	-4.7	-0.001224	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	0	-7.4	-0.001927	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	10	-7.6	-0.001979	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	20	-1	-0.00026	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	30	-10.5	-0.002734	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	40	-5.6	-0.001458	PASS
DC_41A_n77A(3700-3980)	30	5+100	CP-QPSK	M+M	Outer_Full	NV	50	-4.7	-0.001224	PASS

~The End~