SDT 201 UB ARK 6

FCC Software Block Diagram
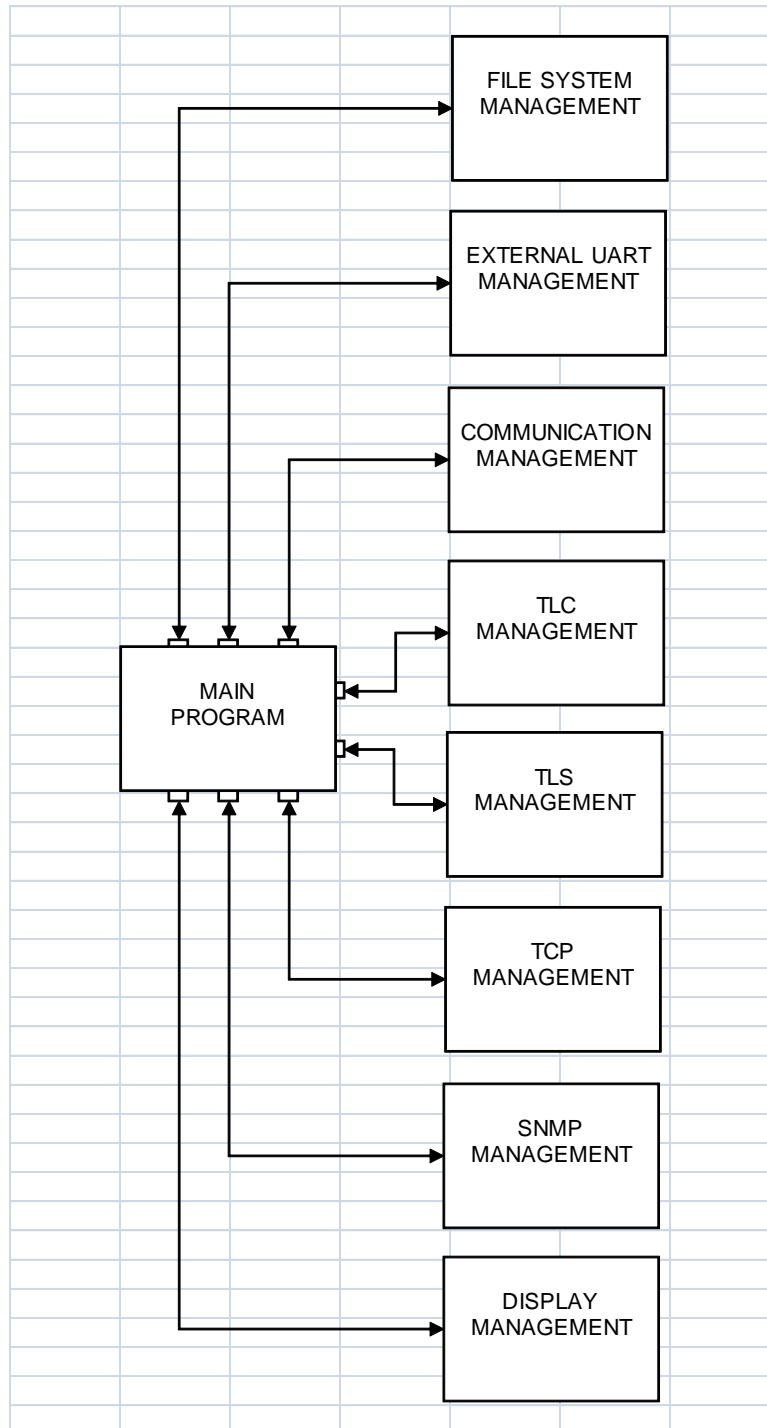
CONTENTS

1.1 INTRODUCTION

This note describes the high level software architecture.
For each block only few sentence will be given.
More detailed description will be provided if required

1.2 BLOCK DIAGRAM

1.3 BLOCK DESCRIPTION

1 MAIN PROGRAM

Initialize the System at the Switch On
By the operative system it manages the tasks hereunder described.
The tasks run all in parallel mode:

a) FILE SYSTEM MANAGEMENT
- o Management of the configuration files
    - o Any save transmitter action creates a file to be saved
    - o Any reload transmitter action
    - o Any parameters modification transmitter action

b) EXTERNAL UART MANAGEMENT
- o By a proprietary protocol it manages, via serial line (232 level), all the W/R equipment registers

c) COMMUNICATION MANAGEMENT
- o Communication management between microprocessor and the function forming the equipment:
    - o Up converter
    - o Down converter
    - o Modulator

d) TLC MANAGEMENT
- o It manages by the opto (four) the whole equipment
    - o RF on/off
    - o Remote standby
    - o Change mode
    - o Alarm reset

e) TLS MANAGEMENT
- o It manages by the relais (four) the whole equipment
    - o RF on/off (or selected alarm)
    - o Mode selcted (or selected alarm)
    - o Alarm defined by web interface mask (two relais)

f) TCP MANAGEMENT
- o Communication management between microprocessor and web interface:
    - o Micro answer to java request

g) SNMP MANAGEMENT
- o It manages by snmp protocol the following standard SNMP operations
    - o Get operation
    - o Get next operation
    - o Walk operation
    - o Set operation
    - o Trap

h) DISPLAY MANAGEMENT
- o By a communication protocol between main micro and display micro it manages the following operations:
    - o Menu Navigation
    - o Data Settings
    - o Data Refresh