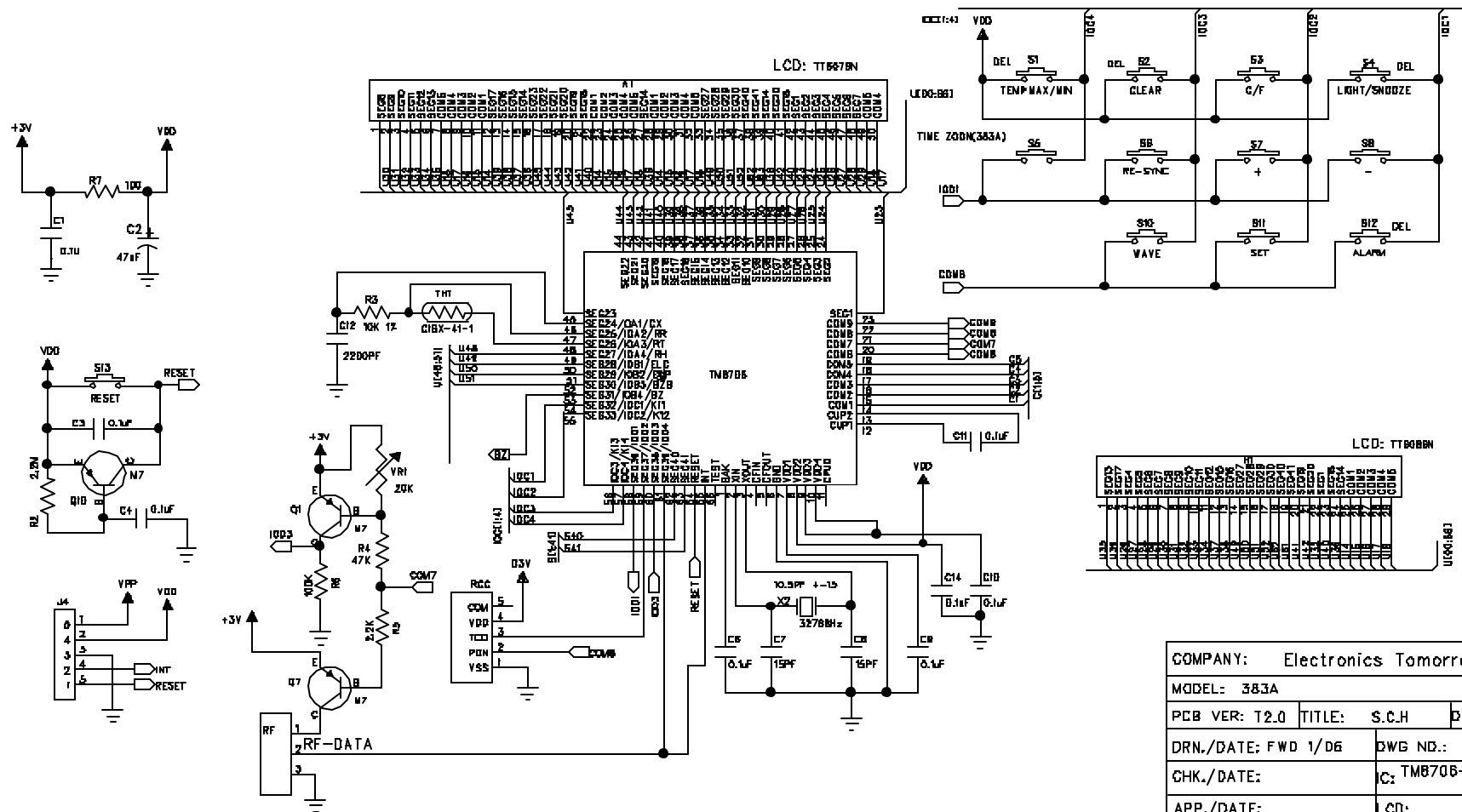


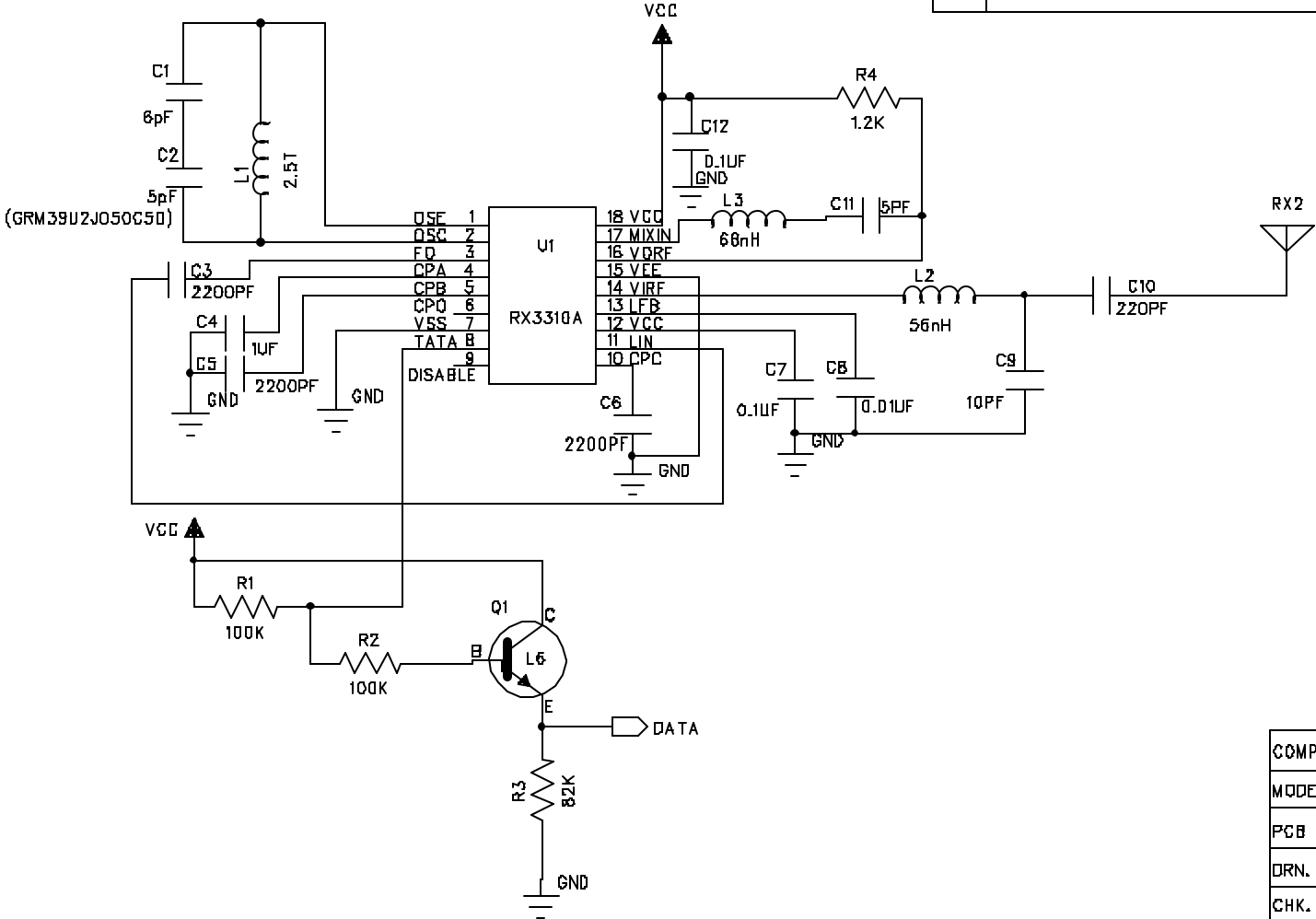
NO.	REVISION	DATE:	CHK:	APP:
1	T1.0			
2				
3				
4				
5				



The substrate of chip should be connected GND

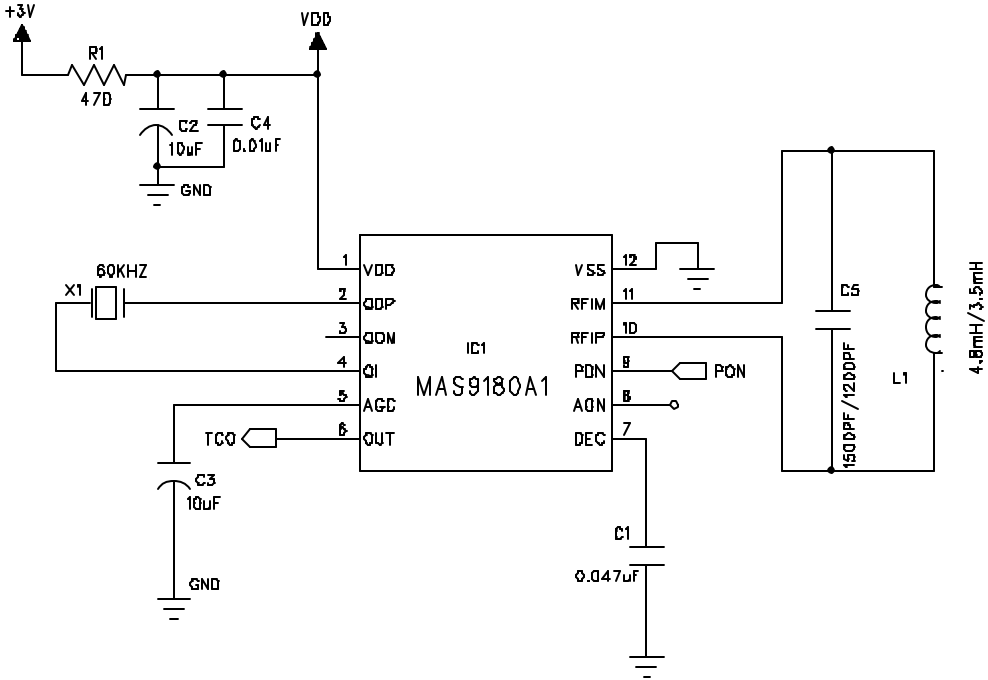
COMPANY: Electronics Tomorrow Ltd.			
MODEL: 383A			
PCB VER: T2.0	TITLE: S.C.H	DDC VER: 00	
DRN./DATE: FWD 1/06	DWG NO.: 838AED0131		
CHK./DATE:	C: TM8706-K400 (838A)		
APP./DATE:	LCD:		
☒ TO FILE ☐ REL		SHEET 0F:1 OF 1	
EE005			

REV.	MODIFICATION	DATE:	CHK:	APP:



COMPANY: Electronics Tomorrow Ltd.				
MODEL:		4670/1		
PCB VER:	79661V2.0	TITLE:	SCH	DOC VER: 00
DRN. /DATE: FWD 12/05		DWG NO: 4670E10431		
CHK. /DATE:		IC: RX3310A		
APP. /DATE:		LCD:		
☒ TO FILE		☐ REL		SHEET OF: EE005

NO.	REVISION	DATE:	CHK:	APP:
1.	T1.0			
	V1.0			



NOTE: 4570: 60KHZ : C5=1500P L1:4.8mH X1:60KHZ

COMPANY: Electronics Tommorrow Ltd		
MODEL: 4570/1		
PCB VER: v1.0	TITLE: SCH	DOC VER: 00
DRN./DATE: FWD 8/05	DWG NUM: 4570E10631	
CHK./DATE:	IC: MAS9180A1	
APP./DATE:	LCD:	
☒ TO FILE ☐ REL	SHEET 1 OF 1	