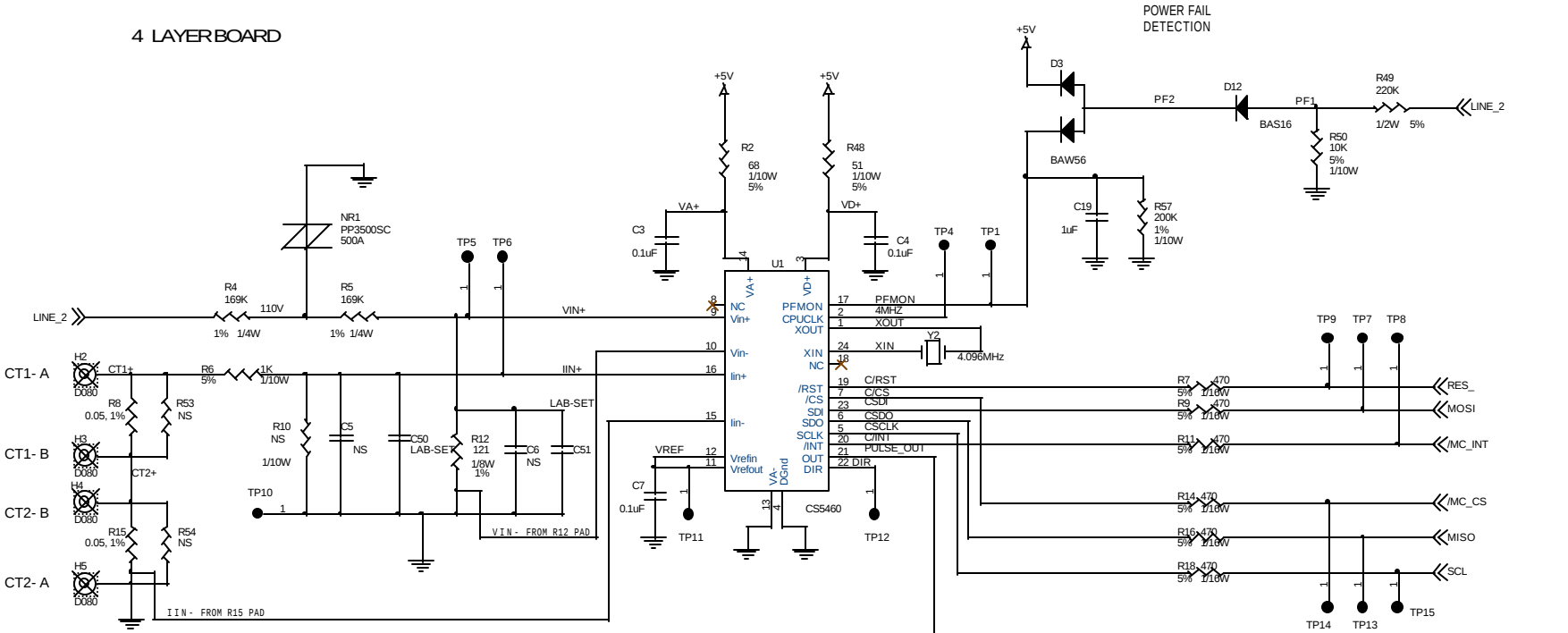
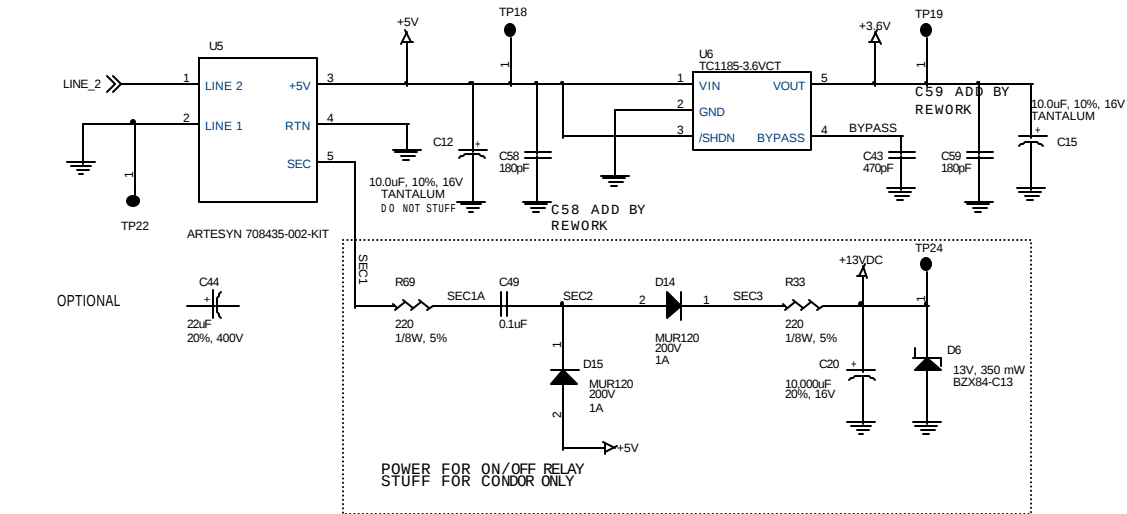
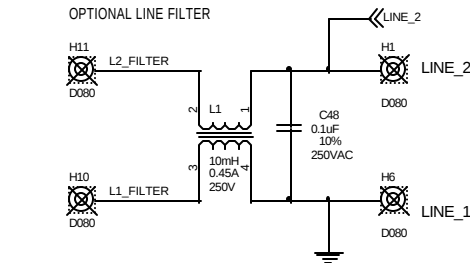


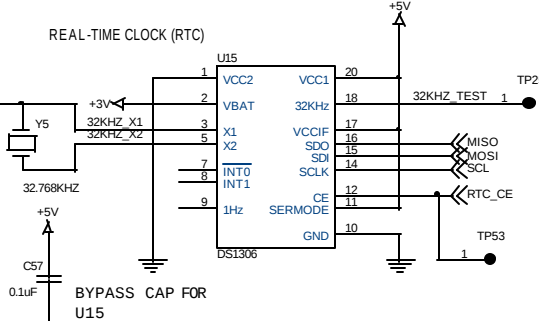
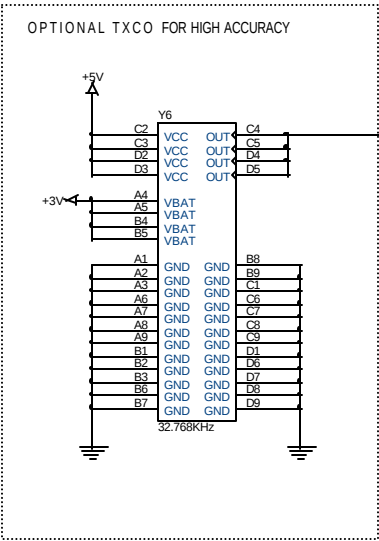
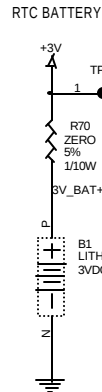
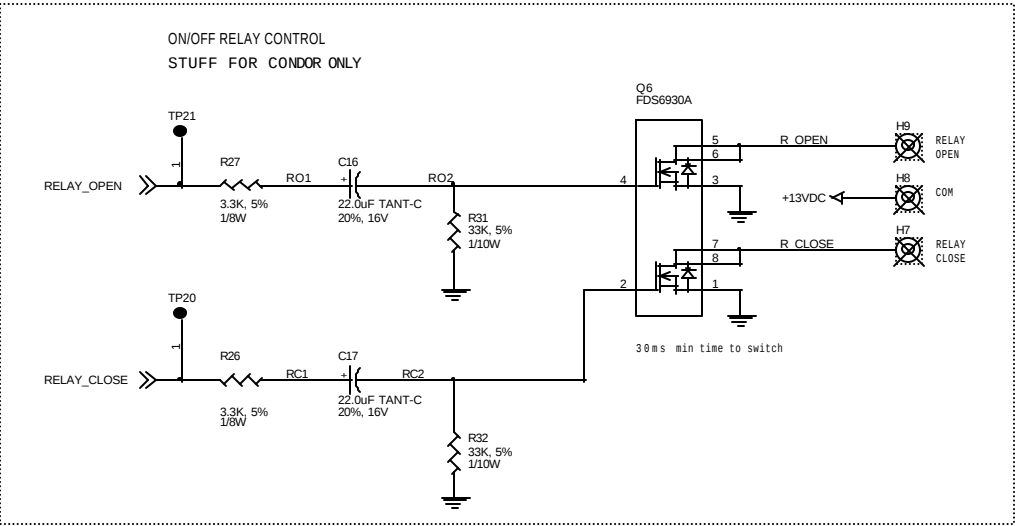
REV	CHANGES / ECO #	APPVD	DATE
1	First cut		3/24/00
2	Eliminated U2,U8,U4; changed power supply and power fail circuit; added U14 , Flash Memory; added frequency measurement circuit		6/13/00
3	ADDED R59,R60,NR1(SIDACTOR). DELETED C47,R17,NR1(VARISTOR) CONNECT D5-3 TO -10V. NO CONNECT TO C44,AND C45. GND-SIGNAL CHANGED TO GND-DIGITAL. CHANGED C12 TO TANT-C		8/8/00
4	ADDED REAL-TIME CLOCK WITH BATTERY BACK-UP. ADDED B1,Y5, AND U15. CHANGED TEST POINT DESIGNATIONS. CHANGED RELAY DRIVER CIRCUIT. ADDED IRDA.		9/5/00
5	DELETED D4; ADDED R69 TO FX +13V POWER SUPPLY. ADDED C57 BYPASS CAP FOR U15; ADDED R70 TO LIMIT BATTERY I.		10/24/00
6	Layout change only.		11/14/00
9	Layout change only.		11/21/00



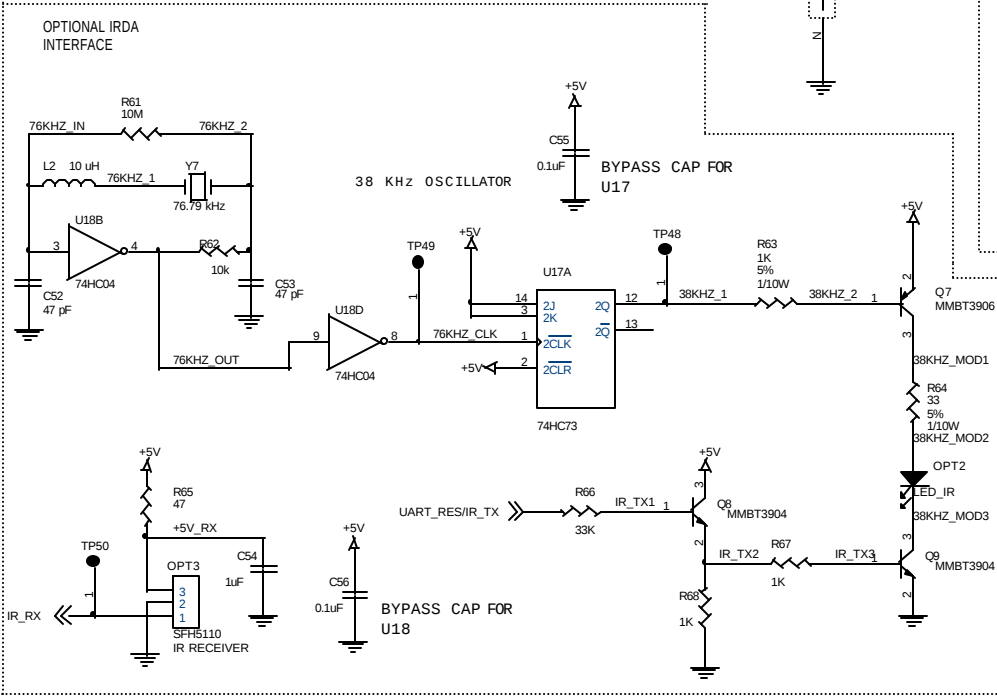
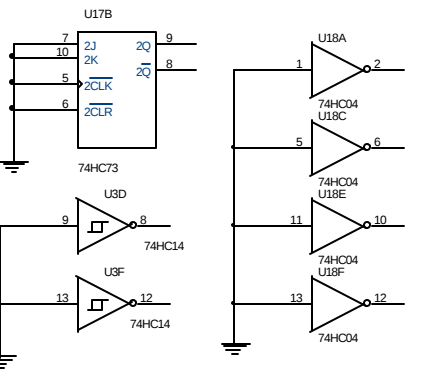
Values of shunt resistors R8, R15 calculated for 200A, 1000:1 CTs



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UNUSED GATES



APPROVALS	DATE	APPROVALS	DATE
DRAWN		MFG	
ENGR		MATL	
ENGR		MKTG	

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