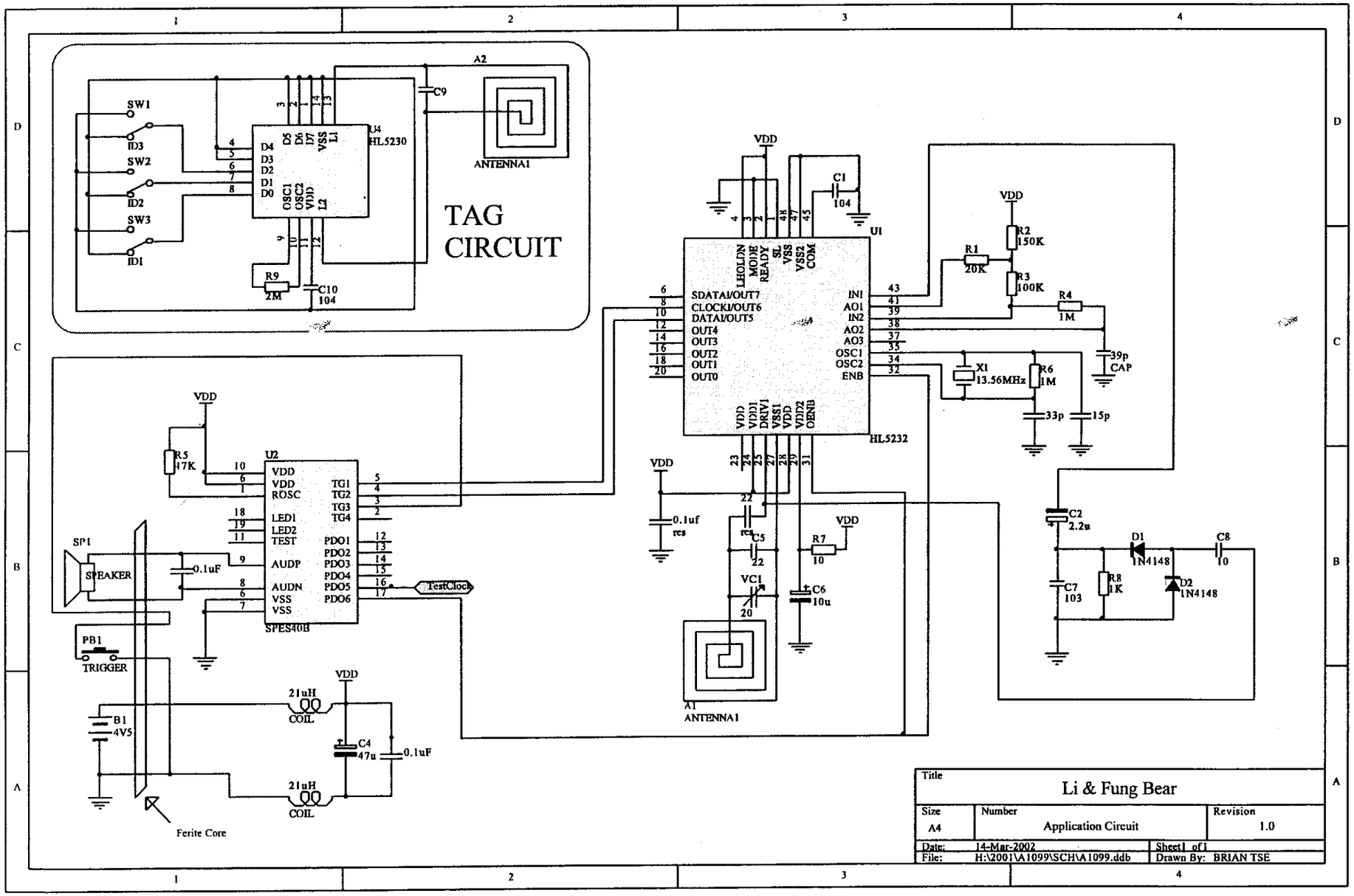
TAG CIRCUIT
SW1
SW2
SW3
ID1
ID2
ID3
U4
HL5230
A2
ANTENNA1
C9
R9
2M
C10
104
U1
HL5232
SDATA1/OUT7
CLOCK1/OUT6
DATA1/OUT5
OUT4
OUT3
OUT2
OUT1
OUT0
IN1
AO1
IN2
AO2
AO3
OSC1
OSC2
ENB
C1
104
R1
20K
R2
150K
R3
100K
R4
1M
R6
1M
X1
13.56MHz
39p
CAP
33p
15p
C2
2.2u
D1
1N4148
D2
1N4148
C8
10
C7
103
R8
1K
R7
10
C5
22
VC1
20
C6
10u
0.1uf
res
A1
ANTENNA1
U2
SPES40B
R5
47K
SP1
SPEAKER
0.1uF
PB1
TRIGGER
B1
4V5
21uH
COIL
C4
47u
0.1uF
Ferite Core
TestClock
VDD
Title
Li & Fung Bear
Size
A4
Number
Application Circuit
Revision
1.0
Date:
14-Mar-2002
Sheet1 of1
File:
H:\2001\A1099\SCH\A1099.ddb
Drawn By:
BRIAN TSE