雷騰電子產品有限公司
LITECH ELECTRONIC PRODUCTS LTD.

Date : Oct 23, 1999

To : Intertek Testing Services
Attn : Liza Chan
From : Gorden Wong

Re : Remote control information

Attached please find the circuit diagram, block diagram and circuit description for the remote control project.

Circuit description:

The input line voltage 120VAC is being step down and rectified by the rectifier circuit (formed by C4, R12, D2-D5). Which is then FED through the voltage regular (formed by Q105, ZD102) to provided a constant DC4.5v supply to the receiver circuit.

The R.F. receiver and detector circuit(formed by Q101, L101-103 and C101-107) amplifying and demodulating the transmitter signal.

The audio signal present across C107 is then amplified by two stage of audio amplifier (formed by Q102 and Q103). Which is then FED through a filter circuit (formed by ICIC, ICIB, R113-R117 and C115-C117) to eliminate the unwant signal.

The signal output from ICIB is then FED through a comparator circuit. If the selected signal is strong enough, the comparator circuit will output a signal to drive the relay LY101 by means of relay driver (Q104 and Q105).

The delay circuit (formed by C122 and R125) maintains the opening of relay LY101 during the test period.

The battery recharge circuit charging the back-up battery through R1, R2 and R4. When the voltage across the battery reaches a cretain level, Q1 will being to conduct, this will limit the charging current and prevent the back-up battery from being over charged.

Regards,

Gorden

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