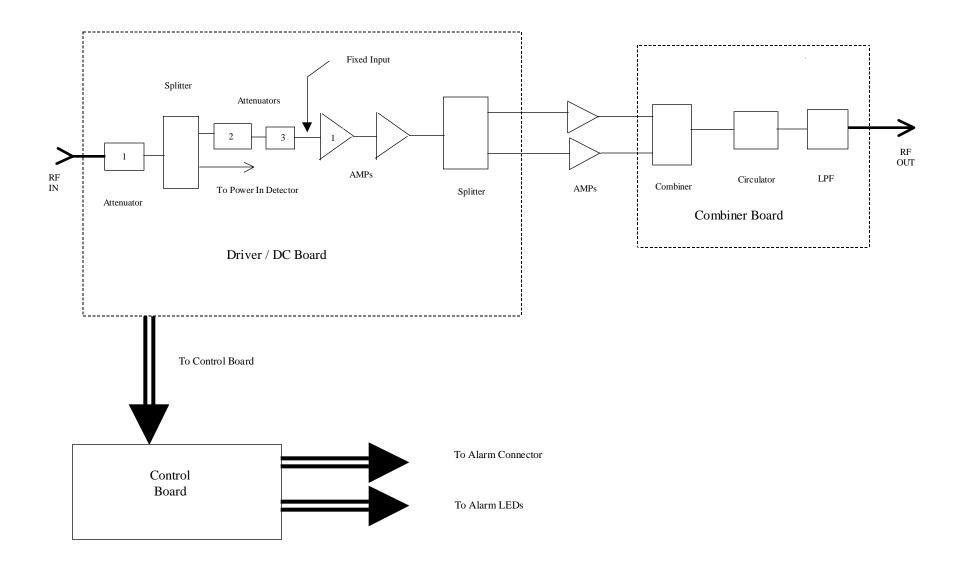
This is to reply to your email correspondence reference number 14801, 731 reference number EA96680, FCC ID OEUPA882M180WS2

1. We are requesting that the spurious, radiated, conducted and occupied bandwidth tests at 20W input to be waived due to the fact that the PA is designed so that the input RF level to the driver gain stage always remains constant. In other words, the PA's overall RF characteristic will not change with the input level's variation.

As show in block diagram below, the variable RF signal at the PA input is always padded down to a fixed level before feeding to AMP1. This is done using a combination of resistive attenuator (1), voltage variable attenuator (2) and resistive network attenuator (3). The resistive attenuator (1) is varied from 5, 10, 15 or 20dB depending on the customer's input level. The voltage variable attenuator (2), which controlled by the microprocessor, is then used to fine tune the RF signal to a fixed level expected at AMP1 input. The result is the same RF signal will be passing through the entire gain lineup every time, independence of the PA input.

- 2. A copy of the occupied bandwidth with digital input file is uploaded as Test Report: occupied bandwidth data. Note that the same copy was also uploaded earlier under Add Attachment as part of the submitted data.
- 3. Intermodulation test is not required for the PA since it is operated as a single channel system.

TRL TECHNOLOGIES, INC. FCC ID: OEUPA882M180WS2



PA BLOCK DIAGRAM