

Circuit Description for BT Module:

BlueCore4-ROM CSP is designed to reduce the number of external components required. This ensures that production costs are minimised.

The device incorporates auto-calibration and built-in self-test (BIST) routines to simplify development, type approval and production test. All hardware and device firmware is fully compliant with the Bluetooth v2.0 Specification (all mandatory and optional features).

To improve the performance of Bluetooth is available including a variety of hardware signalling:
basic activity signalling and Intel WCS activity and channel signalling.