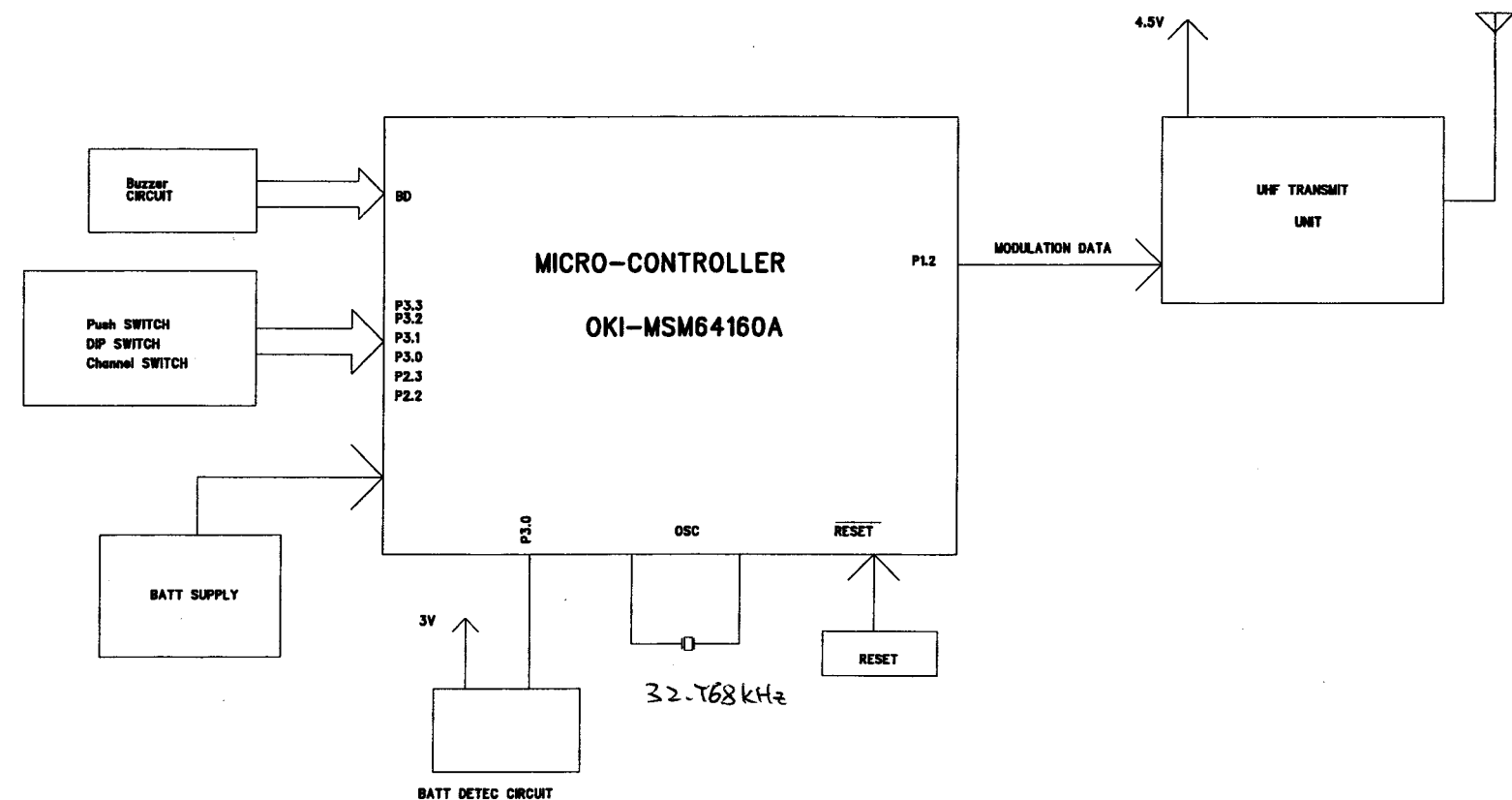


6 5 4 3 2 1

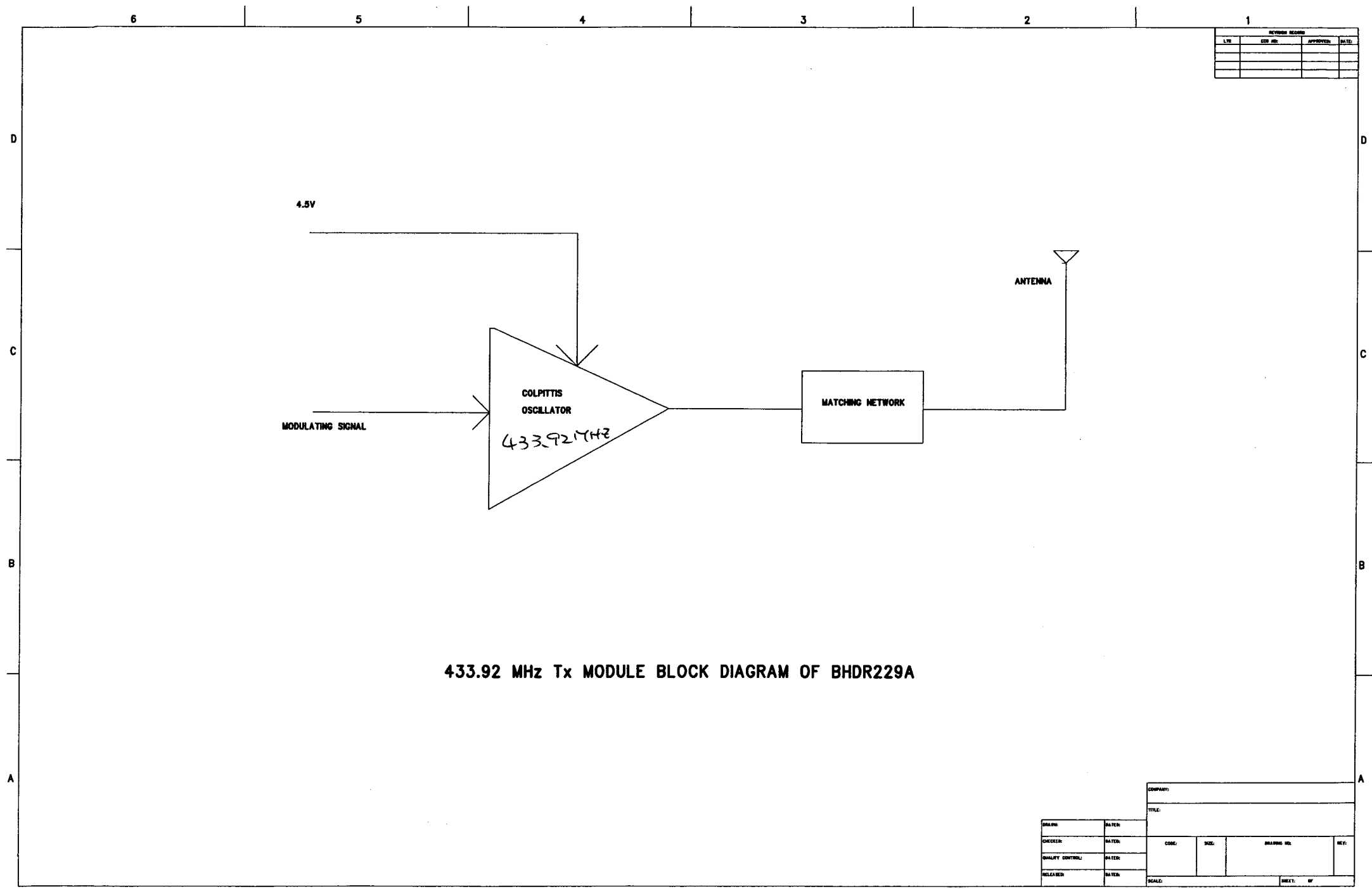
REVISION RECORD			
LTN	REV. NO.	APPROVED	DATE

D
C
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A



SYSTEM BLOCK DIAGRAM OF BHR229A

NAME:				COMPANY:			
DESIGNER:				TITLE:			
DATE:	DATE:	CODE:	SIZE:	ISSUE NO.:	REV.:		
RELEASED:	DATE:	SCALE:			SHEET: OF		



REVISION RECORD			
LT#	CR#	APPROVED	DATE

433.92 MHz Tx MODULE BLOCK DIAGRAM OF BHDR229A

DATE:				DATE:				COMPANY:			
CHECKER:				DATE:				TITLE:			
QUALITY CONTROL:				DATE:				CODE:	SIZE:	DRAWING NO.:	REV.:
RELEASED:				DATE:				SCALE:		SHEET: OF	