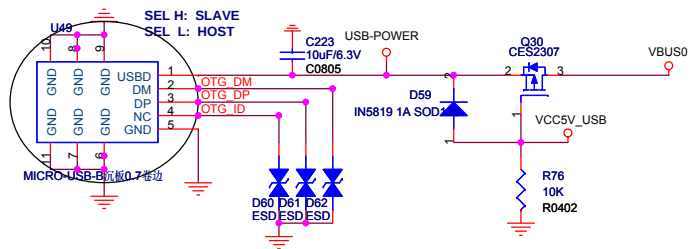
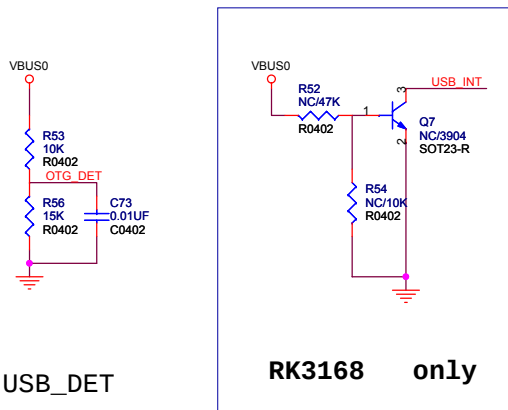


USB OTG

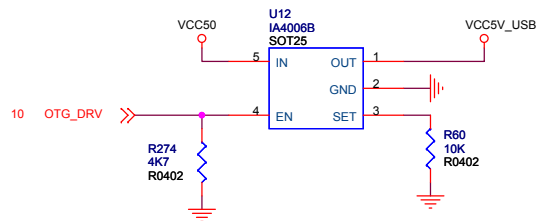


USB OTG CONNECTOR



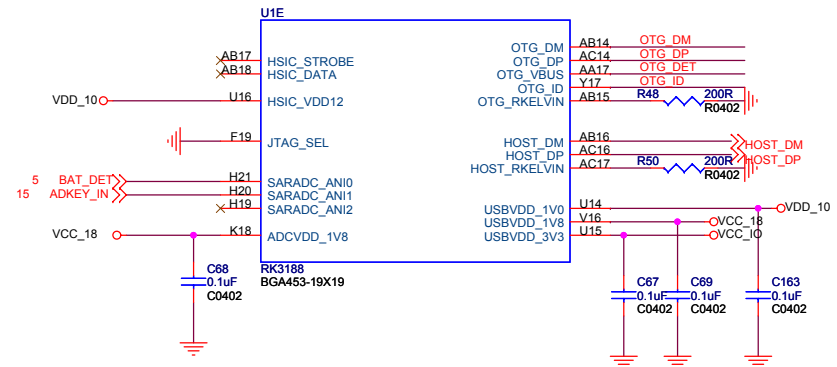
USB_DET

RK3168 only

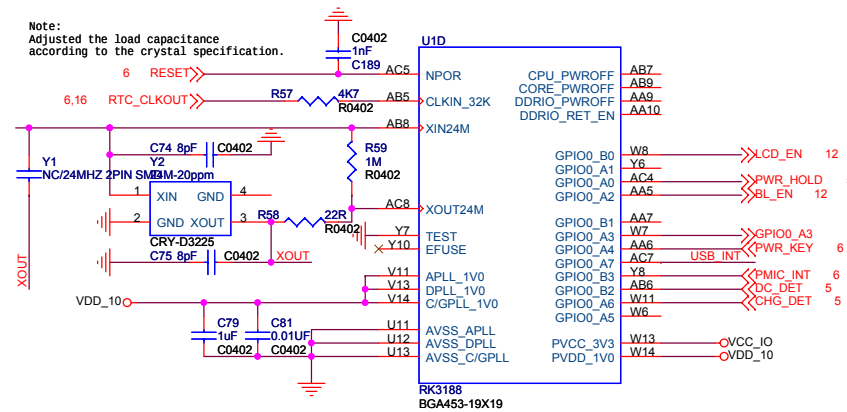


Current limit 640mA

USB HOST POWER

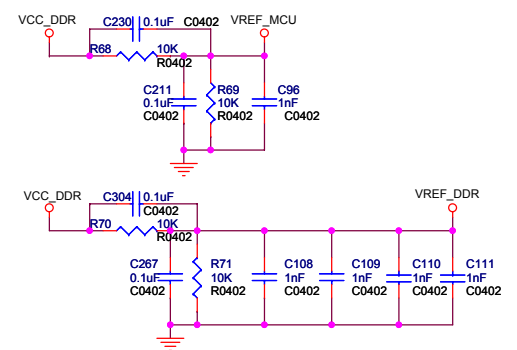
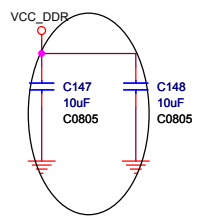
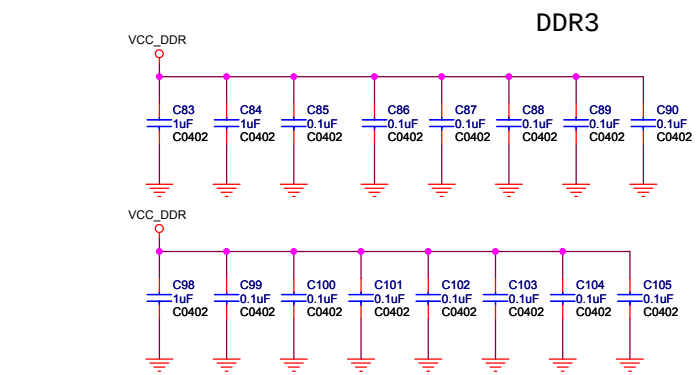
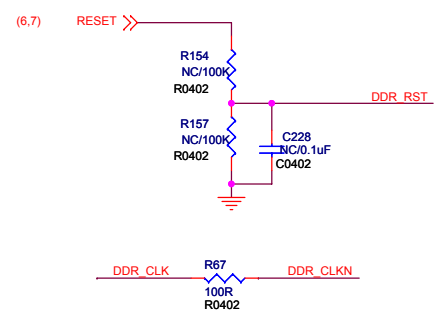
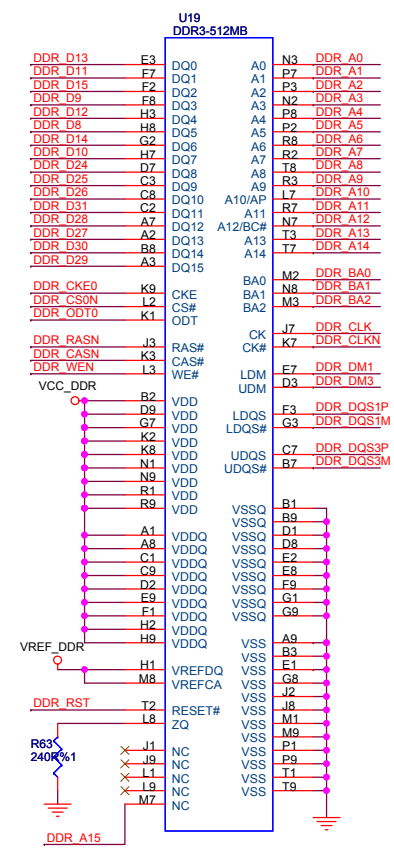
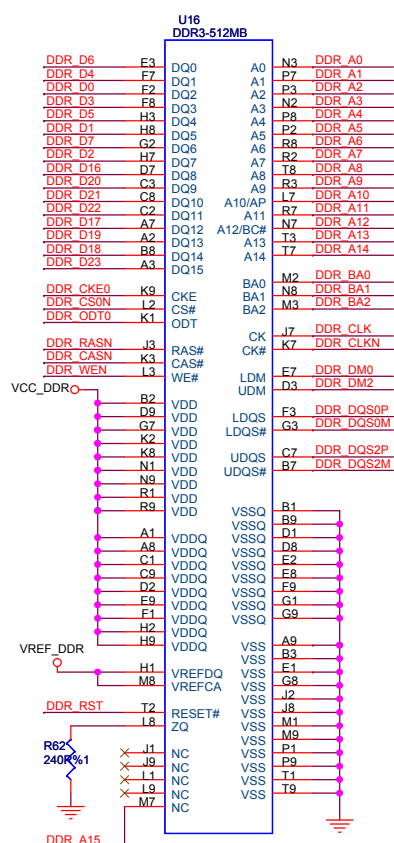
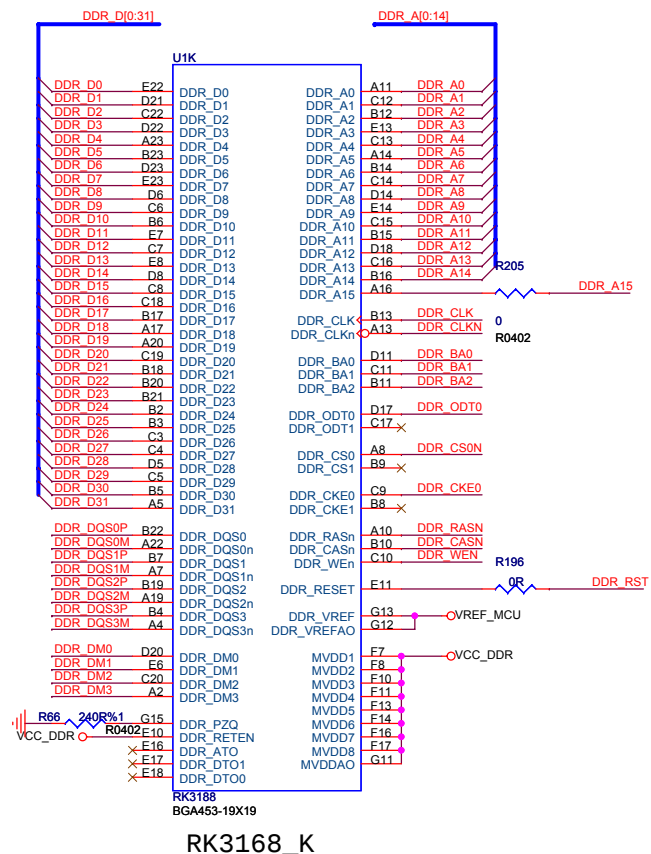


RK3188_E



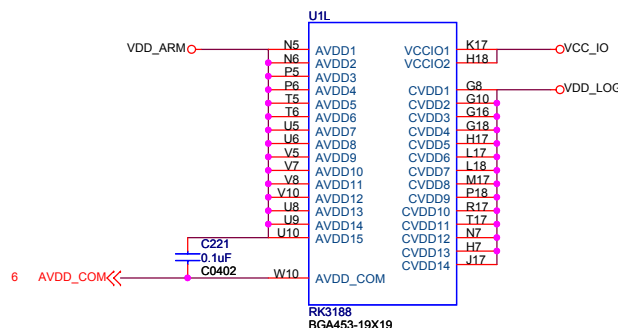
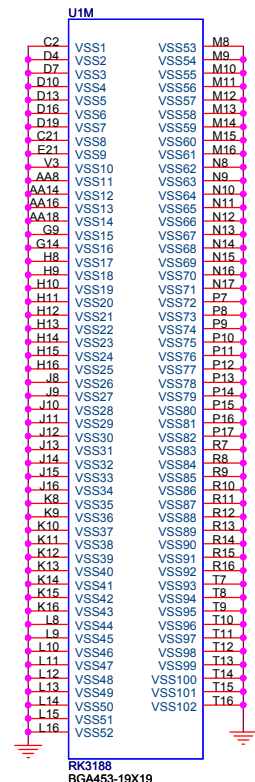
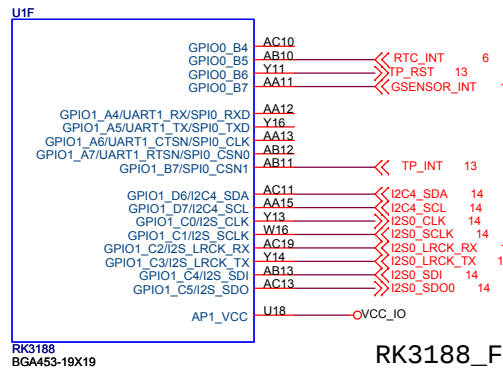
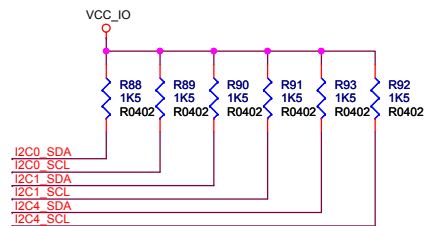
RK3188_D

Title: USB OTG/VIB		
File: RK3188&68_RK610_V1.0		REV: 1.0
Create Date:	Saturday, December 1, 2012	
Modify Date:	Thursday, July 03, 2014	

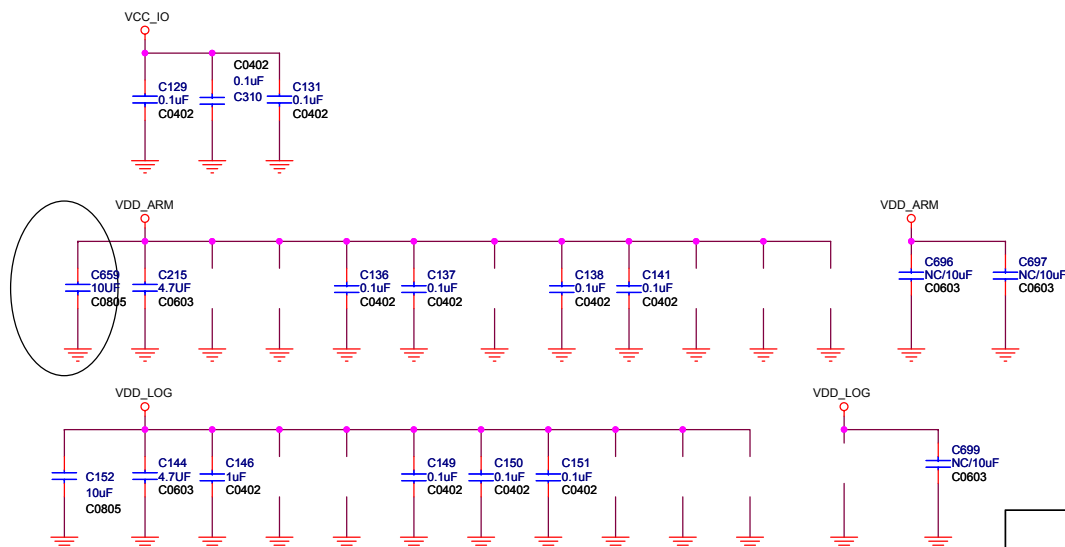
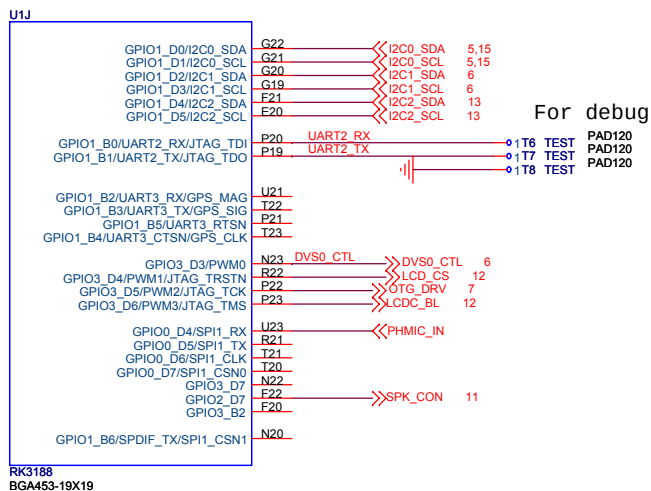


DDR FILTER

Title: DDR3		
File: RK3188&68_RK610_V1.0		REV: 1.0
Create Date: Saturday, December 1, 2012		
Modify Date: Monday, June 30, 2014		



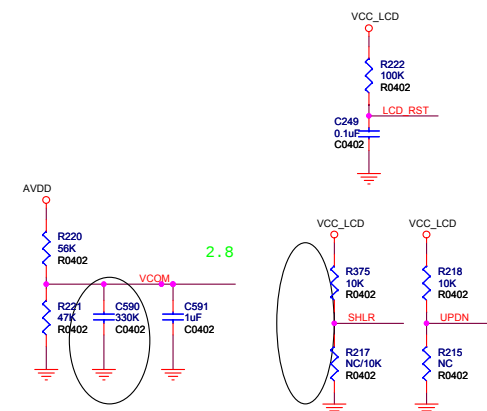
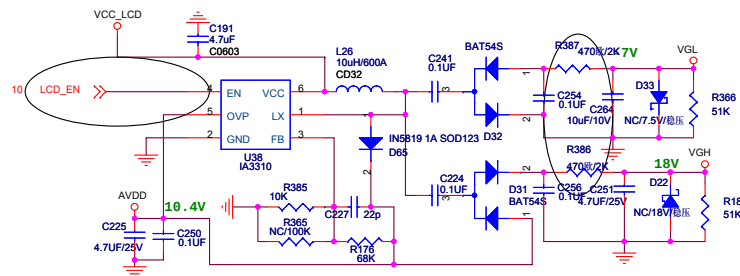
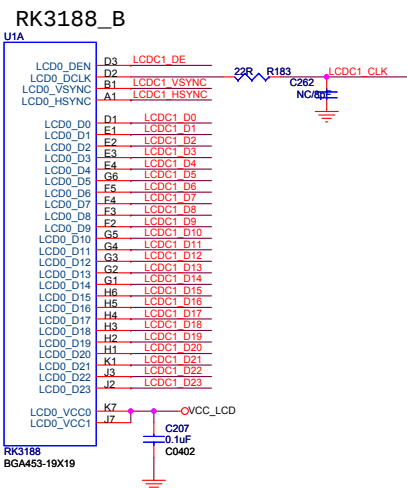
Note:
Place these filter capacitors under CPU.



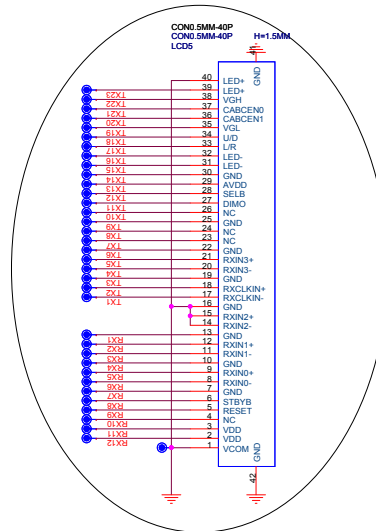
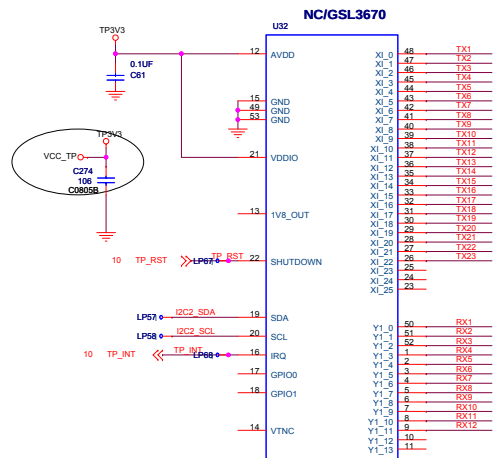
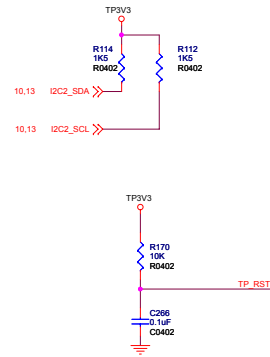
Title: GPIO		
File: RK3188&68_RK610_V1.0		REV: 1.0
Create Date:	Saturday, December 1, 2012	
Modify Date:	Thursday, July 03, 2014	

RK3188_J

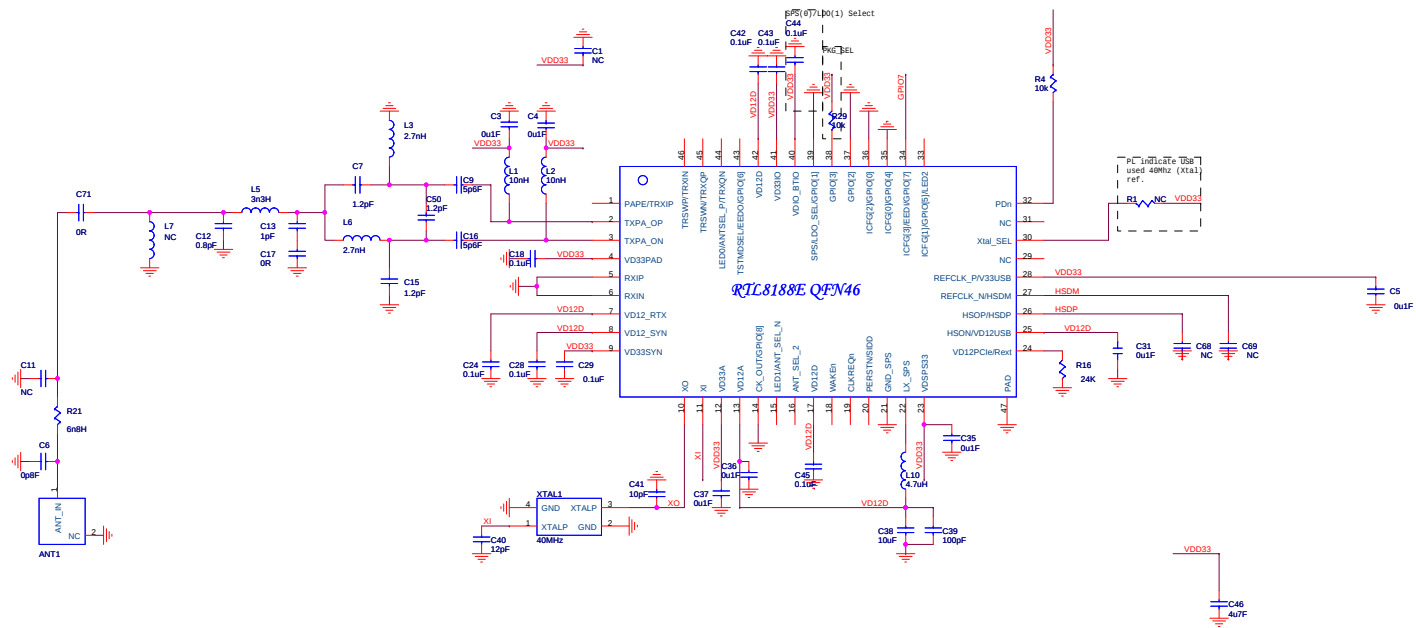
RK3188 CORE POWER FILTER



Title: LCD panel
File: RK3188&68_RK610_V1.0
Create Date: Saturday, December 1, 2012
Modify Date: Tuesday, July 01, 2014

[illegible]

Title: Touch panel	
File: RK3188&68_RK610_V1.0	REV: 1.0
Create Date:	
Modify Date: Friday, July 11, 2014	



wifi module