

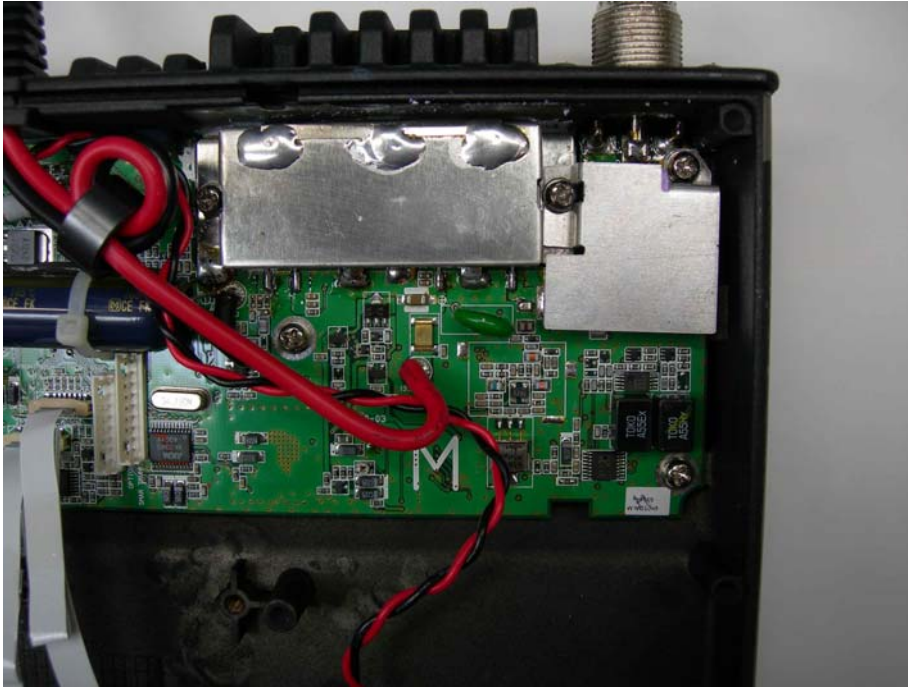
DUT without Case 1



DUT without Case 2



DUT without Case 3

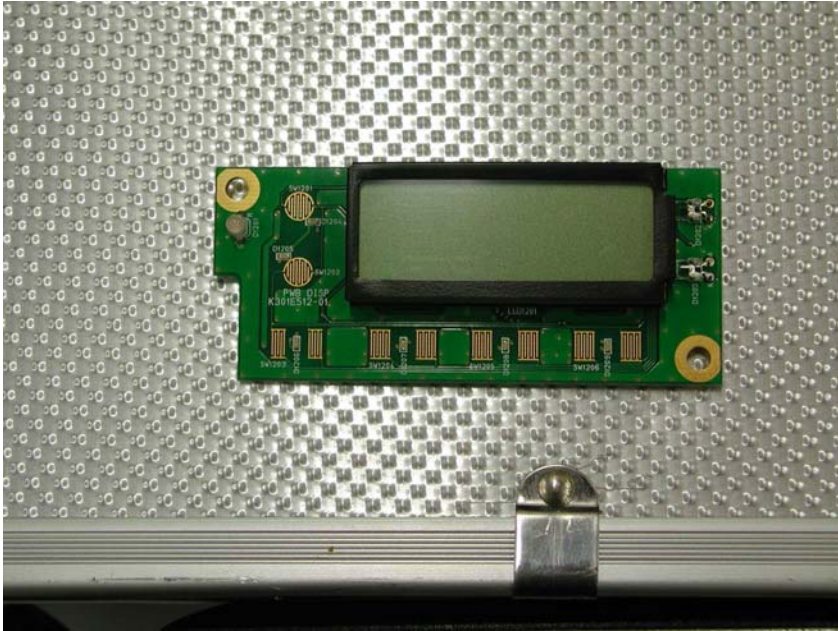


DUT without Case 4

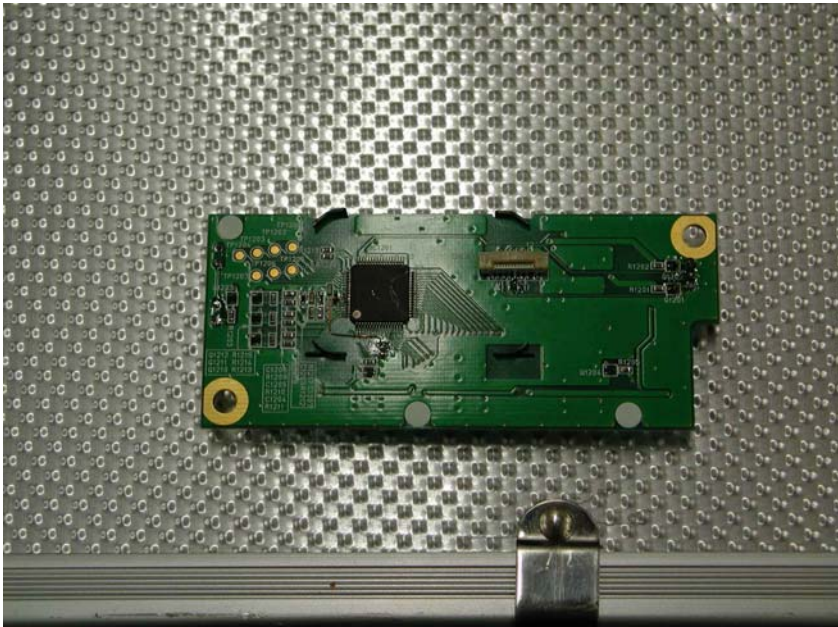




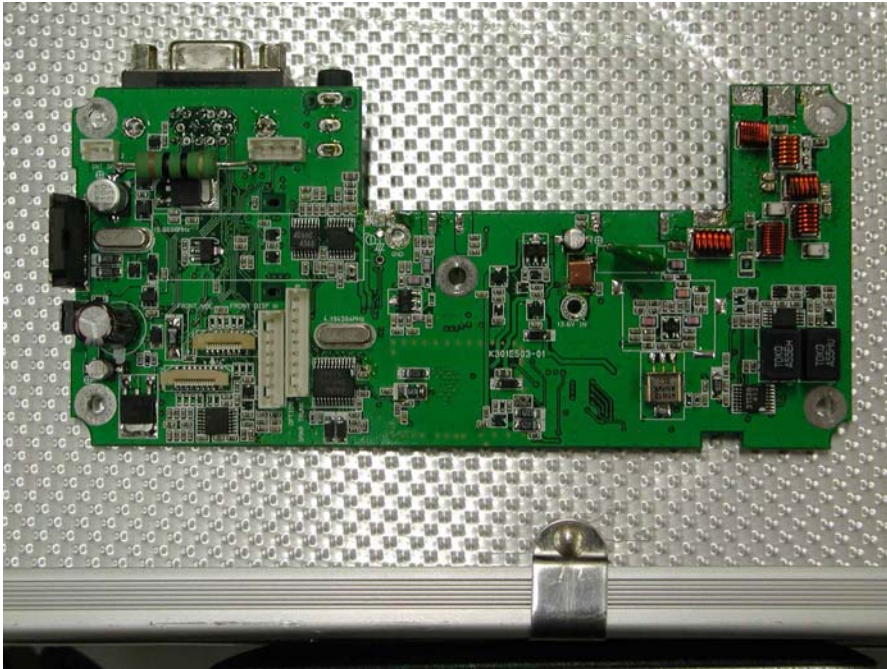
Front PCB 1



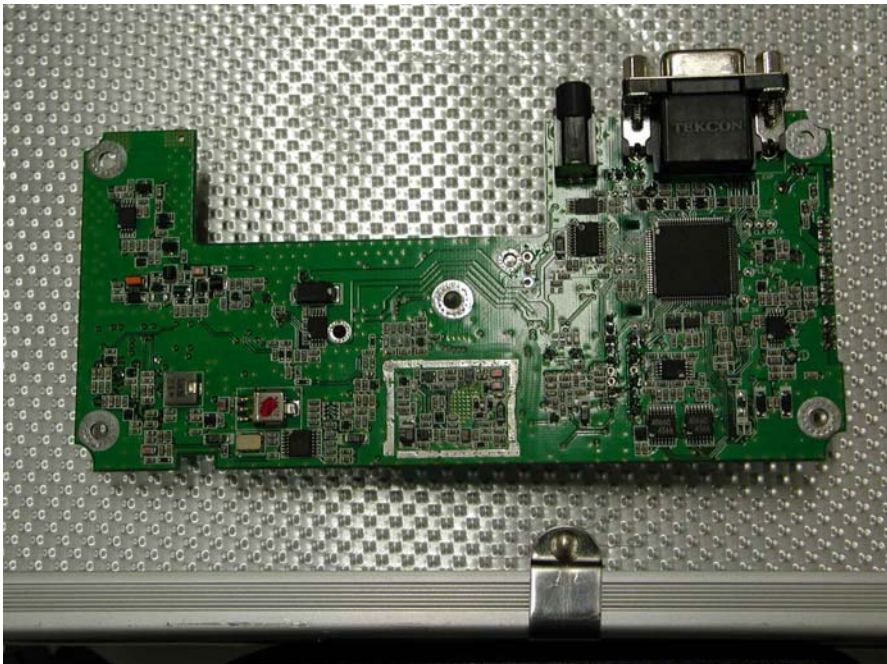
Front PCB 2



Main PCB 1

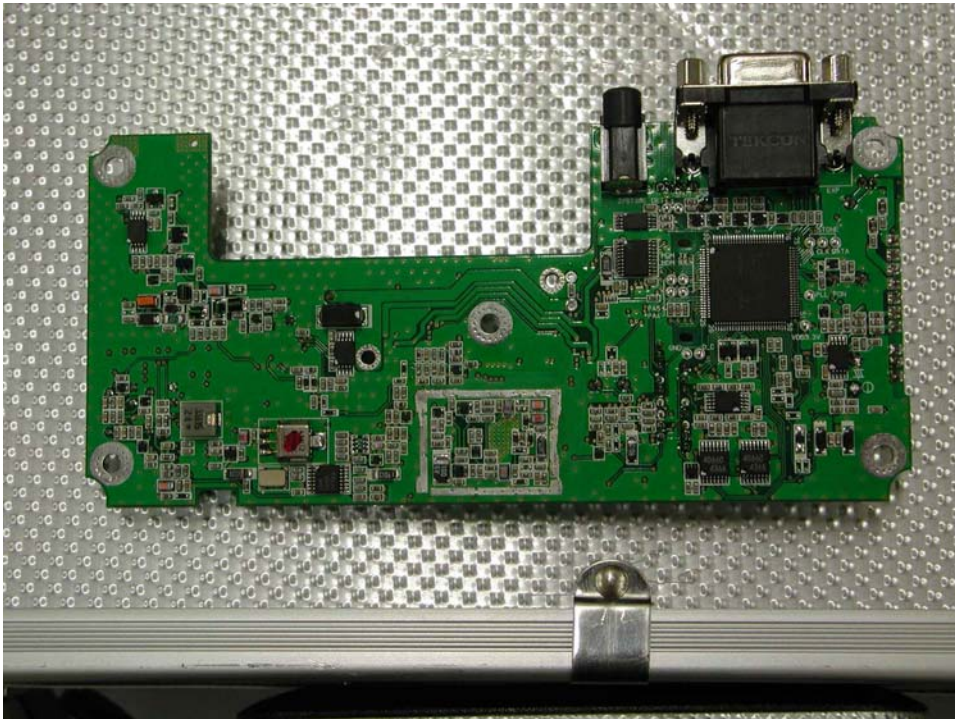


Main PCB 2

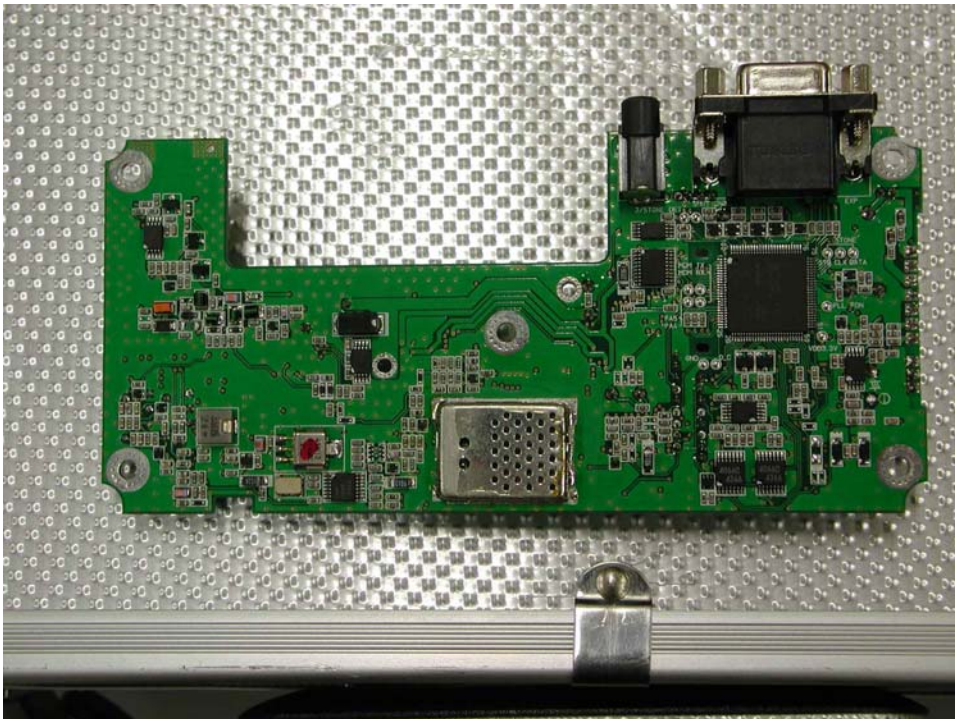




Main PCB 3

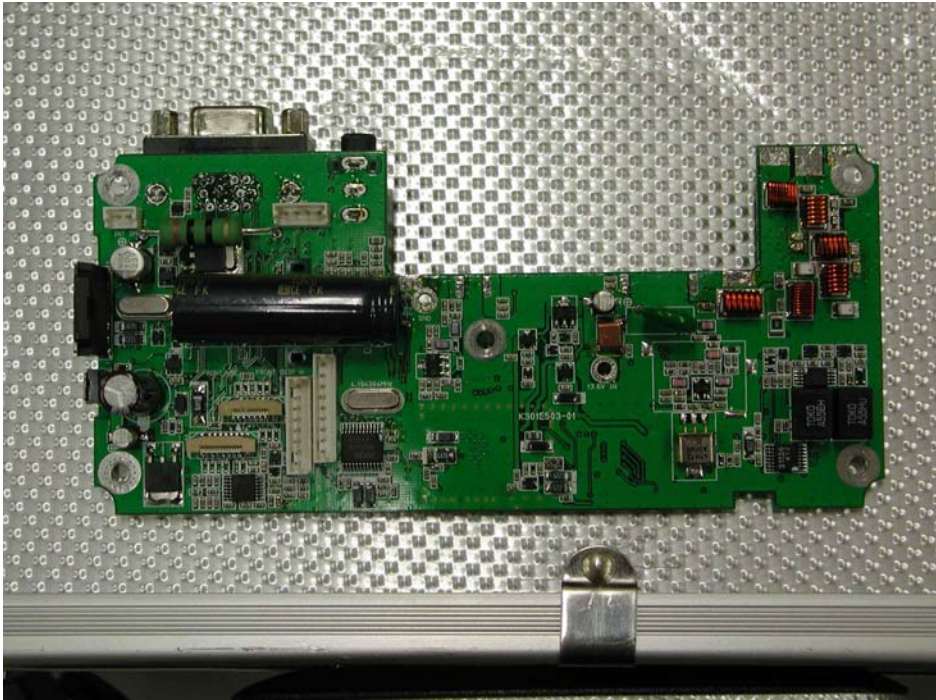


Main PCB 4

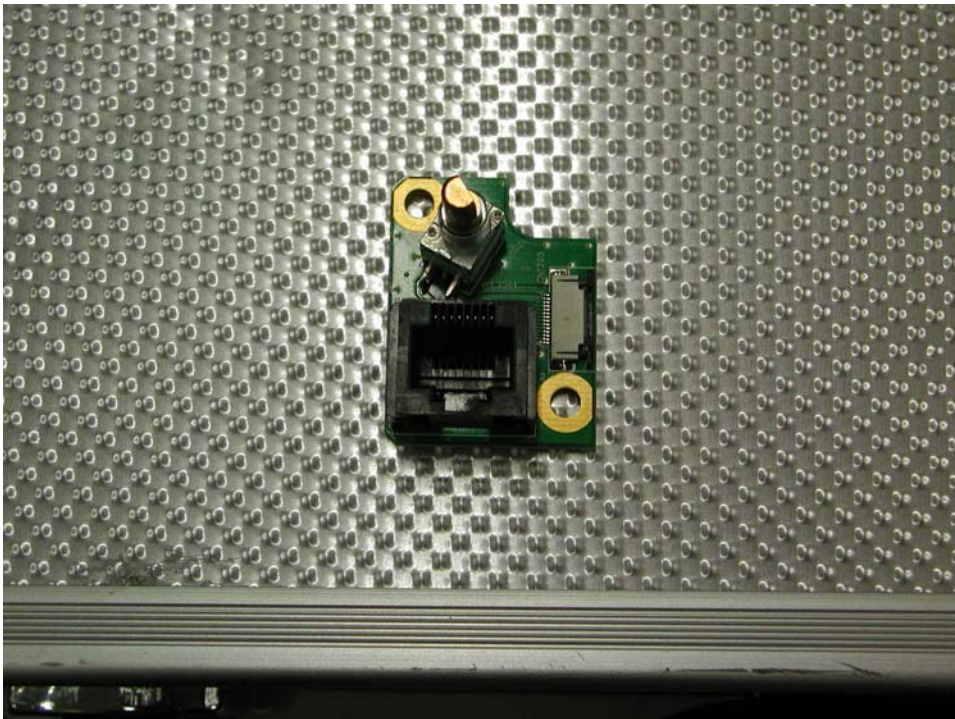




Main PCB 5



VR PCB 1



Main PCB 2 PCB 2

