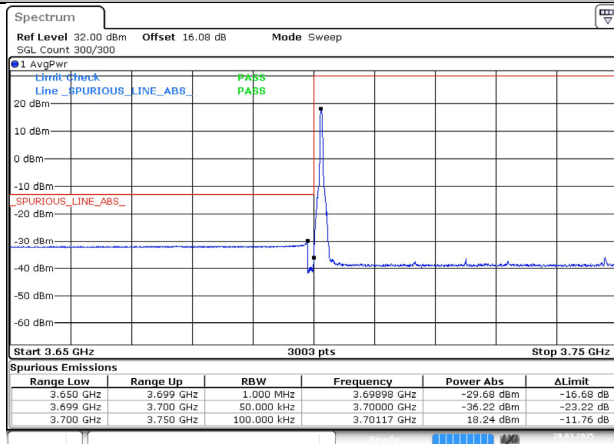


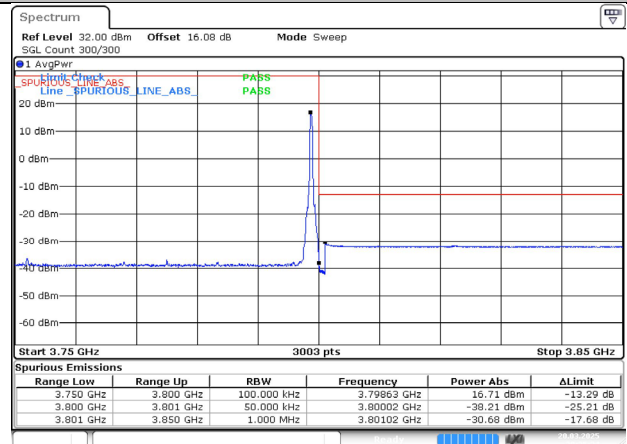
n78A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



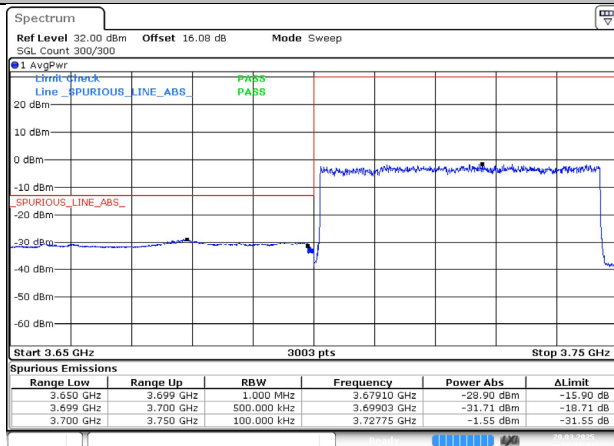
Date: 20 MAR 2025 20:33:22

HCH / DFT-Pi2BPSK / Edge_1RB_Right



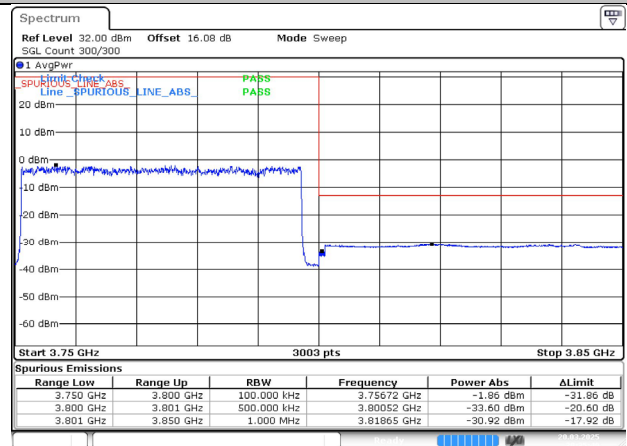
Date: 20 MAR 2025 20:35:34

LCH / DFT-Pi2BPSK / Outer_Full



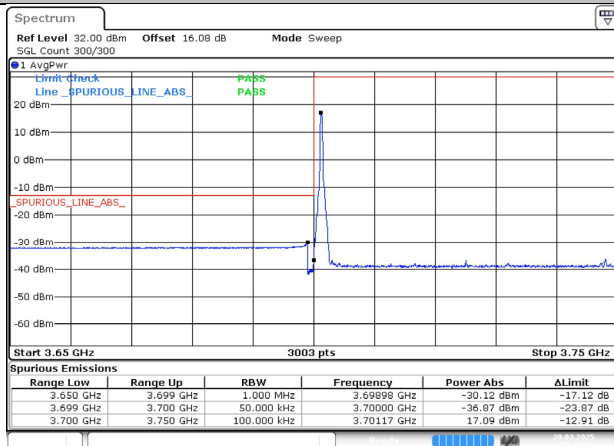
Date: 20 MAR 2025 20:33:44

HCH / DFT-Pi2BPSK / Outer_Full



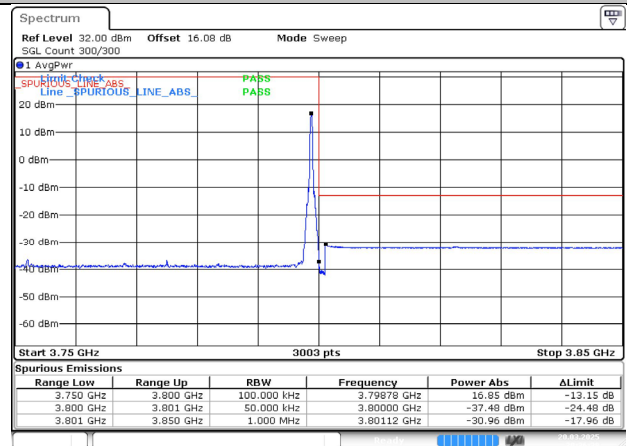
Date: 20 MAR 2025 20:35:50

LCH / DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2025 20:34:30

HCH / DFT-QPSK / Edge_1RB_Right



Date: 20 MAR 2025 20:36:38

