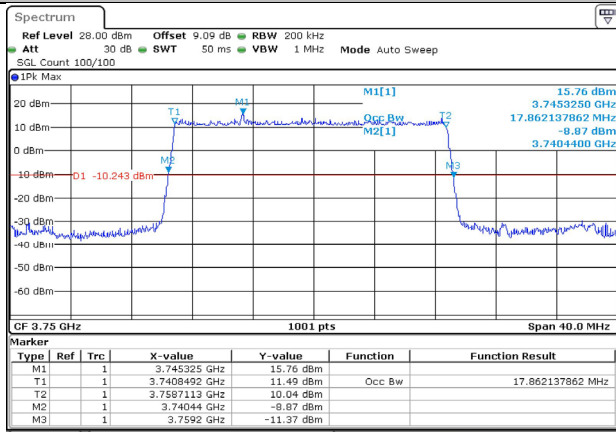


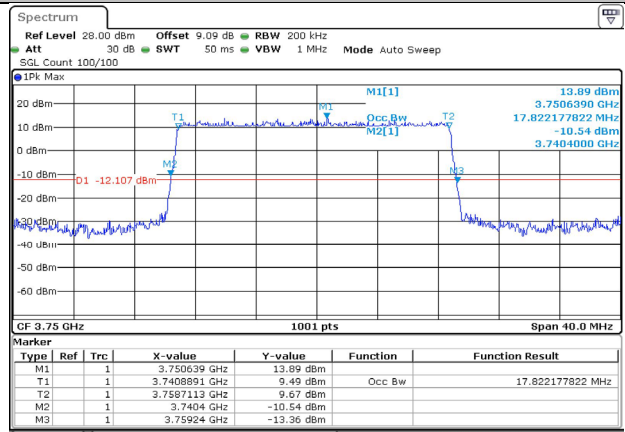
## n78A / 30KHz / 20MHz

## MCH / DFT-Pi2BPSK / Outer\_Full



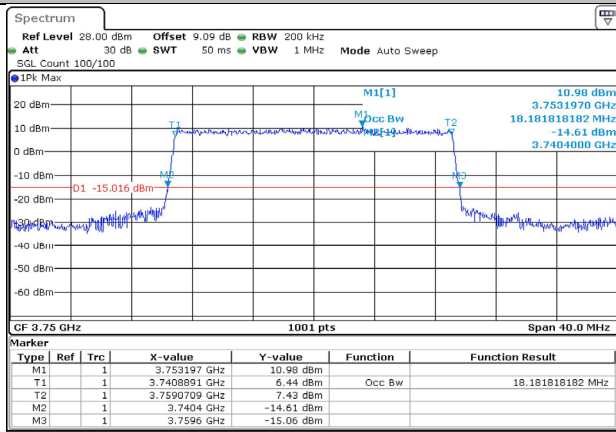
Date: 20 MAR 2025 19:53:20

## MCH / DFT-QPSK / Outer\_Full



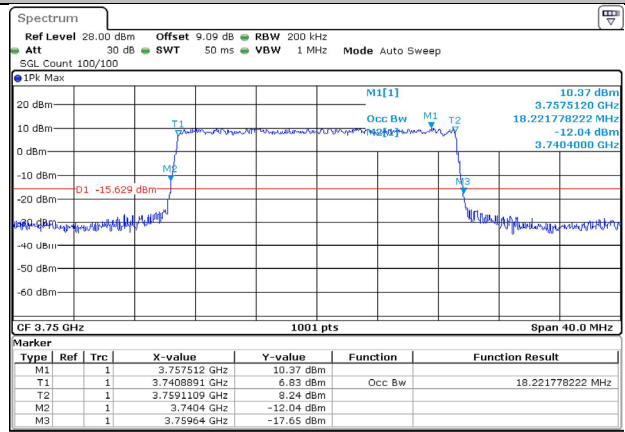
Date: 20 MAR 2025 19:53:39

## MCH / CP-QPSK / Outer\_Full



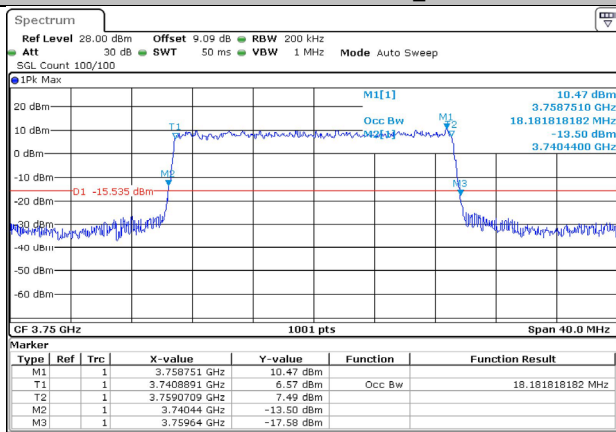
Date: 20 MAR 2025 19:54:18

## MCH / CP-16QAM / Outer\_Full



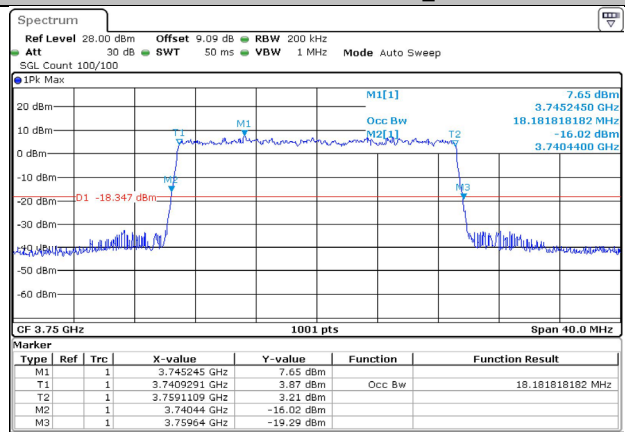
Date: 20 MAR 2025 19:54:26

## MCH / CP-64QAM / Outer\_Full



Date: 20 MAR 2025 19:54:36

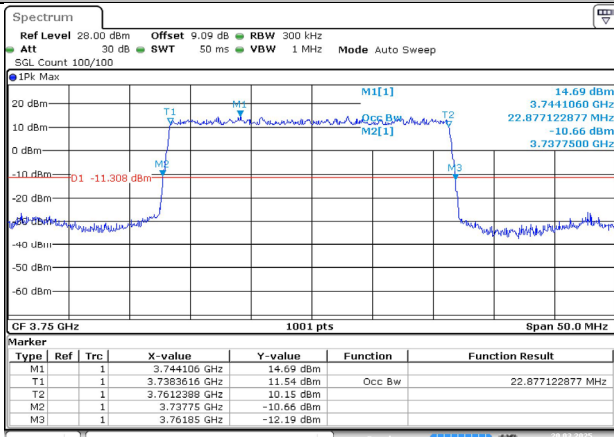
## MCH / CP-256QAM / Outer\_Full



Date: 20 MAR 2025 19:54:47

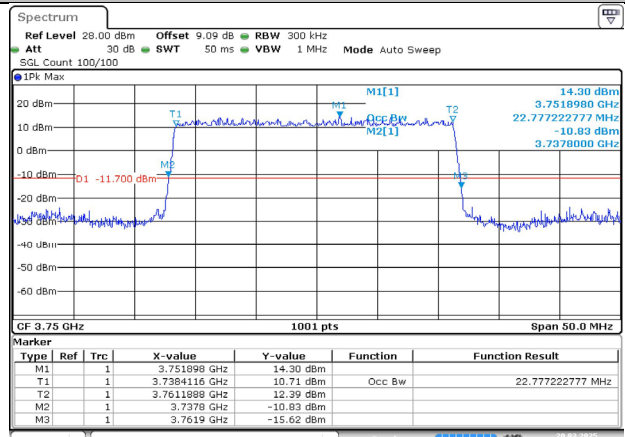
## n78A / 30KHz / 25MHz

## MCH / DFT-Pi2BPSK / Outer\_Full



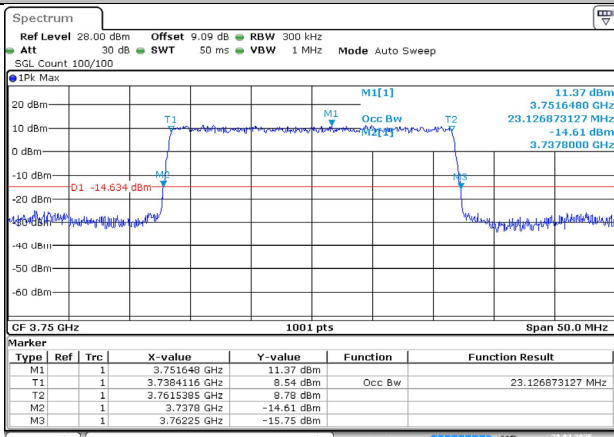
Date: 20 MAR 2025 19:55:09

## MCH / DFT-QPSK / Outer\_Full



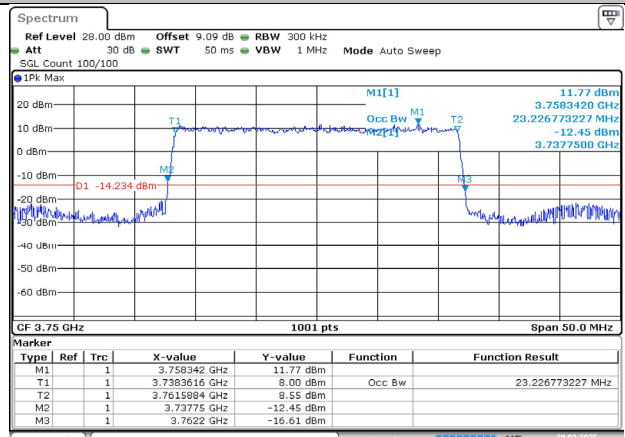
Date: 20 MAR 2025 19:55:27

## MCH / CP-QPSK / Outer\_Full



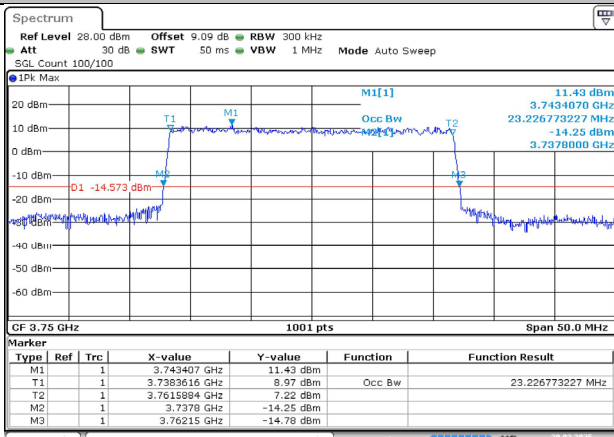
Date: 20 MAR 2025 19:56:03

## MCH / CP-16QAM / Outer\_Full



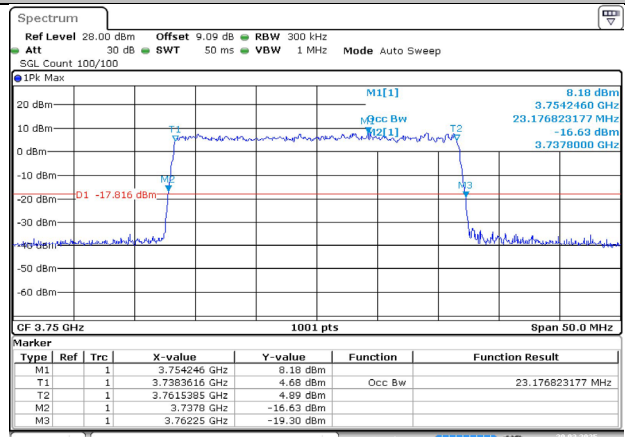
Date: 20 MAR 2025 19:56:11

## MCH / CP-64QAM / Outer\_Full



Date: 20 MAR 2025 19:56:19

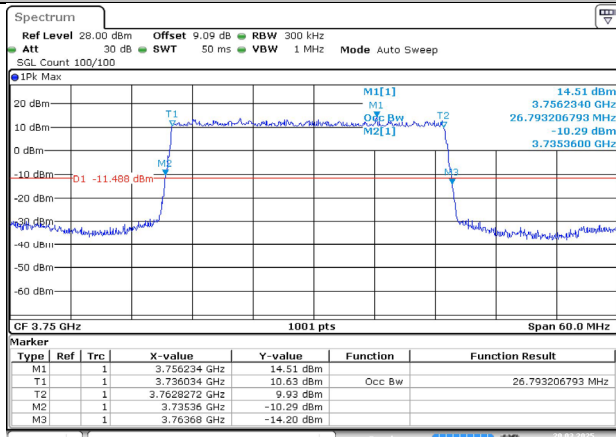
## MCH / CP-256QAM / Outer\_Full



Date: 20 MAR 2025 19:56:28

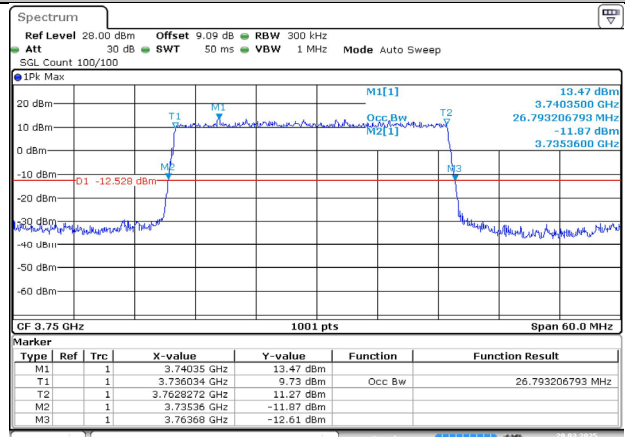
## n78A / 30KHz / 30MHz

## MCH / DFT-Pi2BPSK / Outer\_Full



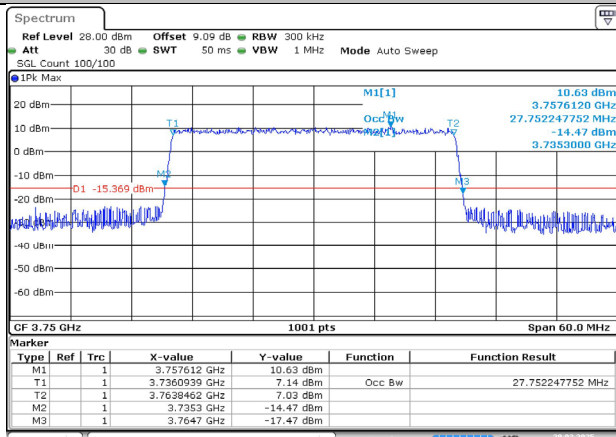
Date: 20 MAR 2025 19:56:49

## MCH / DFT-QPSK / Outer\_Full



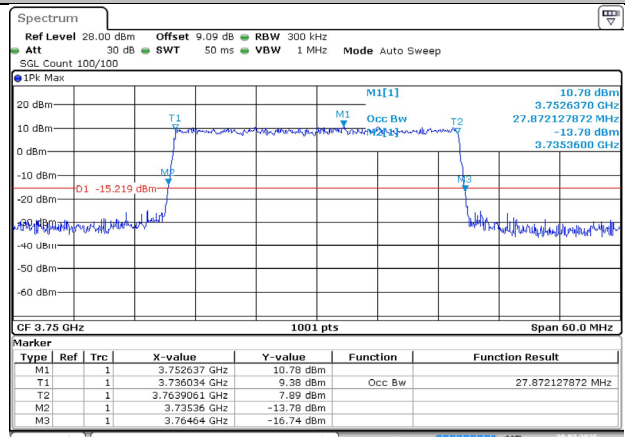
Date: 20 MAR 2025 19:57:13

## MCH / CP-QPSK / Outer\_Full



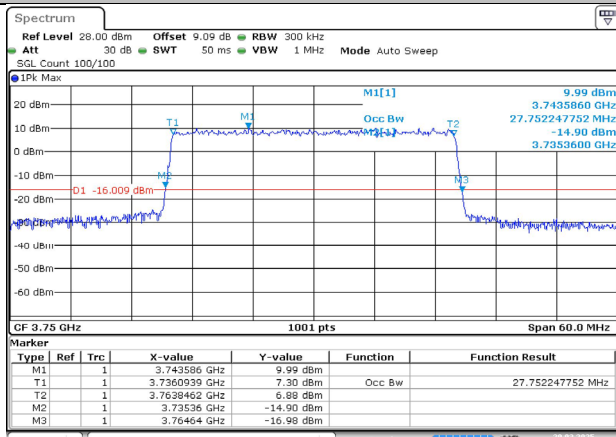
Date: 20 MAR 2025 19:57:49

## MCH / CP-16QAM / Outer\_Full



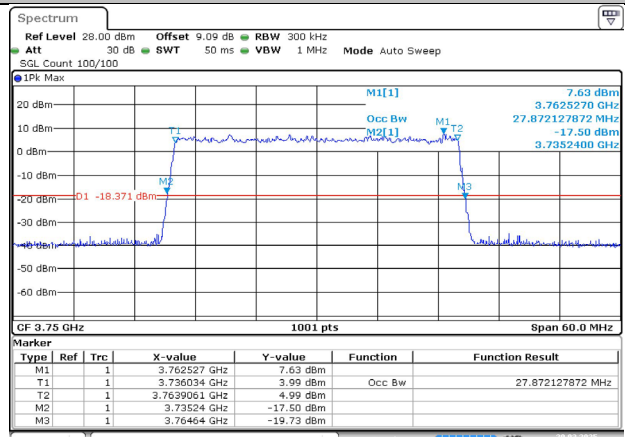
Date: 20 MAR 2025 19:57:57

## MCH / CP-64QAM / Outer\_Full



Date: 20 MAR 2025 19:58:04

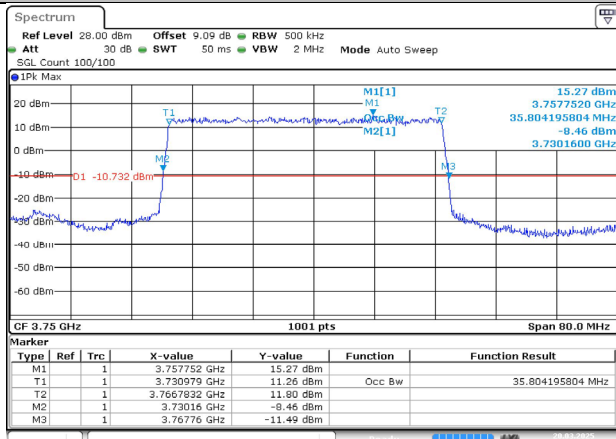
## MCH / CP-256QAM / Outer\_Full



Date: 20 MAR 2025 19:58:13

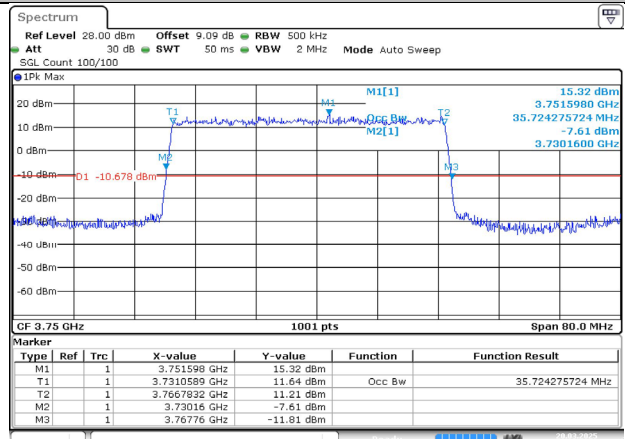
## n78A / 30KHz / 40MHz

## MCH / DFT-Pi2BPSK / Outer\_Full



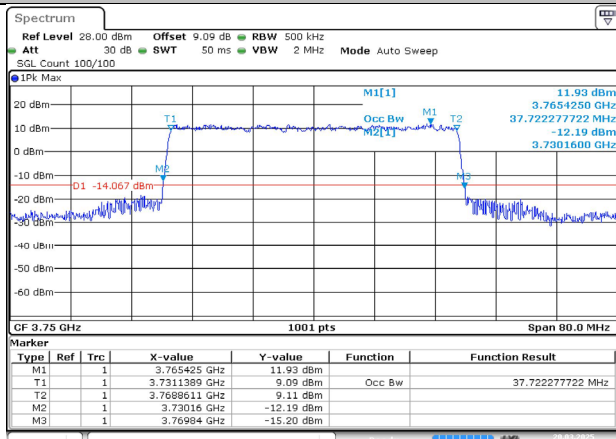
Date: 20 MAR 2025 19:58:34

## MCH / DFT-QPSK / Outer\_Full



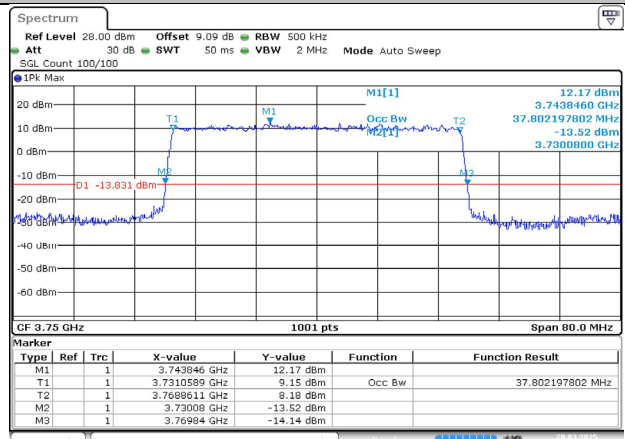
Date: 20 MAR 2025 19:58:53

## MCH / CP-QPSK / Outer\_Full



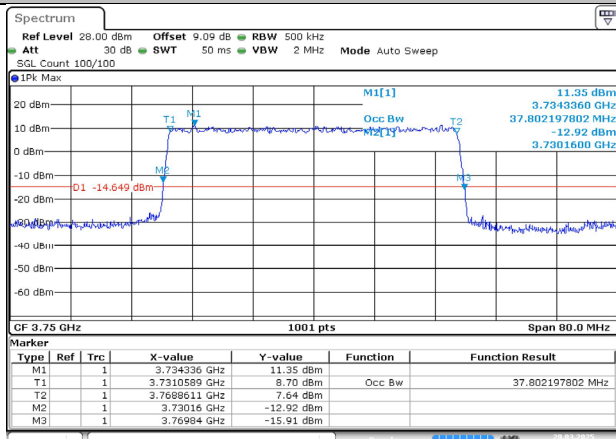
Date: 20 MAR 2025 19:59:27

## MCH / CP-16QAM / Outer\_Full



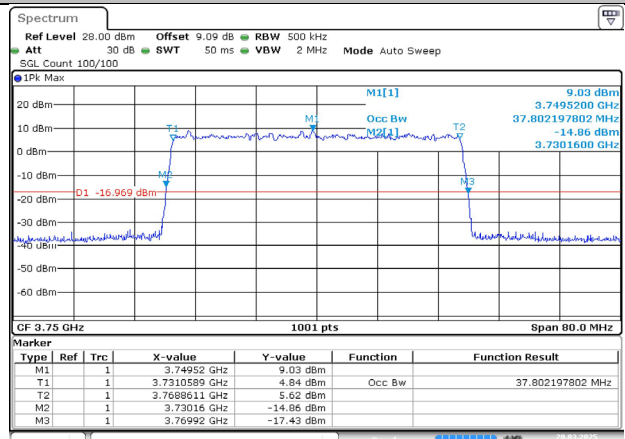
Date: 20 MAR 2025 19:59:35

## MCH / CP-64QAM / Outer\_Full



Date: 20 MAR 2025 19:59:43

## MCH / CP-256QAM / Outer\_Full



Date: 20 MAR 2025 19:59:52