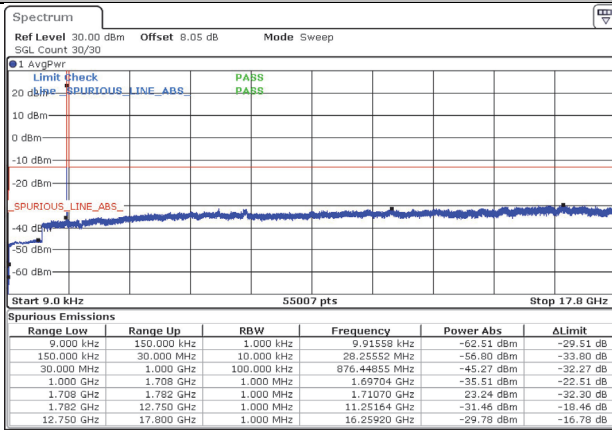


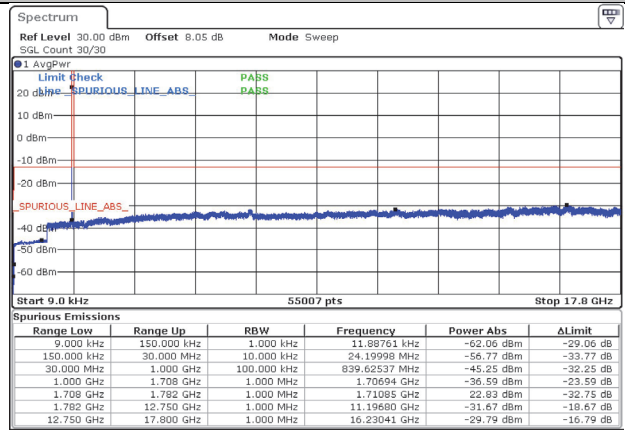
n66A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



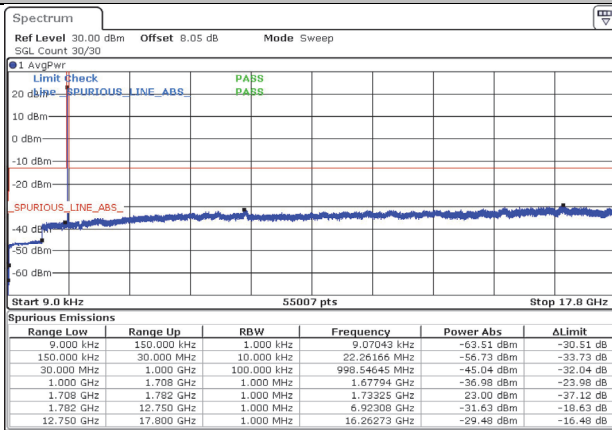
Date: 24 MAR 2025 17:47:06

LCH / DFT-QPSK / Edge_1RB_Left



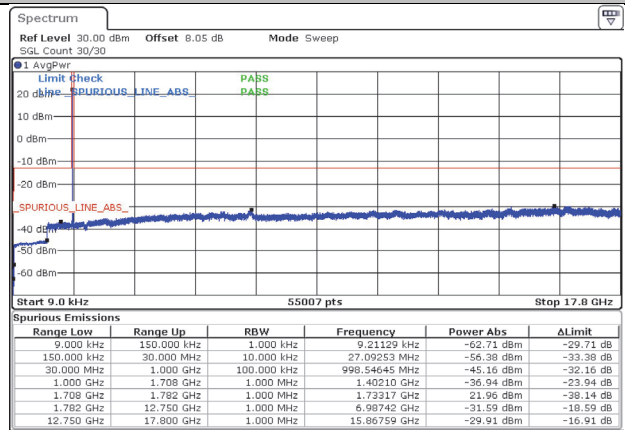
Date: 24 MAR 2025 17:47:29

MCH / DFT-Pi2BPSK / Edge_1RB_Left



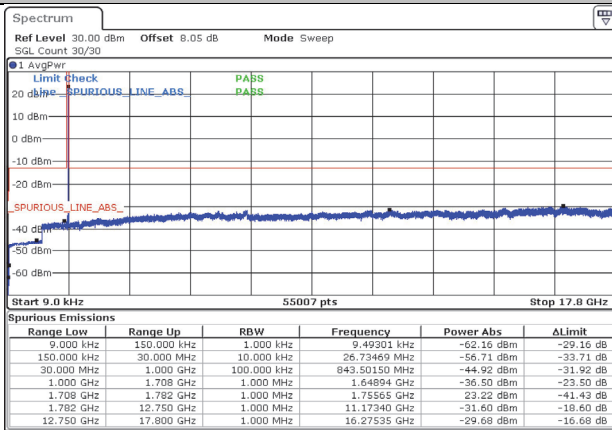
Date: 24 MAR 2025 17:47:52

MCH / DFT-QPSK / Edge_1RB_Left



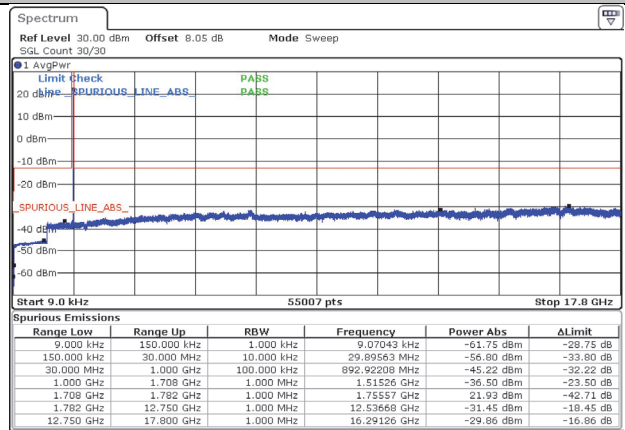
Date: 24 MAR 2025 17:48:15

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:48:39

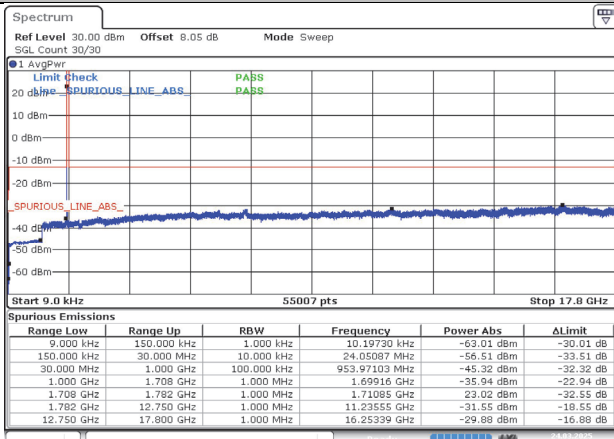
HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:49:11

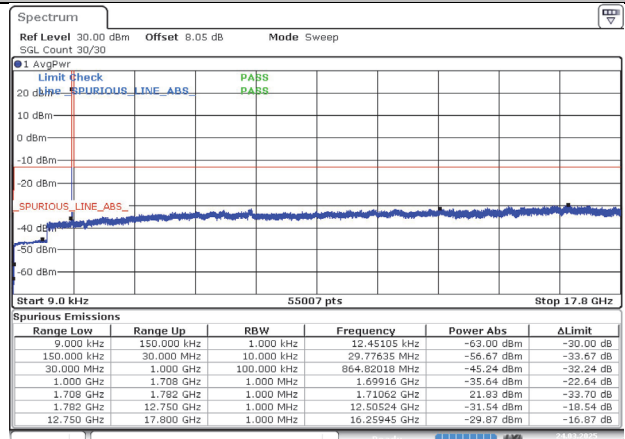
n66A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



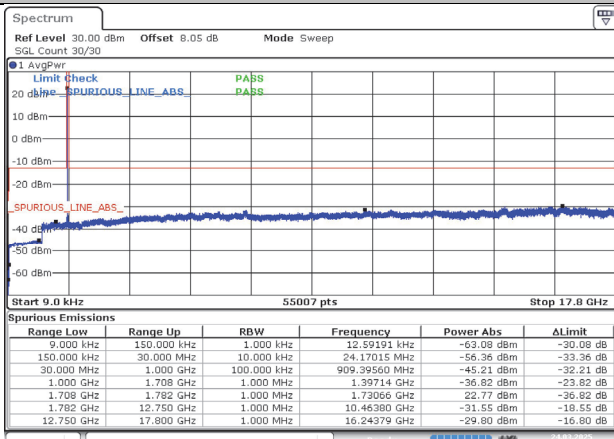
Date: 24 MAR 2025 17:49:45

LCH / DFT-QPSK / Edge_1RB_Left



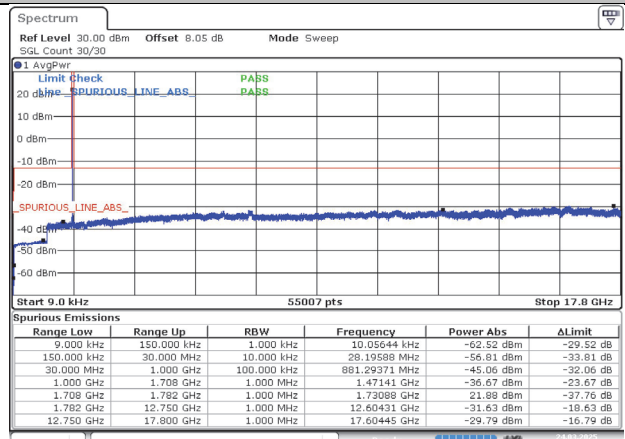
Date: 24 MAR 2025 17:50:07

MCH / DFT-Pi2BPSK / Edge_1RB_Left



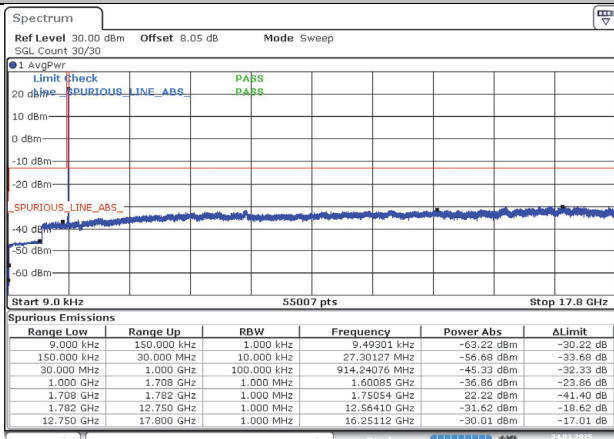
Date: 24 MAR 2025 17:50:41

MCH / DFT-QPSK / Edge_1RB_Left



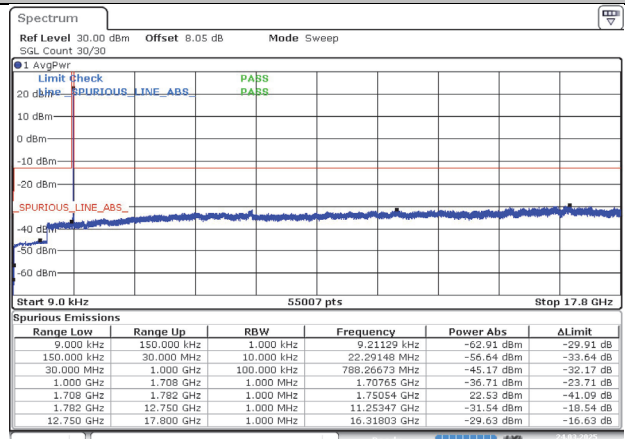
Date: 24 MAR 2025 17:51:14

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:51:38

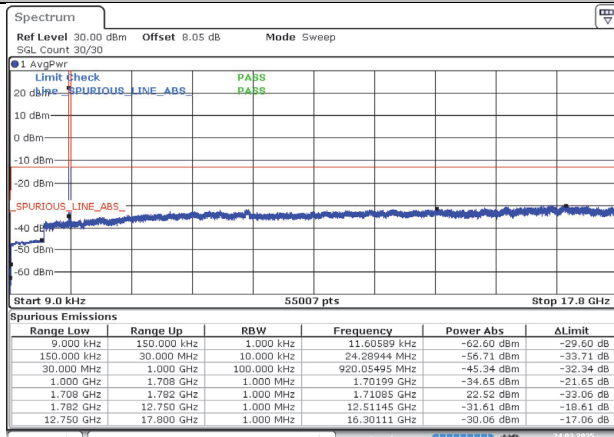
HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:52:00

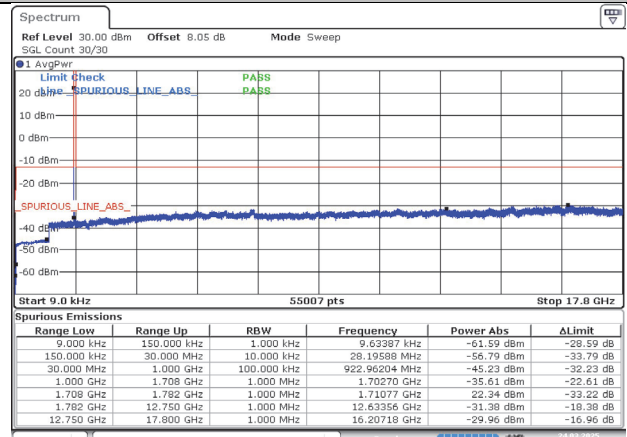
n66A / 15KHz / 35MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



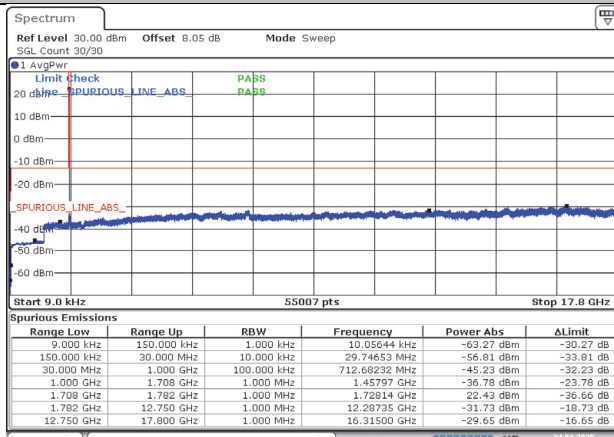
Date: 24 MAR 2025 17:52:33

LCH / DFT-QPSK / Edge_1RB_Left



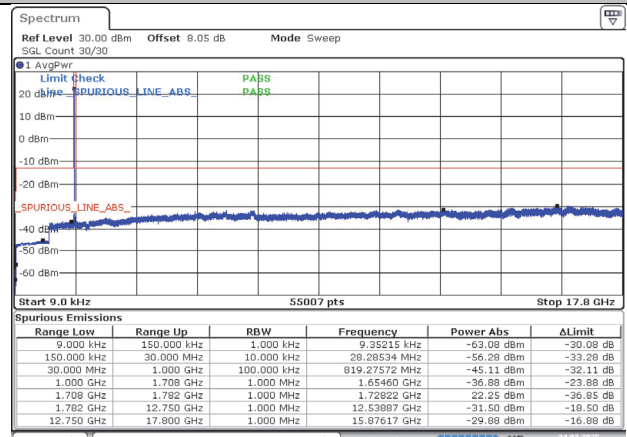
Date: 24 MAR 2025 17:52:56

MCH / DFT-Pi2BPSK / Edge_1RB_Left



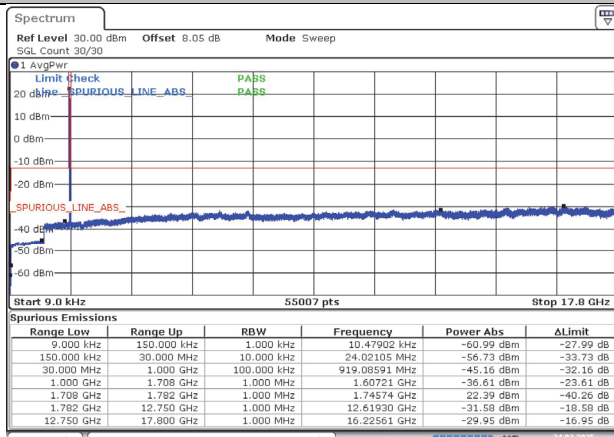
Date: 24 MAR 2025 17:53:20

MCH / DFT-QPSK / Edge_1RB_Left



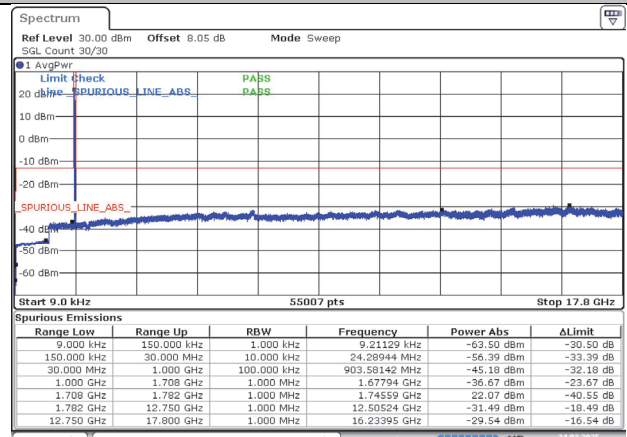
Date: 24 MAR 2025 17:53:42

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:54:06

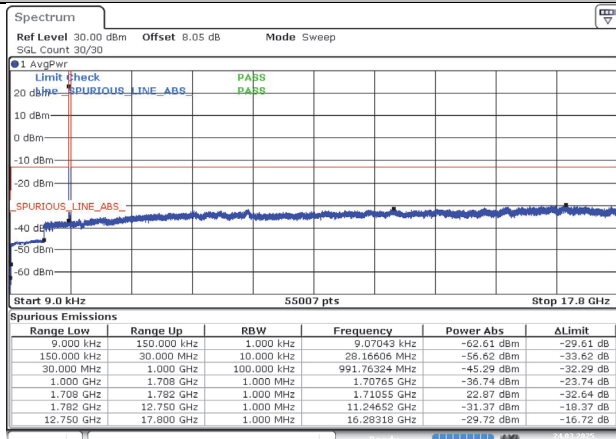
HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:54:38

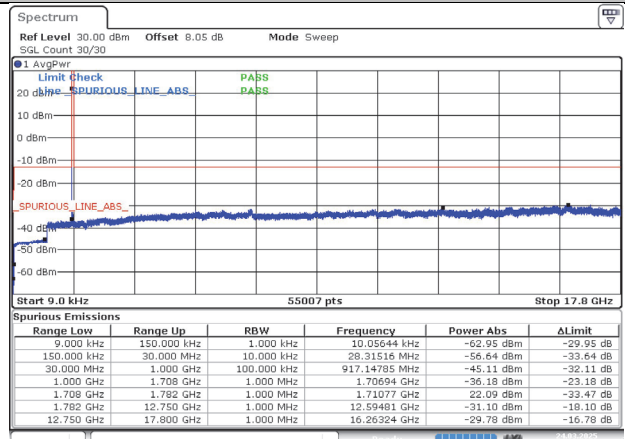
n66A / 15KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



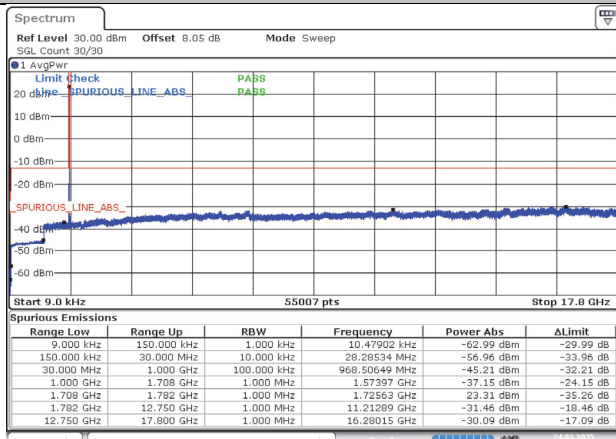
Date: 24 MAR 2025 17:55:12

LCH / DFT-QPSK / Edge_1RB_Left



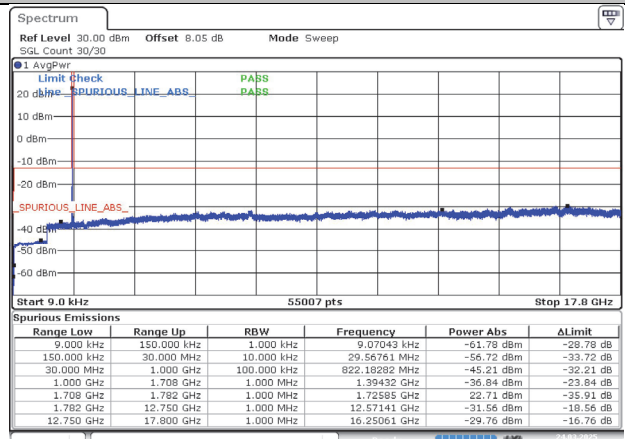
Date: 24 MAR 2025 17:55:34

MCH / DFT-Pi2BPSK / Edge_1RB_Left



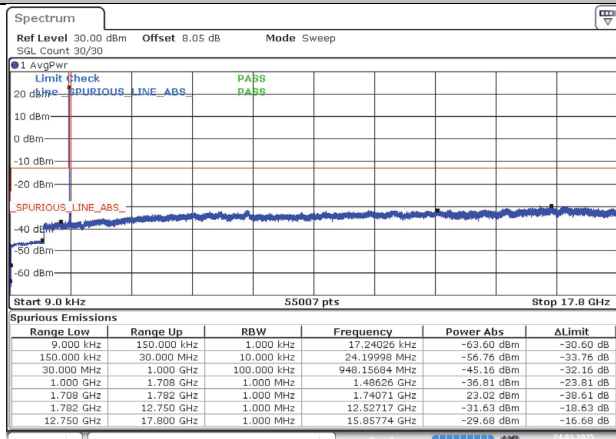
Date: 24 MAR 2025 17:56:08

MCH / DFT-QPSK / Edge_1RB_Left



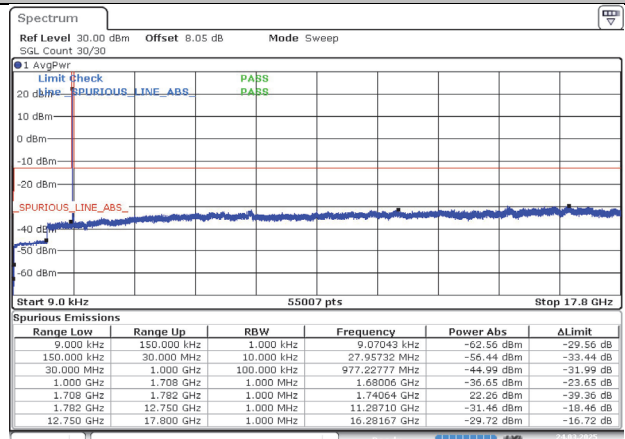
Date: 24 MAR 2025 17:56:31

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:56:55

HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 17:57:18