

Exhibit 10: Information Required for Certification**Venture Base Circuit description:**

(A)

(1)DC Voltage of the RF amplifier is 8V the power supply is offer from J1(12V/500mA) through u9,c101,c45,c47filler and SW4,the power level is setting by SW6,VR10 (low power),VR11(MID power),VR9 (HI power) the VR10,VR11,VR9 are controlled the Q16's(RF amplifier) gain.The current consumption of the power transistor in 1/4, 1/2 and full power are 16mA, 25mA and 33mA.

(2)Function of the active circuit device—

(2-a)U12-this is PLL (phase loop locked) circuit the operating frequency is controlled from CPU by serial data (STB ,DATA,CLK pins)the X1 is reference frequency oscillator which the CPU's reference frequency is 12.5KHz

(2-b)VCO1-This is RF osc. / modulator unit Jv03 is RF output terminal JV04 is power IN (+5VDC from U11 power regulator),JV01 is tuning terminal that is controlled by PLL charge pump output. (Do)
The JV02 is modulator Input.

(2-c)Q20-This is power ripple filter for PLL IC .

(2-d)LED5-This is PLL locked indicator.

(2-e)Q21-This is buffer amplifier for PLL IC.

(2-f)Q14-This is buffer amplifier for transmitter.

(2-g)Q15-This is driver amplifier amplifier for transmitter.

(2-h)Q-16-This is power amplifier for transmitter.

(2-i)U13-This is CPU IC for PLL circuit ,the R204,C200 is RC OSC circuit for the time base, J7 is Input terminal from decimal to binary digit switch.

(2-j)U14A/U14B-This is balance Input buffer Amplifier .

(2-k)U1B-This is Audio Input amplifier .

(2-l)U4B,D1,Q2-There are Auto level controlled circuit the U4B is amplifier for Audio Input, the D1 is rectifier diode to detect the DC voltage to control the Q1,Q2 for ALC purpose.

(2-m)U2A/U2B/U3A/U5-There are buffer amplifier for the compandor (compress & expand) purpose.

(2-n)U7A-This is Tone OSC. for 400HZ test tone.

(2-o)U7B-This is amplifier for AX function.

(2-p)U4A-This is buffer amplifier for Audio output.

(2-q)U6-This is Audio amplifier for earphone.

(2-r)U1A-This is buffer circuit for reference voltage.

(2-s)U8,LD2,LD3,LD4-This is Audio level circuit for level indicator LD2,LD3,LD4.

(2-t)D9-This is protect diode for U9 regulator.

(3)Description of all circuit and devices provided for determining and stabilizing frequency—

please see (2-a)(2-b) and (2-i)the stabilizing of operating frequency is determined by X1,C132,C129,the X1 is crystal which frequency stability is +/-10ppm between -10°C ~ + 60°C,the trimmer capacity C132 was tuned in corrected frequency within 10ppm in 25 °C,so the frequency stability is less than 50ppm (FCC specification)

(4)Description of suppression of spurious radiation ,limiting modulation and limiting power—

(4-a)Suppression of spurious radiation—The VC01 is a oscillator device for operating frequency ,so, this is not spurious frequency for lower band (the PLL and CPU OSC frequency aren't couple to the RF amplifier , so it should be eliminated by ground and power filter), the L4 ,C80,C81 are filter of operating frequency and L5 is low Q wide load,

so most of the spurious frequency are 2nd, 3rd, 4th ect—harmonic frequency .

The L7,C207,C86,L8,C87,C88,L9,C97,C89,C90 are 5th buffer worth low pass filter that are eliminated the harmonic more than 50db of the operating frequency.

(4-b)Limiting modulation —There are built in ALC(auto level controlled)circuit into audio amplifier with 70 db dynamic range , so the max. deviation must be limit in 10 KHz – 15KHz .

(4-c)Limiting power—Regarding the full, 1/2, 1/4 power level setting was controlled by potential meter that are made by cermet material (the temperature stability is about 50 ppm)so the limiting power variation are less than 1db after tuning.

(5)Detailed description of the modulation —The modulation of the VCO is linearly FM modulator (please see the attached VCO schematic Diagram).The input signal is sine wave with <2% distortion in all of Audio band (100HZ-10KHZ)by the ALC circuit and R1 31 ,C106 audio filter .

Tune up Procedures

SCOPE

1 This procedure details the process for testing the Venture Base Transmitter.

2 APPLICABLE DOCUMENTS/SPECIFICATIONS

2.1 N/A

3 FORMS

3.1 Testing Log

3.2 Failed Product Disposition Log

4 SPECIAL TOOLS

4.1 1 - HP 8901A Modulation Analyzer

4.2 1 - 1701A Distortion Measurement System

4.3 1 - ALS Venture Receiver

5 PROCEDURE

5.1 Output Frequency Test

5.1.1 Open the Venture Base case lid to expose components.

5.1.2 Turn on the HP 8901A Modulation Analyzer and push the Frequency Measurement button ON.

5.1.3 Connect the antenna output of the Venture Base to HP 8901A Modulation Analyzer.

5.1.4 Select channel 01 on the Venture Base.

5.1.5 Provide power to the Venture Base.

5.1.6 Depress the power switch on the front of the Venture Base.

5.1.7 Verify the POWER LED and RF LED illuminate.

5.1.8 On the front and back of the Venture Base, verify all of the switches are in the out position.

5.1.9 On the back of the Venture Base, verify the input selector knob is turned to the MIC position.

5.1.10 The frequency measurement on the analyzer should be 216.025MHz +/-1kHz. If necessary, adjust C57 trimmer on the main circuit board until output frequency is correct (216,024,000 - 216,026,000).

5.1.11 Turn the Venture Base channel selector to channel 19.

5.1.12 Verify that output frequency 216.975MHz +/-1kHz (216,974,000 -216,976,000).

5.1.13 Adjust the trimmer (if necessary).

5.2 Output Level Test

5.2.1 On HP 8901A Modulation Analyzer, push RF level measurement button.

- 5.2.2 On the back of the Venture Base, set RF level setting to FULL.
- 5.2.3 Set the Venture Base to channel 01.
- 5.2.4 The power measurement on the analyzer should be 100 mW +0, -10%. If necessary, adjust potentiometer VR9 (full power).
- 5.2.5 On the back of the Venture Base, set RF level setting to HALF.
- 5.2.6 Adjust potentiometer VR11 (one-half power) to 50.0mW +/-10%.
- 5.2.7 On the back of the Venture Base, set RF level setting to QUARTER.
- 5.2.8 Adjust potentiometer VR10 (one-quarter power) to 25.0mW +/-10%.

5.3 RF Modulation Test

- 5.3.1 Set VENTURE BASE Transmitter for balanced line level input on channel 01.
- 5.3.2 On the back, set TONE and HI-PASS switched to the OFF position.
- 5.3.3 On the front panel, set the APHEX switch to OFF.
- 5.3.4 Set AUDIO INPUT and PROCESS level trimmers to maximum (216.025 MHz).
- 5.3.5 Connect a 1kHz, 0dBm signal to the balanced line-level XLR input.
- 5.3.6 If necessary, adjust VR4 for 30kHz +/- 4kHz of deviation.
- 5.3.7 Set APHEX switch to ON.
- 5.3.8 If necessary, adjust VR7 for 36kHz +/- 3kHz of deviation.
- 5.3.9 Turn APHEX OFF.
- 5.3.10 Adjust VR8 until the HIGH front-panel indicator just begins to illuminate. The HIGH LED should extinguish when the AUDIO LEVEL front-panel control is rotated back to the 3 o'clock position.
- 5.3.11 Disconnect the 1kHz test signal and turn ON the internal test tone.
- 5.3.12 Change the broadcast channel to channel 19 (216.975 MHz).
- 5.3.13 If necessary, adjust VR5 for tone of 400Hz and minimum THD.
- 5.3.14 If tone is measured by adjusting VR6 to 30kHz then DO NOT adjust VR5.
- 5.3.15 Adjust VR6 for 30kHz +/-3kHz deviation.
- 5.3.16 If adjustment cannot be made to 30kHz then turn VR5 one quarter turn and adjust VR6 to the proper setting.

5.4 System Test

- 5.4.1 Press the TONE button to the OFF position.
- 5.4.2 Switch to unbalanced line position.
- 5.4.3 Plug in audio RCA jacks.
- 5.4.4 Plug in the headphone-to-headphone jack on the VENTURE BASE's front panel.
- 5.4.5 Listen to the audio as generated by the audio source. Verify the sound quality.
- 5.4.6 Turn dials on panel and verify that there is no unwanted noise.
- 5.4.7 Push the APHEX button in and out to verify the increase in power/volume boost.

5.5 Pass/Fail Specification

- 5.5.1 If the product passes test specifications, complete assembly of product, stamp with appropriate QC stamp and enter on test log form.
- 5.5.2 If the product fails to meet test specifications, follow procedures for failed product.

6 RECORDS/MAINTENANCE RESPONSIBILITY

- 6.1 This procedure is part of the Manufacturing Engineering Test Procedures, Sub Manual 03 and is to be maintained by the Manufacturing Engineering department.