ATTACHMENT F – BLOCK DIAGRAM(S)

Block Diagram (LG883D)

Inverter

12V →

Micom 68HC08-BD48

Inverter CTL →
Power CTL →
Module CTL →

12V ↓

LCD Module

R,G,B differential

3.3V Reg.

DVDD 1.3V →

DC-DC Converter

5V →

LVDS (LVDS23)

Out. CLK
DE, H/V Sync out
R,G,B Even

AVDD 3.3V
AVDD 2.5V
ADC CLK

LVDS Low Voltage Differential Signaling

Gm5020 including (ADC / TMDS Rx)

H / V Sync →

M-CLK Address, Data 48 Bit

Memory (KM416S102BT)

2.5V Reg.

DVDD2.5V
AVDD2.5V

5V →

3.3V Reg.

PVDD 3.3V

5V →

D-SUB

12V ↓

V-Sync A
H-Sync A

R,G,B even, odd

DVI-D

Digital Signal

H-Sync DA
V-Sync DA

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