

TEST SETUP PHOTOGRAPHS

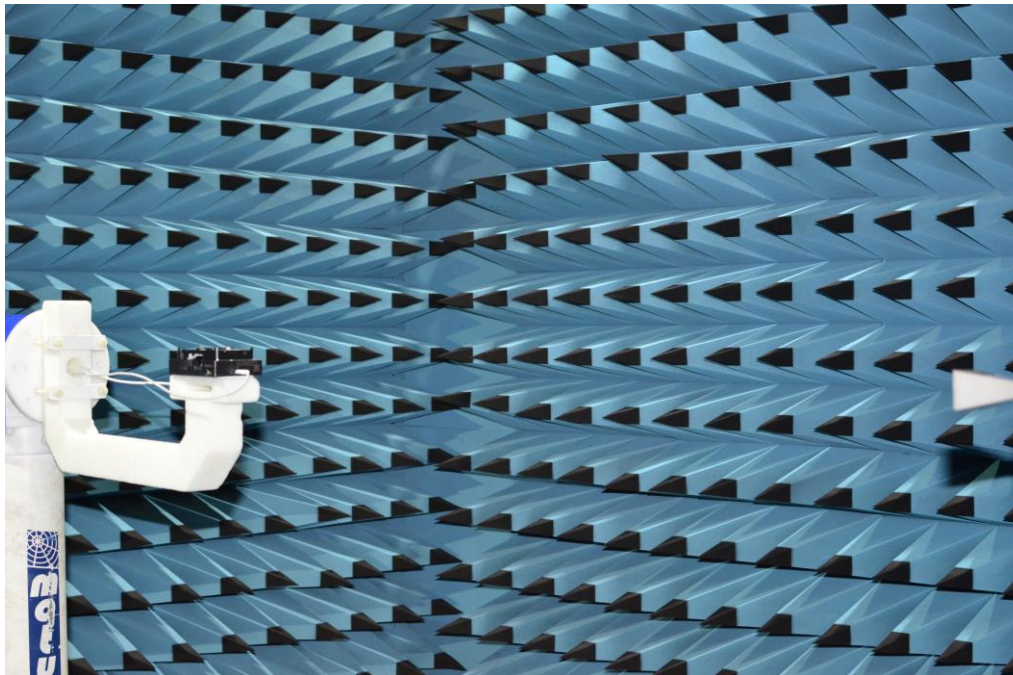


Figure 1: 18-40GHz Test Setup

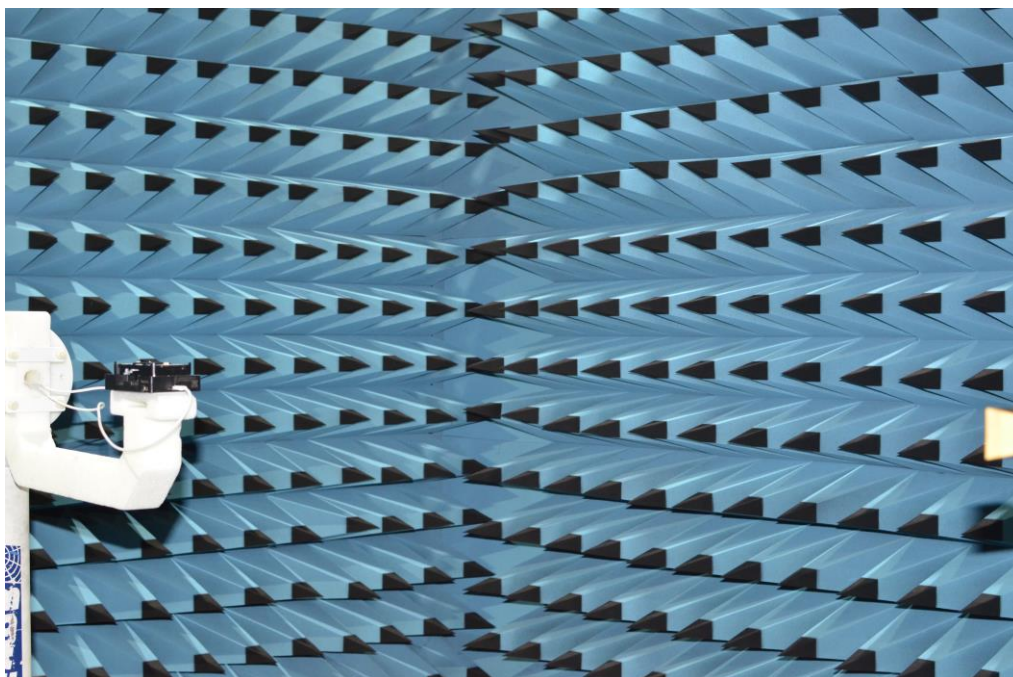


Figure 2: 40-60GHz Test Setup

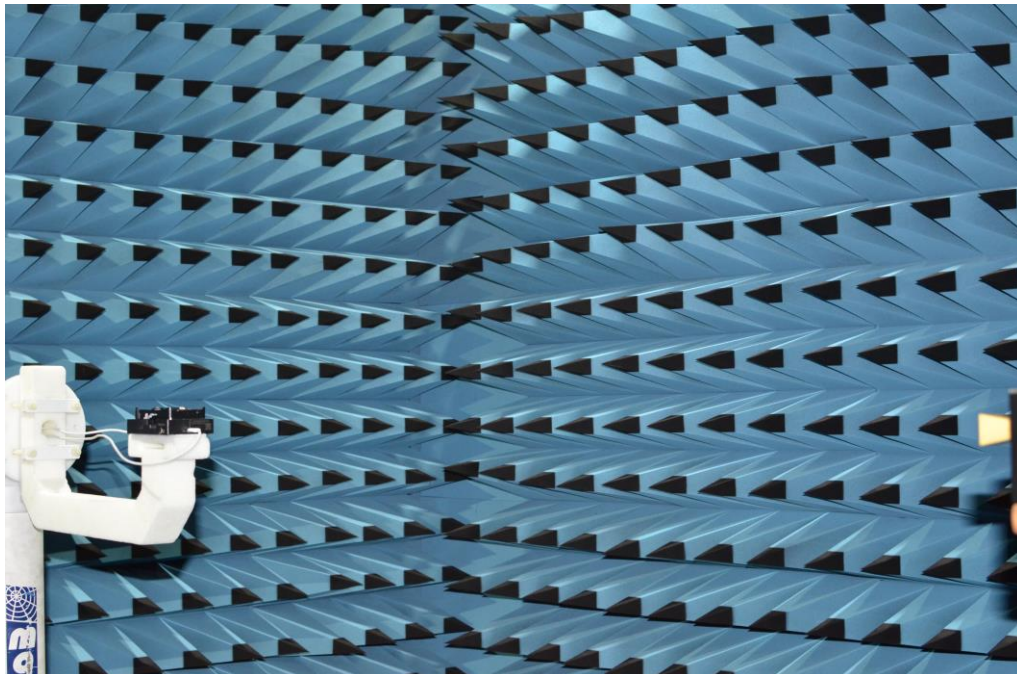


Figure 3: 60-90GHz Test Setup

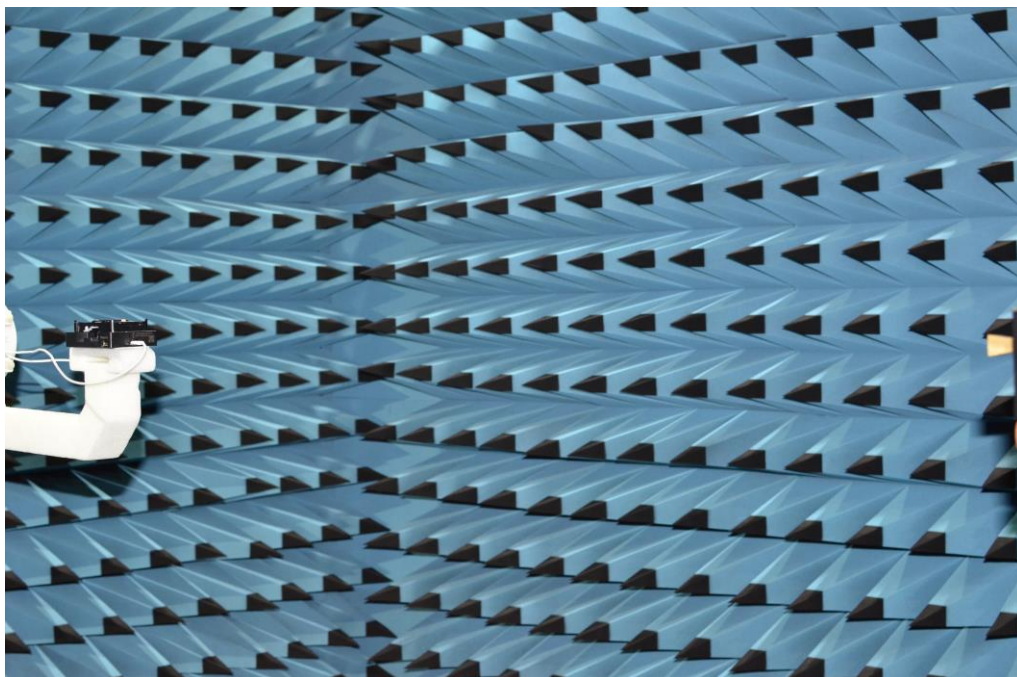


Figure 4: 90-140GHz Test Setup

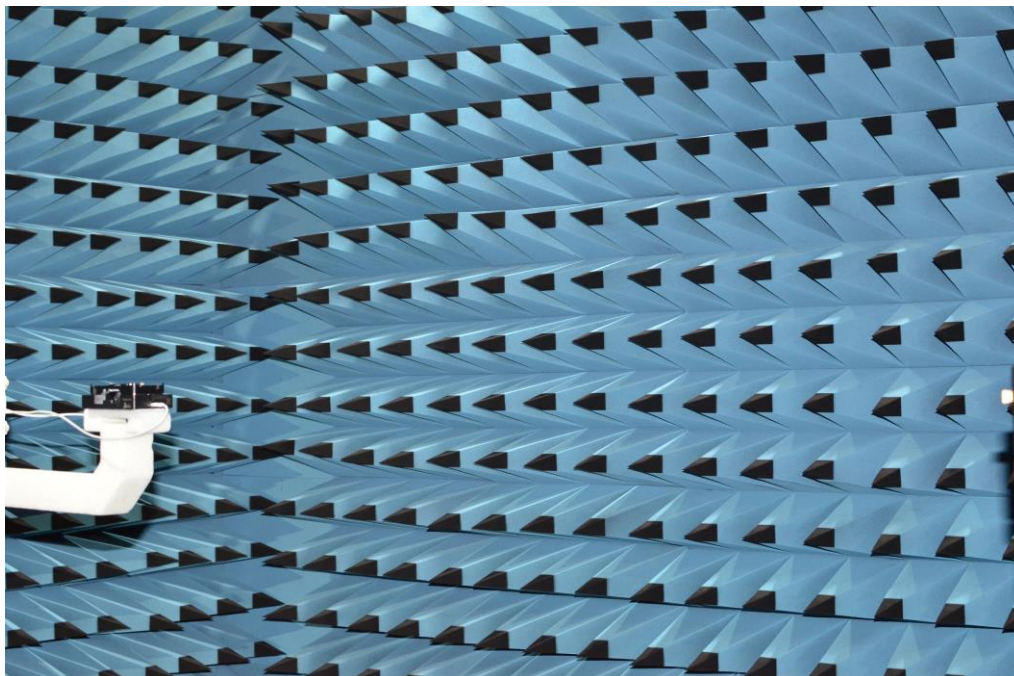


Figure 5: 140-200GHz Test Setup

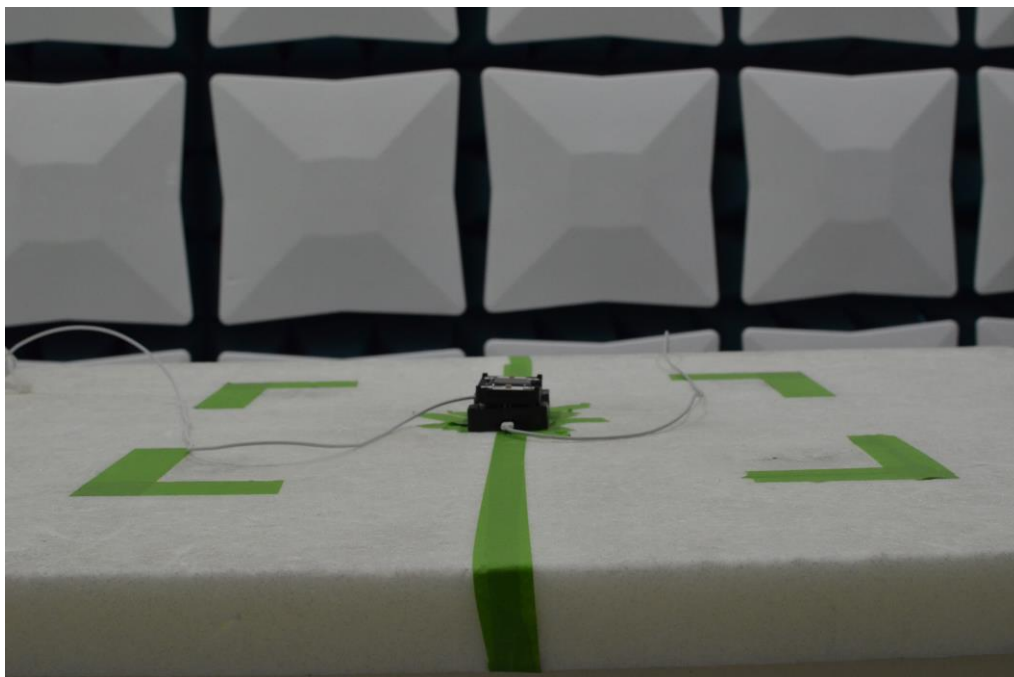


Figure 6: 1-18GHz Test Setup (Front View)

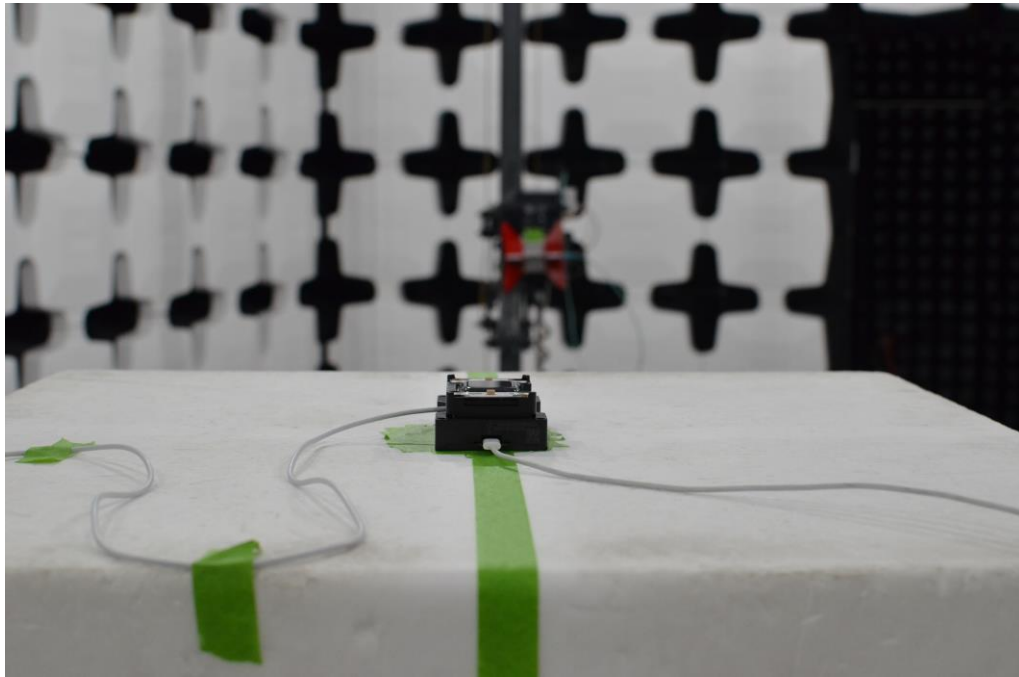


Figure 7: 1-18GHz Test Setup (Rear View)

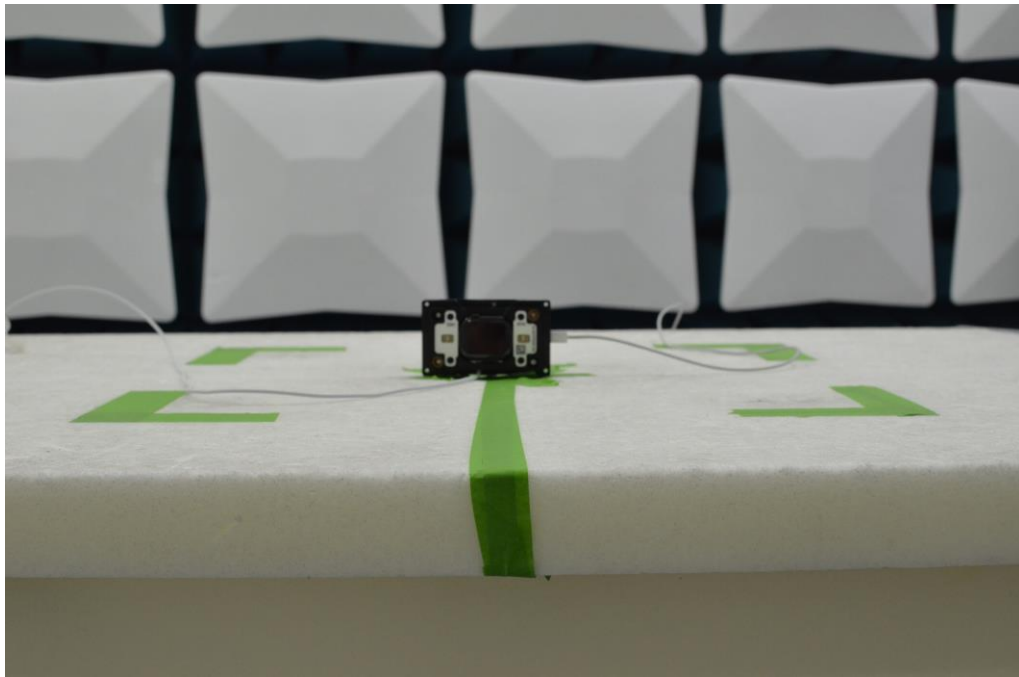


Figure 8: 1-18GHz Test Setup (Front View)

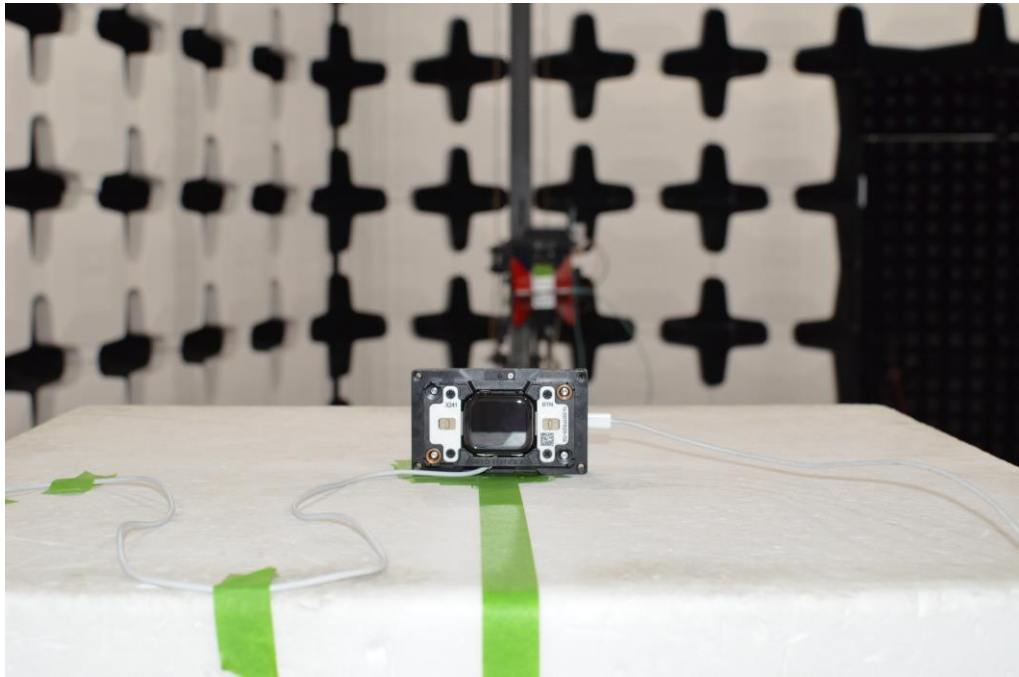


Figure 9: 1-18GHz Test Setup (Rear View)

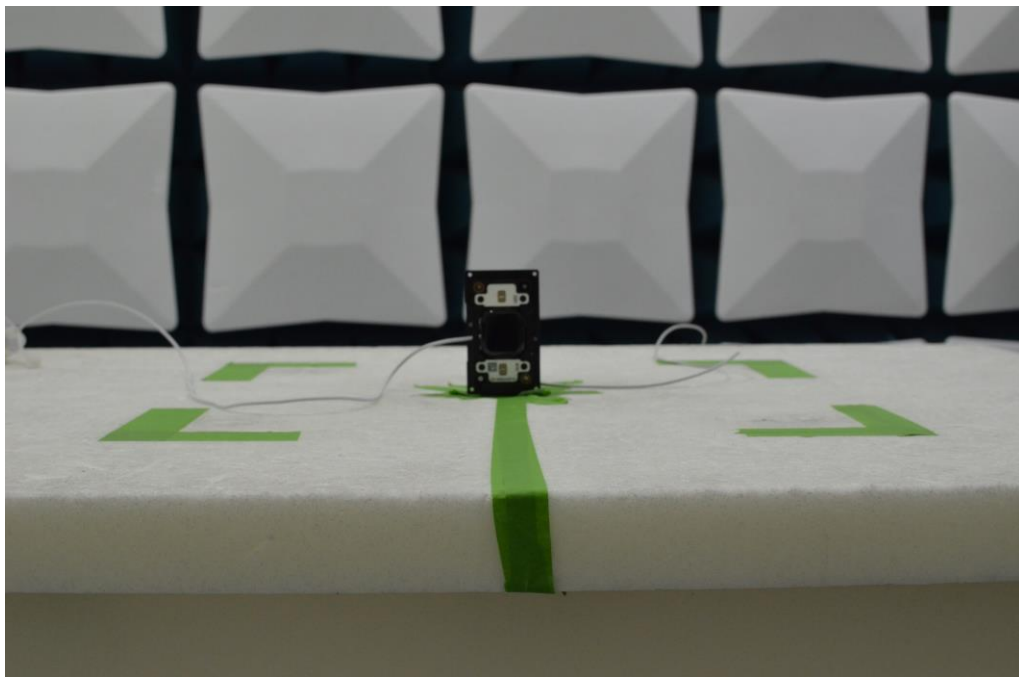


Figure 10: 1-18GHz Test Setup (Front View)

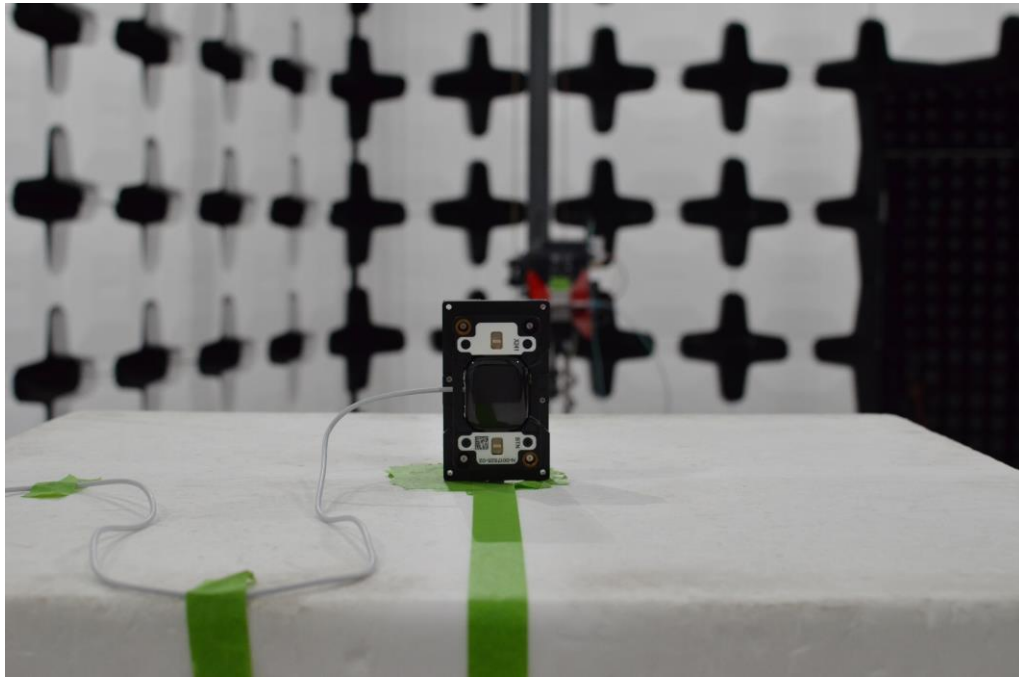


Figure 11: 1-18GHz Test Setup (Rear View)

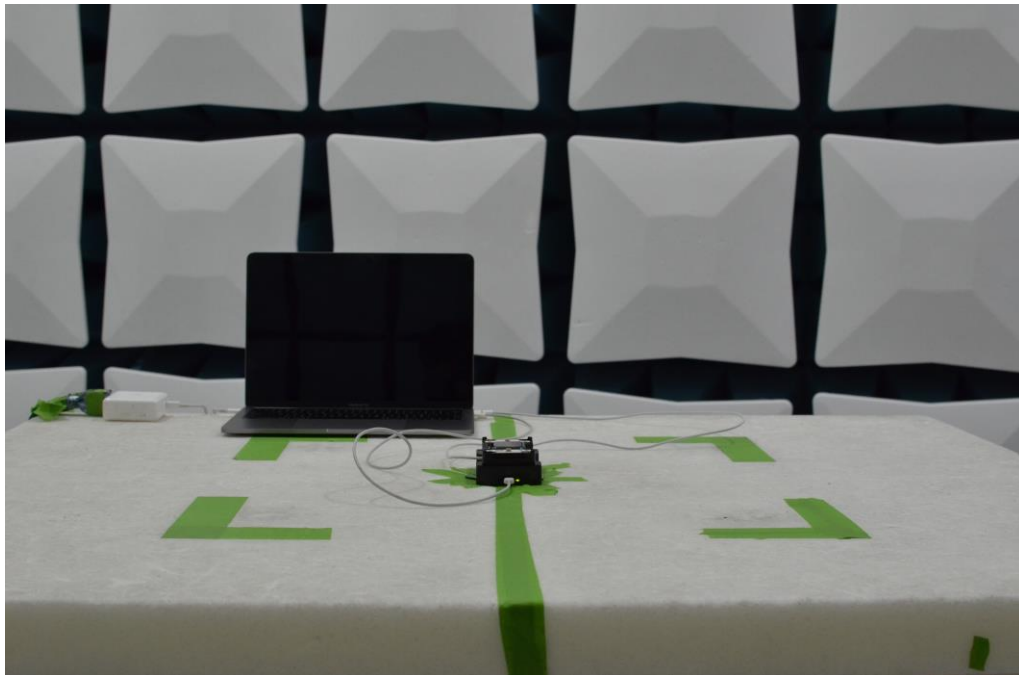


Figure 12: 30MHz-1GHz Test Setup (Front View)



Figure 13: 30MHz-1GHz Test Setup (Rear View)



Figure 14: 30MHz-1GHz Test Setup (Front View)



Figure 15: 30MHz-1GHz Test Setup (Rear View)



Figure 16: 30MHz-1GHz Test Setup (Front View)

 element	Apple Inc. FCC ID: BCG-A2775, IC: 579C-A2775	Model/HVIN: A2775 (A2858)
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Figure 17: 30MHz-1GHz Test Setup (Rear View)

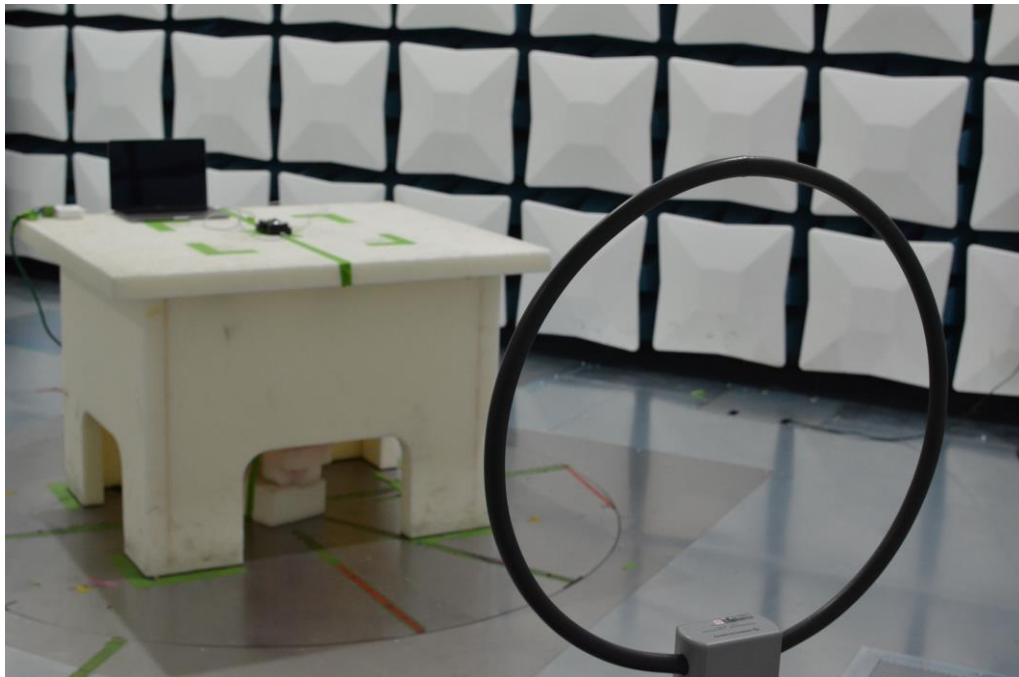


Figure 18: 9kHz-30MHz Test Setup (Front View)

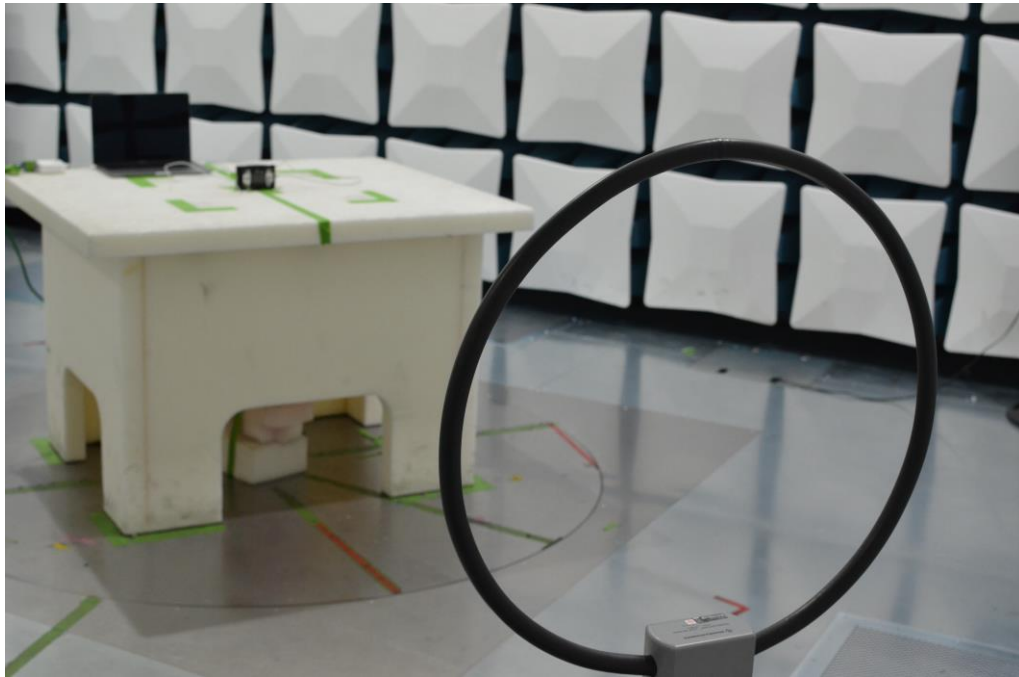


Figure 19: 9kHz-30MHz Test Setup (Front View)

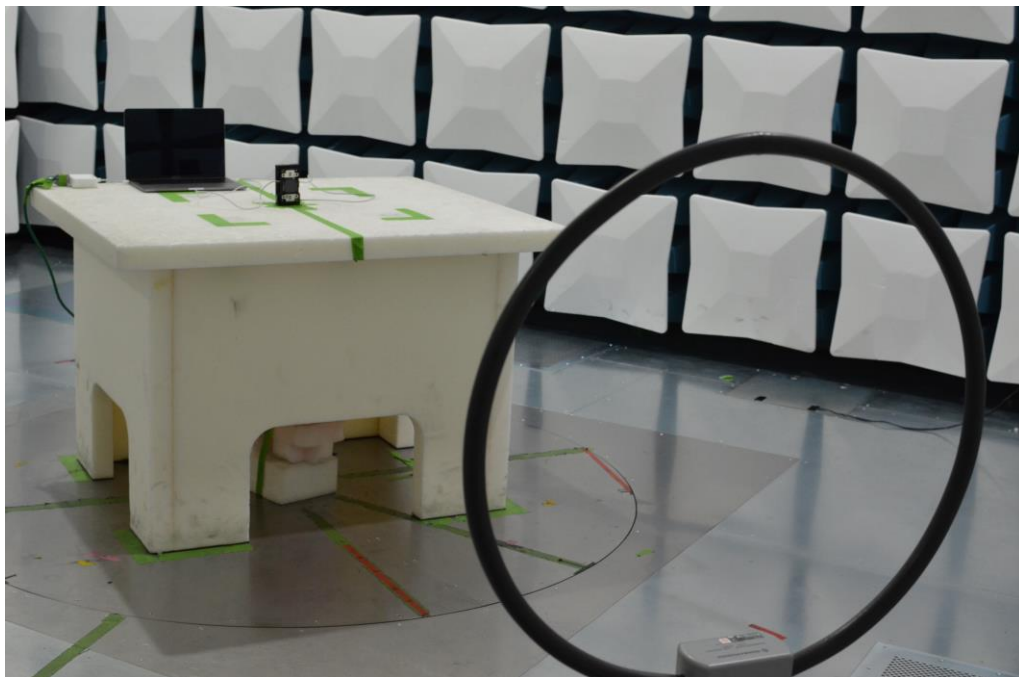


Figure 20: 9kHz-30MHz Test Setup (Front View)