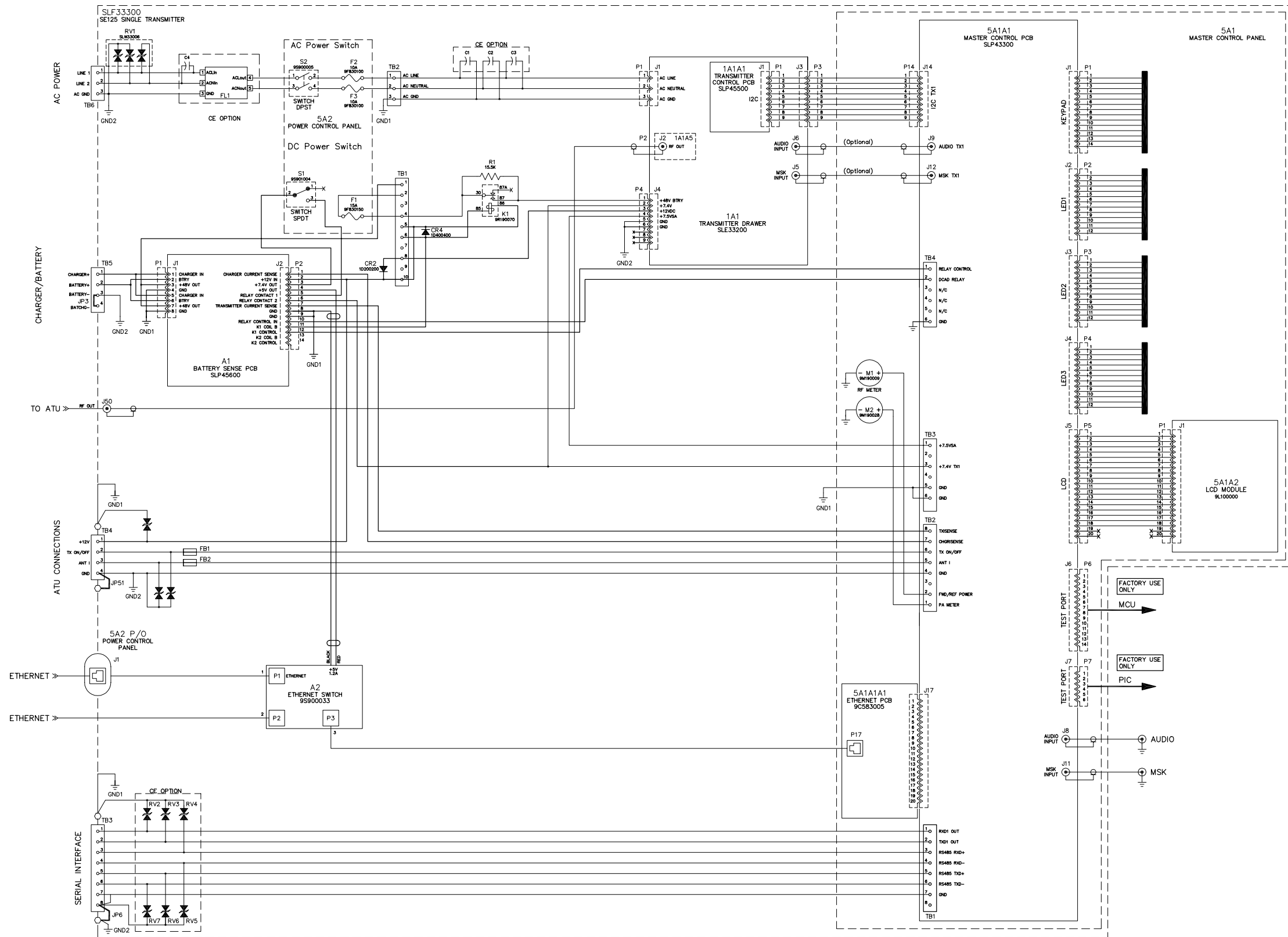


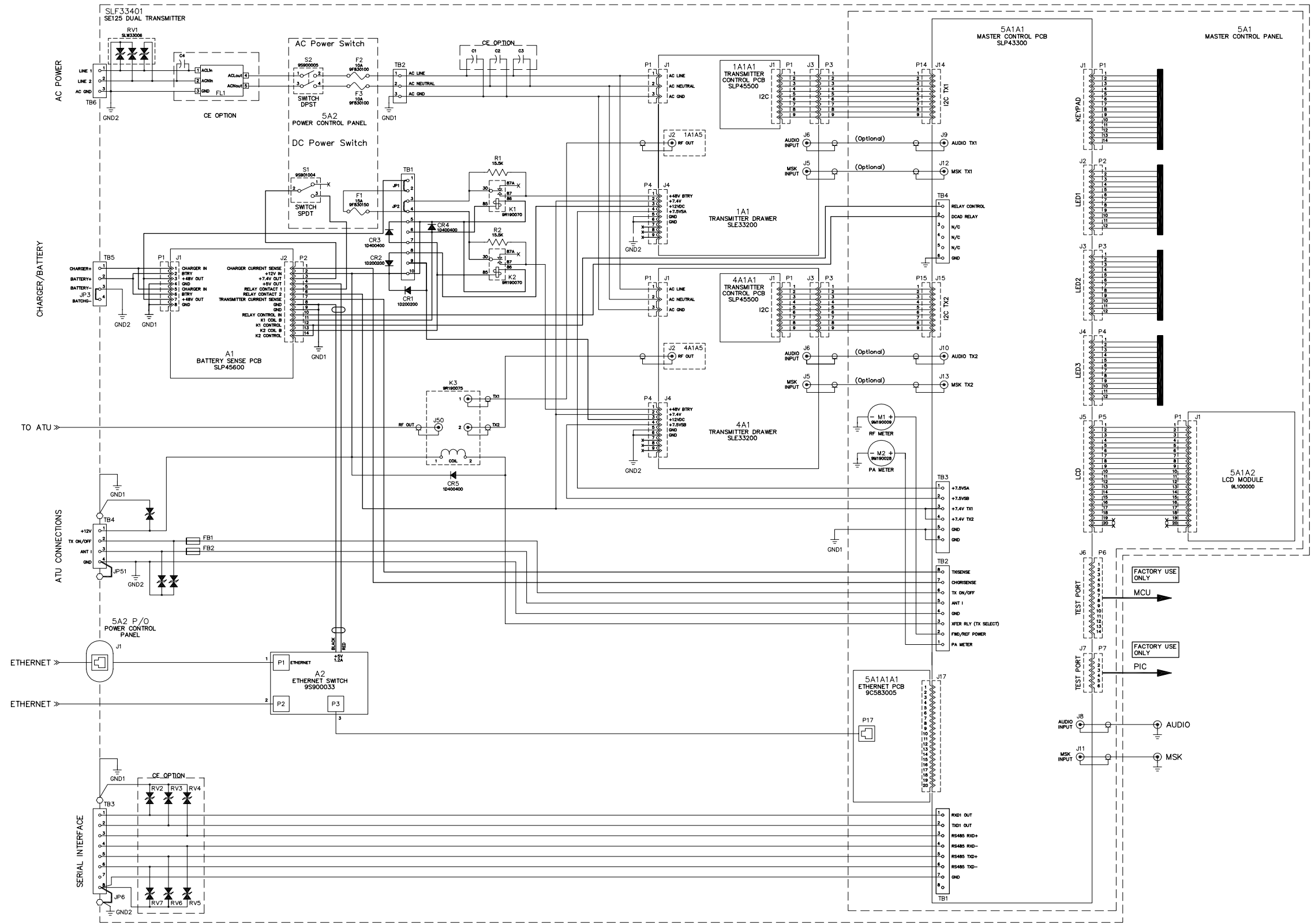
SE Series Schematics

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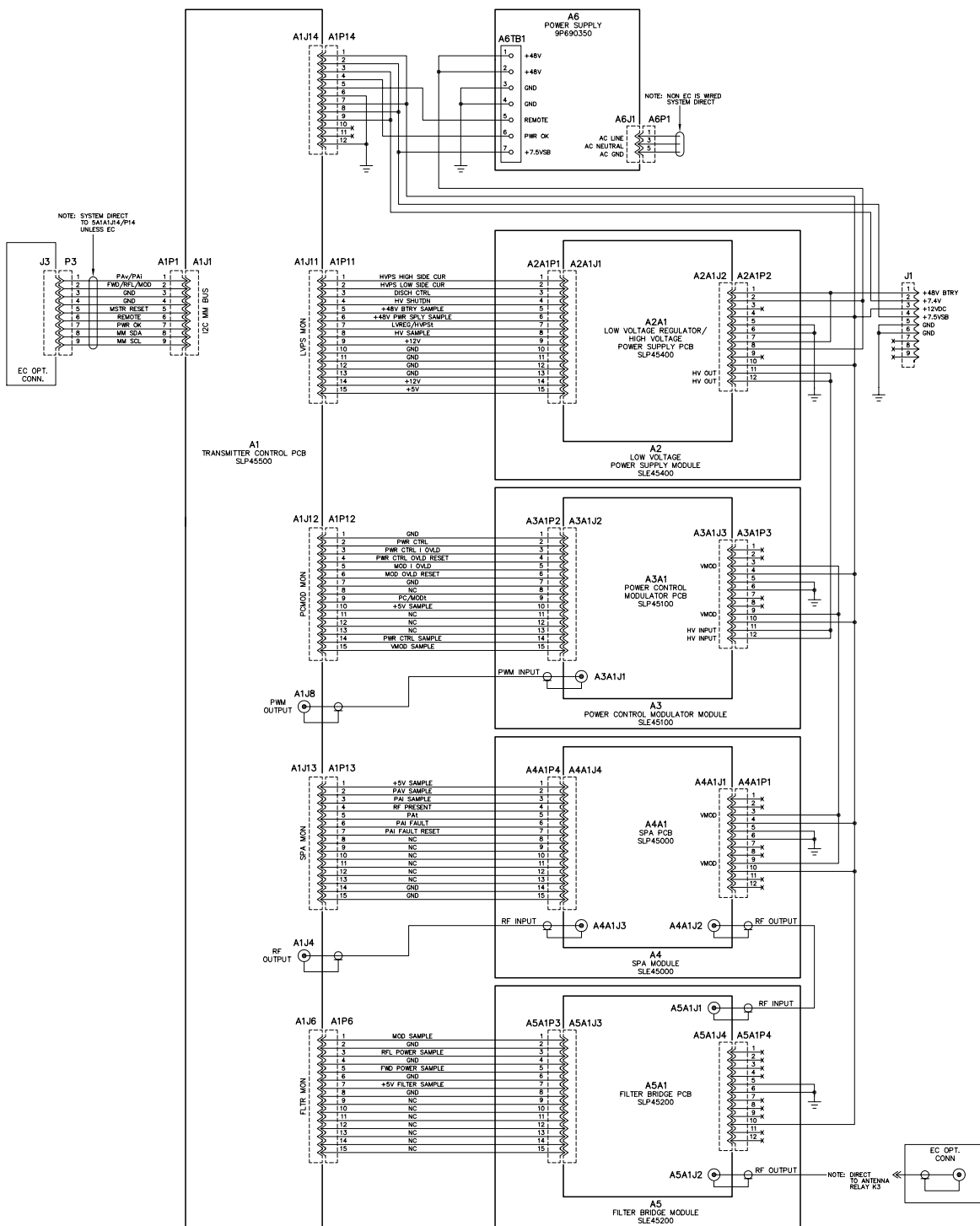
SEM33300 - SE Series Single Overall Schematic	2
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SEM33300 - SE SERIES SINGLE OVERALL SCHEMATIC

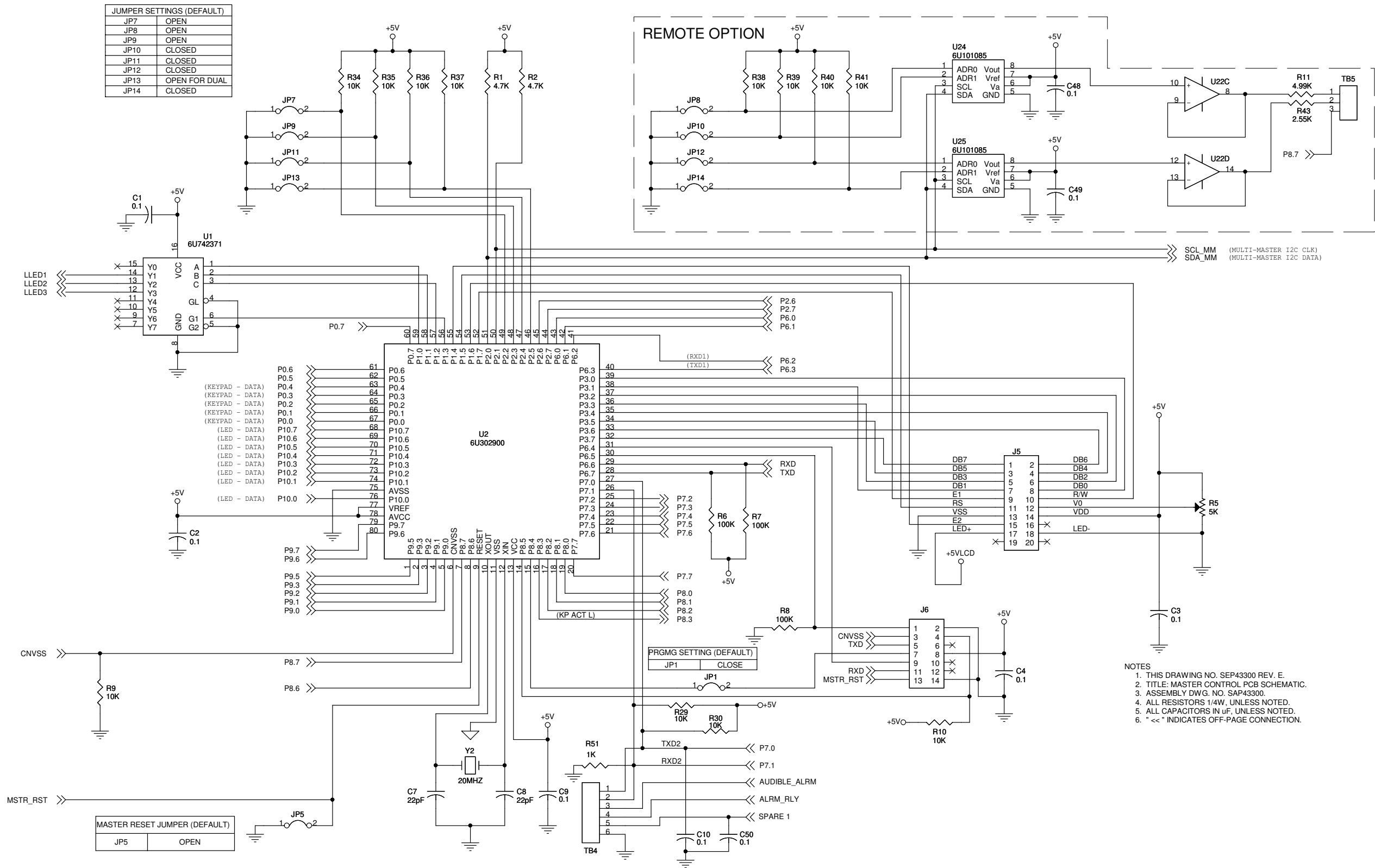


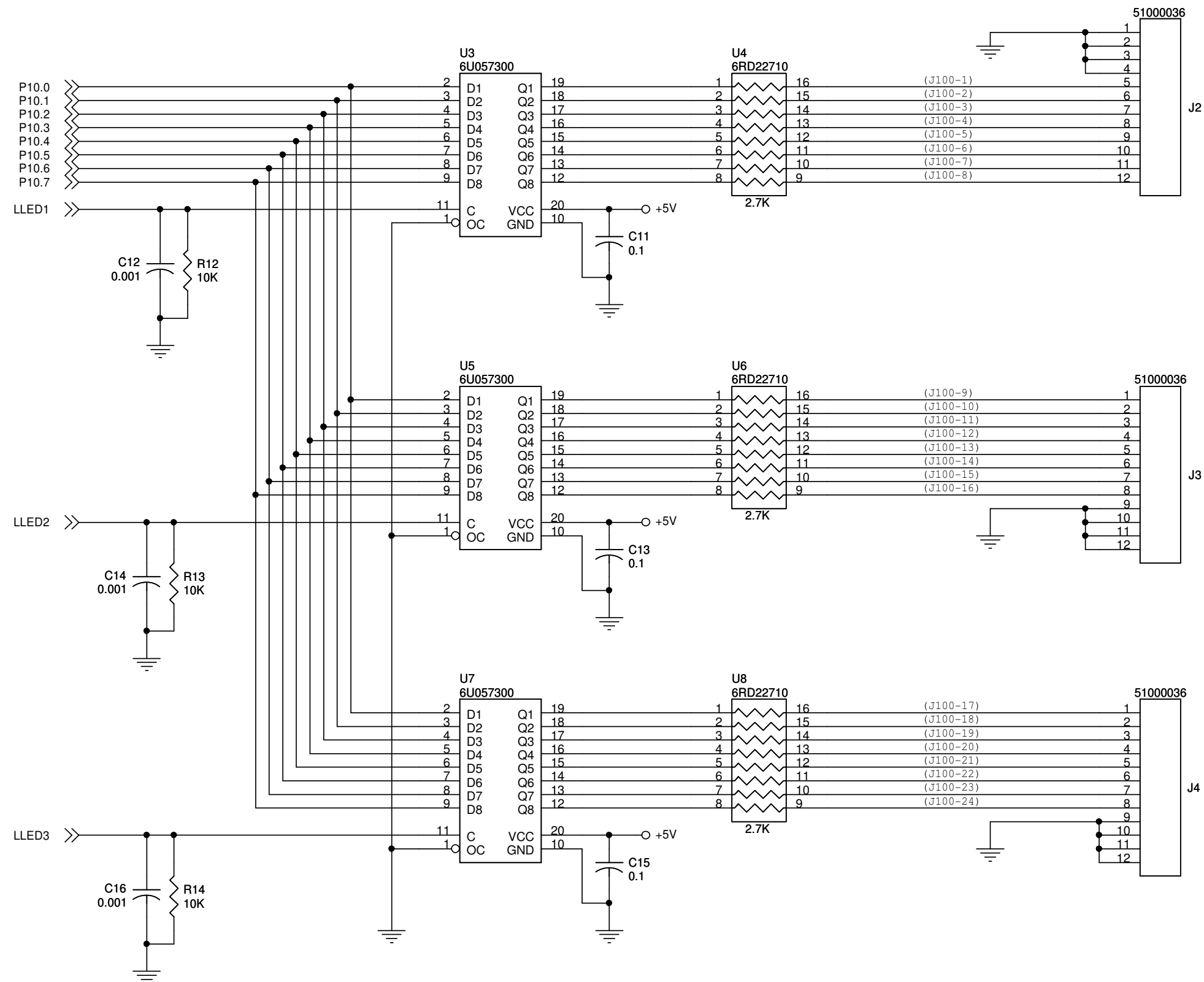
NOTES: 1. THIS DWG NO. SEM33401 REV. C.
2. TITLE: SE125 DUAL TRANSMITTER OVERALL SCHEMATIC.



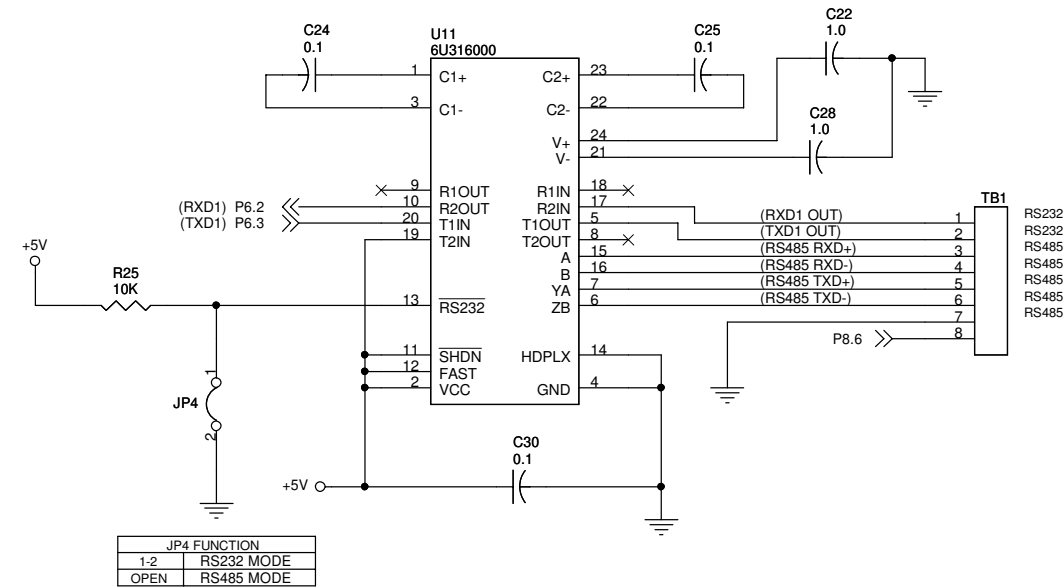
NOTES: 1. THIS DWG NO. SEM33200 REV. 1.
2. TITLE: SE SERIES TRANSMITTER DRAWER OVERALL SCHEMATIC.
3. ASSEMBLY DWG NO. SAE33100.

SEM33200 Rev 1
Figure 2-13. SE Series Transmitter Drawer Schematic

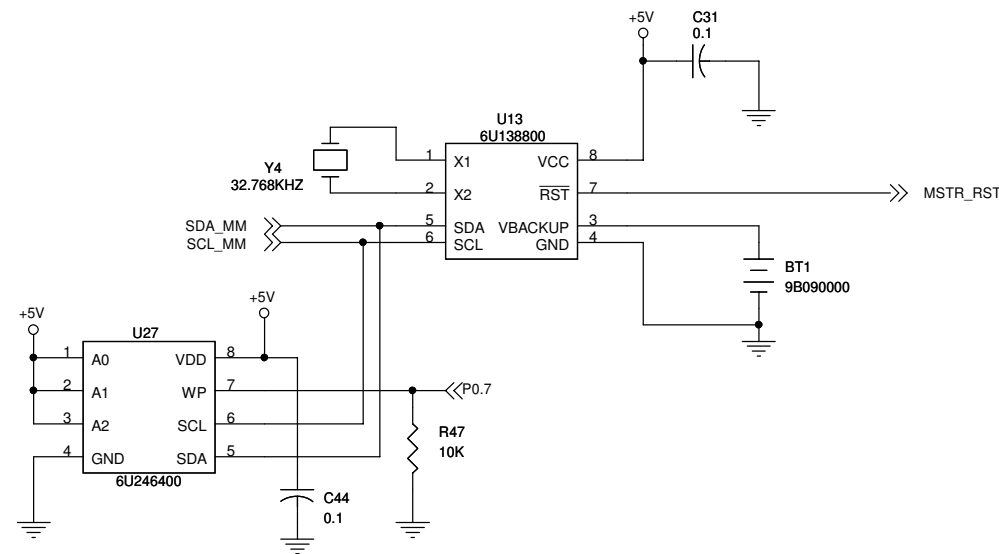




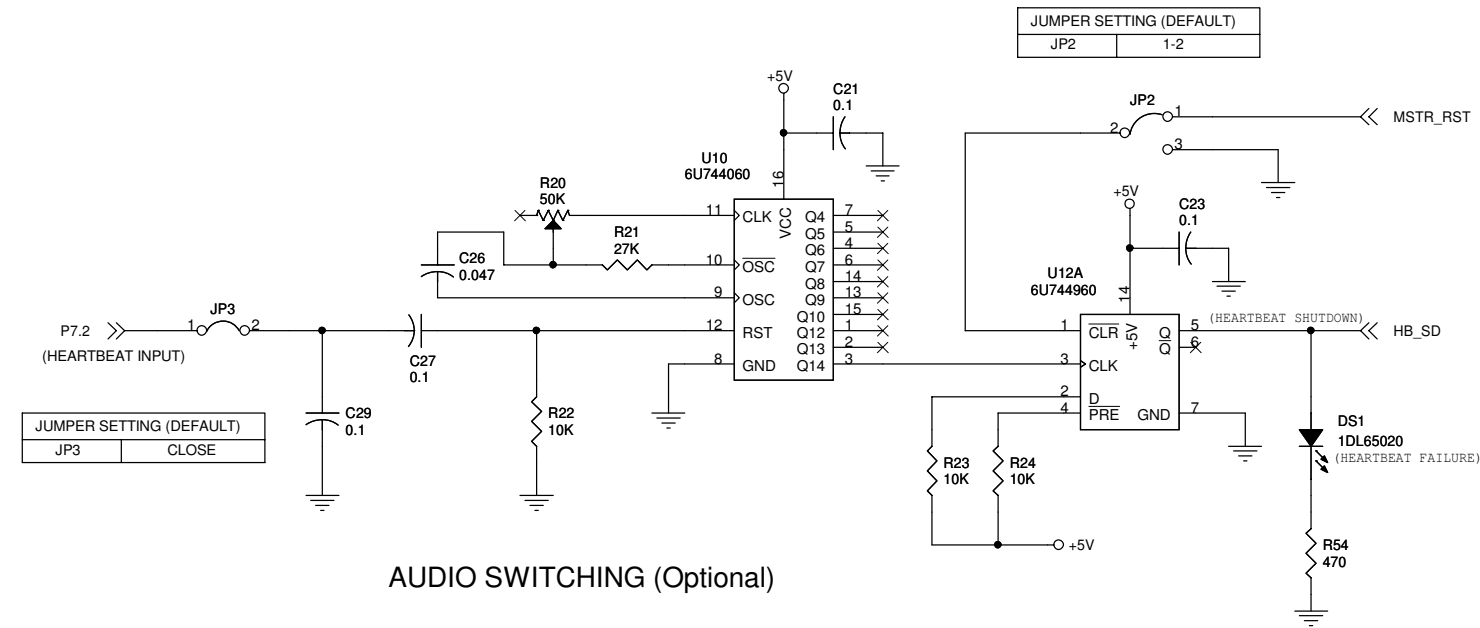
RS-232 / RS-485 INTERFACE



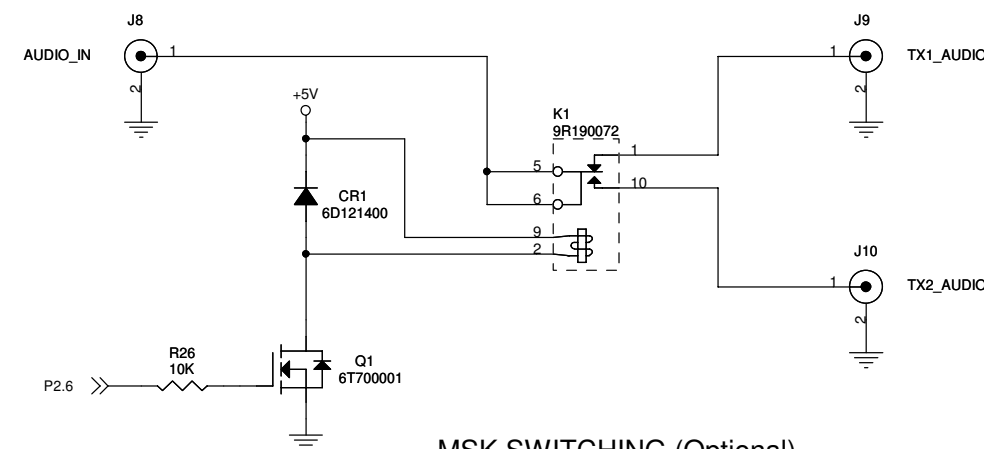
RTC / EEPROM / FRAM



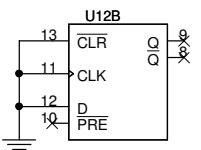
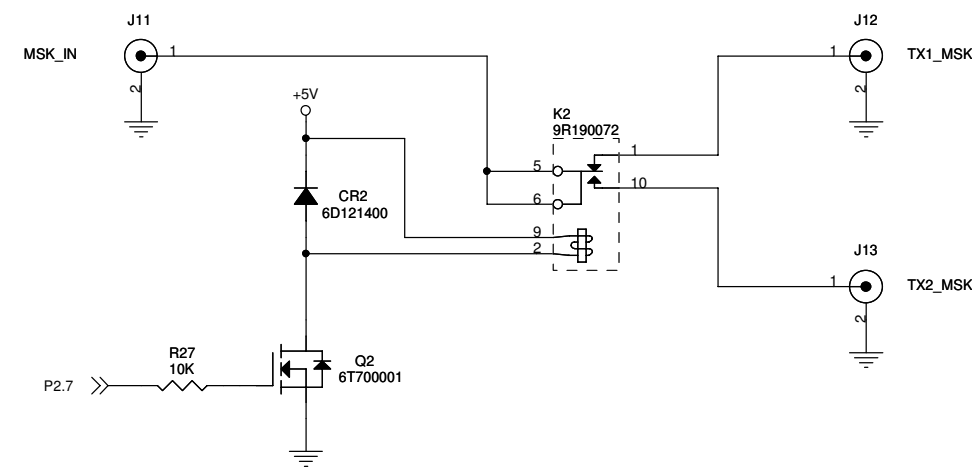
HEARTBEAT CIRCUIT

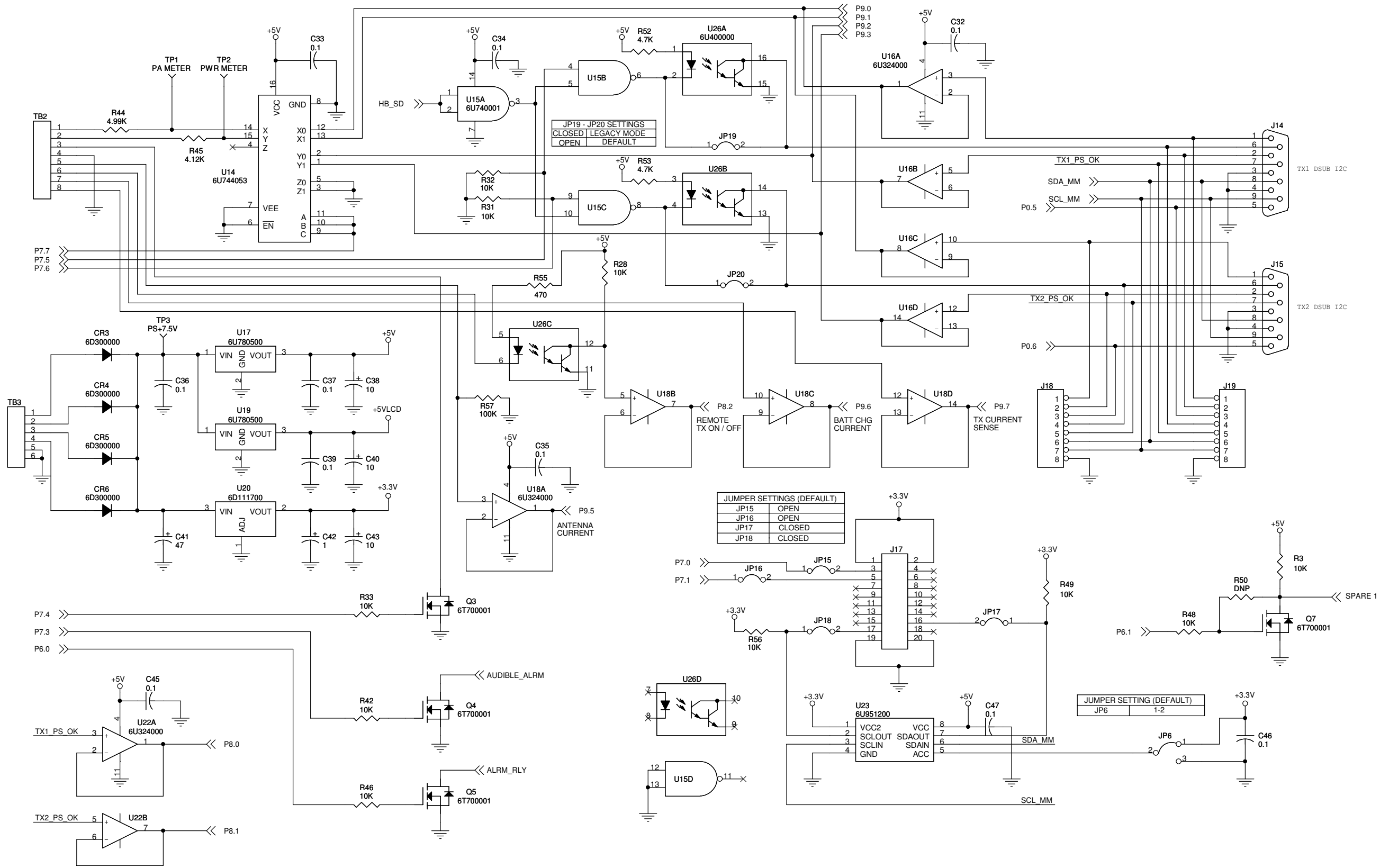


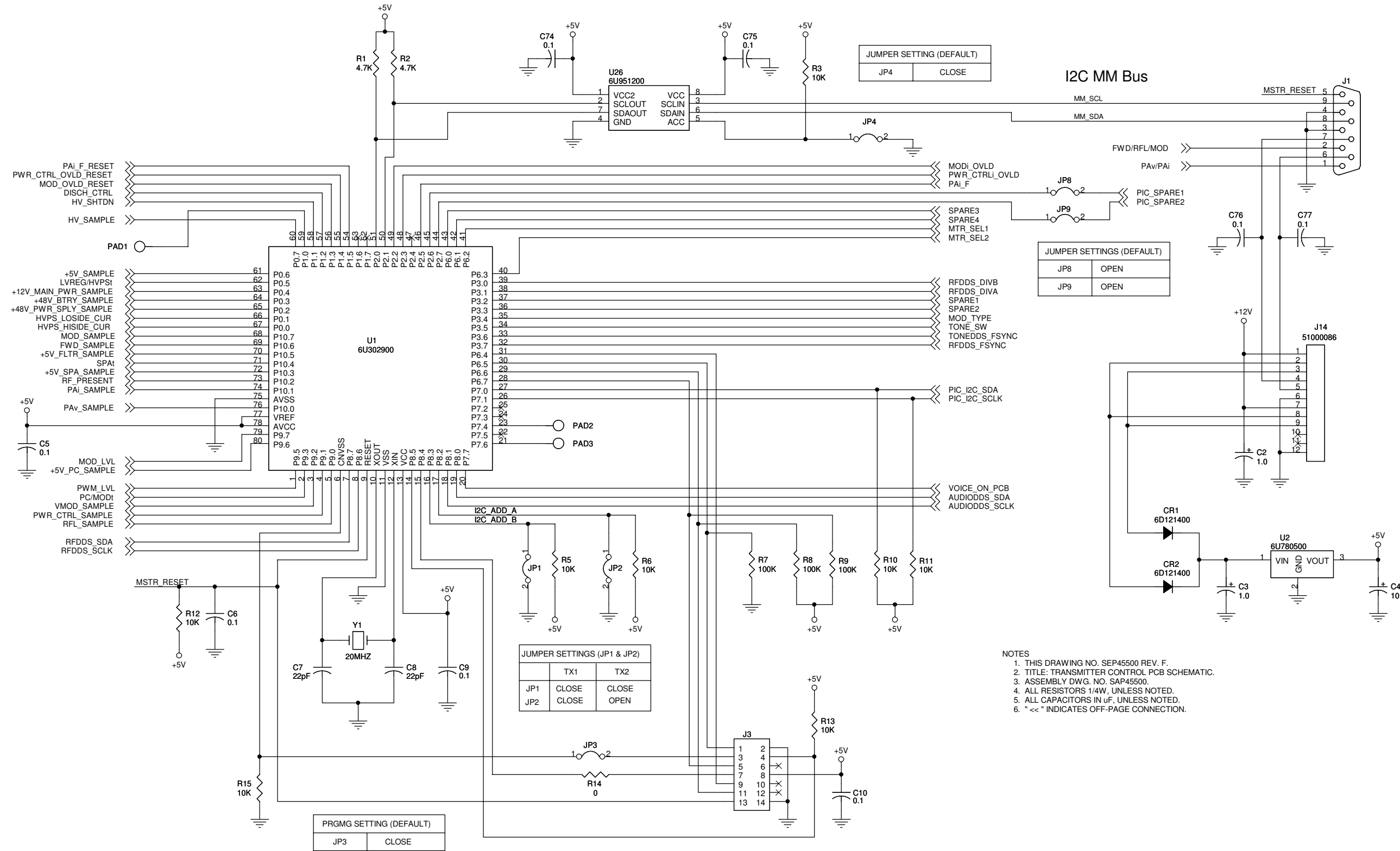
AUDIO SWITCHING (Optional)



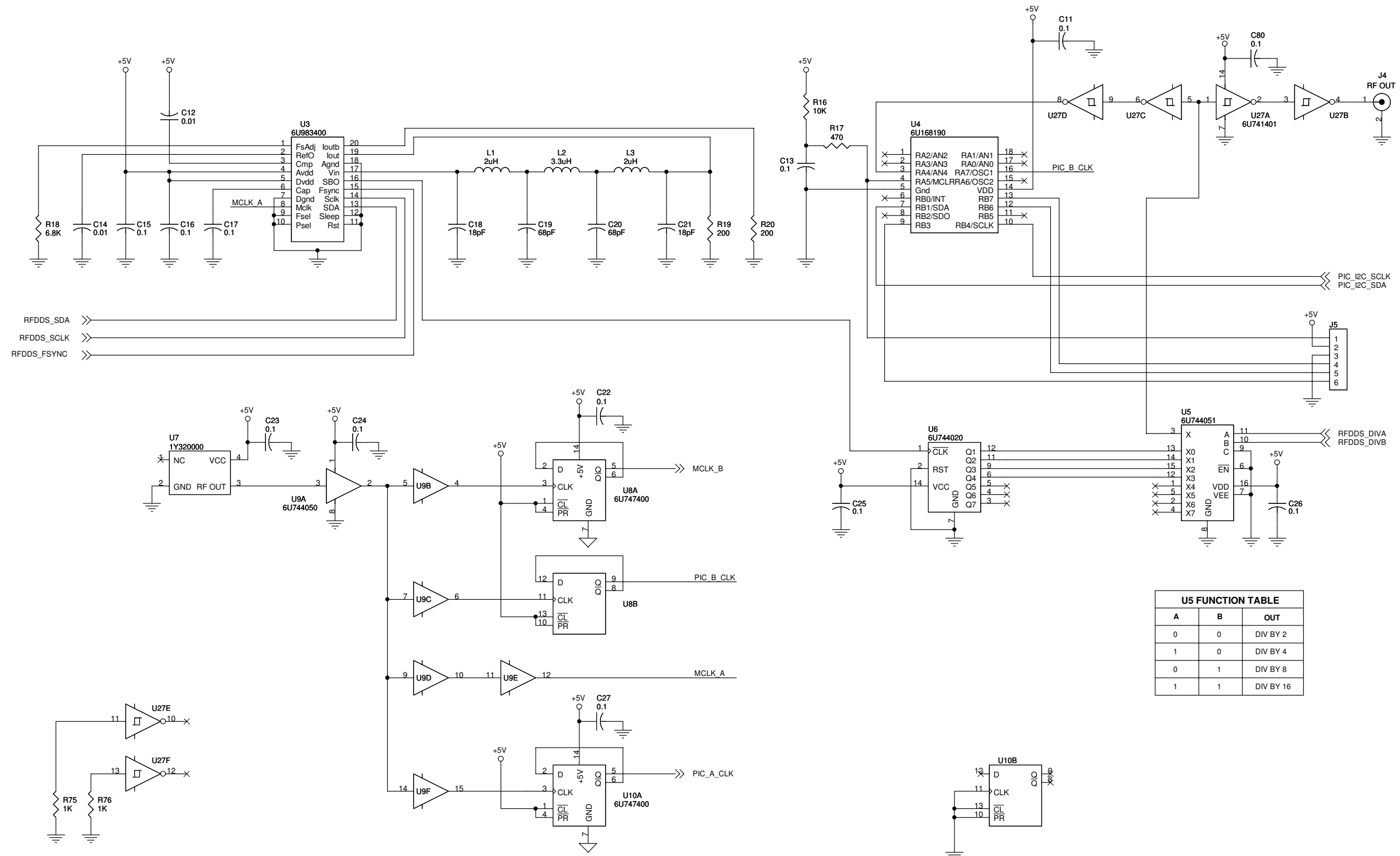
MSK SWITCHING (Optional)

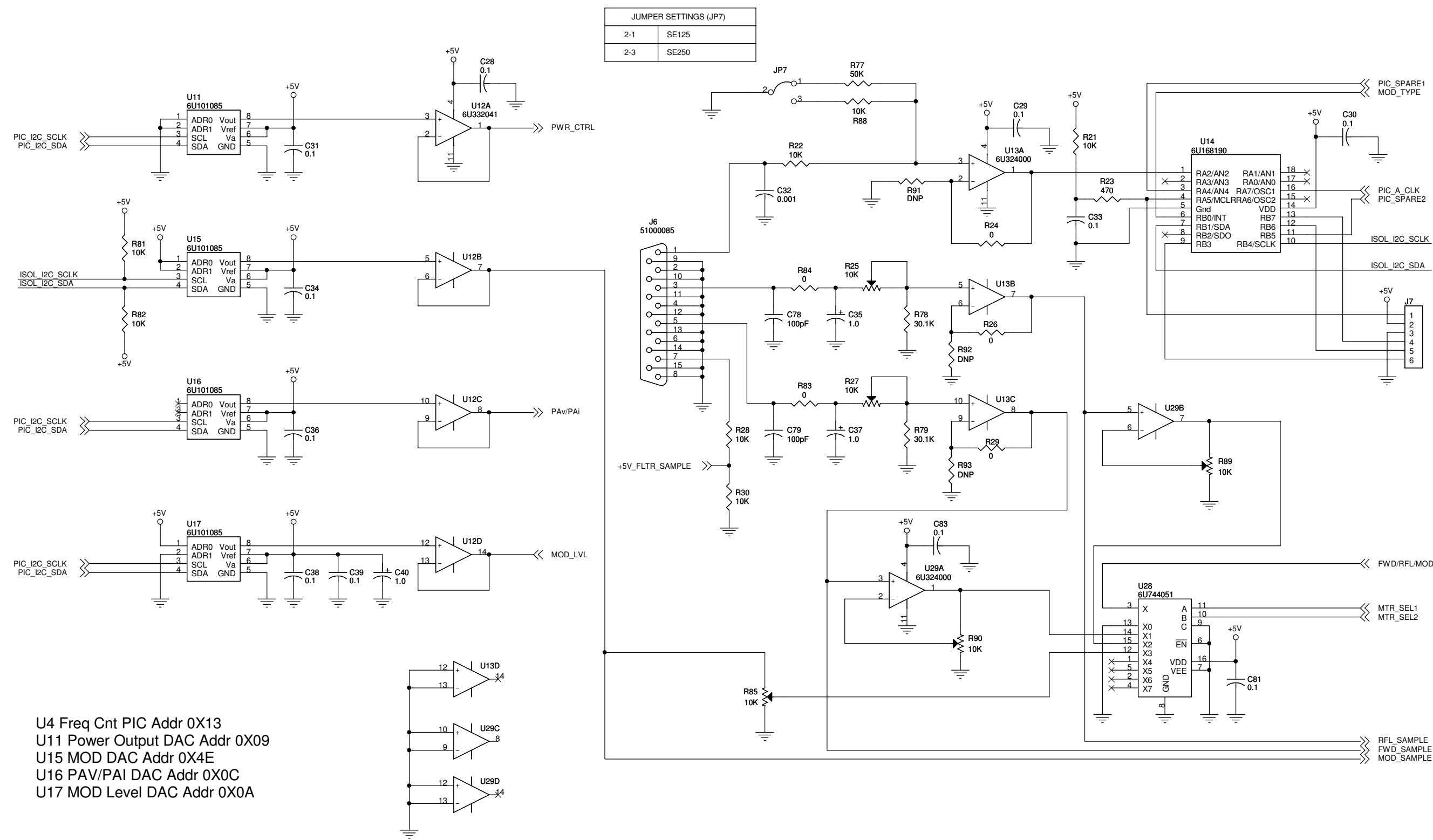


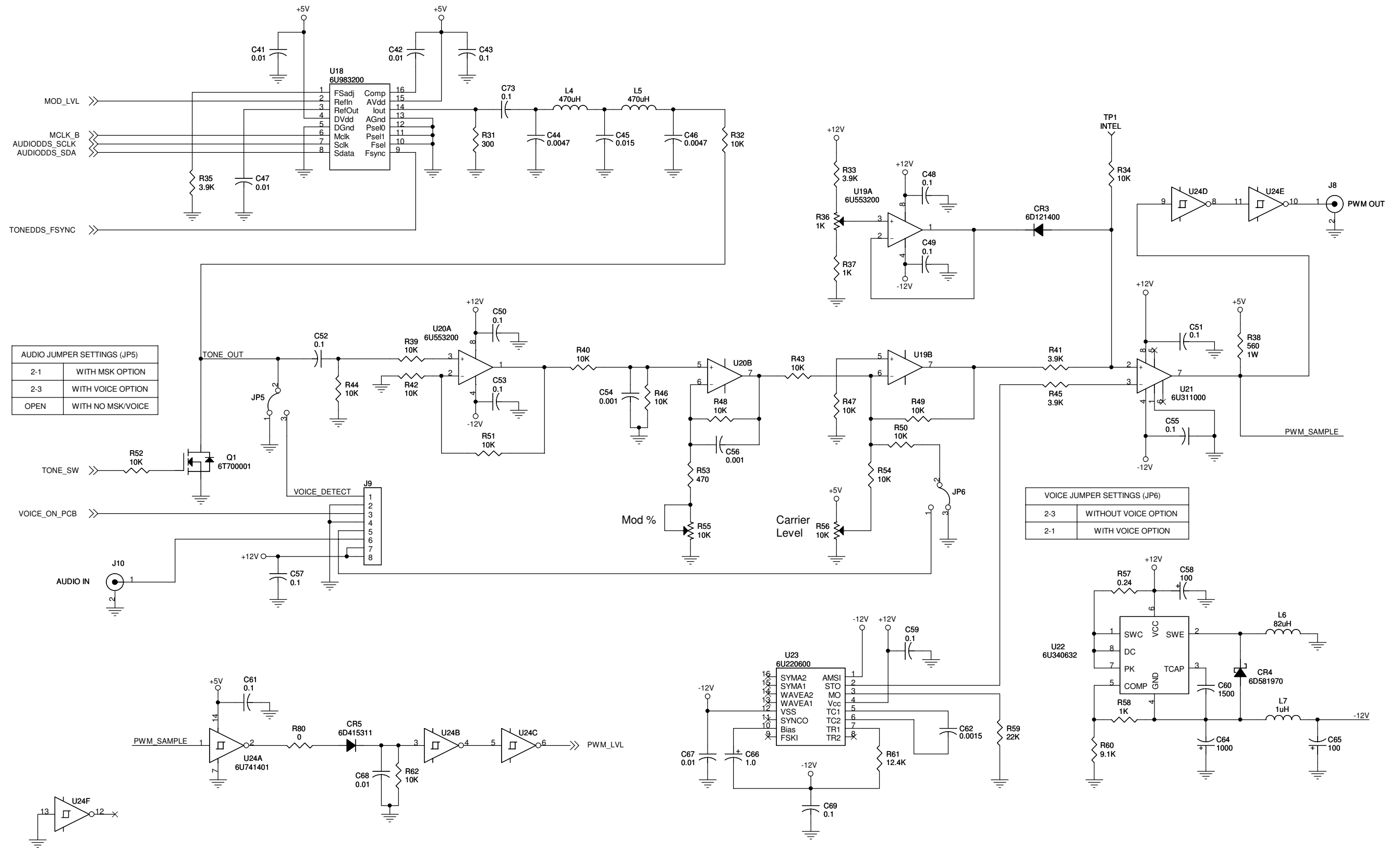


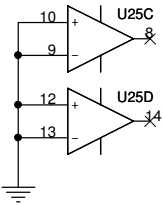
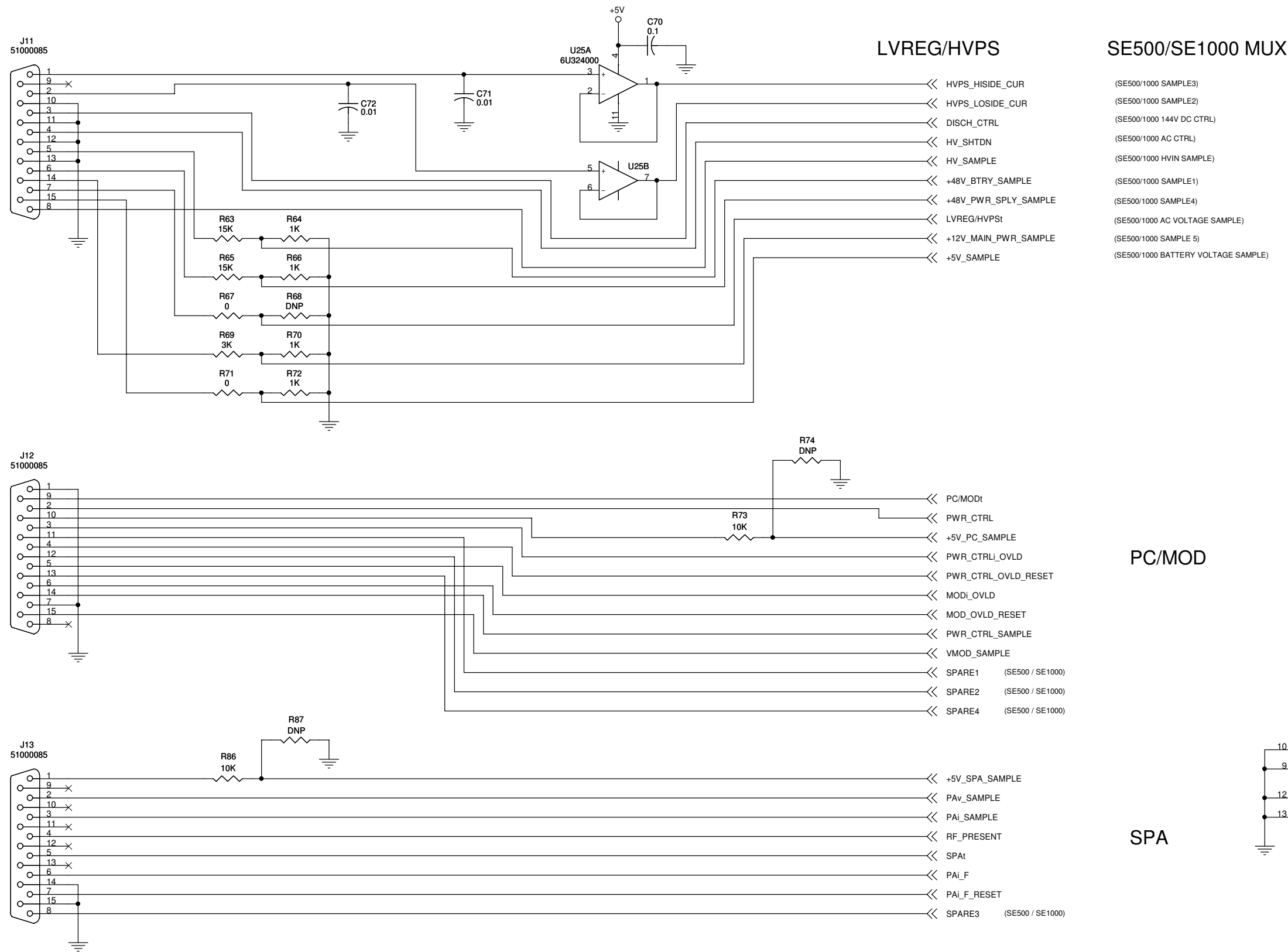


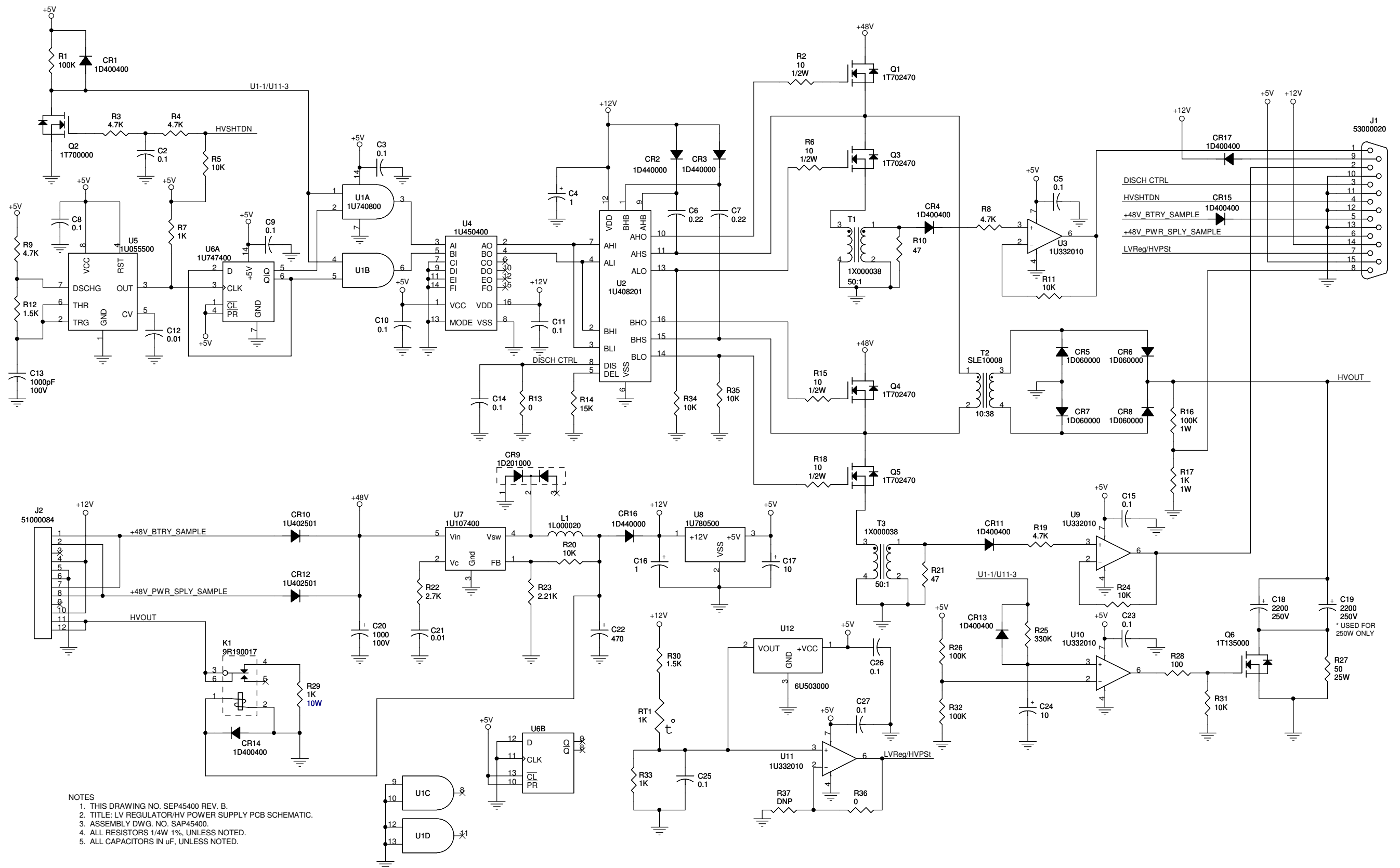
NOTES
1. THIS DRAWING NO. SEP45500 REV. F.
2. TITLE: TRANSMITTER CONTROL PCB SCHEMATIC.
3. ASSEMBLY DWG. NO. SAP45500.
4. ALL RESISTORS 1/4W, UNLESS NOTED.
5. ALL CAPACITORS IN uF, UNLESS NOTED.
6. "<<" INDICATES OFF-PAGE CONNECTION.



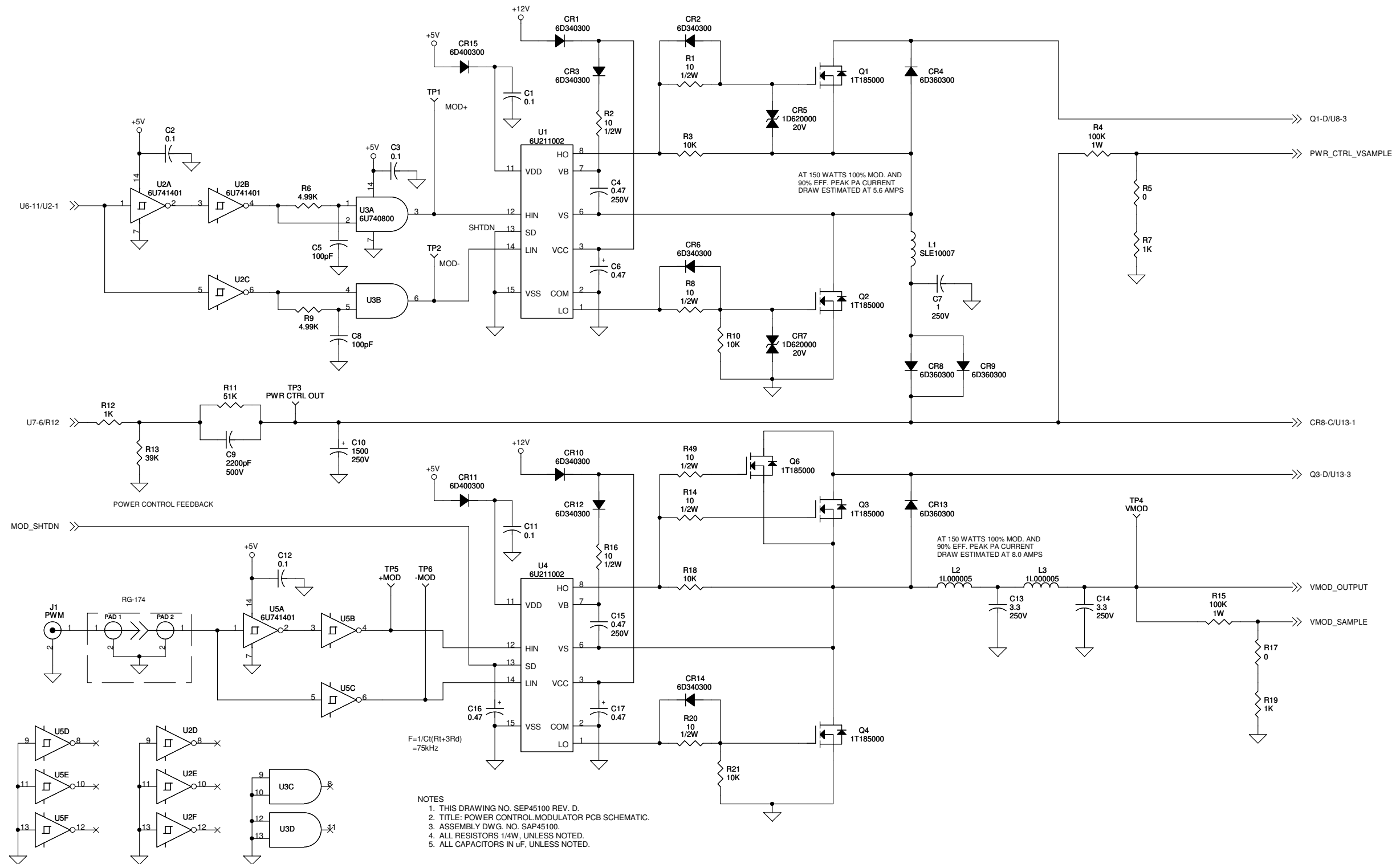


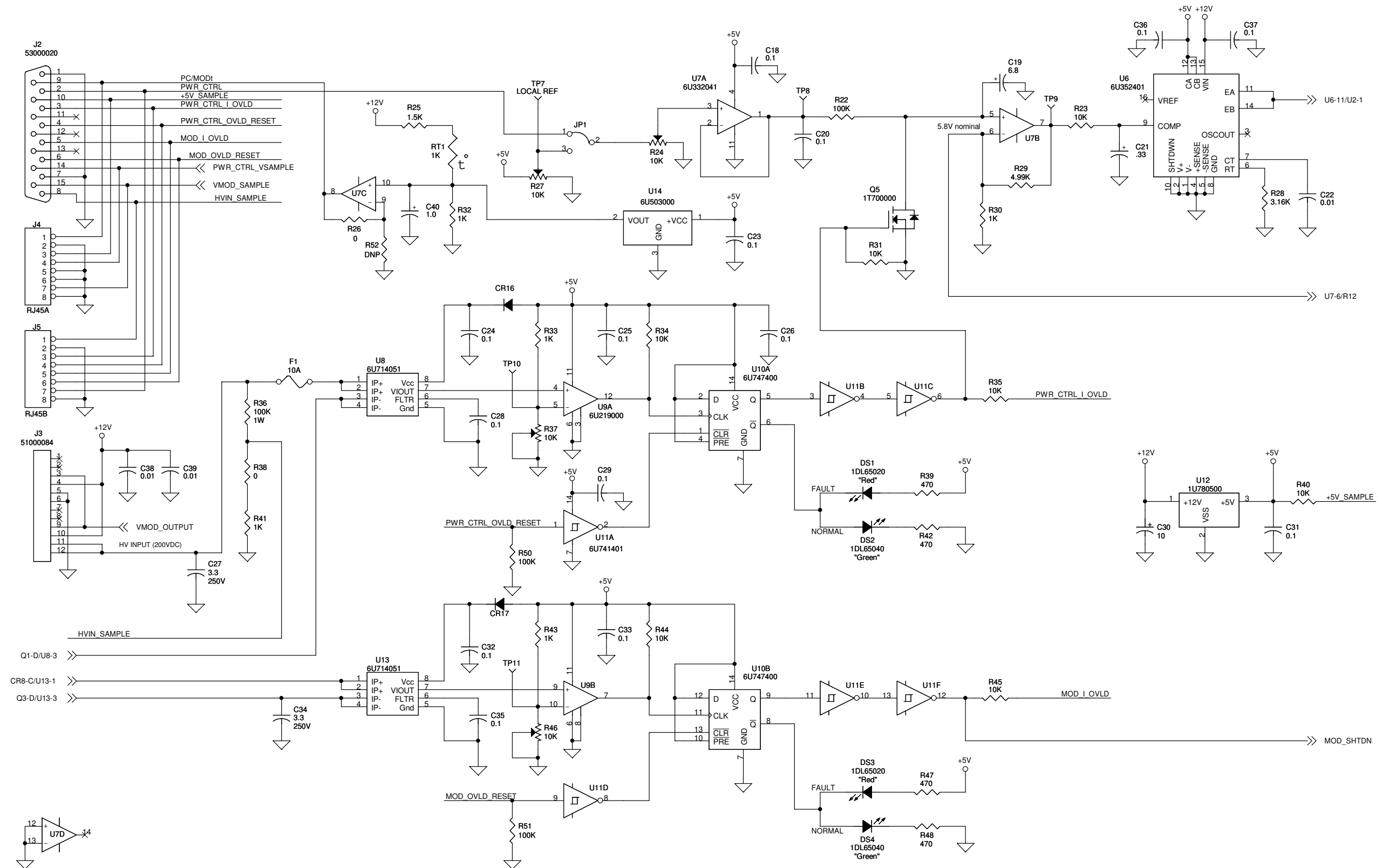


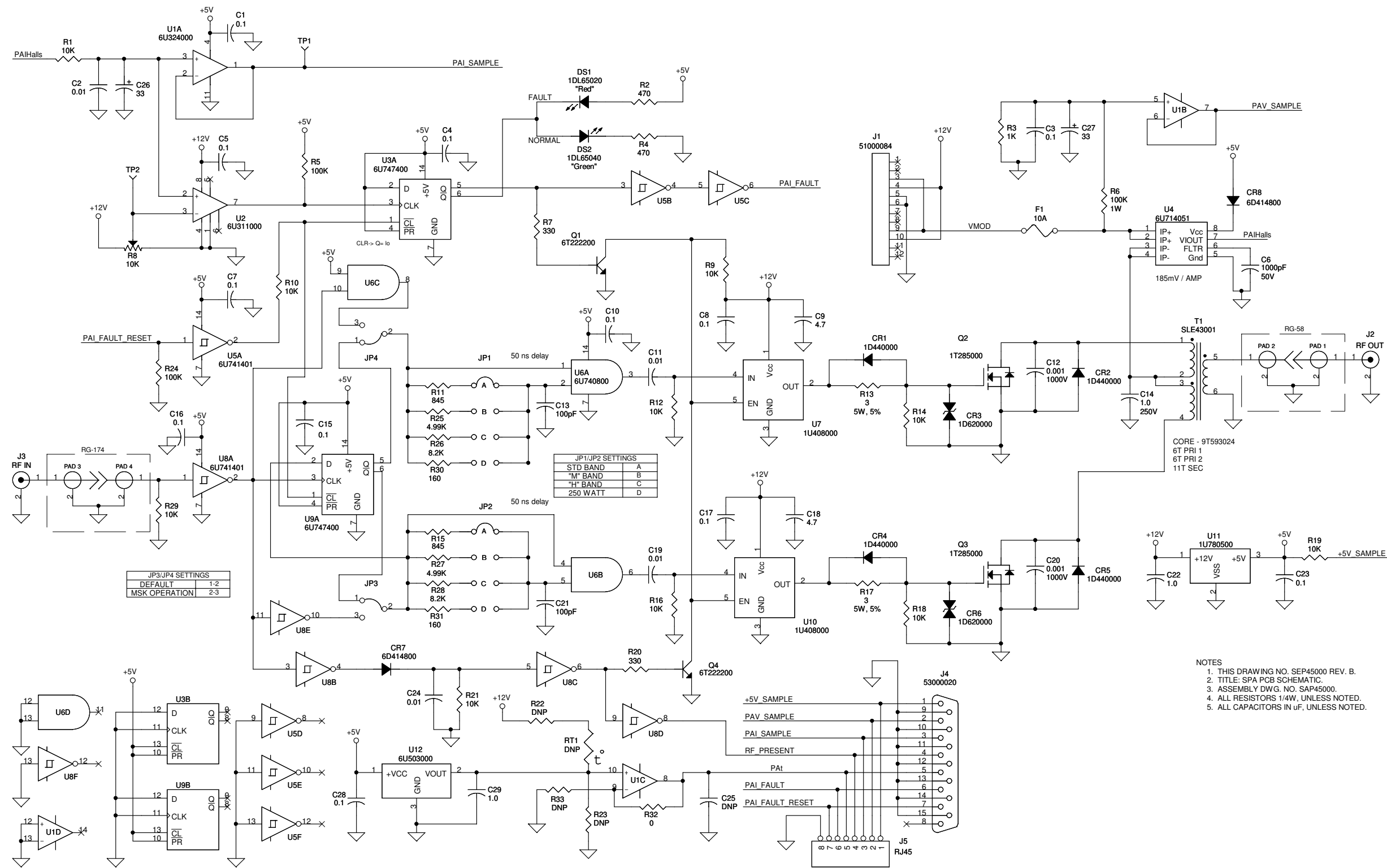


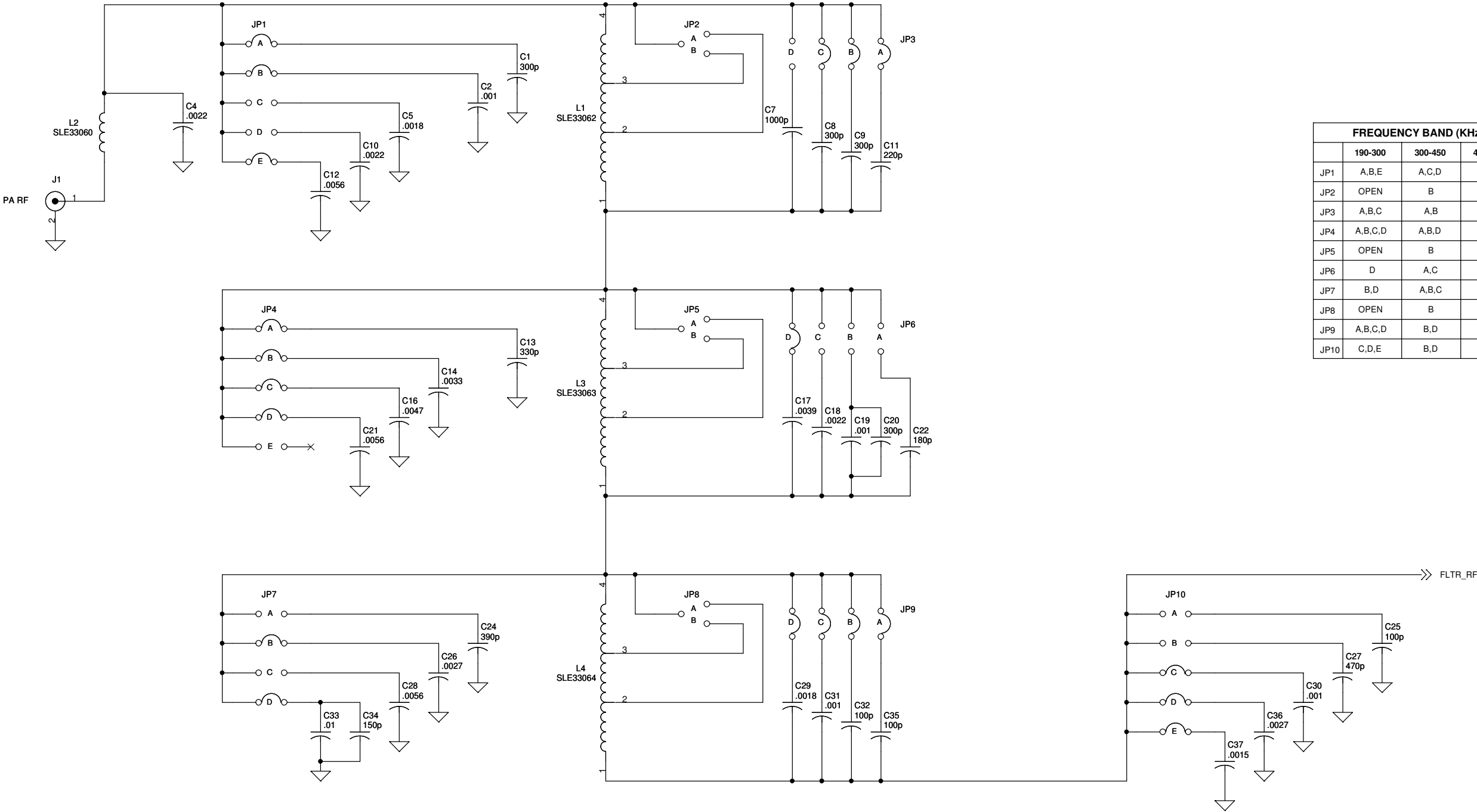


SEP45400 - LOW VOLTAGE/HIGH VOLTAGE REGULATOR SCHEMATIC

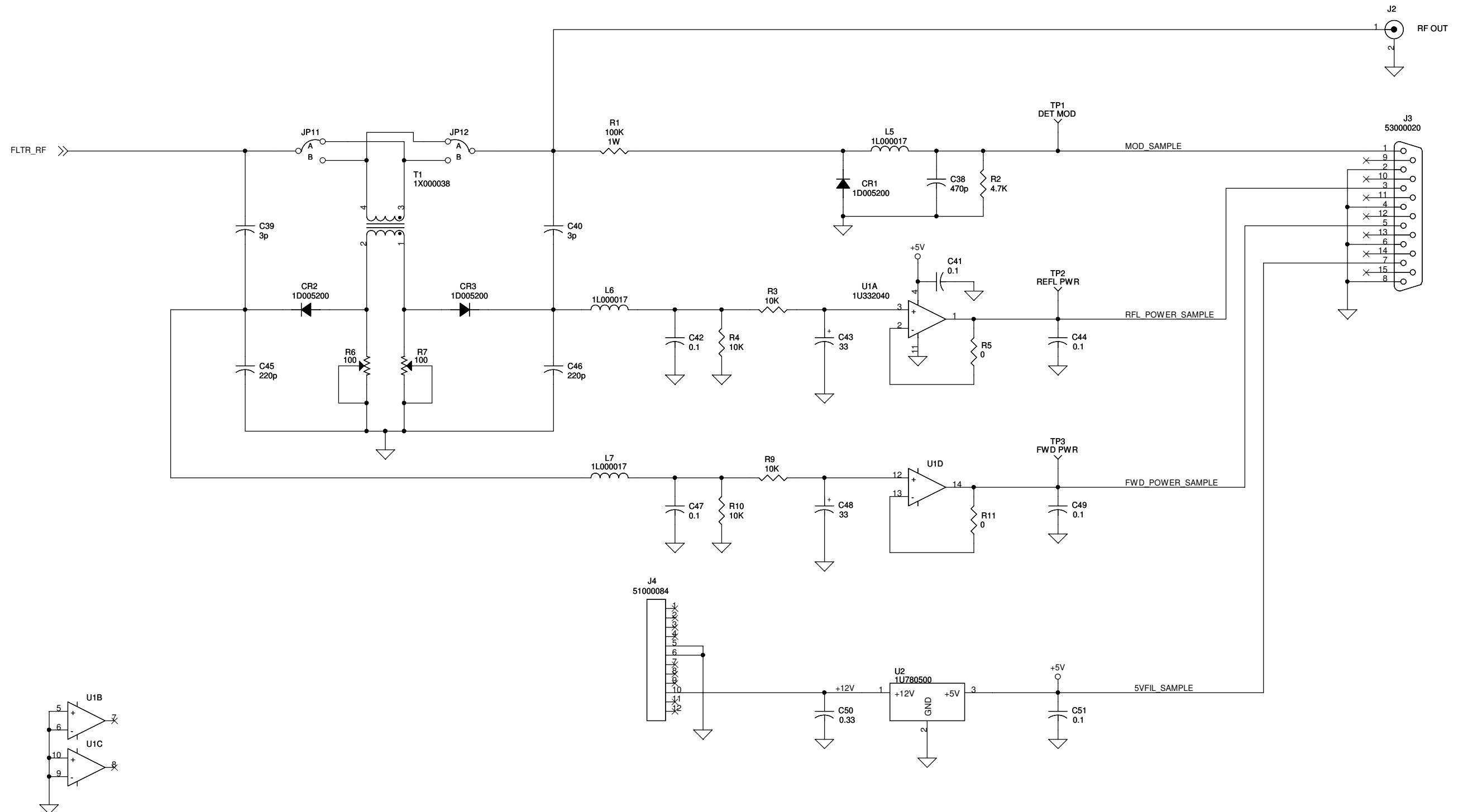


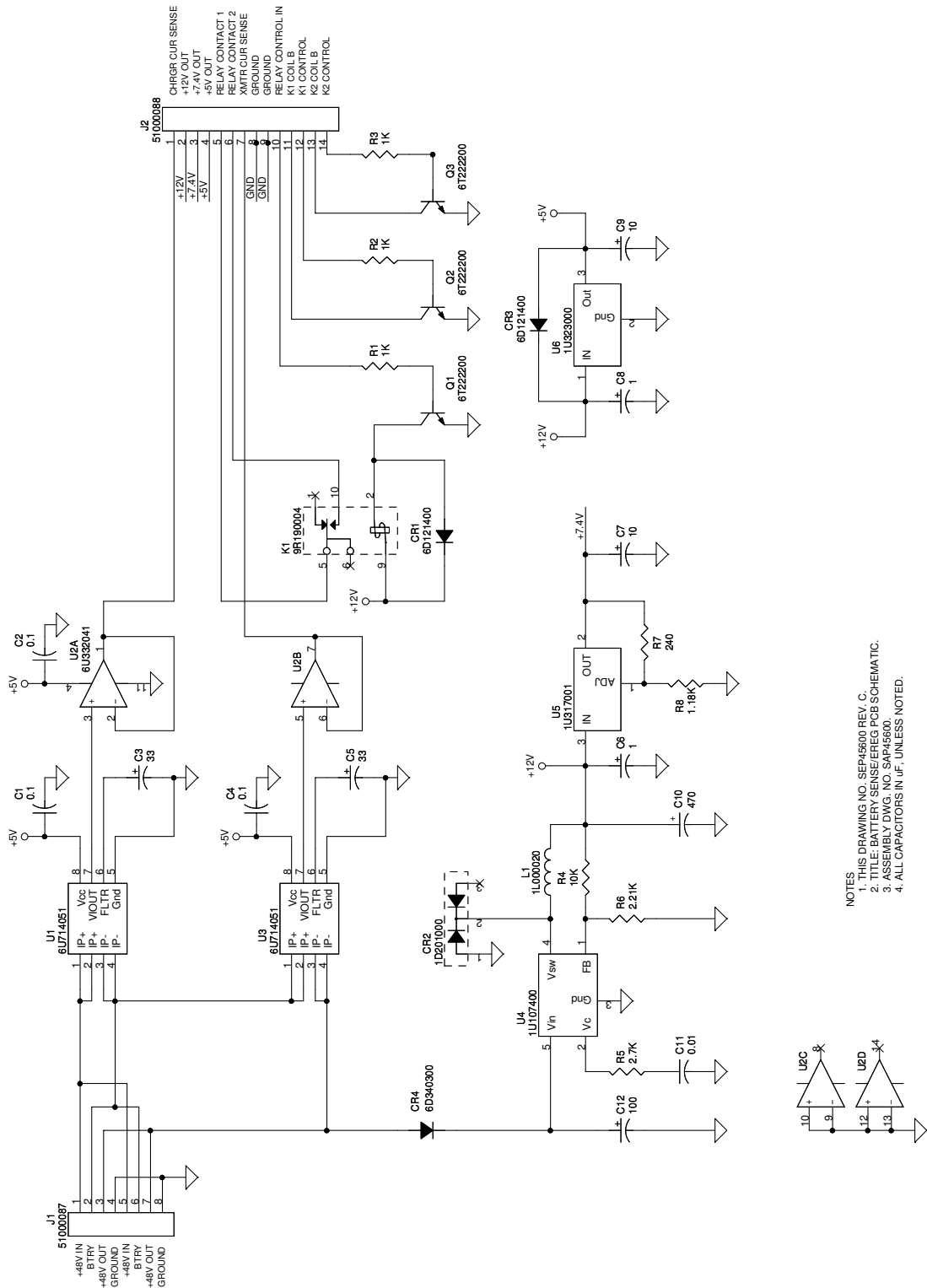






- NOTES
1. THIS DRAWING NO. SEP45200 REV B.
 2. TITLE: FILTER/BRIDGE PCB (STANDARD BAND) SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP45200.
 4. ALL RESISTORS 1/4W, 1%, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. C3, C6, C15, C23, L1 NOT USED FOR STANDARD BAND.
 7. POLY FILM CAPS (IN uF) ARE RATED AT 1600V.
 8. SILVER MICA CAPS (IN pF) ARE RATED AT 500V.
 9. FILTER SHOWN JUMPED FOR 190-300kHz.
 10. ">>" INDICATES OFF-PAGE CONNECTION.





SEP45600 Rev C

Figure 2-38. Battery Sense PCB Schematic