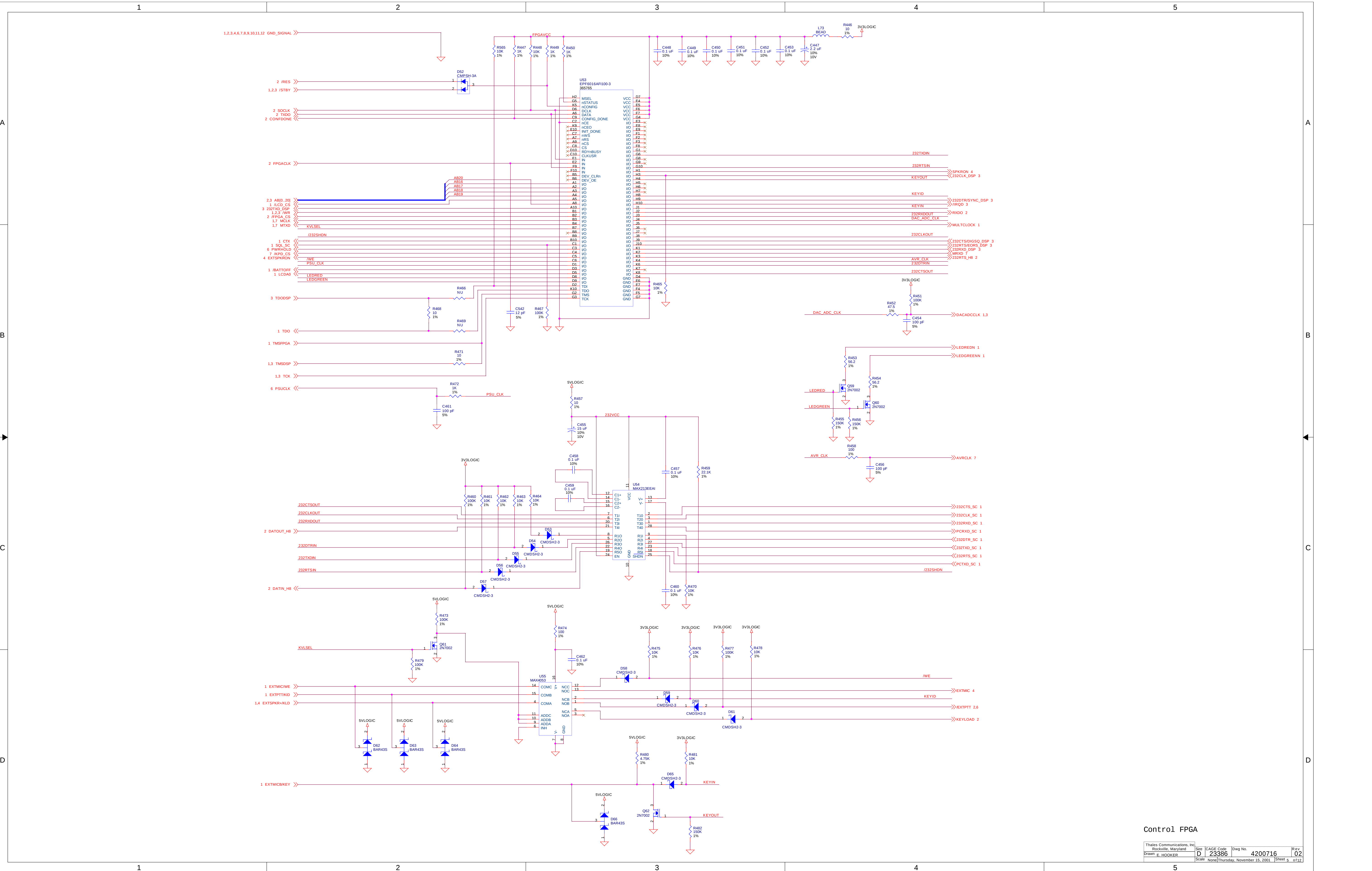
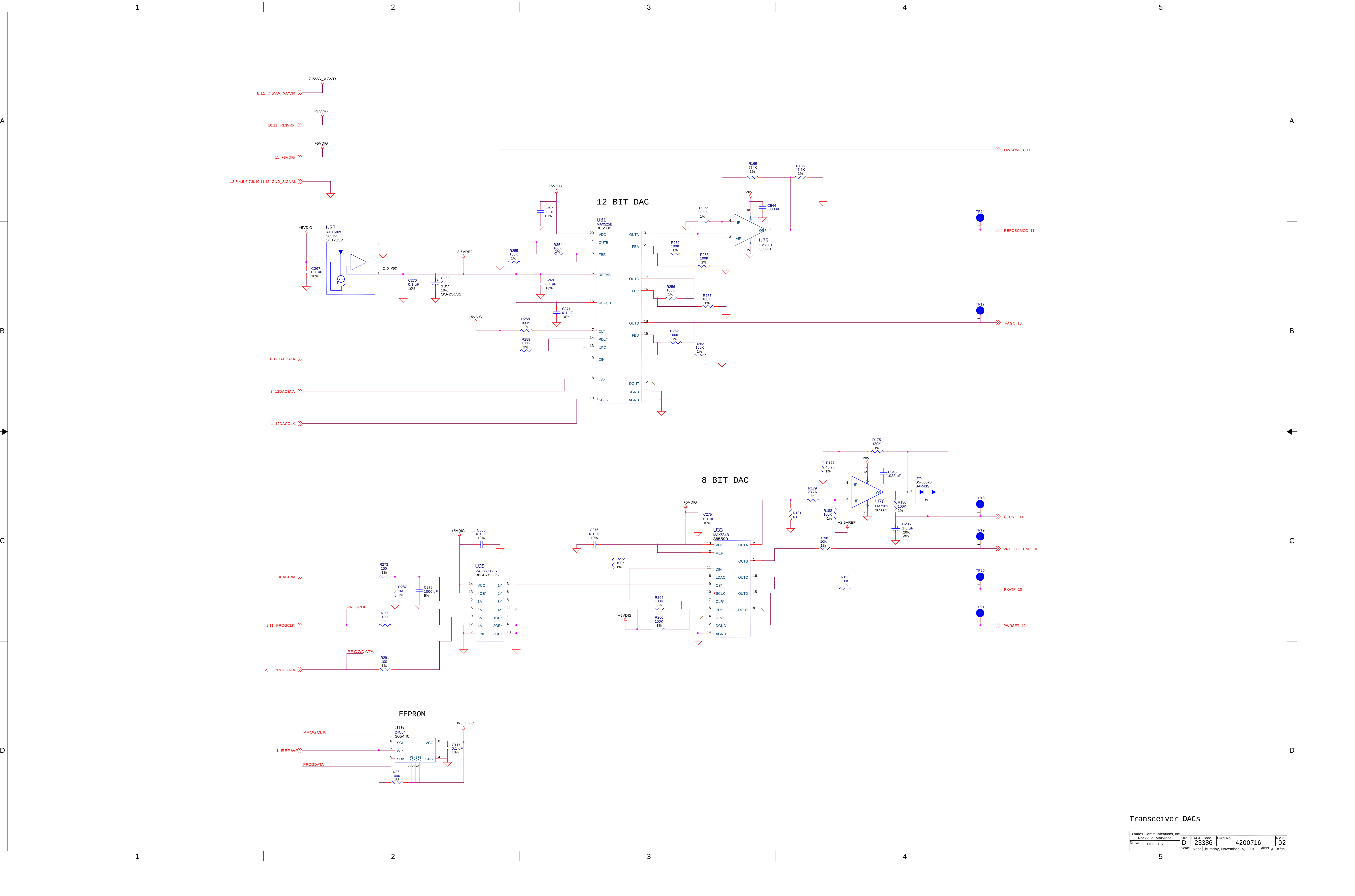
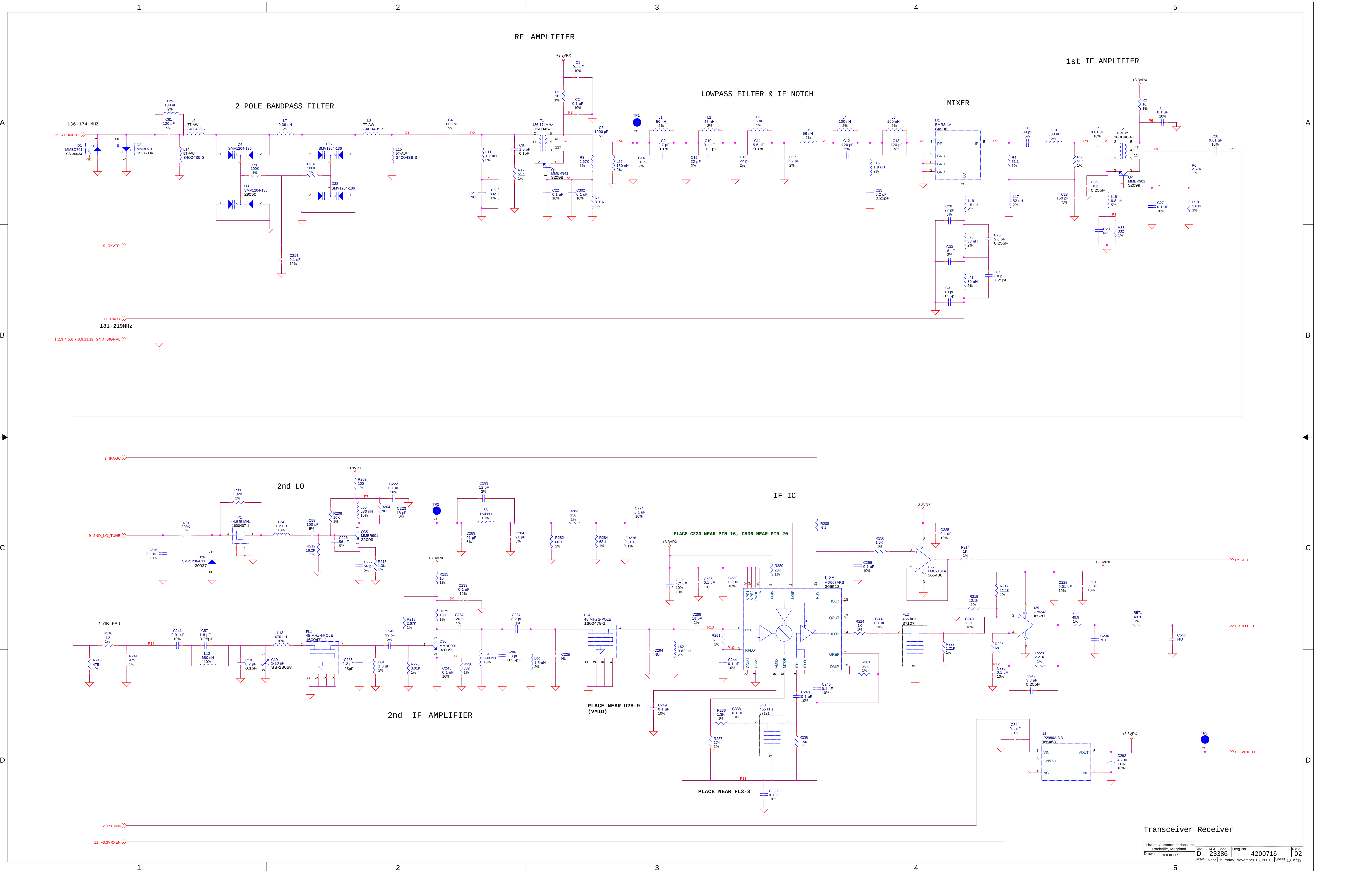


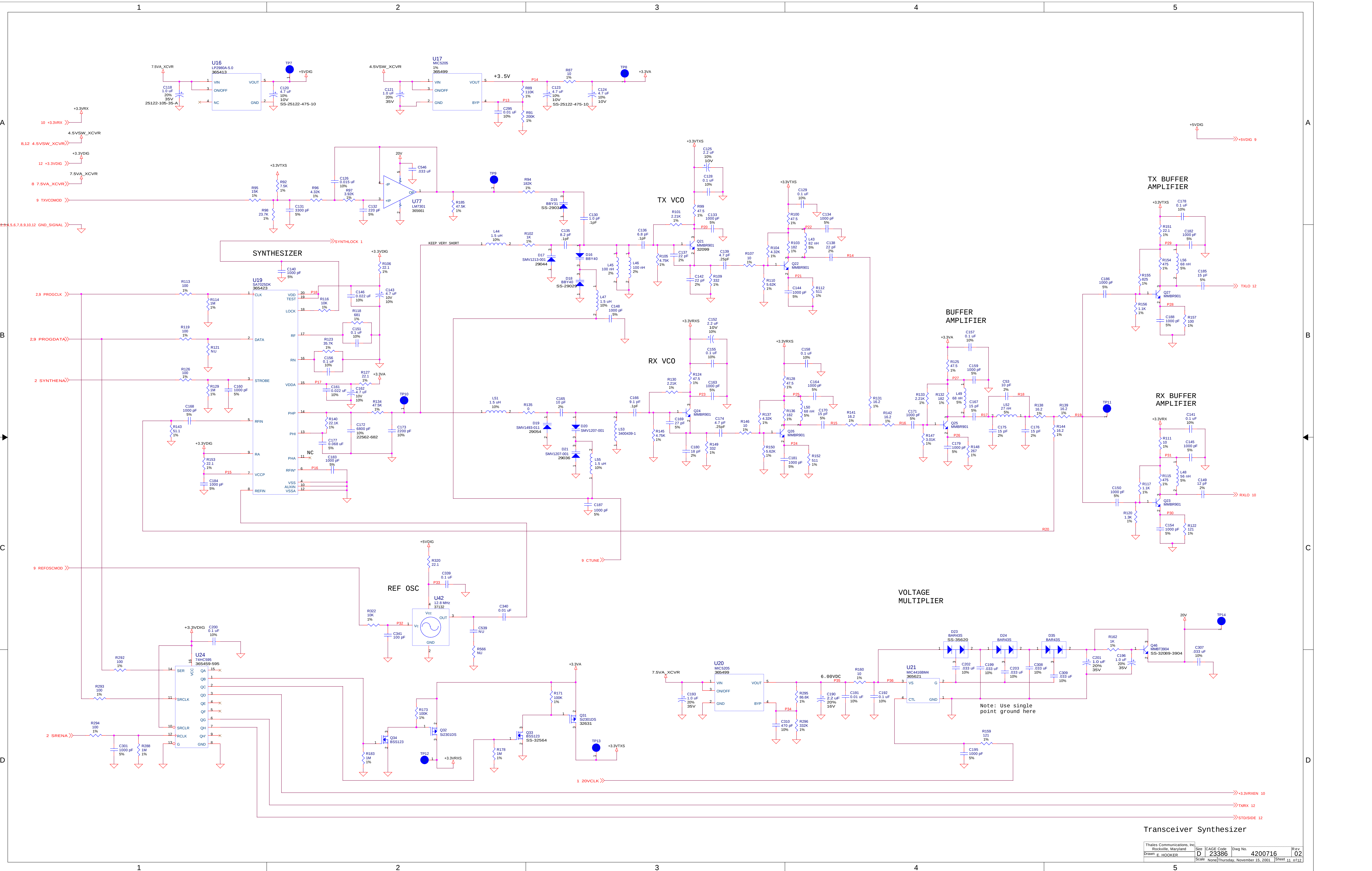


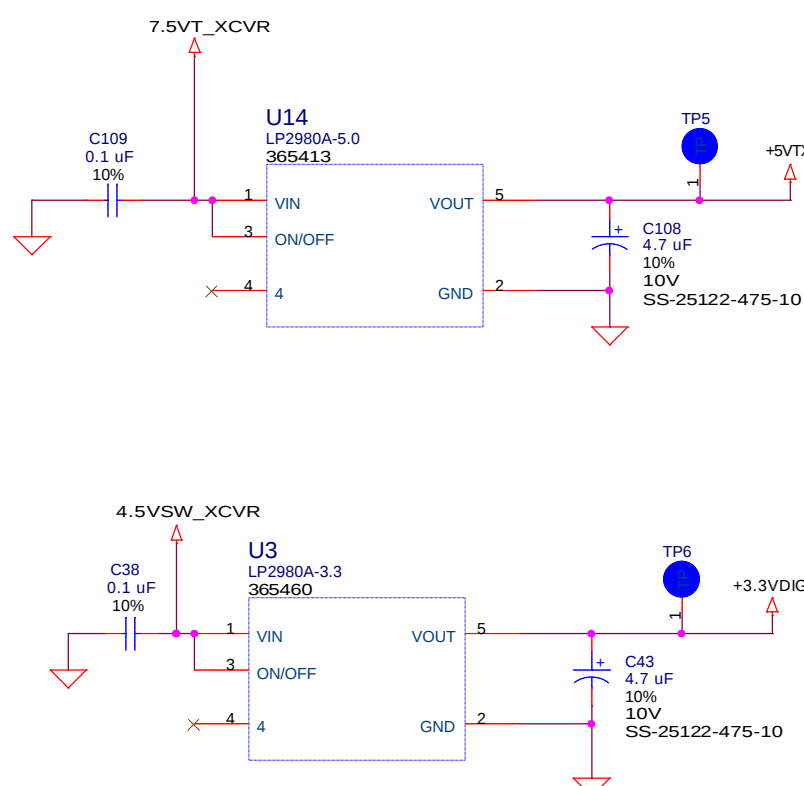
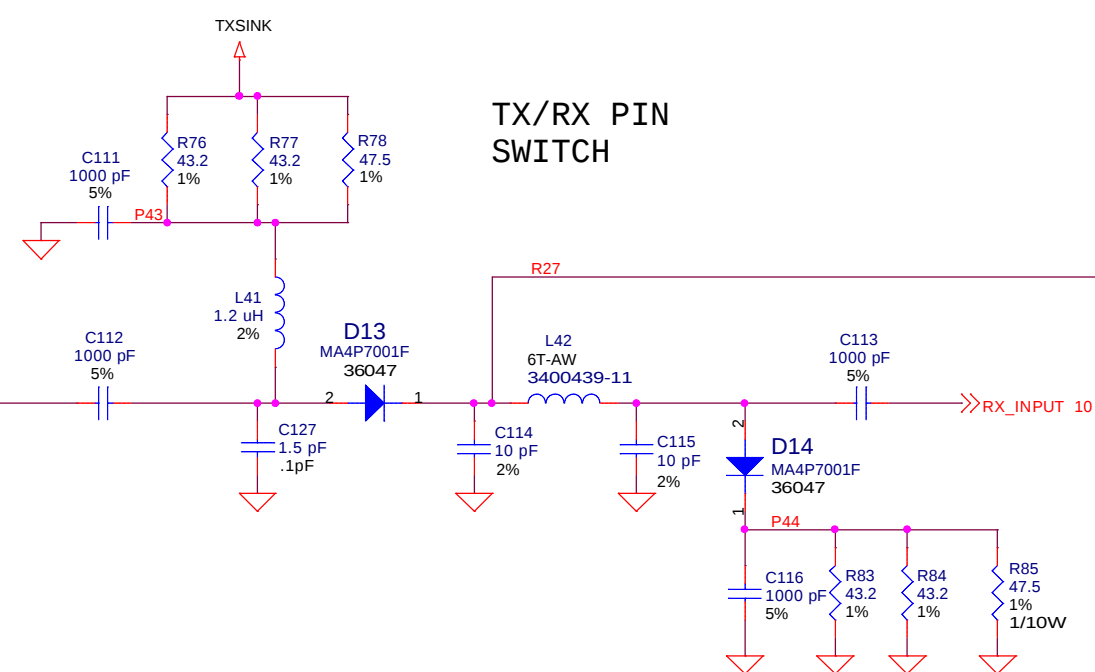
Control FPGA

Thales Communications, Inc. Rockville, Maryland	Size D	CAGE Code 23386	Dwg No. 4200716	Rev 02
Drawn: E. HOOKER	Scale None	Thursday, November 15, 2001	Sheet 5	of 12

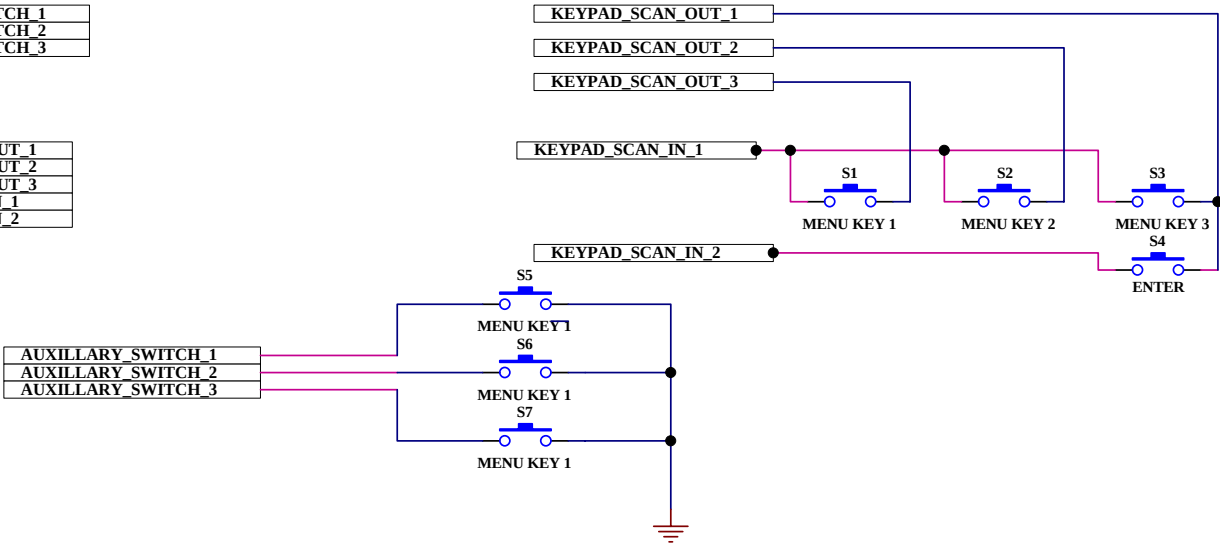
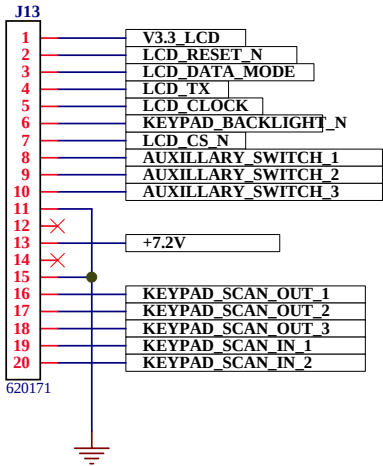
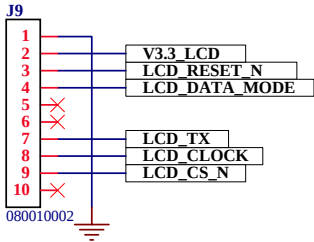
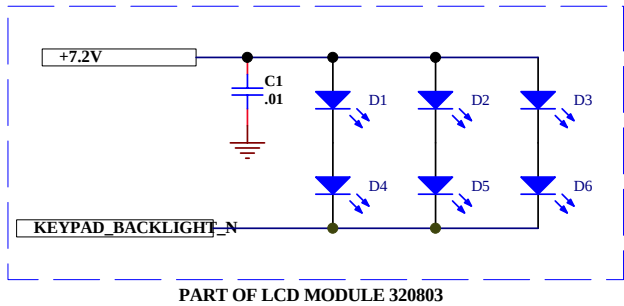








REV	ECN	DESCRIPTION	DATE	APPR
Q1	NONE	PROTOTYPE	04-12-01	
Q2	NONE	REDLINE CHANGES	WS 04/25/01	
Q3	NONE	REDLINE CHANGES	WS 05-10-01	
Q4	NONE	REDLINE CHANGES		
Q5	NONE	REDLINE CHANGES	WS 9/14/01	



			3030 Enterprise Ct. Vista, CA 92083 (760)597-3777	
DATRON WORLD COMMUNICATIONS INC.				
Title: <i>Schematic</i>				
<i>SCHEMATIC, KEYPAD/DISPLAY</i>				
Size: B	Drawn: ABE SABET	Date: 04-12-01	Drawing Number: 994466	Rev: Q5
	Appr:	Date:		
File: 994466Q5.Sch		Date: 5-Dec-2001	Time: 15:56:45	Sheet 1 of 1

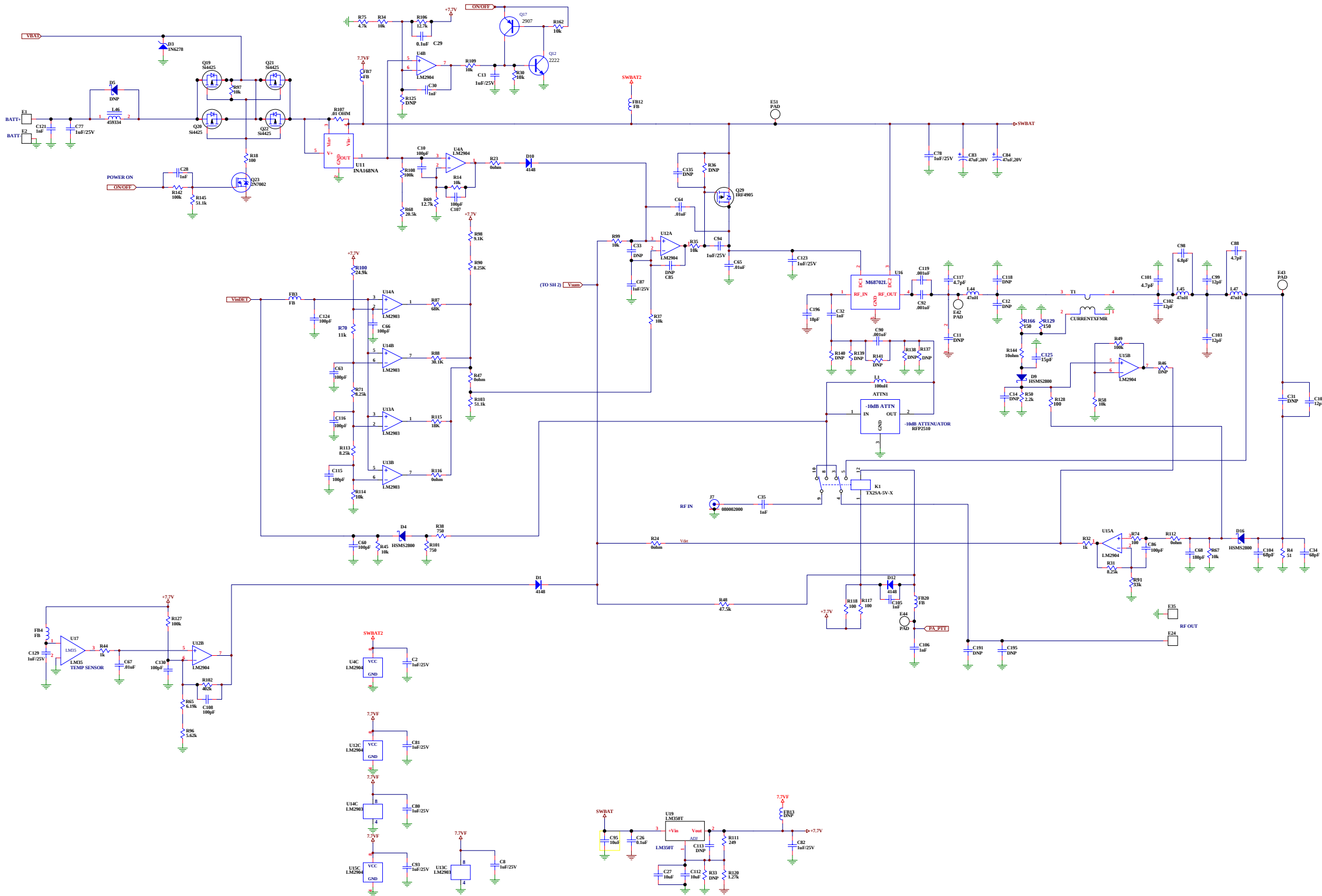
REV	ECN	DESCRIPTION	DATE	APPR
A	02-0051	RELEASE		

[illegible]

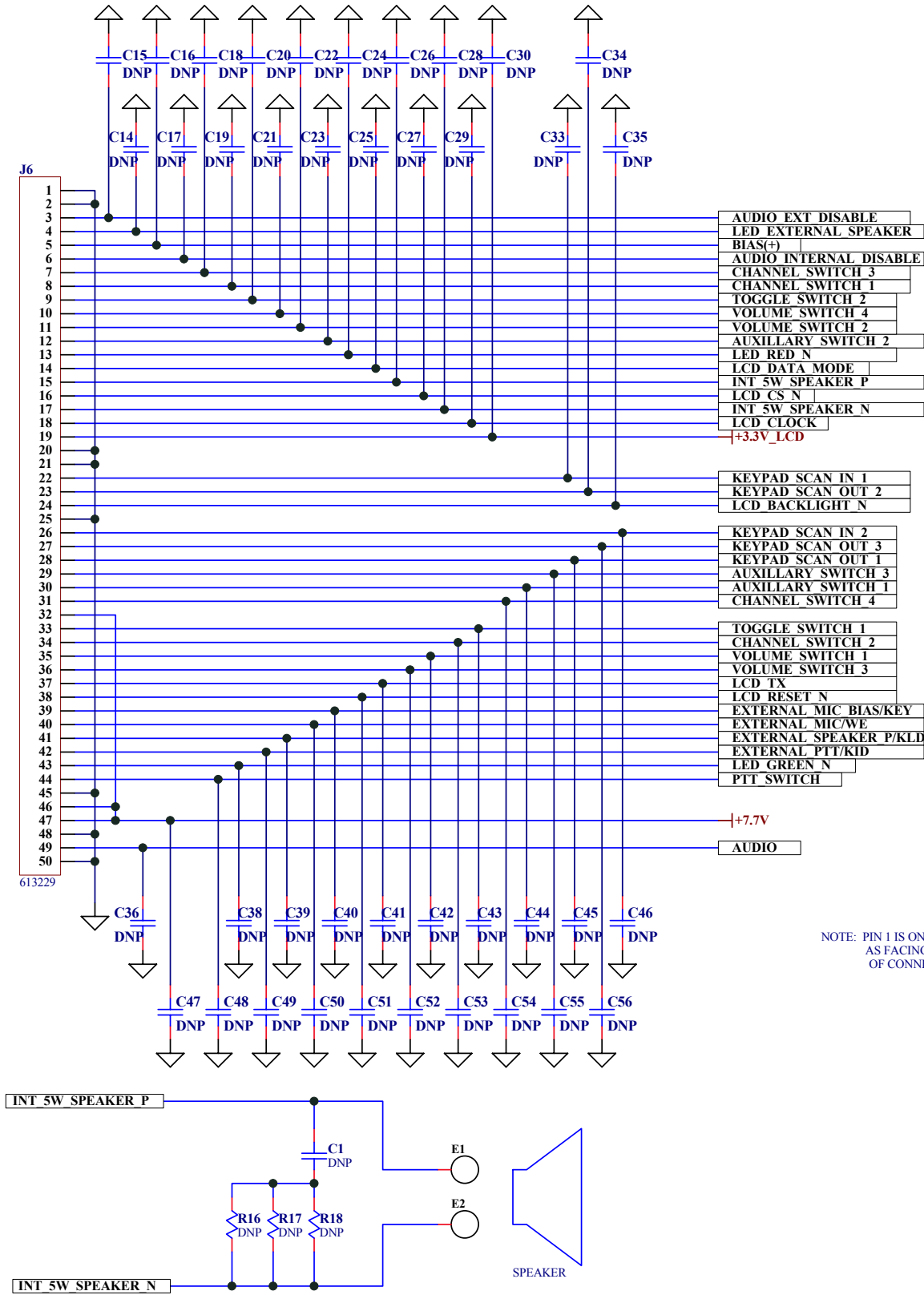
☐	ON/OFF
☐	PA_PTT
☐	VinDET
☐	VBAT
☐	Vsum
☐	ON/OFF

994473Ash3.sch

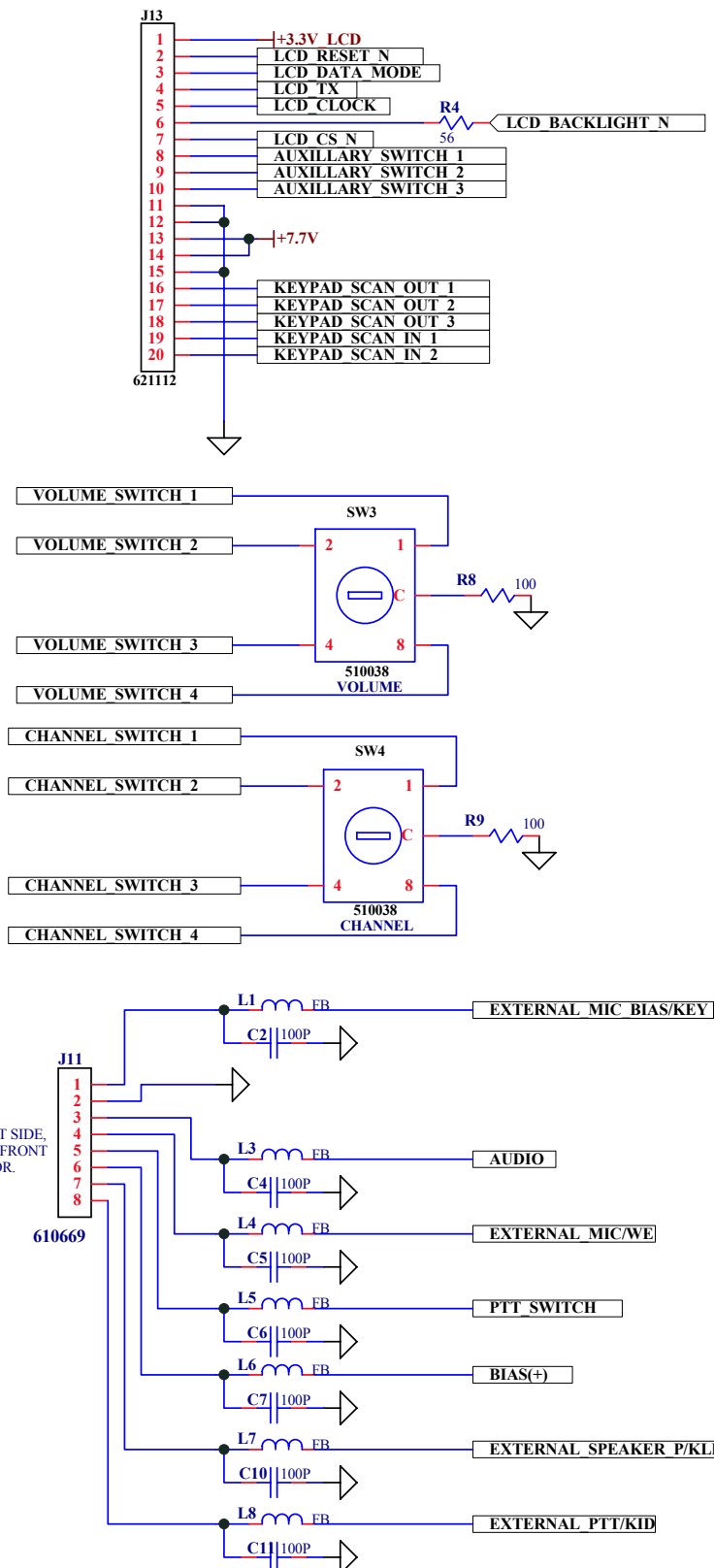
REV	ECN	DESCRIPTION	DATE	APPR



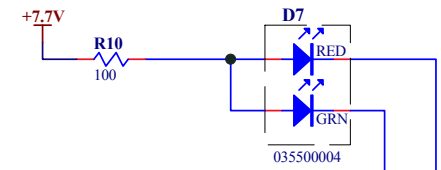
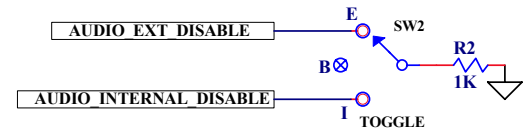
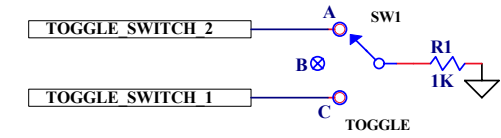
Title: Schematic		Date: 8-16-2002		Rev: A
Size: D		Drawing Number: 994473A		Rev: A
BOM: 125-27000		Date: 8-16-2002		Time: 135412
File: 994473A03.sch		Sheet: 3 of 3		



NOTE: PIN 1 IS ON LEFT SIDE,
AS FACING AT FRONT
OF CONNECTOR.



REV	ECN	DESCRIPTION	DATE	APPR
Q1	NONE	PROTOTYPE	04-12-01	
Q2	NONE	REDLINE CHANGE UPDATE	WS 4-25-01	
Q3	NONE	REDLINE CHANGE UPDATE	WS 5-10-01	
Q4	NONE	REDLINE CHANGE UPDATE	WS 5-23-01	
Q5	NONE	REDLINE CHANGE UPDATE	WS	
Q6	NONE	REDLINE CHANGE UPDATE	BB 09-12-01	
Q7	NONE	REDLINE CHANGE UPDATE	BB 09-26-01	
Q8	NONE	REDLINE CHANGE UPDATE	BB 11-01-01	
Q9	NONE	J6 CHANGED - WAS 769344	BB 11-13-01	



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DATRON WORLD COMMUNICATIONS INC.

Title: *Schematic*
SCHEMATIC, INTERFACE BOARD

Size: **B** | Drawn: BILL BROOKS | Date: 09-12-01 | Drawing Number: **994467** | Rev: **Q9**

Appr: | Date: |

File: 994467Q9.SCH | Date: 8-Feb-2002 | Time: 16:35:29 | Sheet 1 of 1