

NOTES:
 1. POST COAT BOTH SIDES OF P.C. BOARD WITH PN 016-01040-0000. THE FOLLOWING SHOULD BE FREE OF POST COATING:
 E1-E6, MTG1-MTG6, (BOTH SIDES OF P.C. BOARD). P1, R11, R28 TP1-TP6, POST COAT SOLDER JOINTS ONLY ON THE FOLLOWING COMPONENTS: Q2, Q3, Q6 AND Q10.

2. PRINTED CIRCUIT ASSEMBLY IDENTIFICATION MUST BE IN ACCORDANCE WITH SPEC. 001-01101-0000.
 3. WHERE APPLICABLE, LIQUID STAKE ALL FASTENERS PER SPEC. 001-01080-0000.
 4. Q2, Q3, Q6, Q10, MUST BE PERFORMED SO THAT CENTER OF SCREW HOLE IS 0.595 FROM CENTER OF LEAD PADS.
 5. MAX HEIGHT OF C3, C5, C11, AND C21 NOT TO EXCEED .390 INCH. ALL OTHER COMPONENTS NOT TO EXCEED .335 INCH. MAX DEPTH BELOW BOARD NOT TO EXCEED .110 INCH.
 6. APPLY RTV (016-01082-0000) TO C1, C2, C3, C5, C11, C14, C15, C20, C21, C22 AND C24. RTV IS TO BE APPLIED, AT A MINIMUM, TO BOTH SIDES, FOR THE ENTIRE LENGTH OF THE COMPONENT.
 7. TEST MAY SHORT THE ANODE AND CATHODE OF CR10 WITH MAG WIRE (026-00032-0008) DURING TRANSMITTER ALIGNMENT TO INCREASE FALL TIME AS NEEDED.

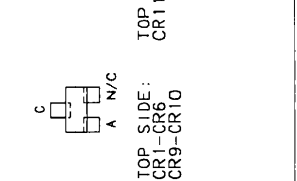
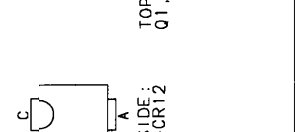
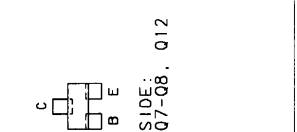
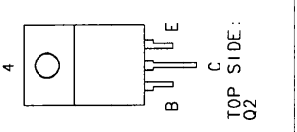
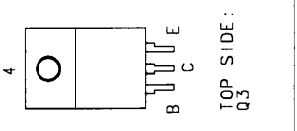
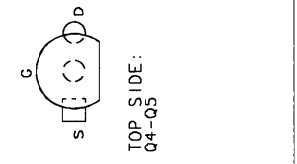
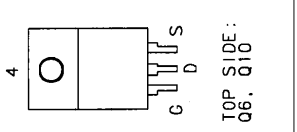
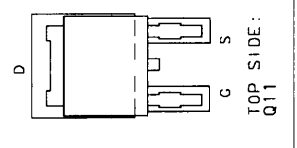
NEARSIDE VIEW OF PC BOARD

THIS DRAWING IS NOT COMPLETE WITHOUT PARTS LIST 200-08274-0020

REV	DATE	BY	UNIT
706295	07/01	SAL	
706298	10/01	SAL	
715696	05/02	NICD	
714031	05/02	KN01T	
714031A	05/02	LUST	
716397	07/02	LUST	
717319	08/02	LUST	
716882	09/02	LUST	

200-05049-0003 KT 70/71/73

MENTOR GRAPHICS DRAWING TO BE MAINTAINED BY P.C. DESIGN



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MENTOR GRAPHICS DRAWING TO BE MAINTAINED BY P.C. DESIGN

SIZE: D	Honeywell International Inc
ELEC ENG: OSMA	Olathe, Kansas USA
DRAWN: MATTHEW	CAGE: 23373
DATE: 07/00	MODULATOR BOARD
CHK (ES):	
P/N RANGE: NONE	
ANGLES: #/	
TOL. UNLESS NOTED:	
.XX ± # INCHES	
.XX ± .010 INCHES	
FINISH:	
DOC. NUMBER:	300-08274-0020
SHEET:	1 OF 1

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