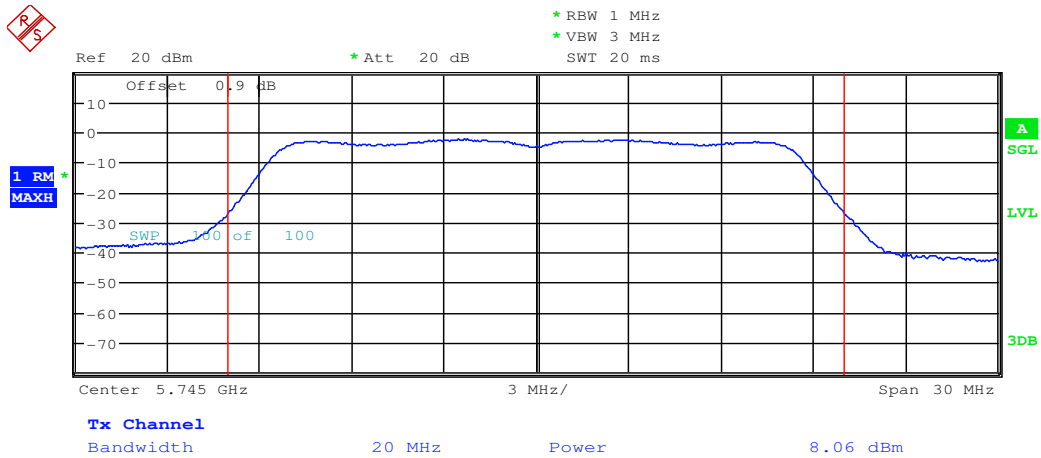
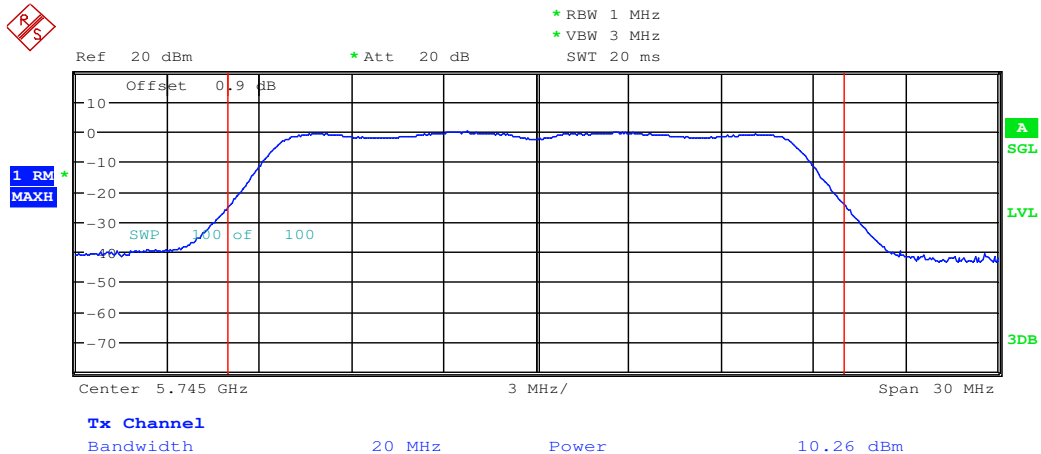




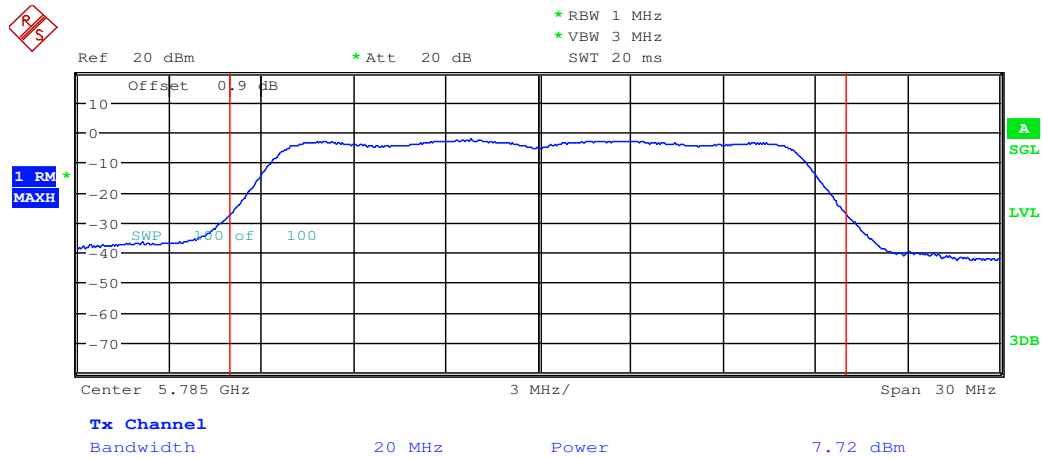
Maximum Conduct Output Power\_11A\_5745\_Ant1



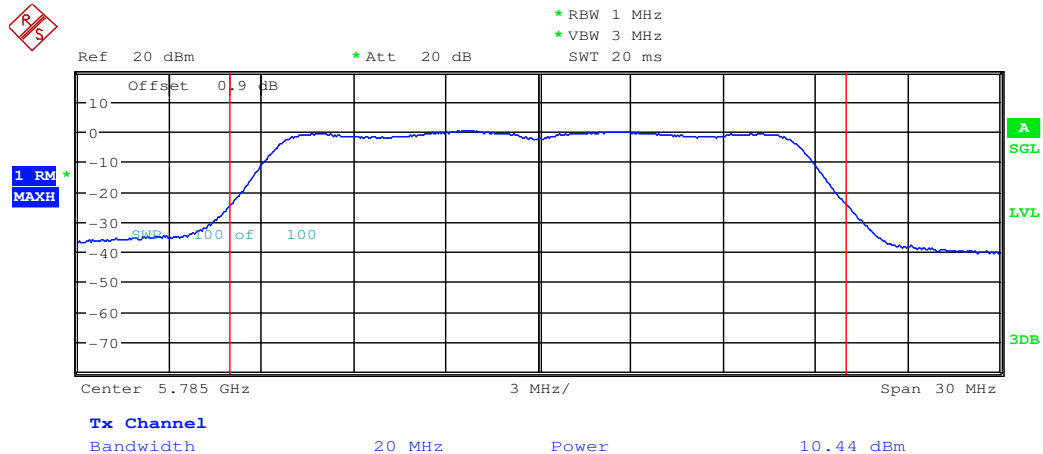
Maximum Conduct Output Power\_11A\_5745\_Ant2



## Maximum Conduct Output Power\_11A\_5785\_Ant1

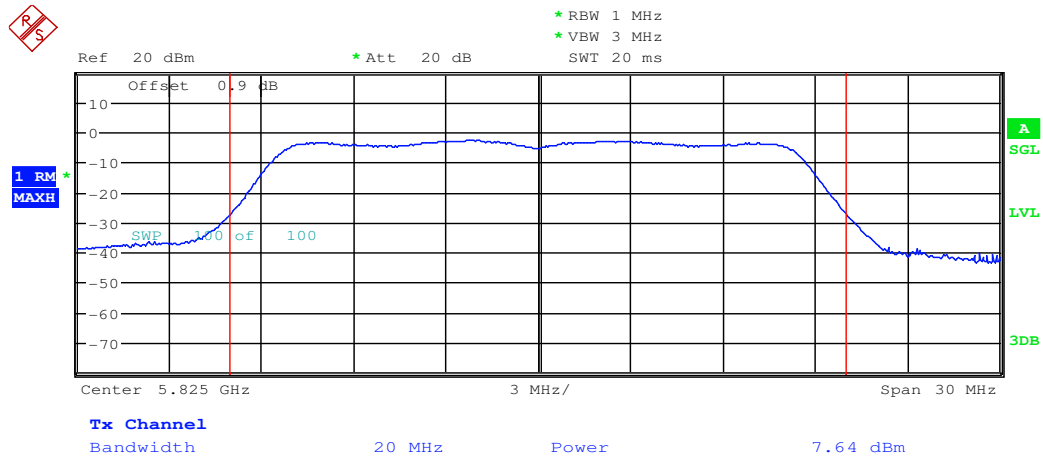


## Maximum Conduct Output Power\_11A\_5785\_Ant2

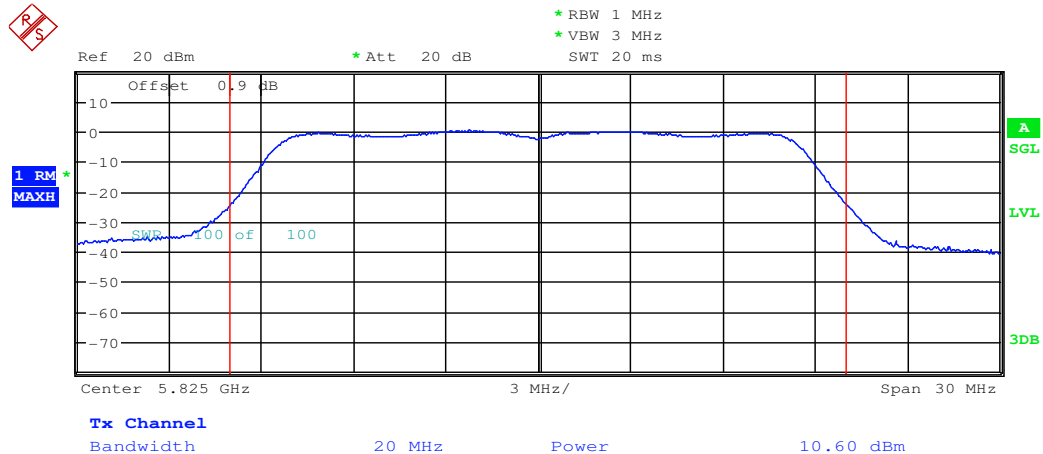




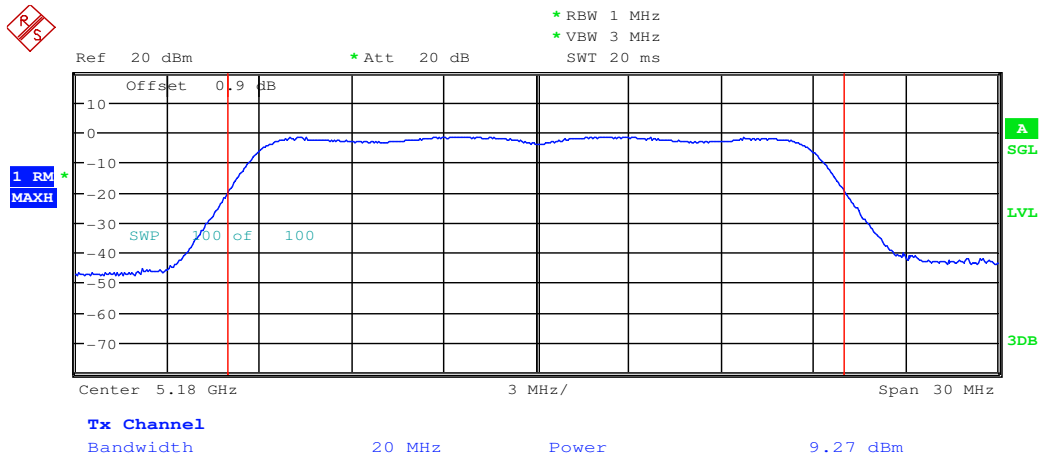
Maximum Conduct Output Power\_11A\_5825\_Ant1



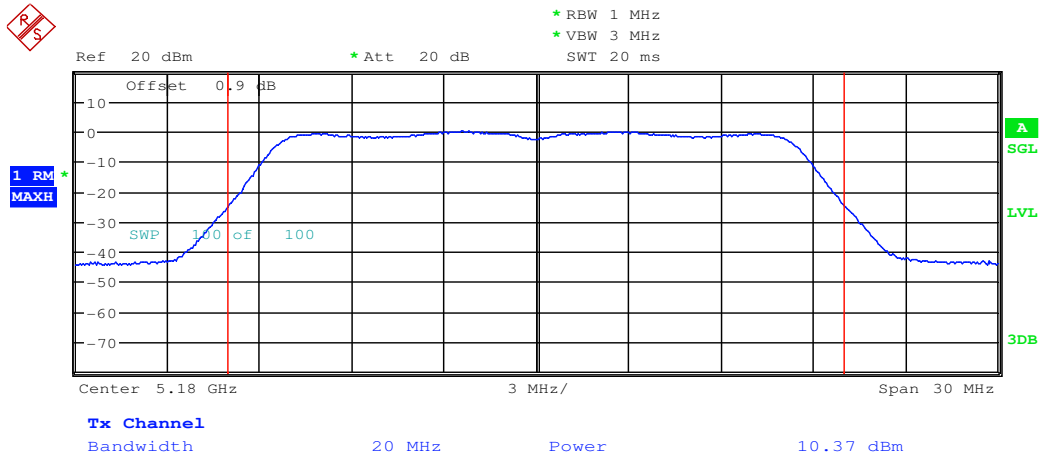
Maximum Conduct Output Power\_11A\_5825\_Ant2



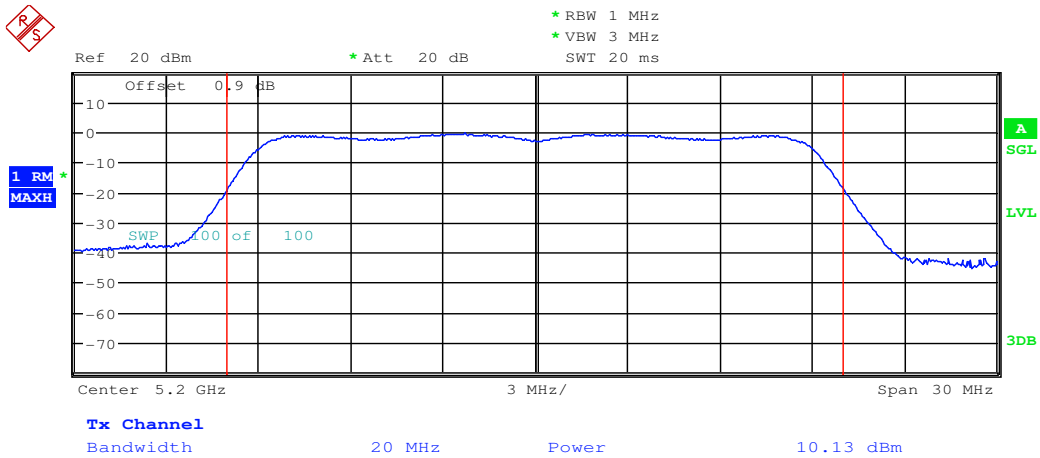
**Maximum Conduct Output Power\_11N20\_5180\_Ant1**



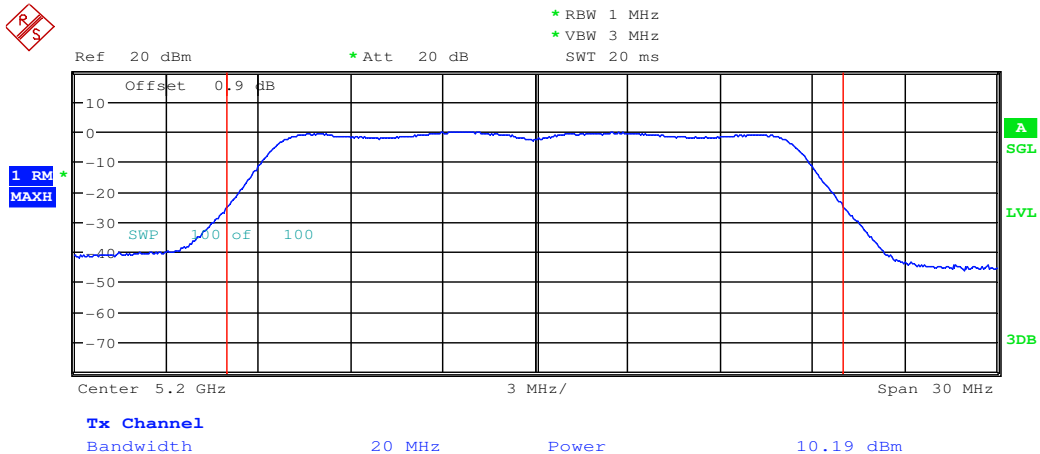
**Maximum Conduct Output Power\_11N20\_5180\_Ant2**



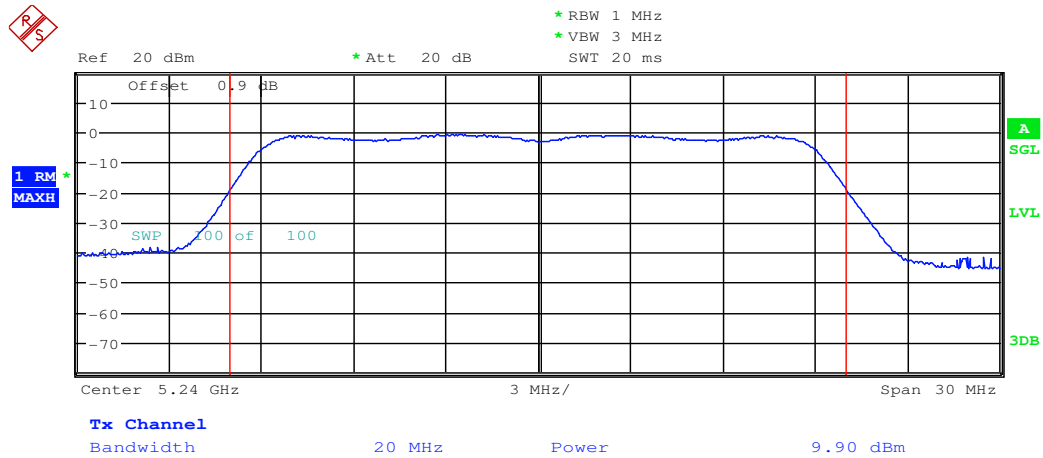
**Maximum Conduct Output Power\_11N20\_5200\_Ant1**



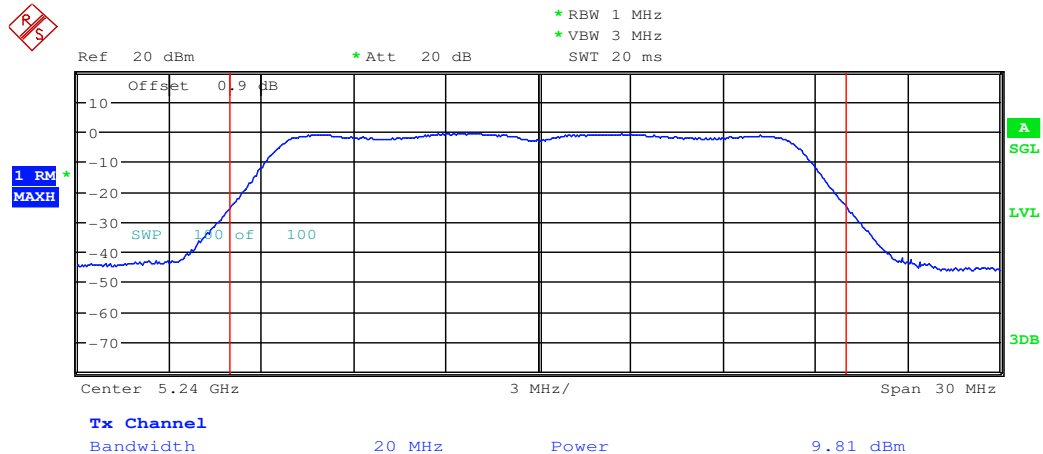
**Maximum Conduct Output Power\_11N20\_5200\_Ant2**



## Maximum Conduct Output Power\_11N20\_5240\_Ant1

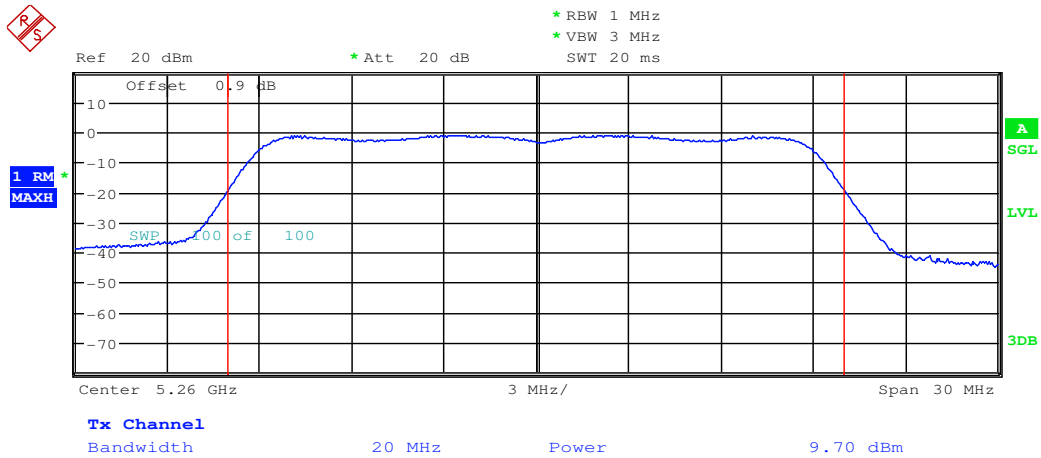


## Maximum Conduct Output Power\_11N20\_5240\_Ant2

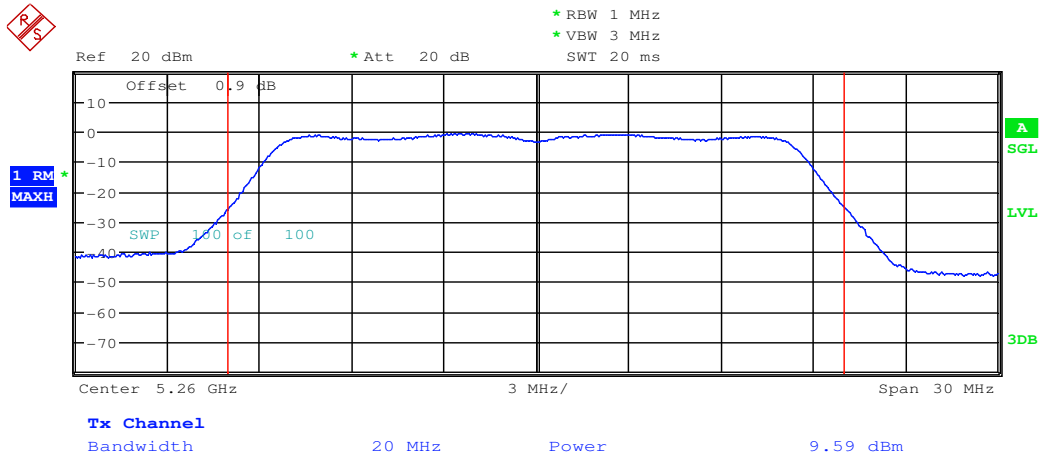




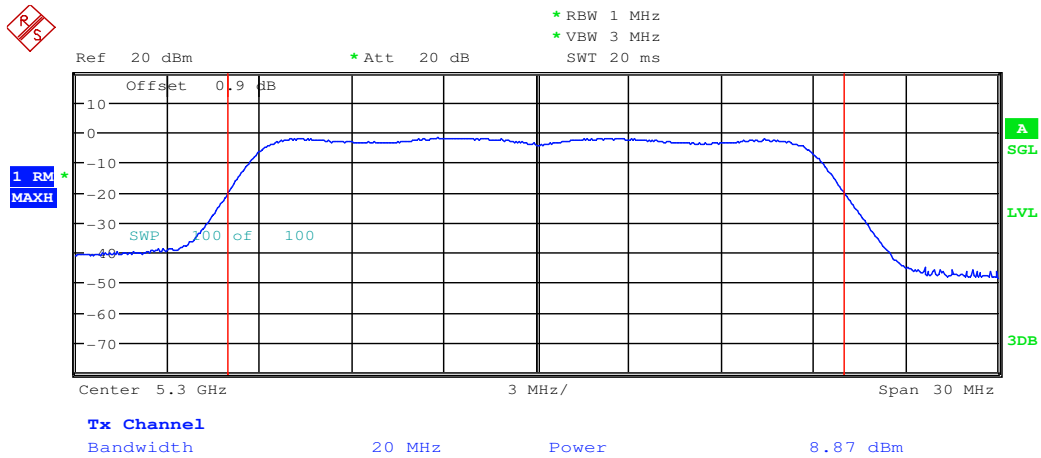
Maximum Conduct Output Power\_11N20\_5260\_Ant1



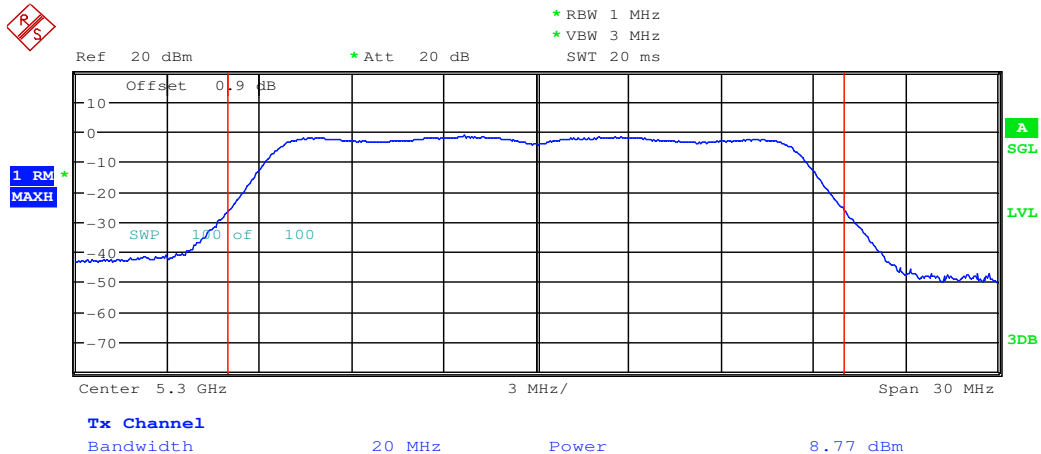
Maximum Conduct Output Power\_11N20\_5260\_Ant2



Maximum Conduct Output Power\_11N20\_5300\_Ant1

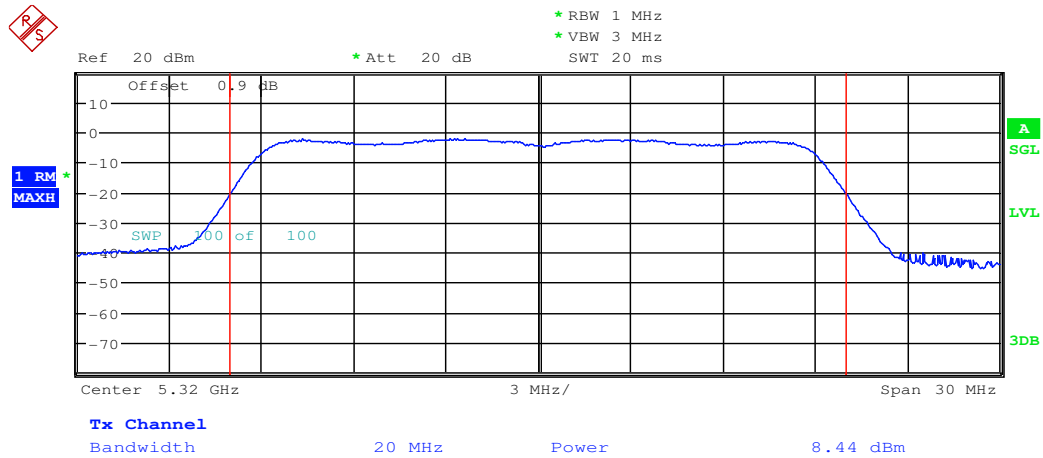


Maximum Conduct Output Power\_11N20\_5300\_Ant2

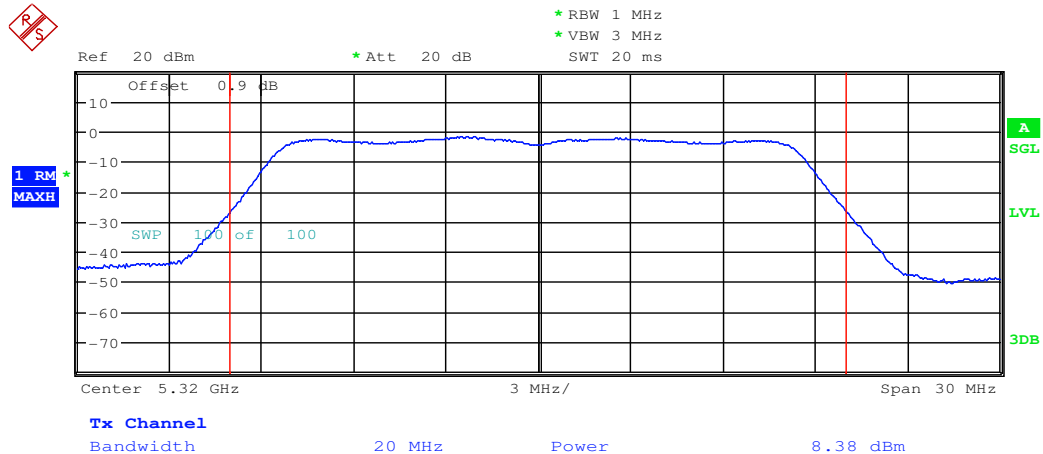




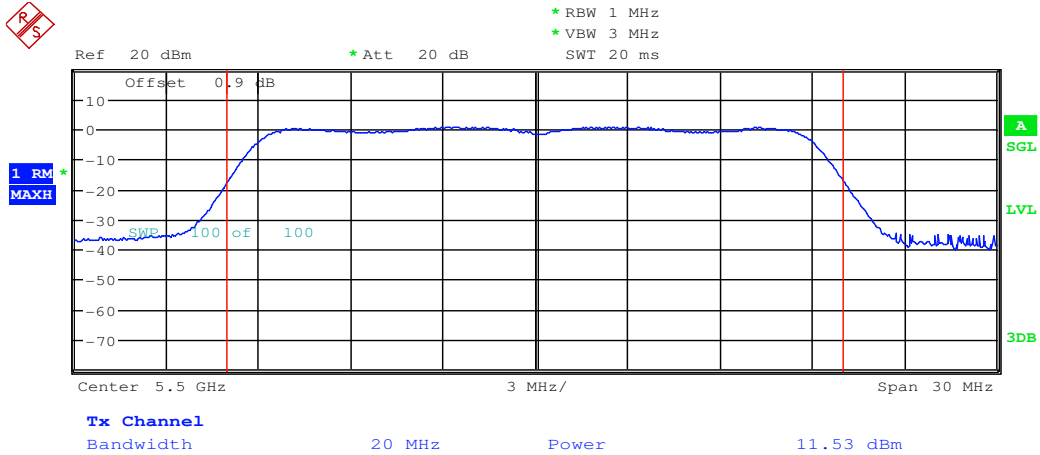
## Maximum Conduct Output Power\_11N20\_5320\_Ant1



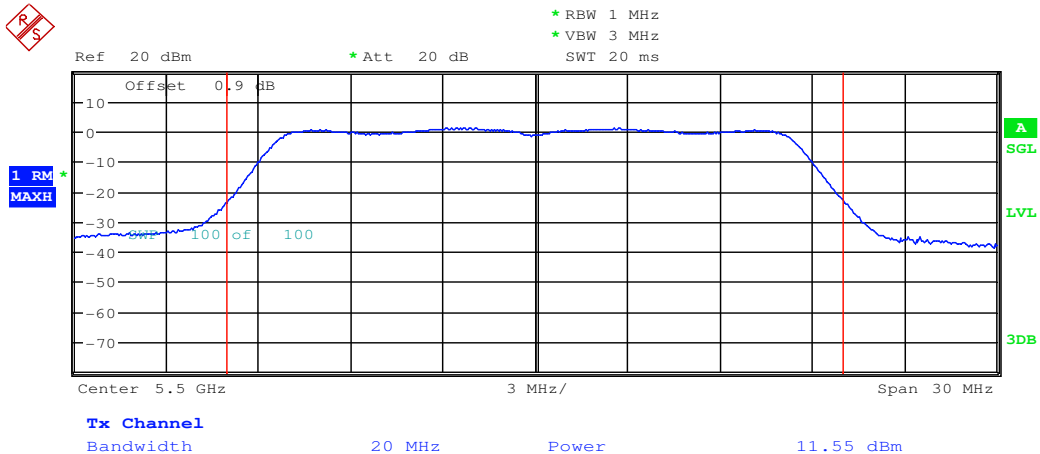
## Maximum Conduct Output Power\_11N20\_5320\_Ant2



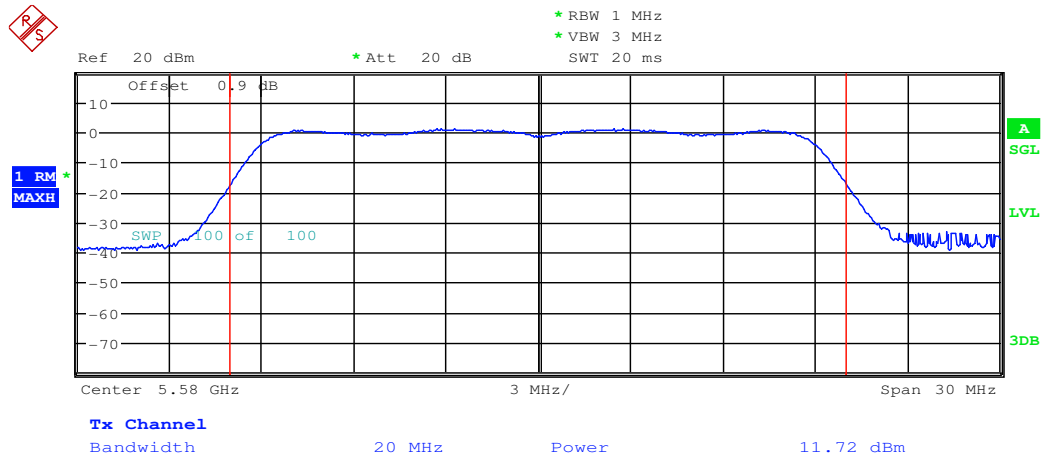
**Maximum Conduct Output Power\_11N20\_5500\_Ant1**



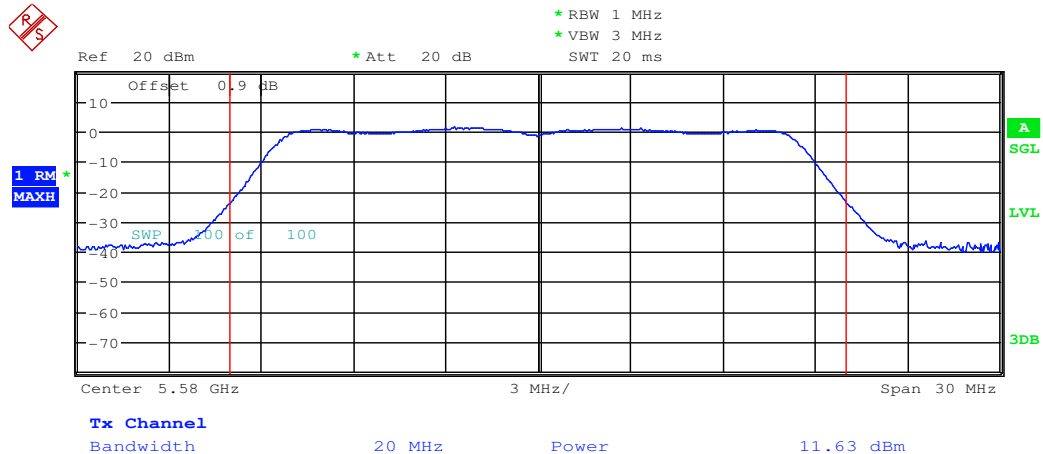
**Maximum Conduct Output Power\_11N20\_5500\_Ant2**



## Maximum Conduct Output Power\_11N20\_5580\_Ant1

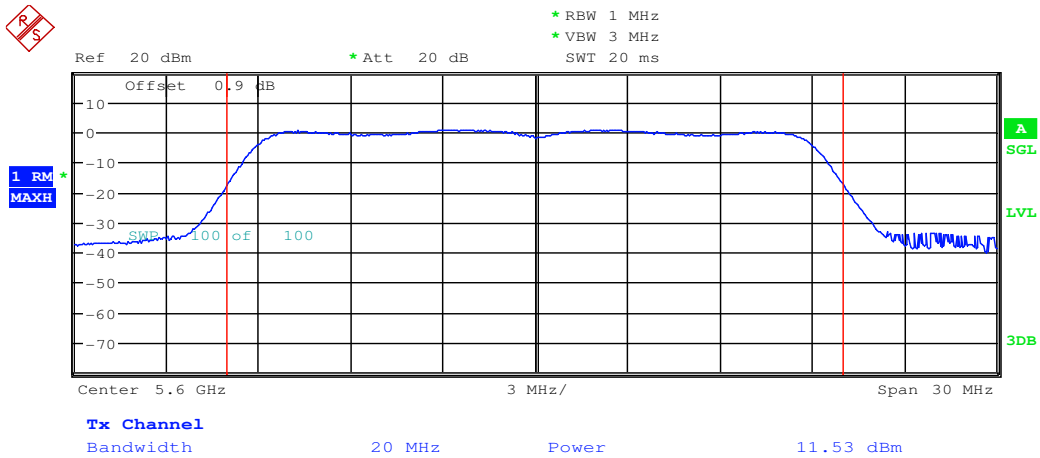


## Maximum Conduct Output Power\_11N20\_5580\_Ant2

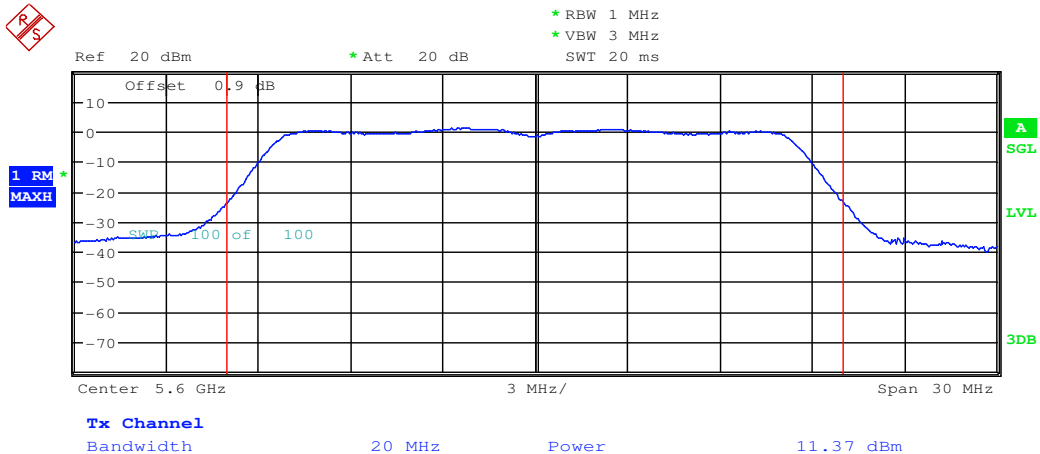




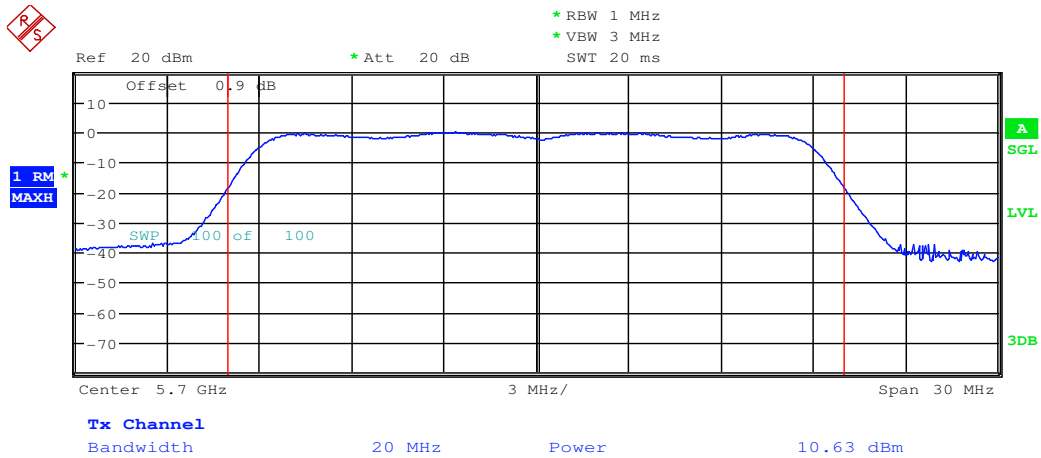
Maximum Conduct Output Power\_11N20\_5600\_Ant1



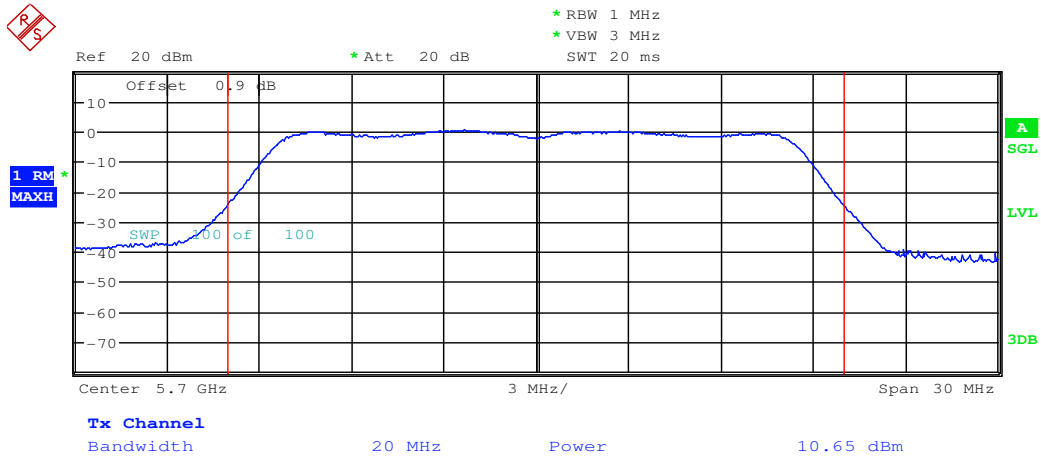
Maximum Conduct Output Power\_11N20\_5600\_Ant2



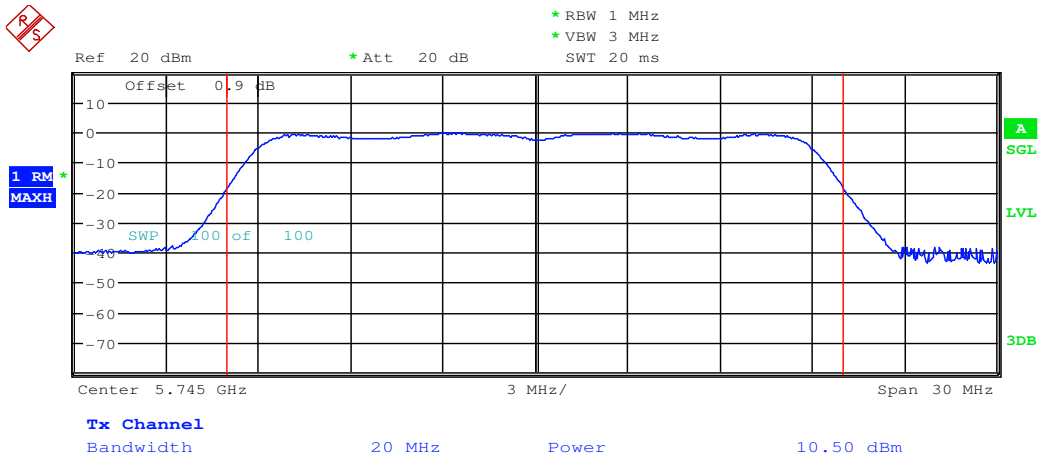
**Maximum Conduct Output Power\_11N20\_5700\_Ant1**



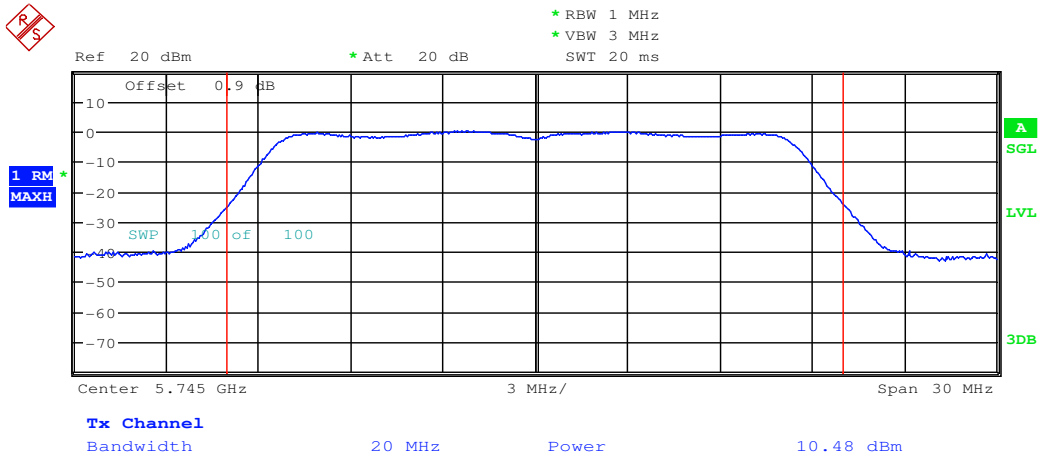
**Maximum Conduct Output Power\_11N20\_5700\_Ant2**



**Maximum Conduct Output Power\_11N20\_5745\_Ant1**

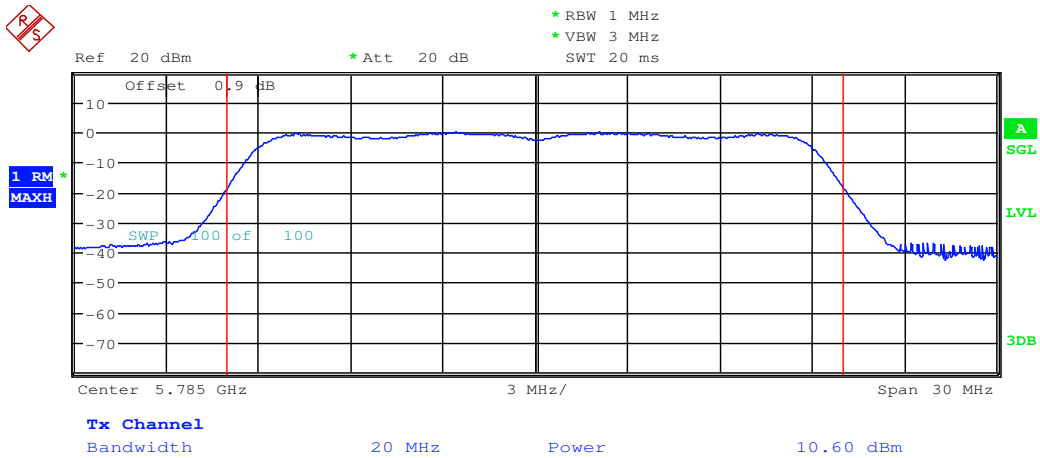


**Maximum Conduct Output Power\_11N20\_5745\_Ant2**

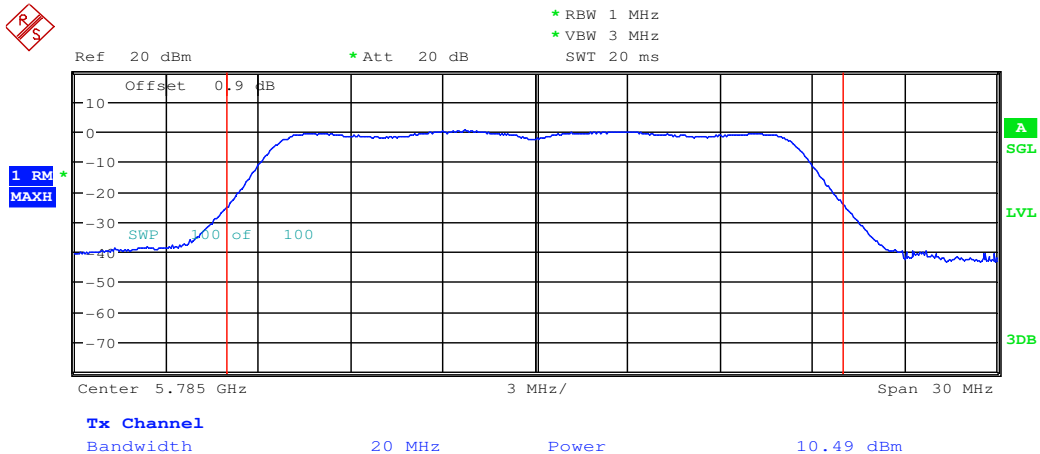




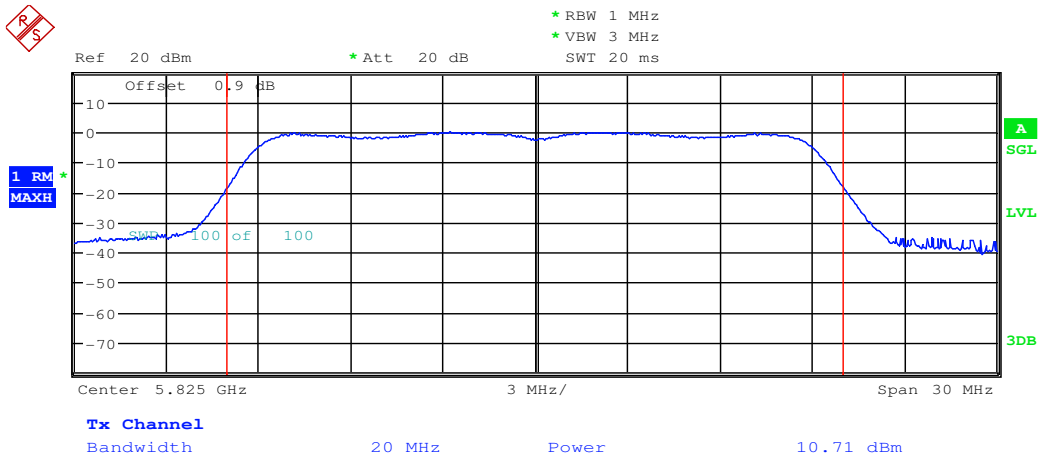
Maximum Conduct Output Power\_11N20\_5785\_Ant1



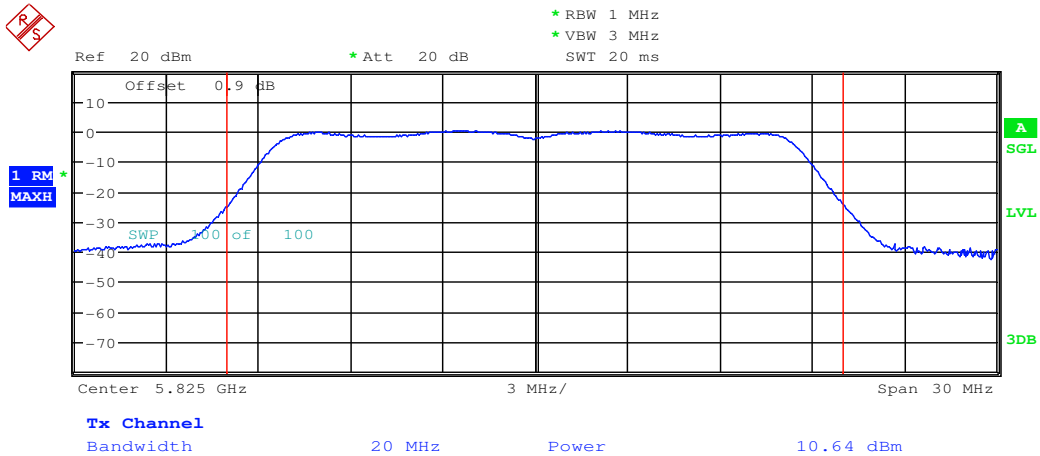
Maximum Conduct Output Power\_11N20\_5785\_Ant2



**Maximum Conduct Output Power\_11N20\_5825\_Ant1**



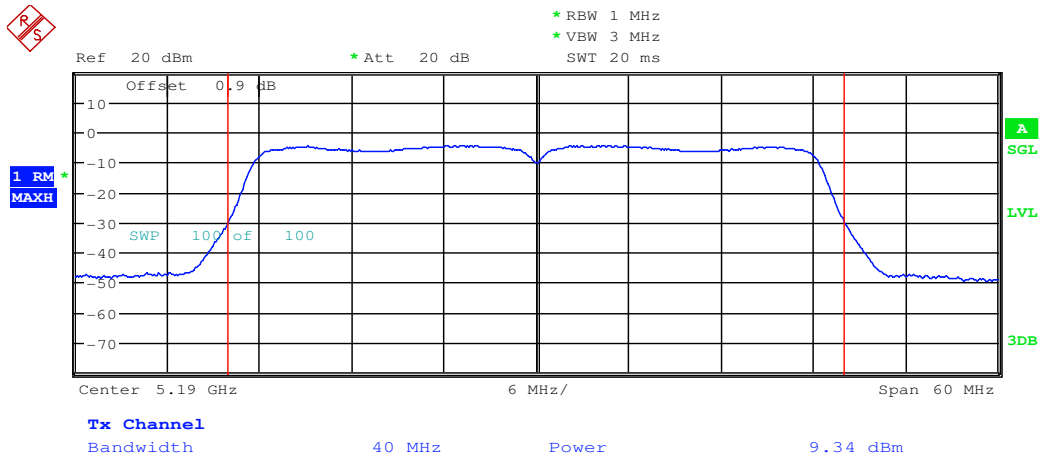
**Maximum Conduct Output Power\_11N20\_5825\_Ant2**



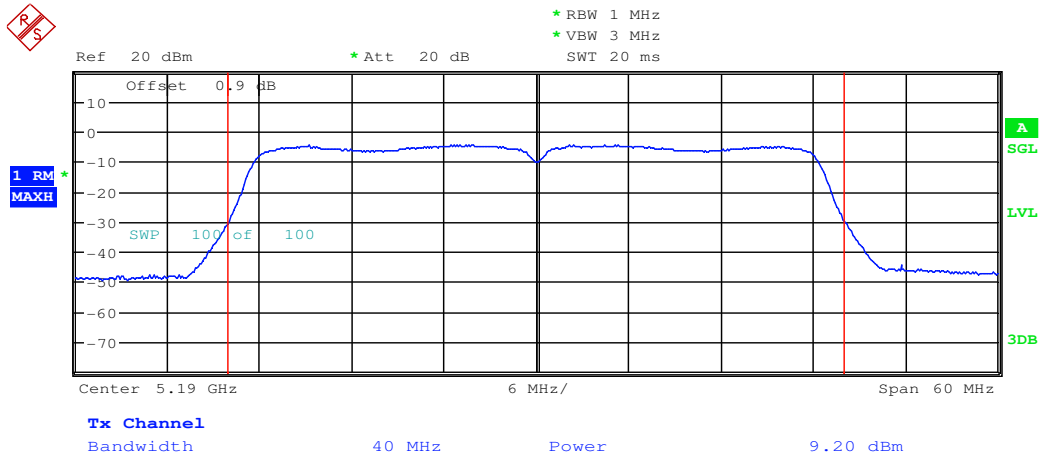




Maximum Conduct Output Power\_11N40\_5190\_Ant1

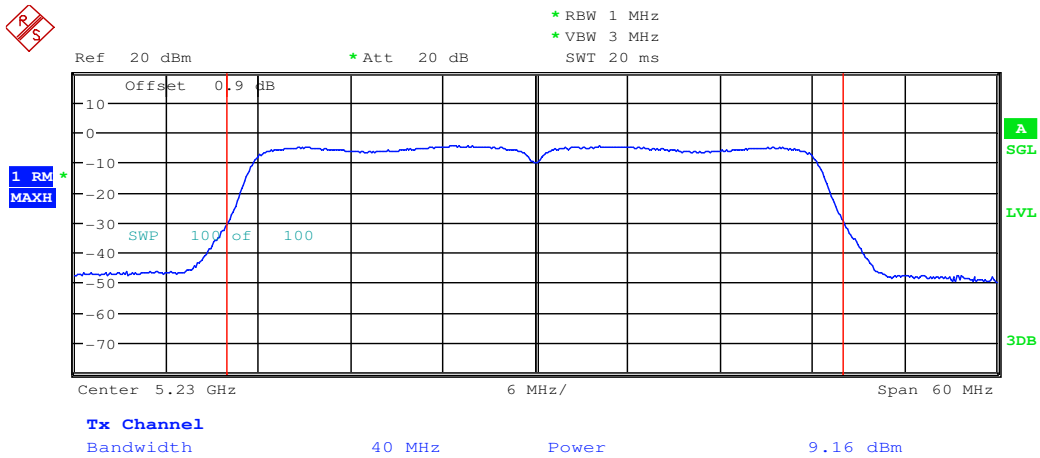


Maximum Conduct Output Power\_11N40\_5190\_Ant2

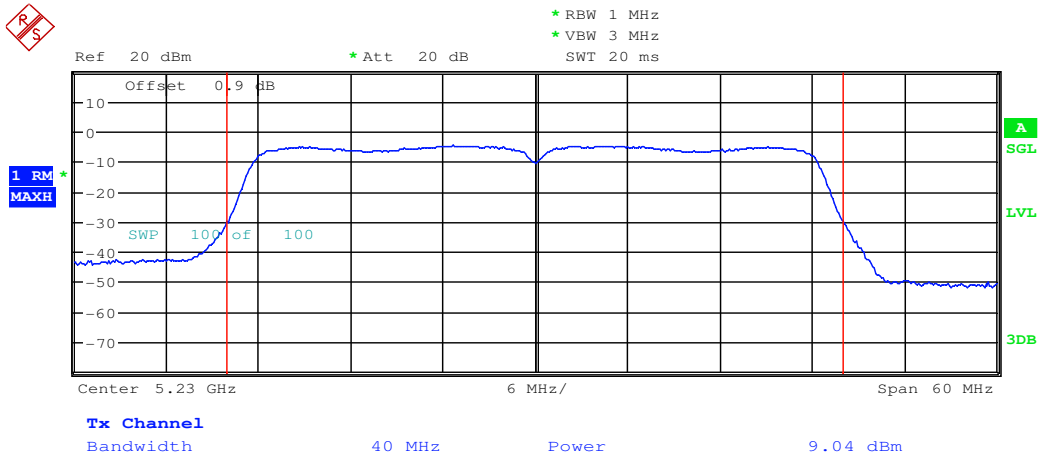




Maximum Conduct Output Power\_11N40\_5230\_Ant1

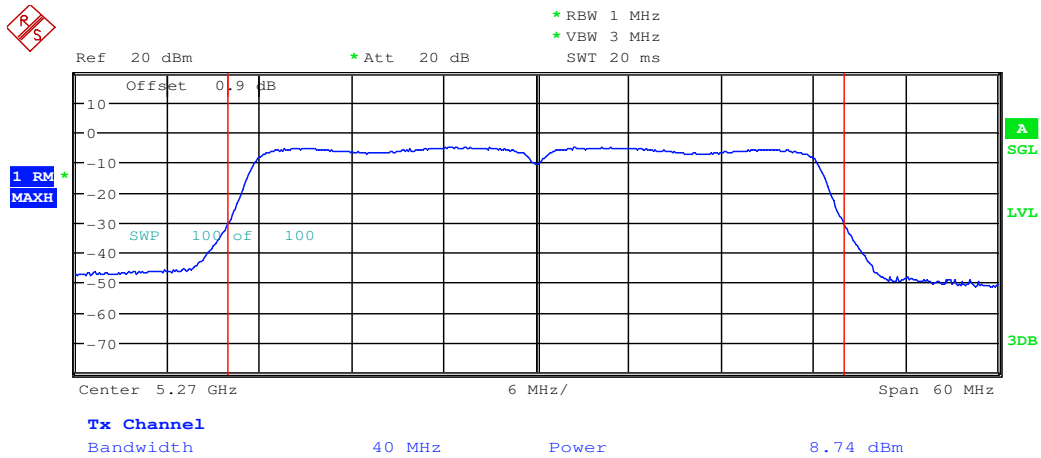


Maximum Conduct Output Power\_11N40\_5230\_Ant2

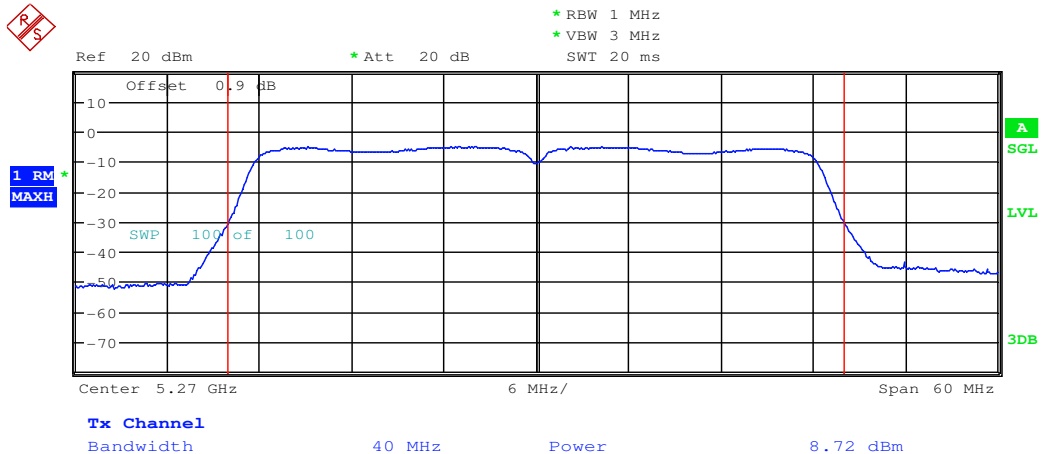




Maximum Conduct Output Power\_11N40\_5270\_Ant1

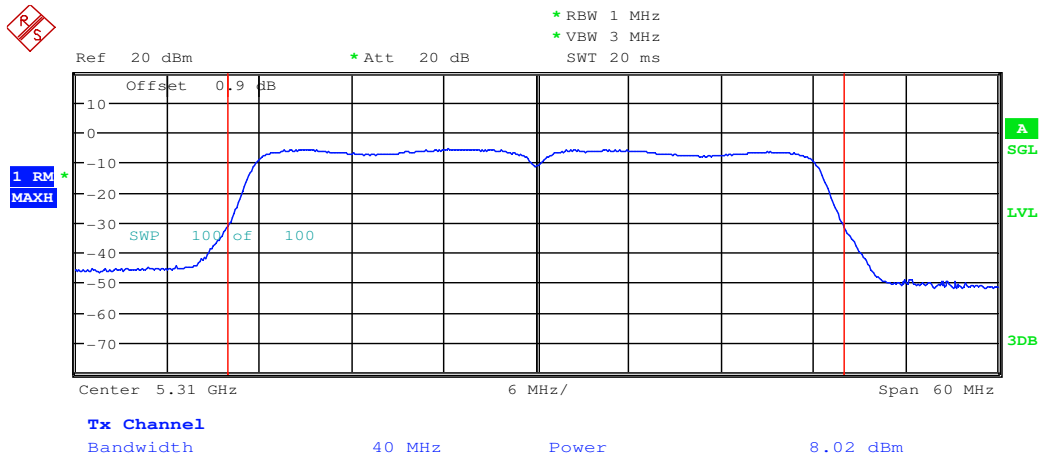


Maximum Conduct Output Power\_11N40\_5270\_Ant2

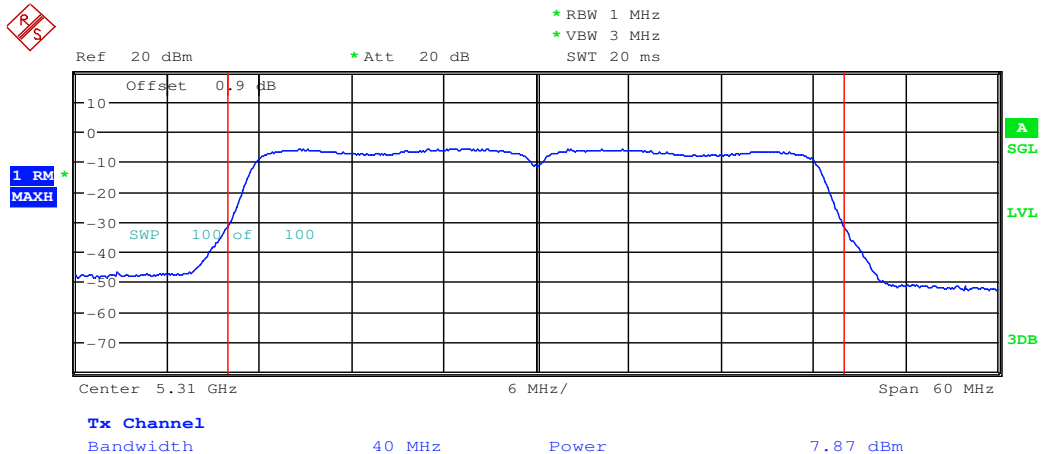




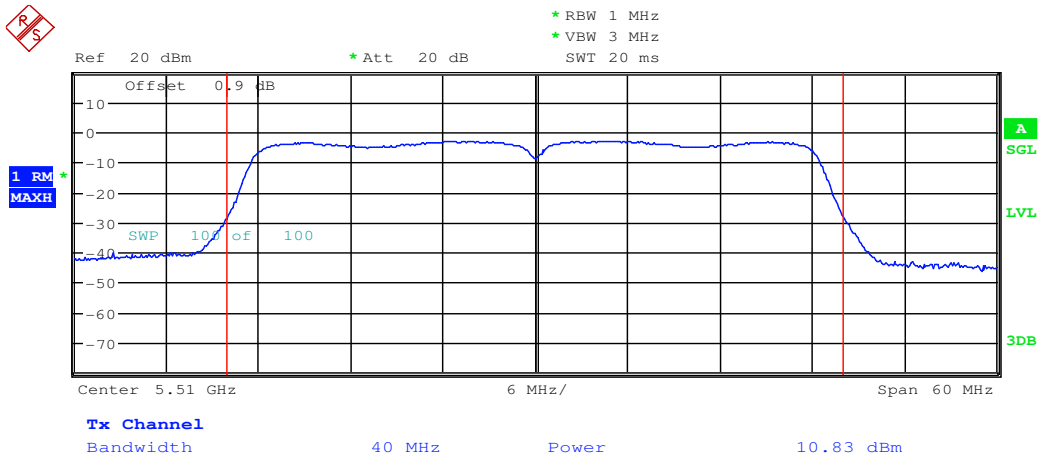
Maximum Conduct Output Power\_11N40\_5310\_Ant1



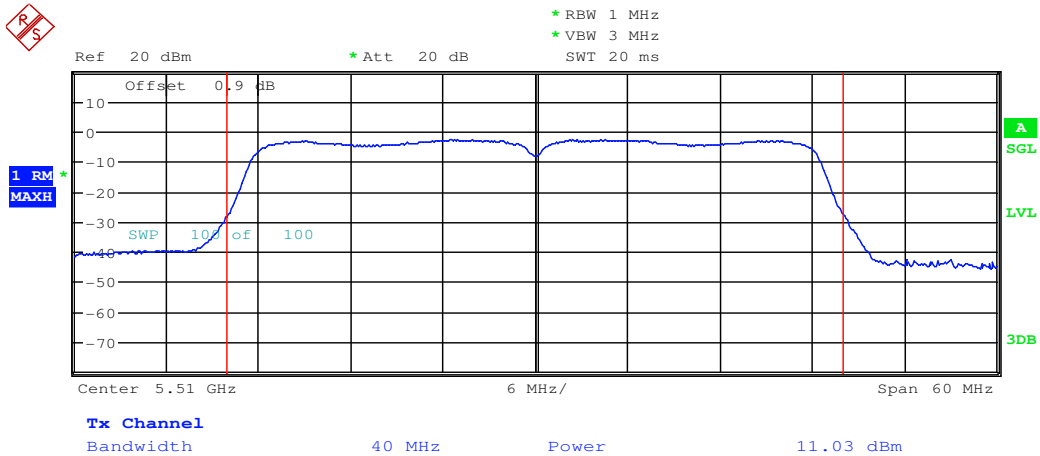
Maximum Conduct Output Power\_11N40\_5310\_Ant2



**Maximum Conduct Output Power\_11N40\_5510\_Ant1**

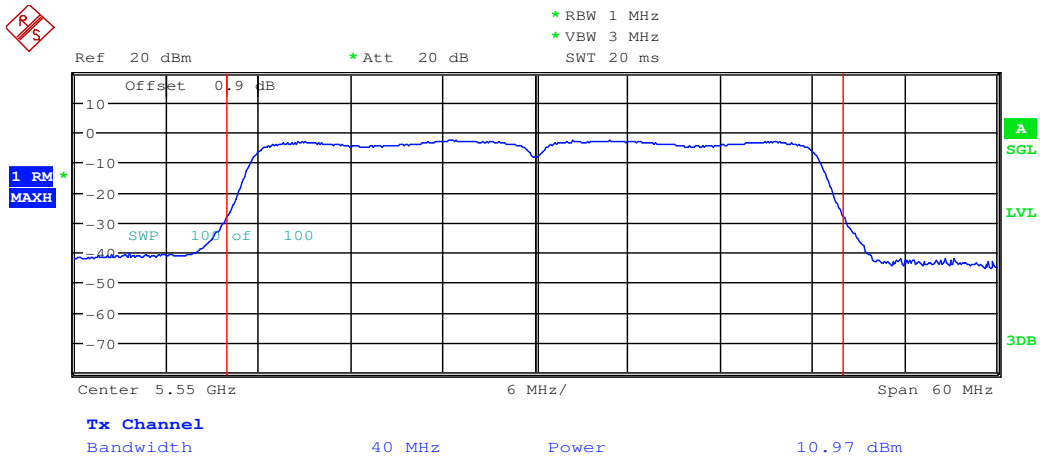


**Maximum Conduct Output Power\_11N40\_5510\_Ant2**

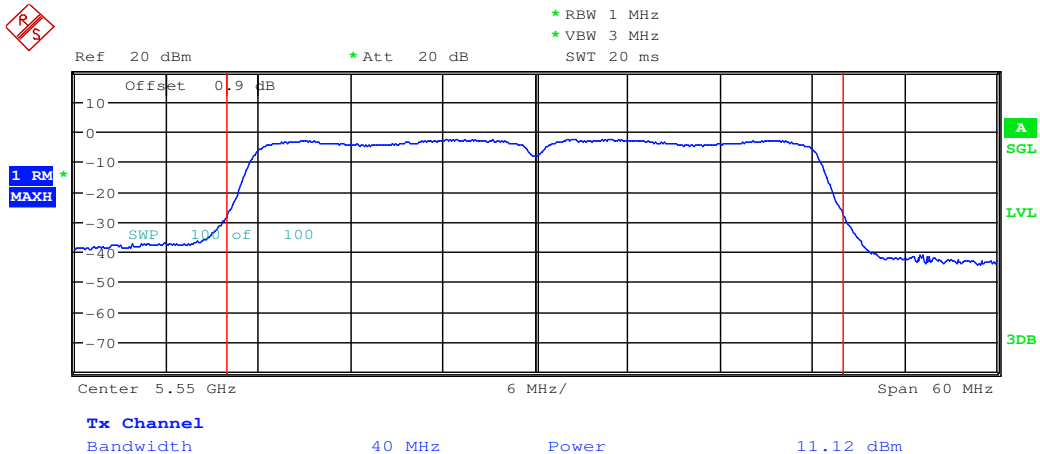




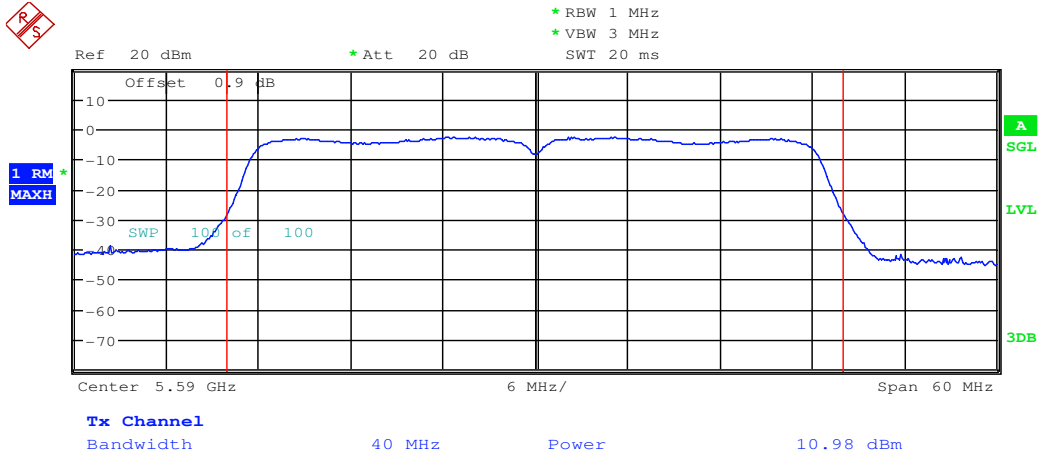
Maximum Conduct Output Power\_11N40\_5550\_Ant1



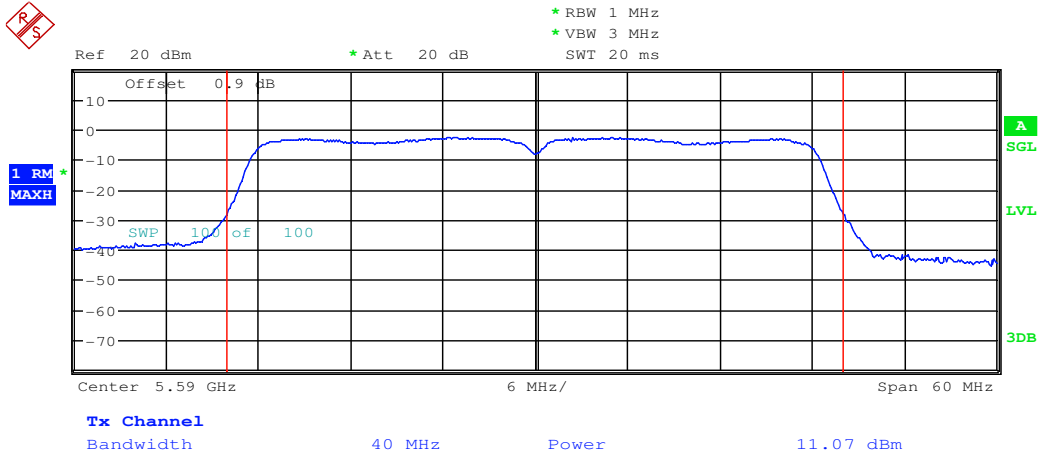
Maximum Conduct Output Power\_11N40\_5550\_Ant2



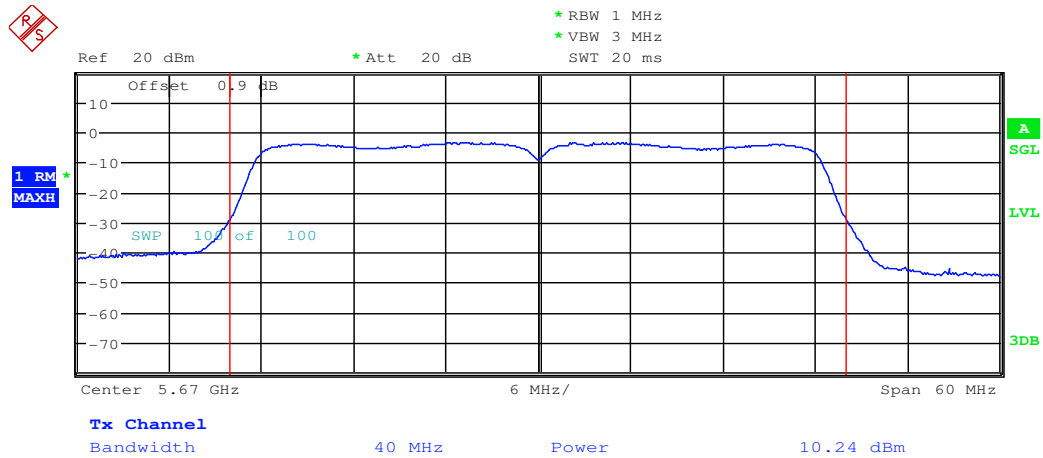
**Maximum Conduct Output Power\_11N40\_5590\_Ant1**



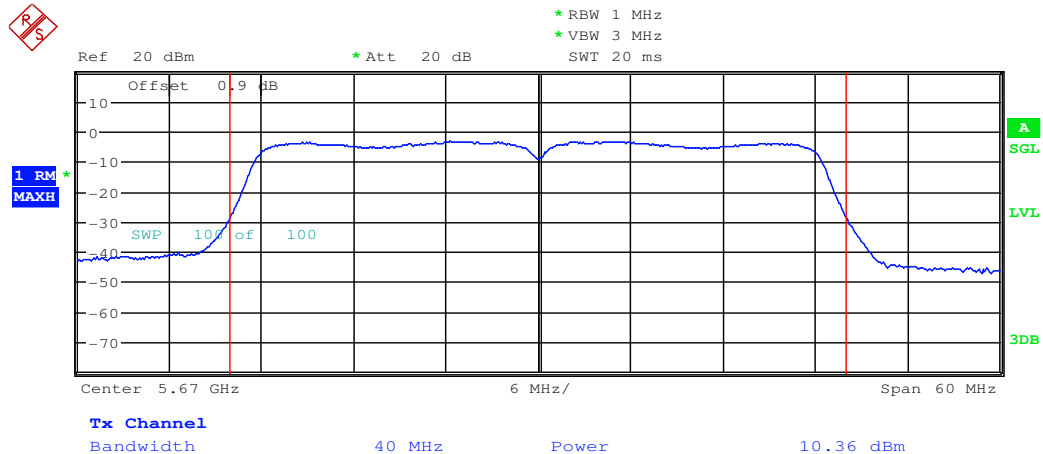
**Maximum Conduct Output Power\_11N40\_5590\_Ant2**



## Maximum Conduct Output Power\_11N40\_5670\_Ant1

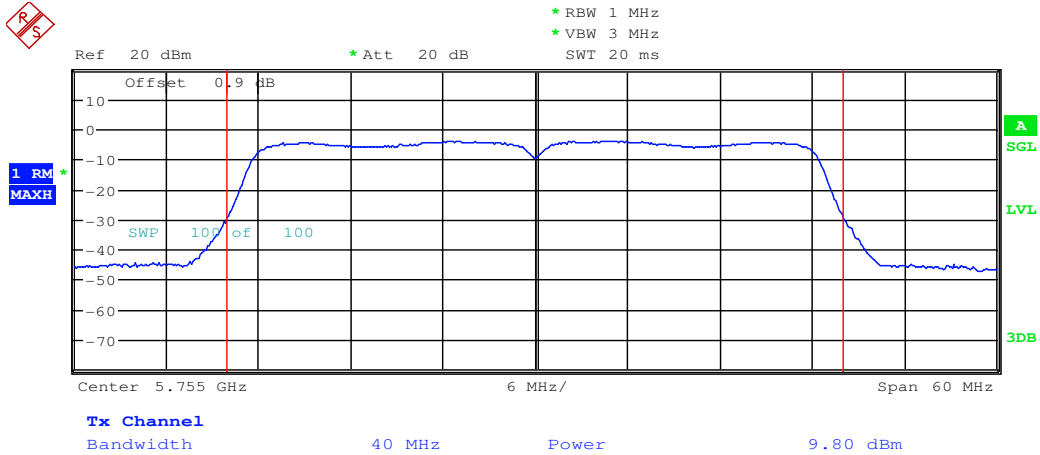


## Maximum Conduct Output Power\_11N40\_5670\_Ant2

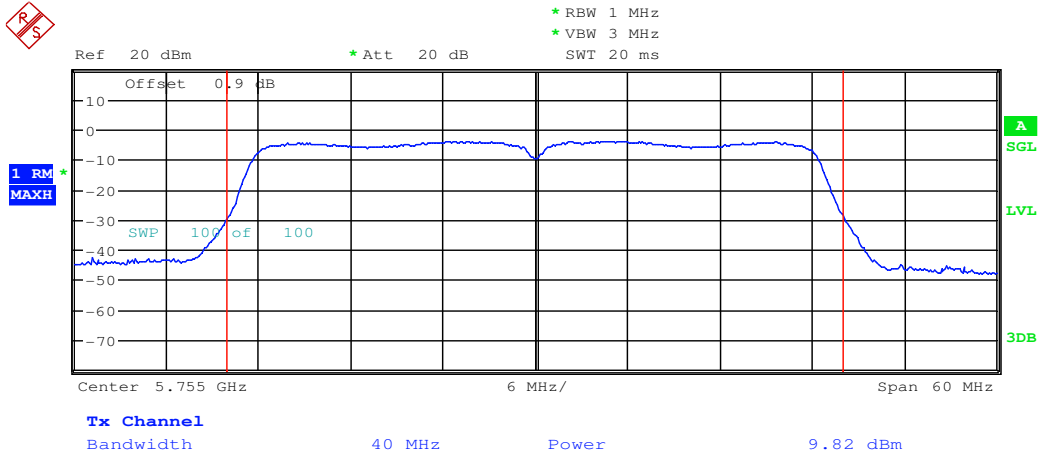




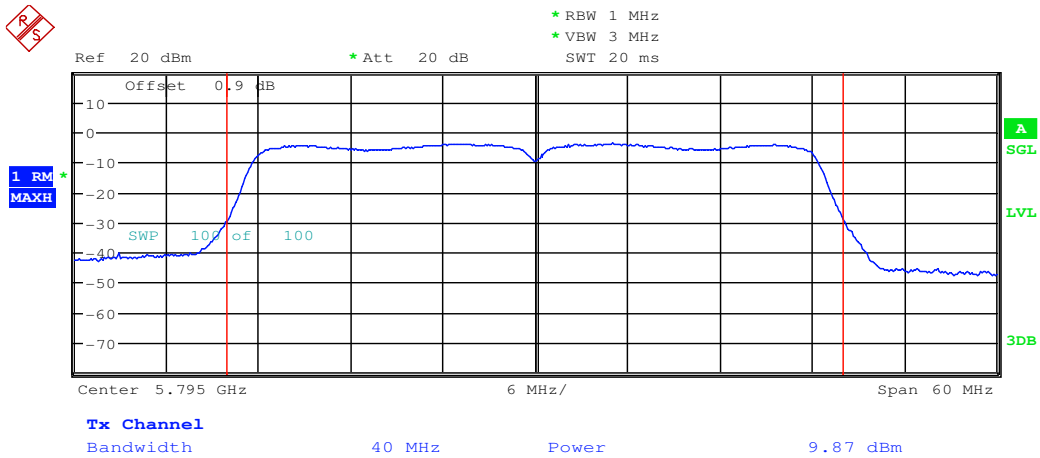
**Maximum Conduct Output Power\_11N40\_5755\_Ant1**



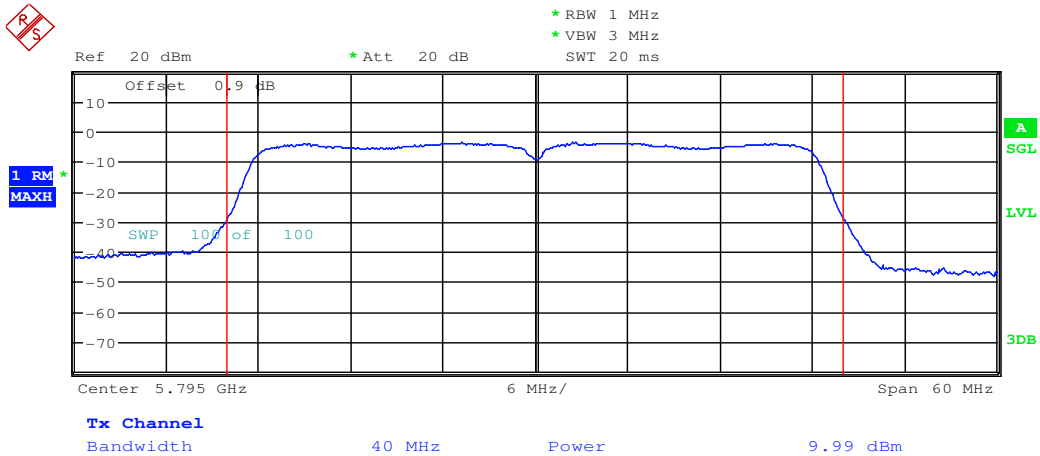
**Maximum Conduct Output Power\_11N40\_5755\_Ant2**



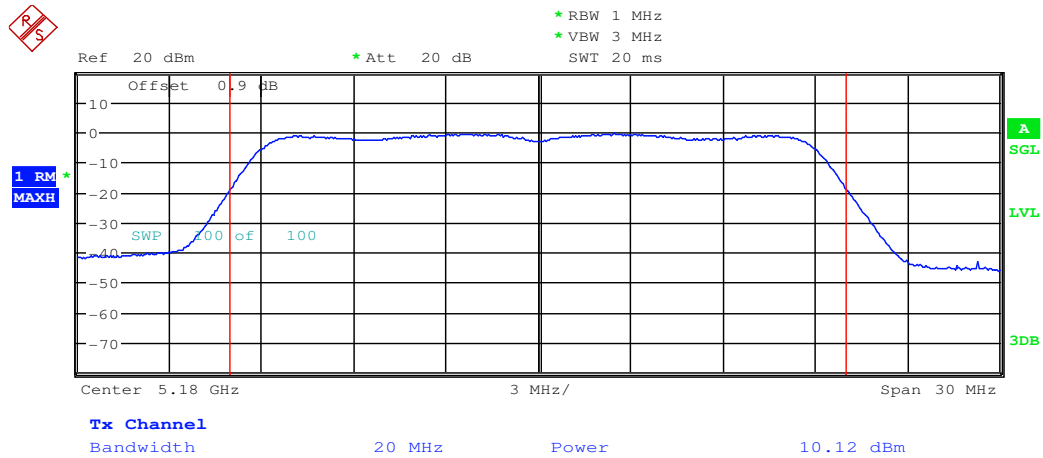
**Maximum Conduct Output Power\_11N40\_5795\_Ant1**



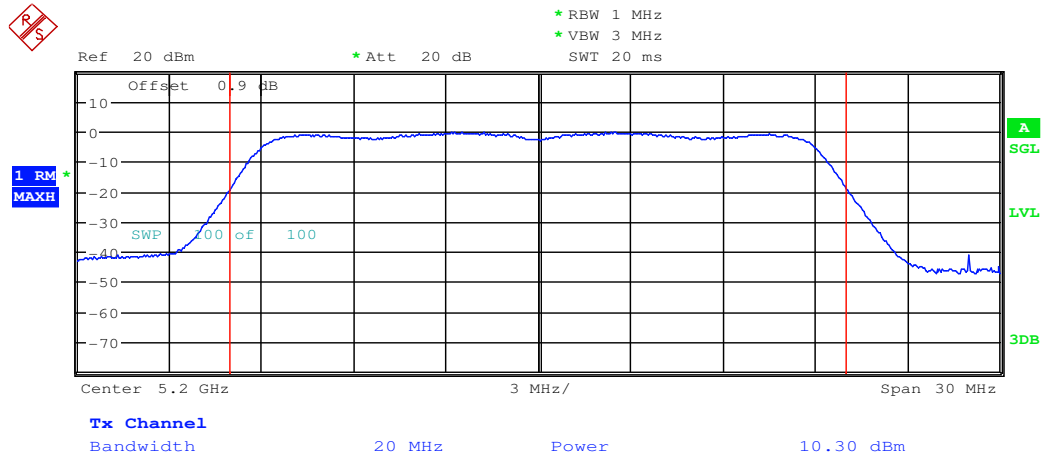
**Maximum Conduct Output Power\_11N40\_5795\_Ant2**



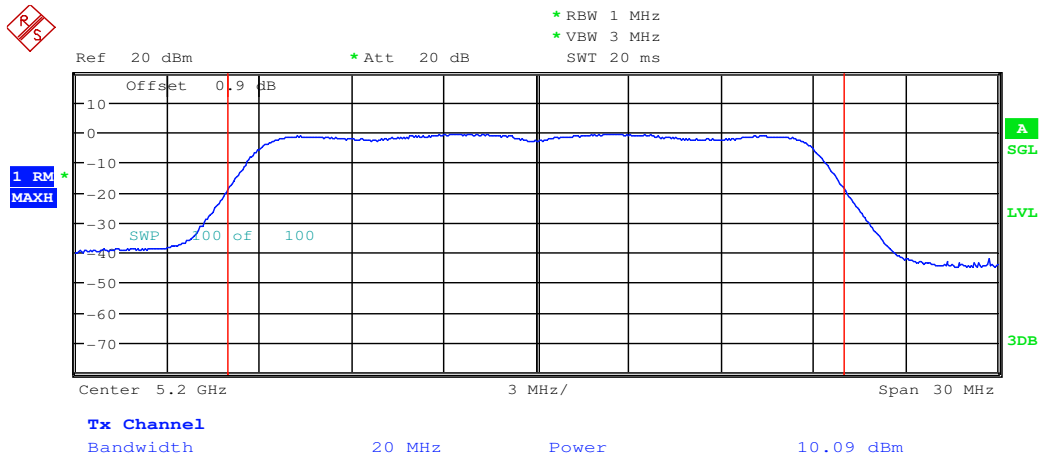
## Maximum Conduct Output Power\_11AC20\_5180\_Ant1



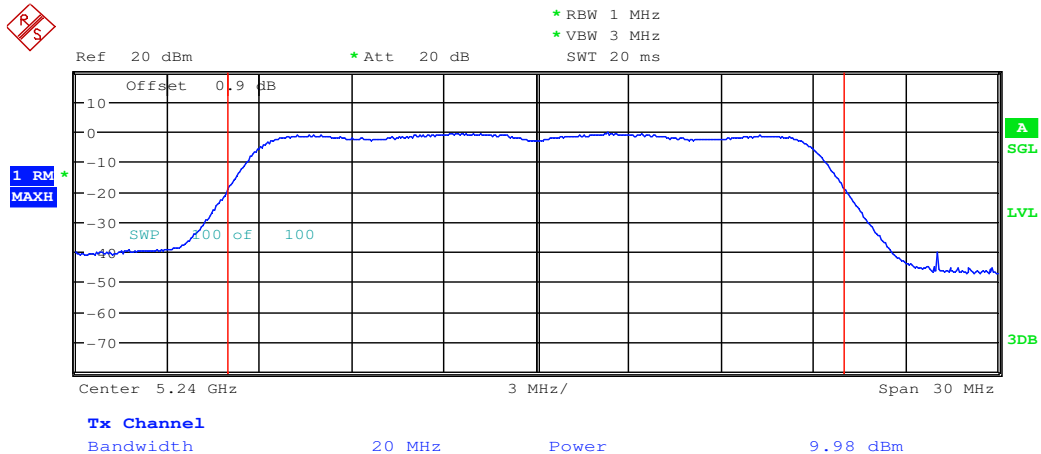
## Maximum Conduct Output Power\_11AC20\_5200\_Ant1



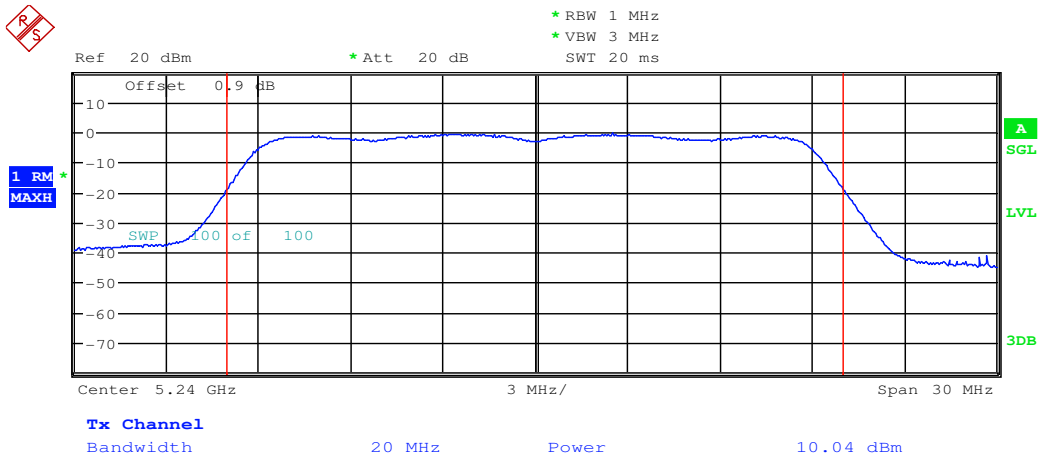
**Maximum Conduct Output Power\_11AC20\_5200\_Ant2**



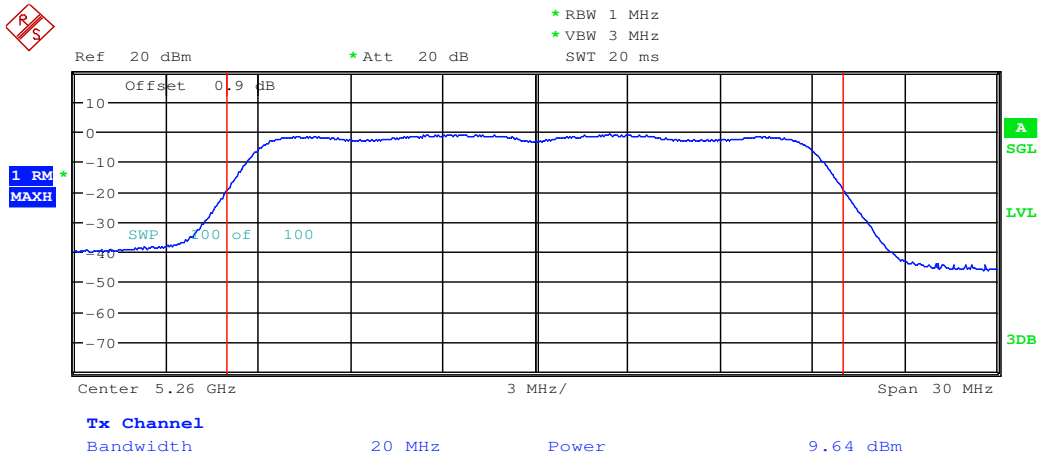
**Maximum Conduct Output Power\_11AC20\_5240\_Ant1**



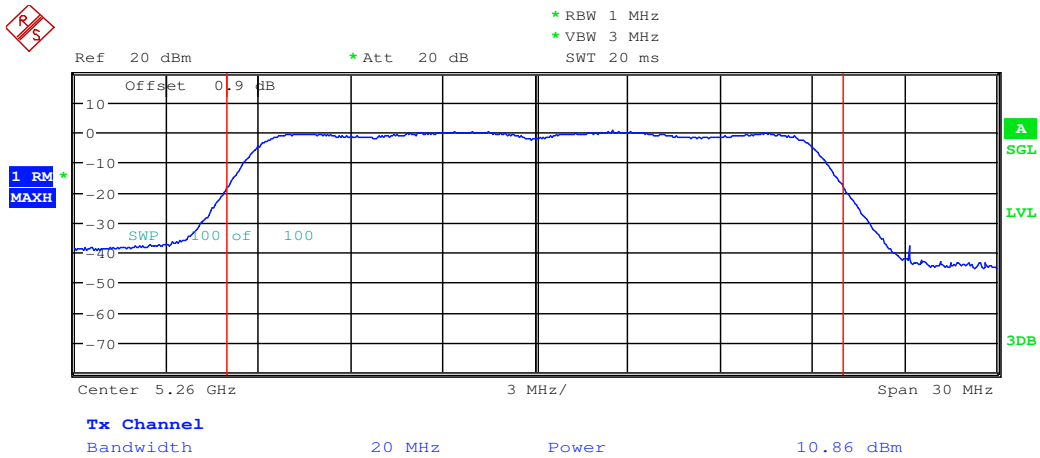
**Maximum Conduct Output Power\_11AC20\_5240\_Ant2**



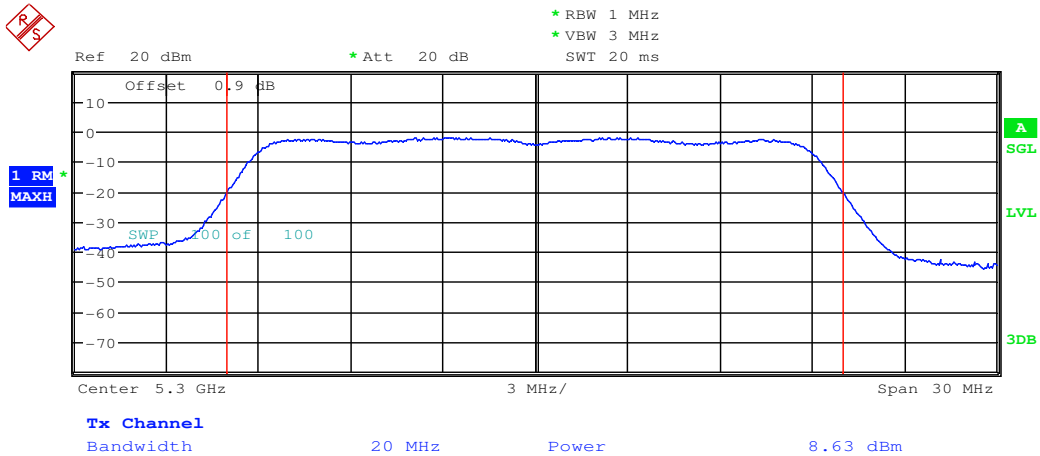
**Maximum Conduct Output Power\_11AC20\_5260\_Ant1**



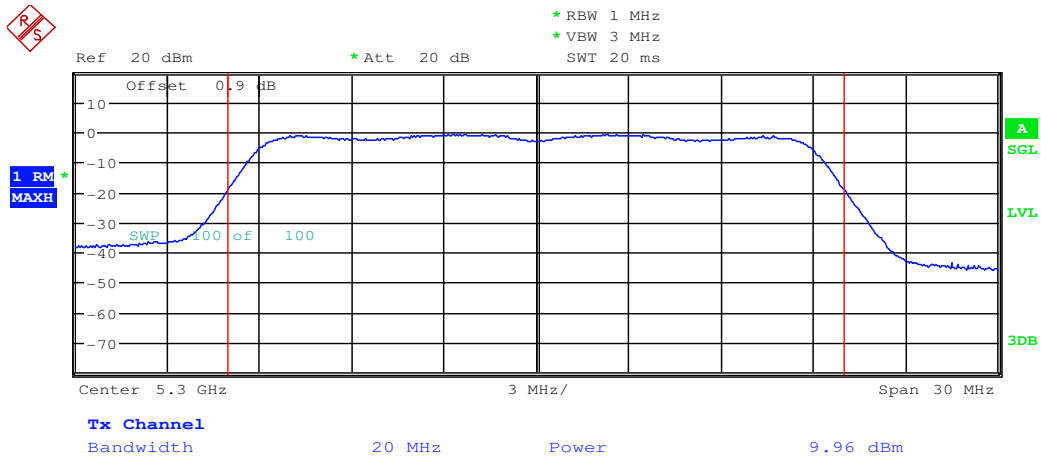
**Maximum Conduct Output Power\_11AC20\_5260\_Ant2**



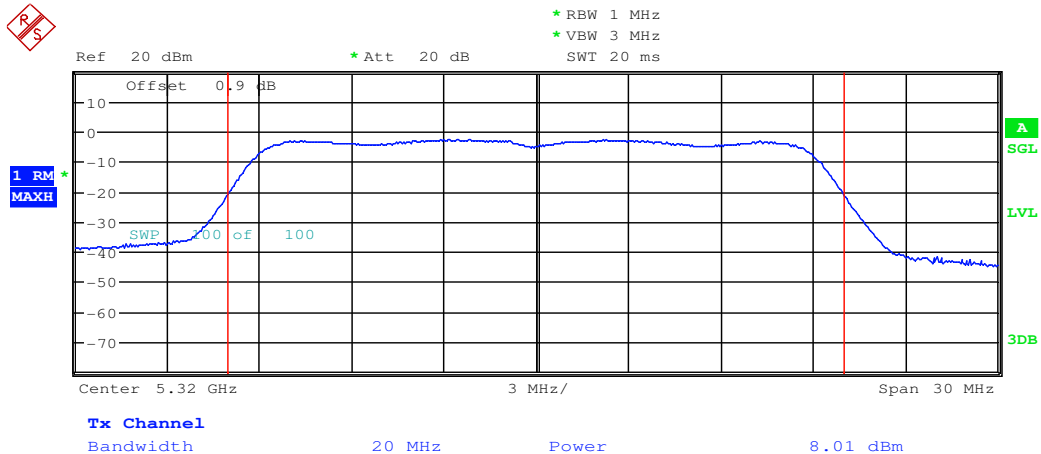
**Maximum Conduct Output Power\_11AC20\_5300\_Ant1**



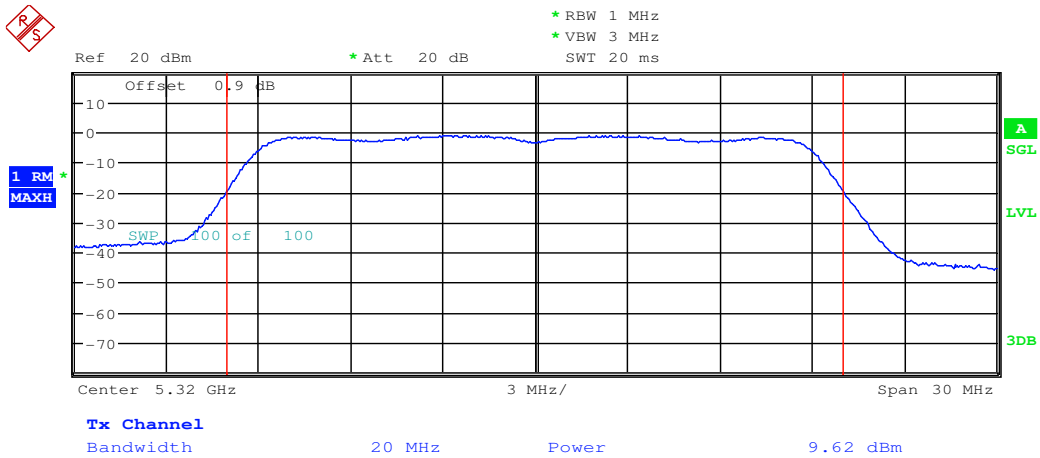
**Maximum Conduct Output Power\_11AC20\_5300\_Ant2**



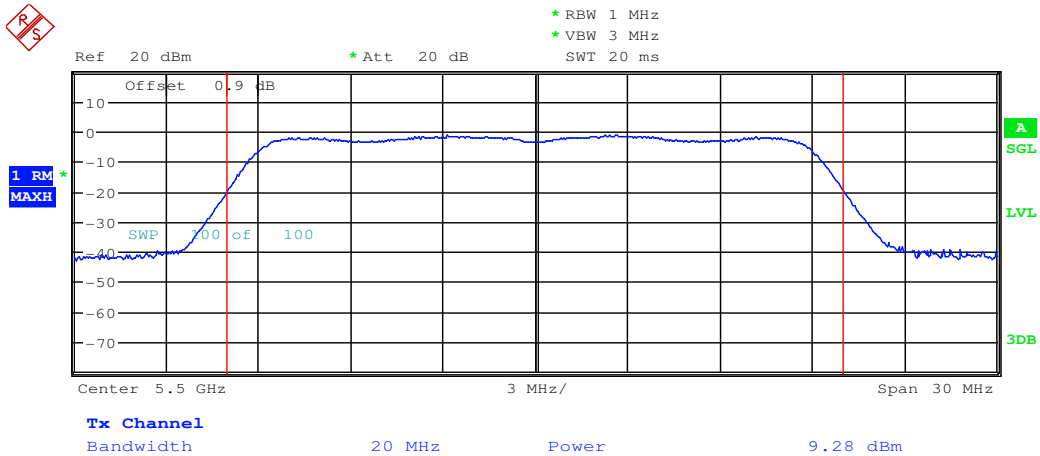
**Maximum Conduct Output Power\_11AC20\_5320\_Ant1**



**Maximum Conduct Output Power\_11AC20\_5320\_Ant2**

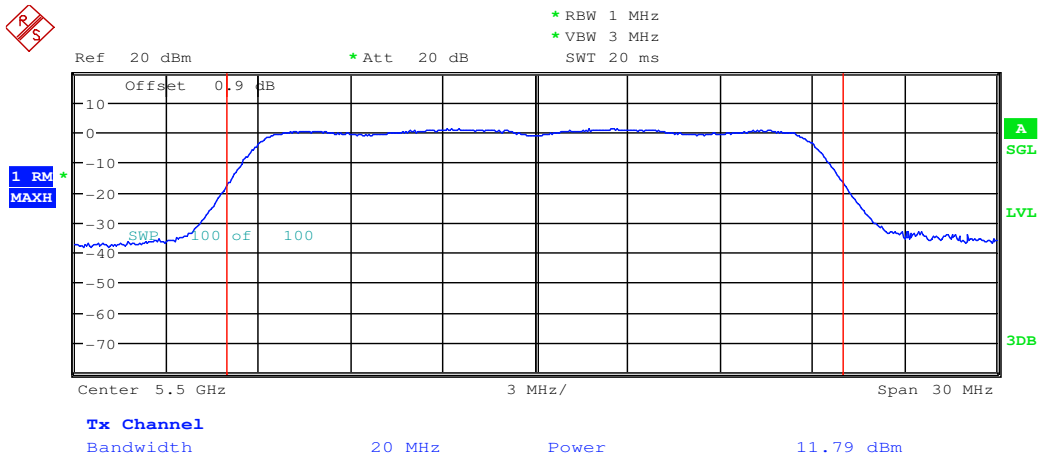


**Maximum Conduct Output Power\_11AC20\_5500\_Ant1**

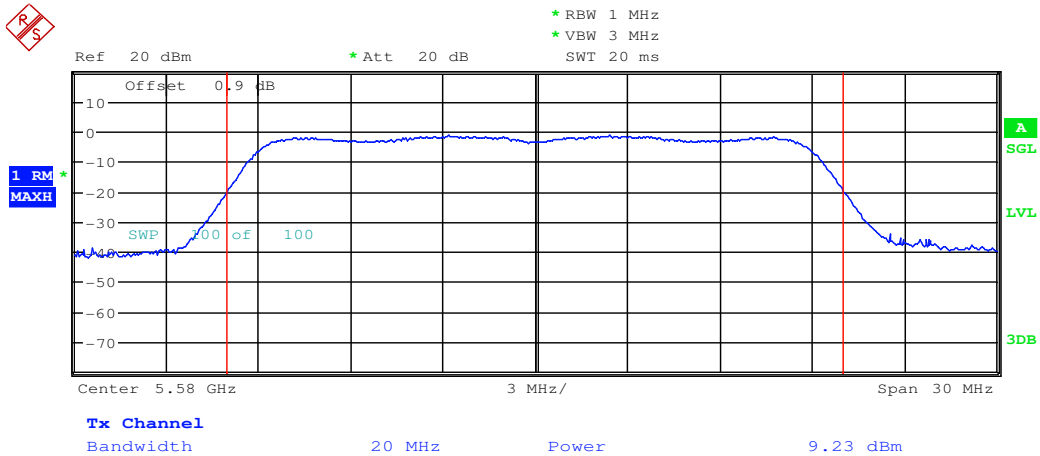




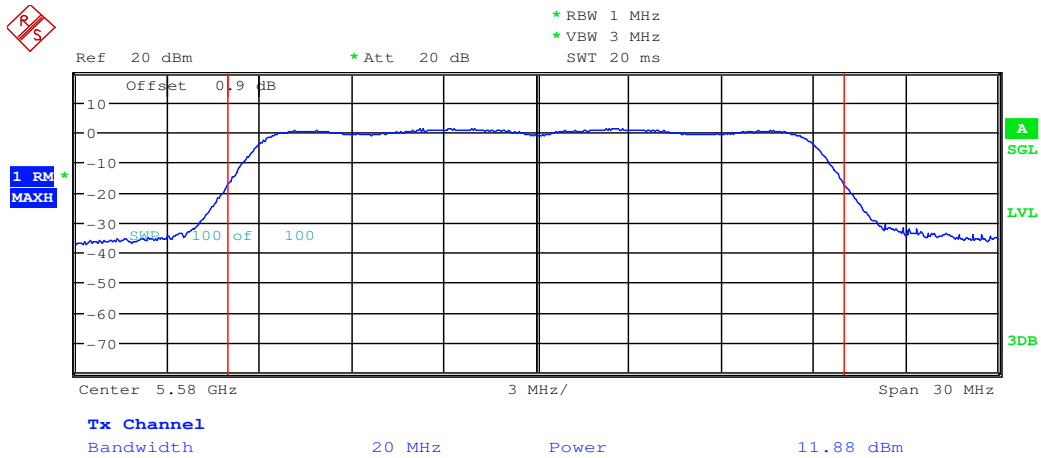
**Maximum Conduct Output Power\_11AC20\_5500\_Ant2**



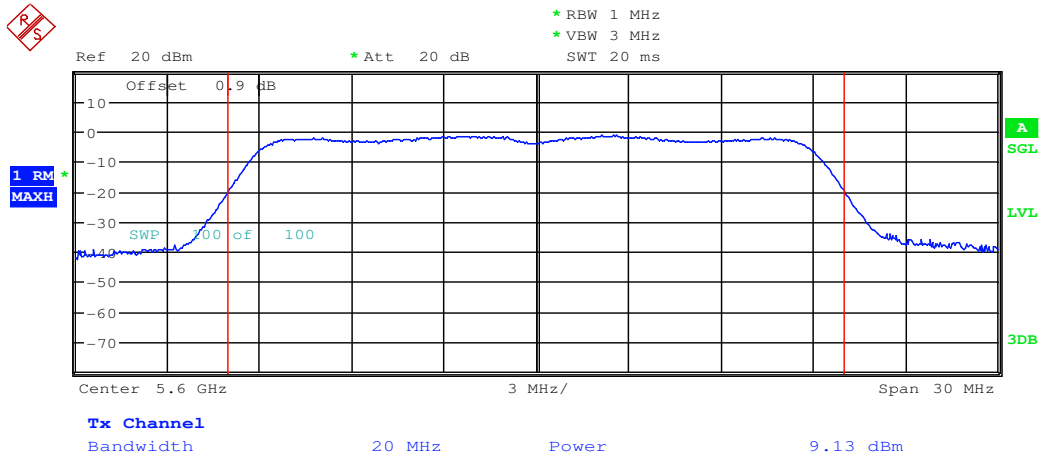
**Maximum Conduct Output Power\_11AC20\_5580\_Ant1**



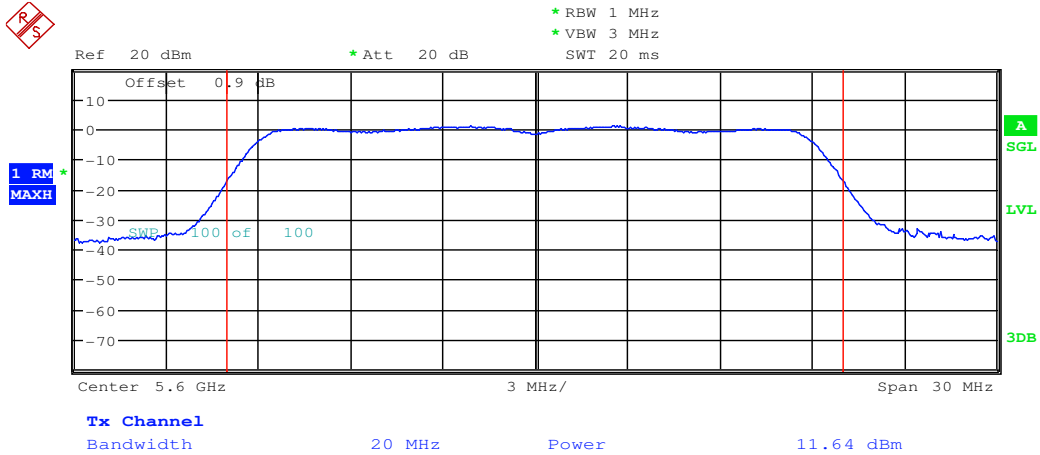
**Maximum Conduct Output Power\_11AC20\_5580\_Ant2**



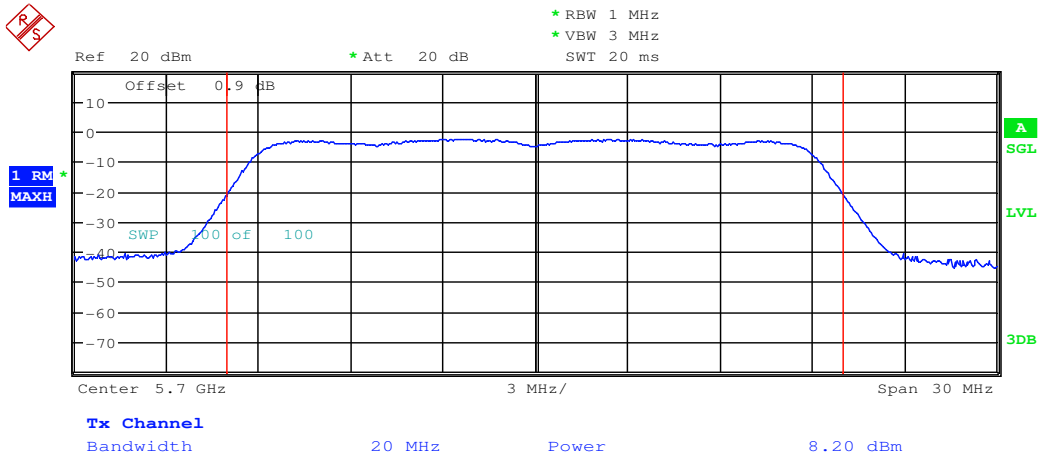
**Maximum Conduct Output Power\_11AC20\_5600\_Ant1**



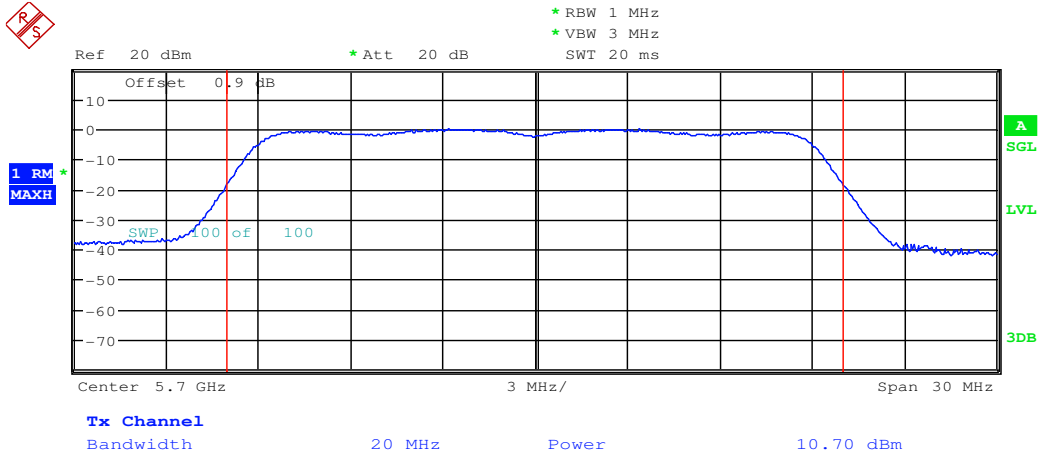
**Maximum Conduct Output Power\_11AC20\_5600\_Ant2**



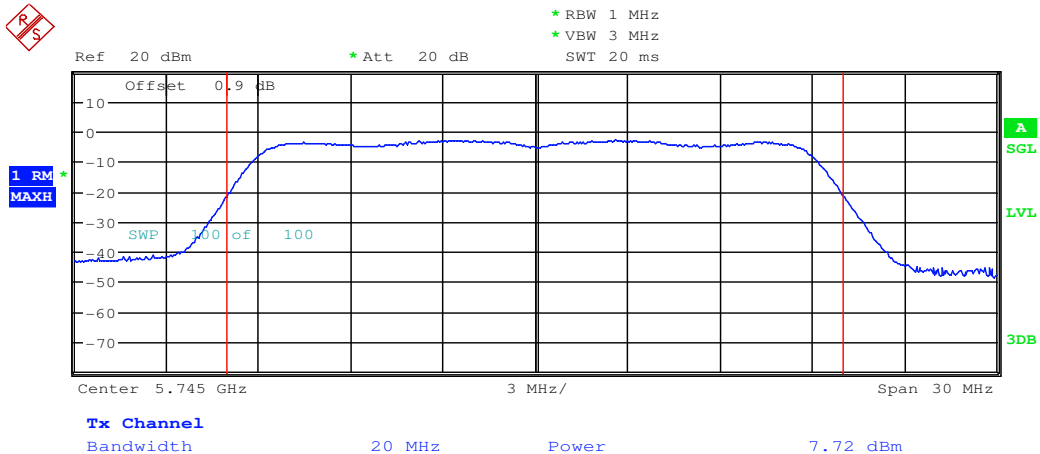
**Maximum Conduct Output Power\_11AC20\_5700\_Ant1**



**Maximum Conduct Output Power\_11AC20\_5700\_Ant2**

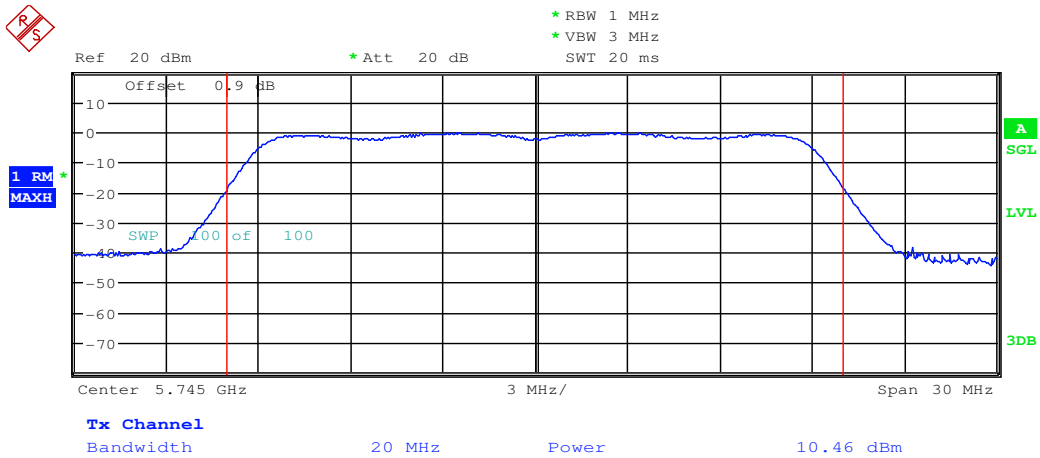


**Maximum Conduct Output Power\_11AC20\_5745\_Ant1**

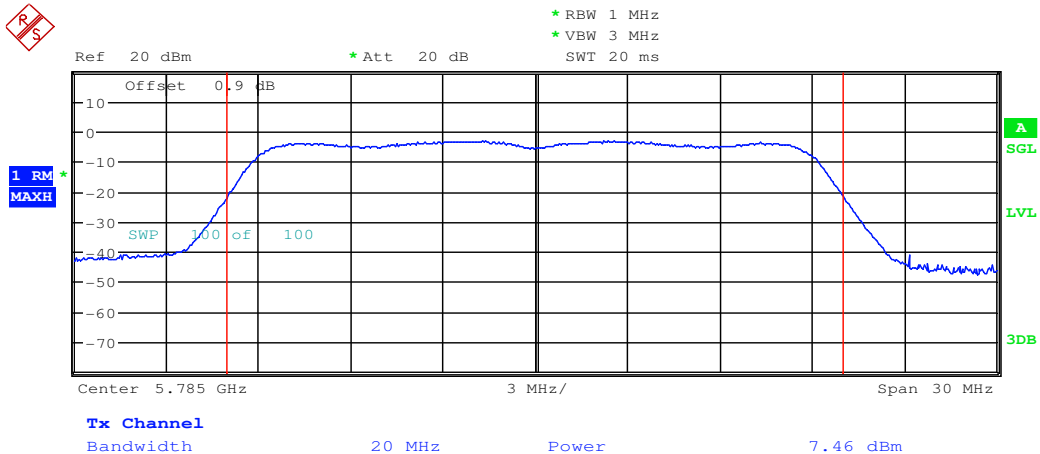




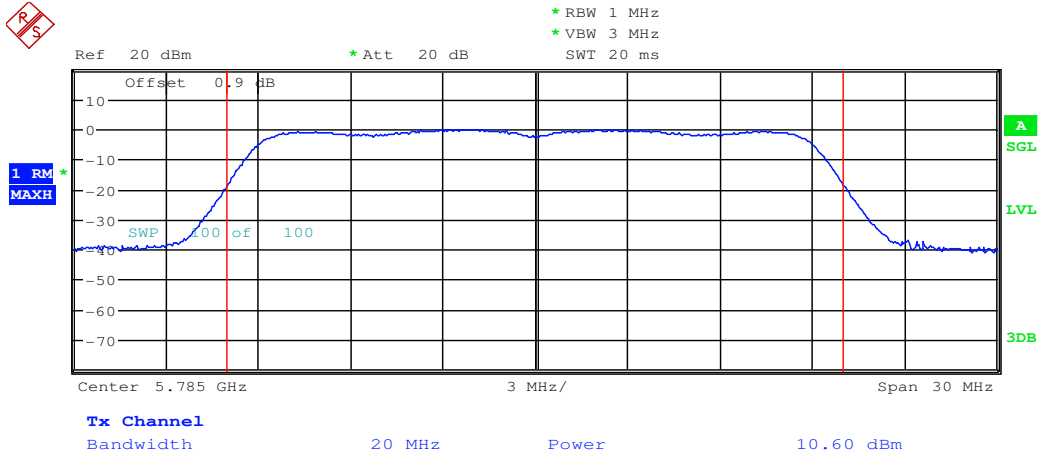
Maximum Conduct Output Power\_11AC20\_5745\_Ant2



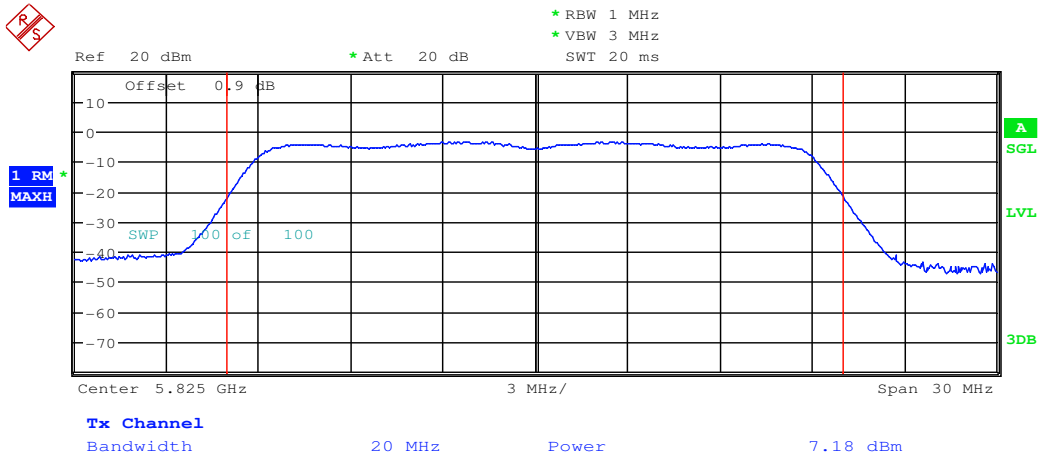
Maximum Conduct Output Power\_11AC20\_5785\_Ant1



**Maximum Conduct Output Power\_11AC20\_5785\_Ant2**

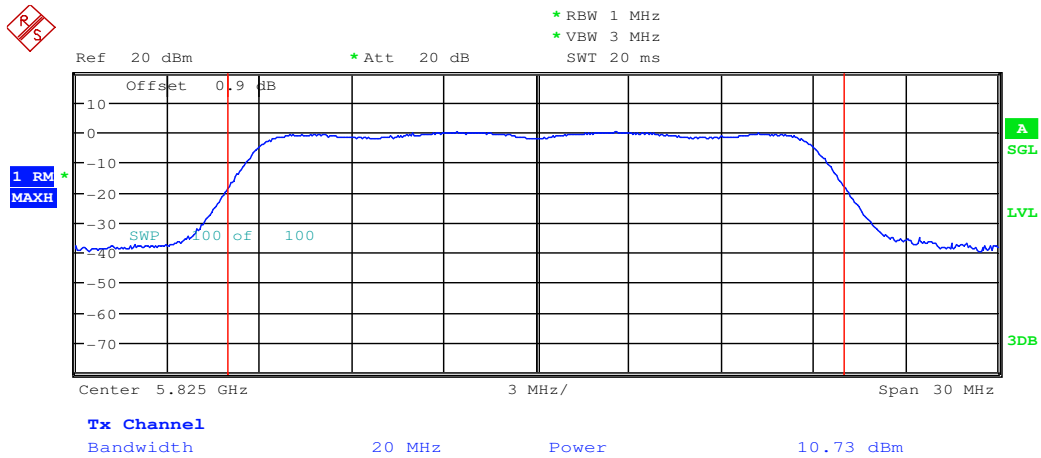


**Maximum Conduct Output Power\_11AC20\_5825\_Ant1**

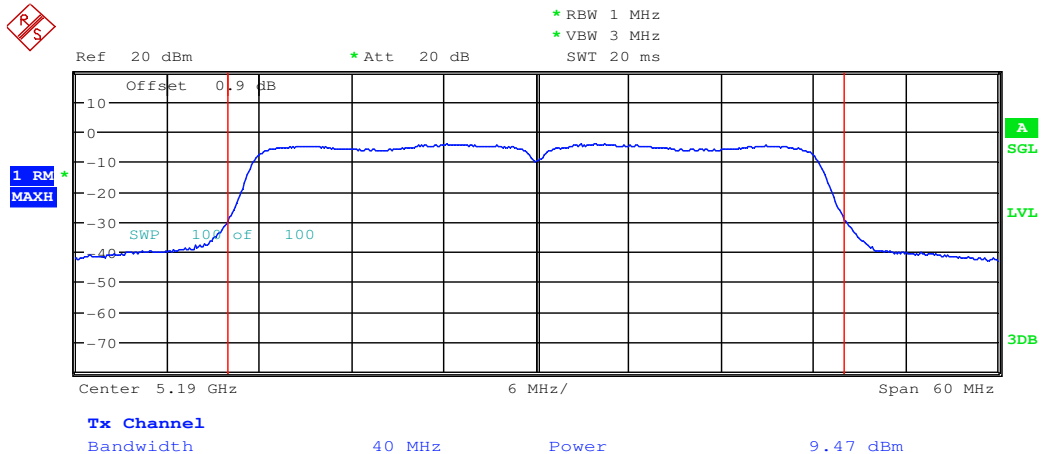




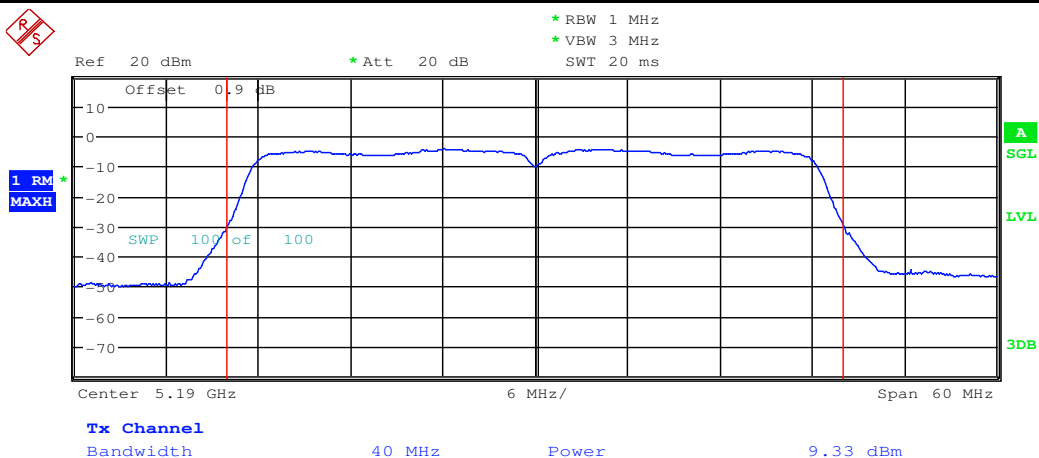
Maximum Conduct Output Power\_11AC20\_5825\_Ant2



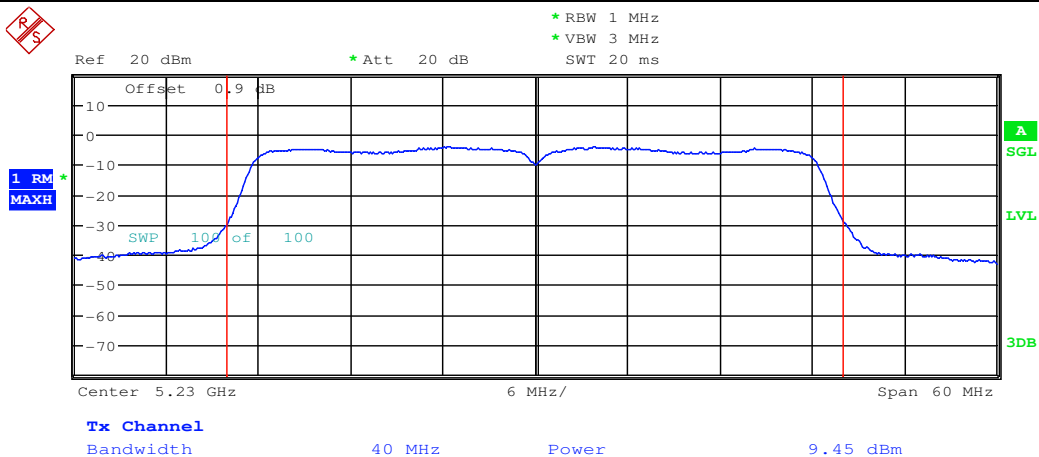
Maximum Conduct Output Power\_11AC40\_5190\_Ant1



Maximum Conduct Output Power\_11AC40\_5190\_Ant2

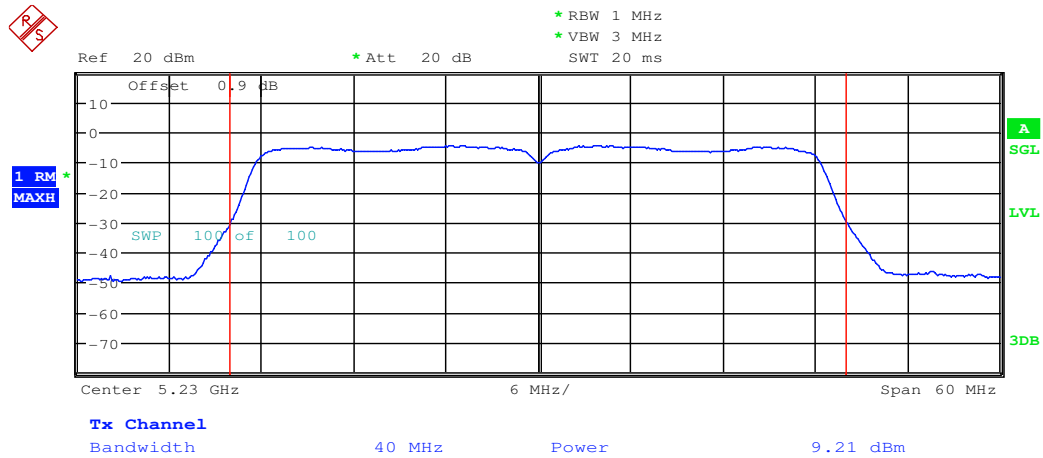


Maximum Conduct Output Power\_11AC40\_5230\_Ant1

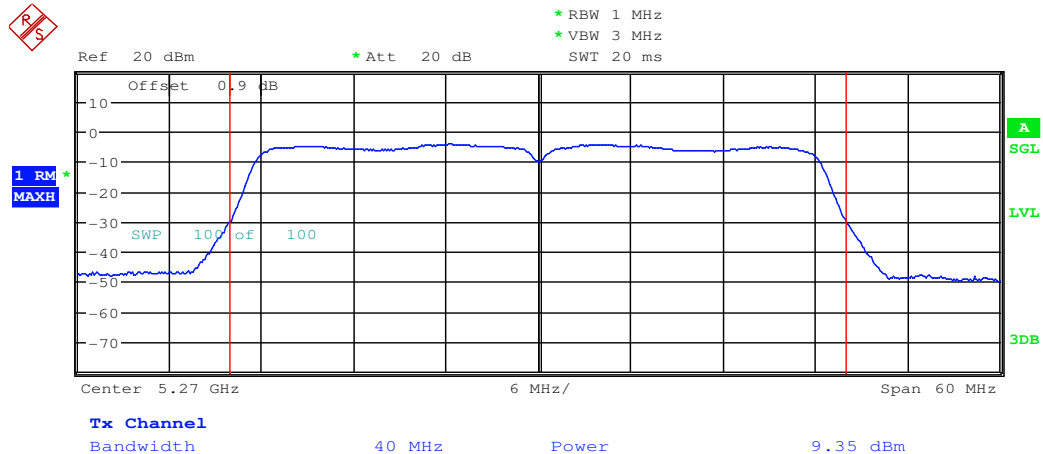




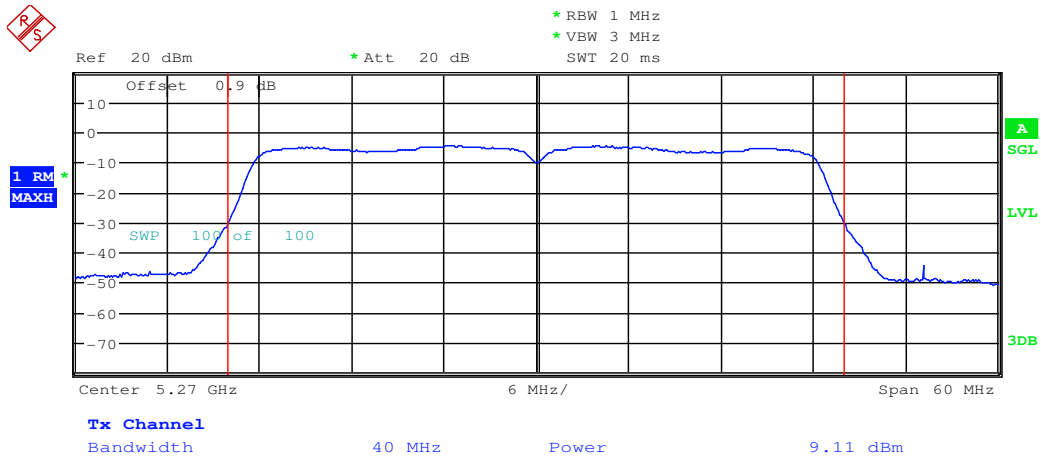
## Maximum Conduct Output Power\_11AC40\_5230\_Ant2



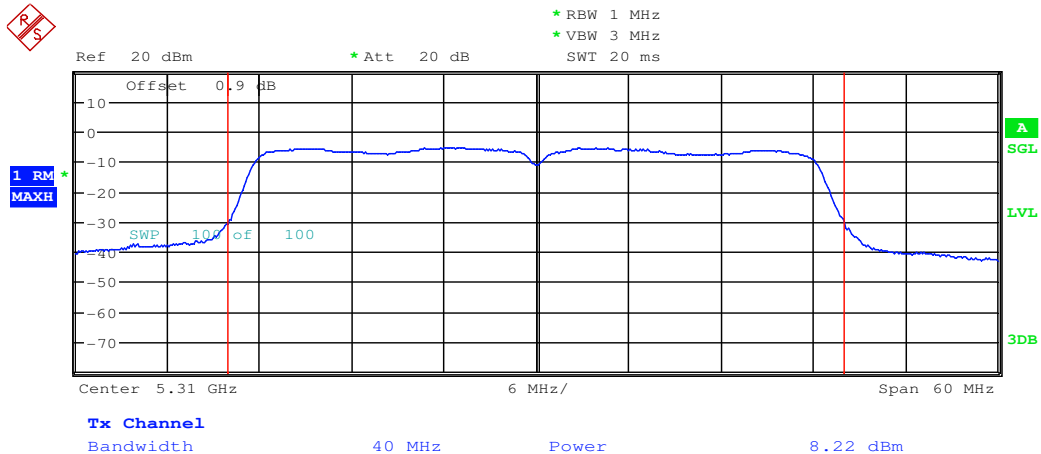
## Maximum Conduct Output Power\_11AC40\_5270\_Ant1



**Maximum Conduct Output Power\_11AC40\_5270\_Ant2**

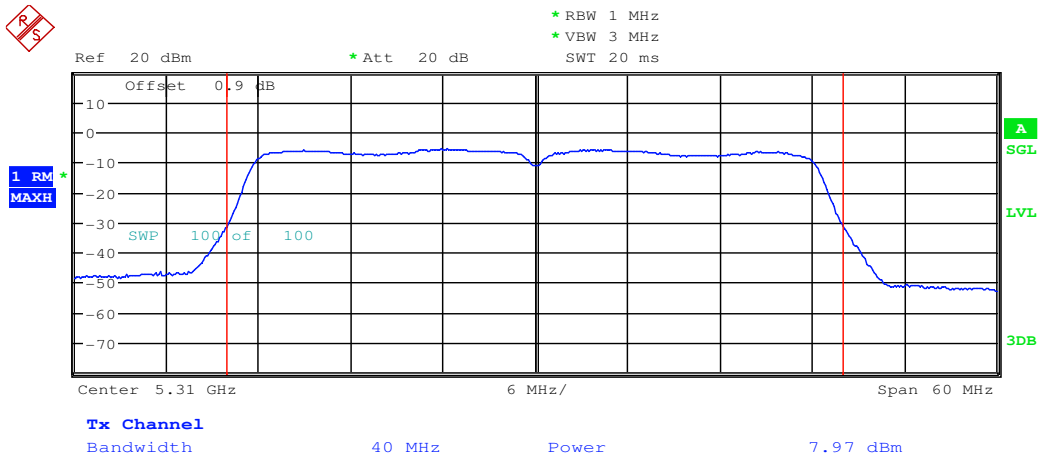


**Maximum Conduct Output Power\_11AC40\_5310\_Ant1**

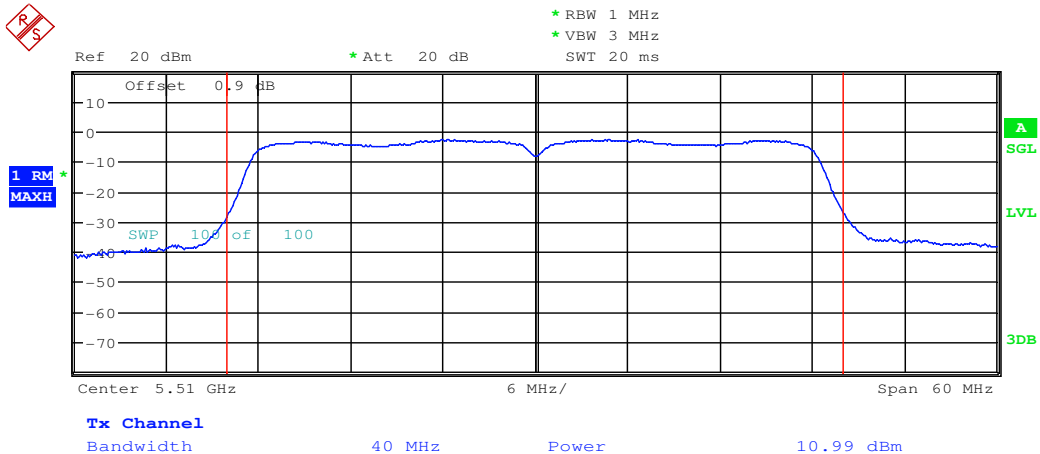




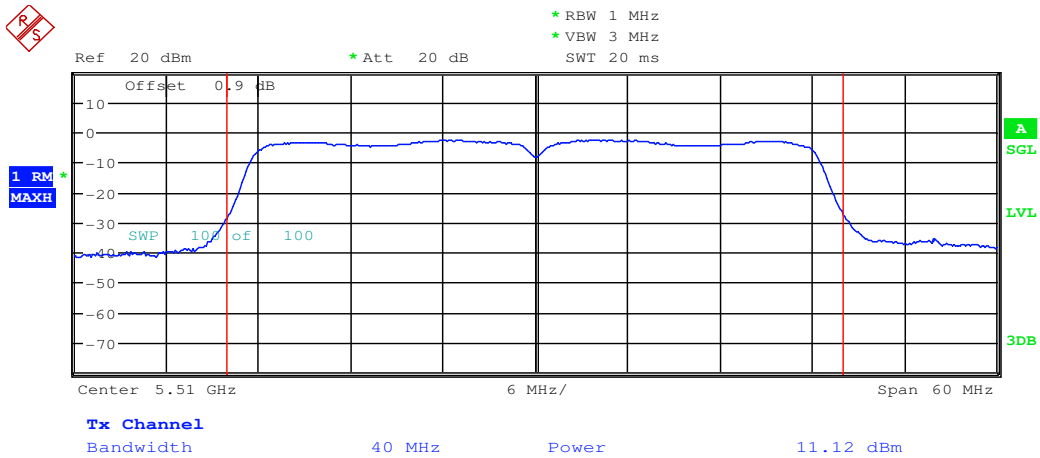
Maximum Conduct Output Power\_11AC40\_5310\_Ant2



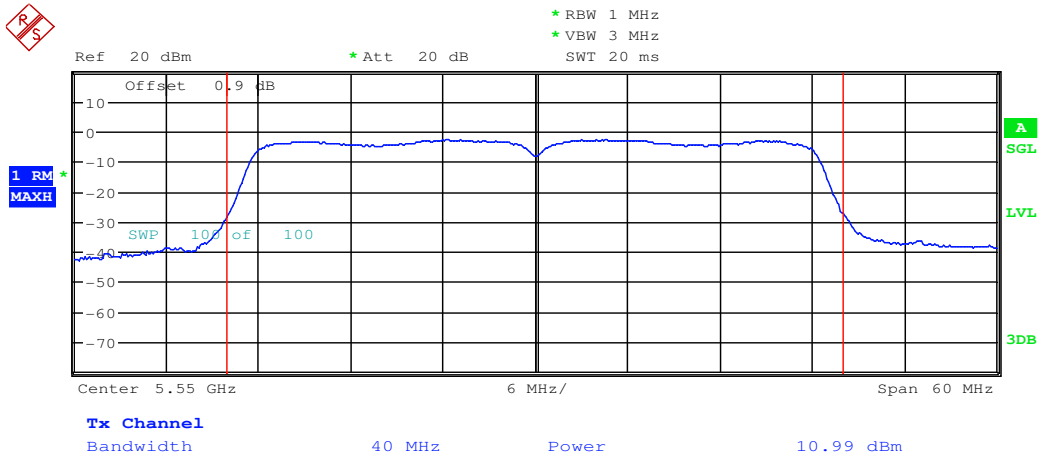
Maximum Conduct Output Power\_11AC40\_5510\_Ant1



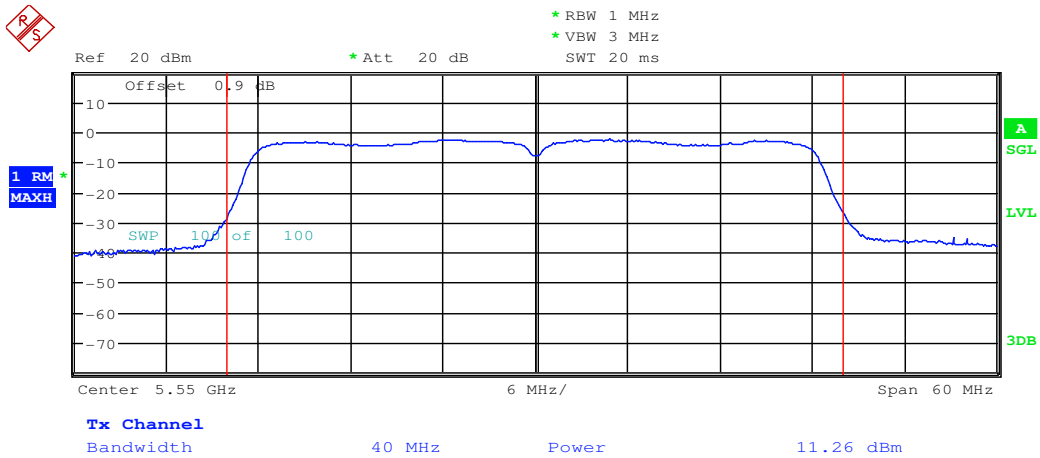
**Maximum Conduct Output Power\_11AC40\_5510\_Ant2**



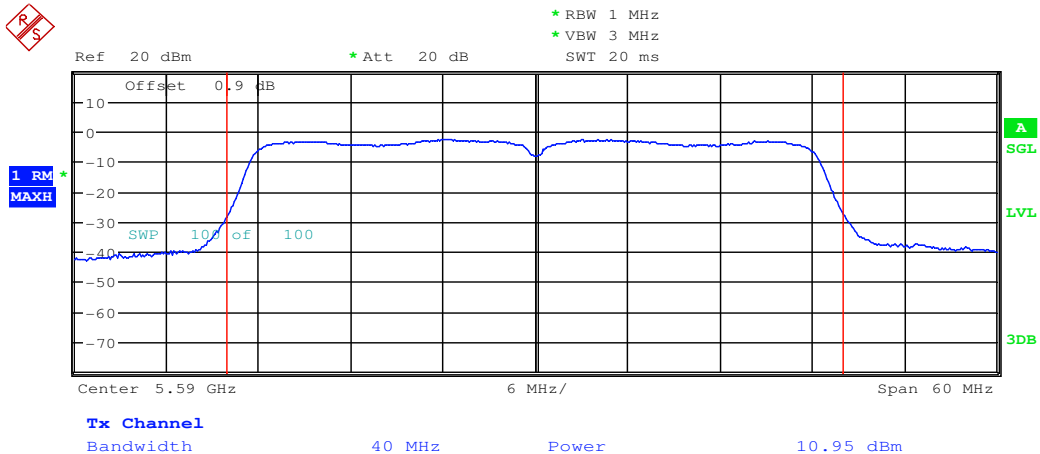
**Maximum Conduct Output Power\_11AC40\_5550\_Ant1**



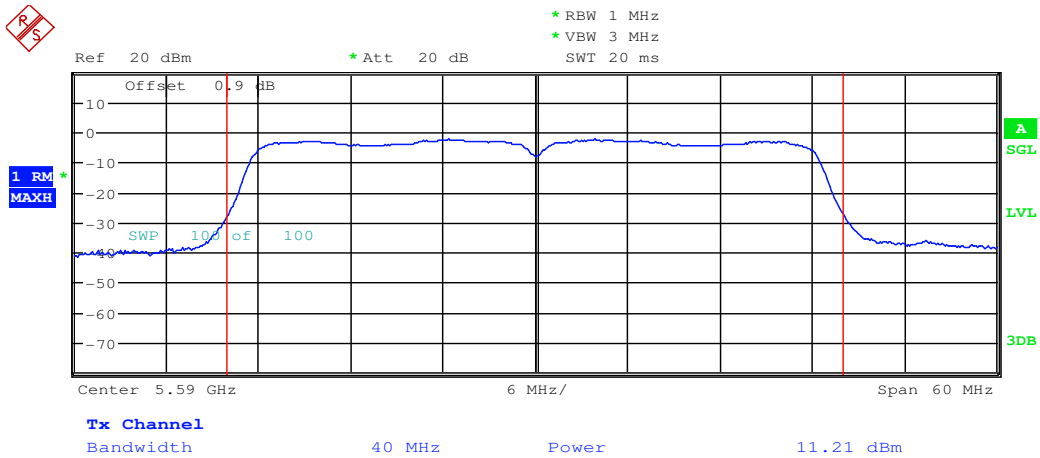
**Maximum Conduct Output Power\_11AC40\_5550\_Ant2**



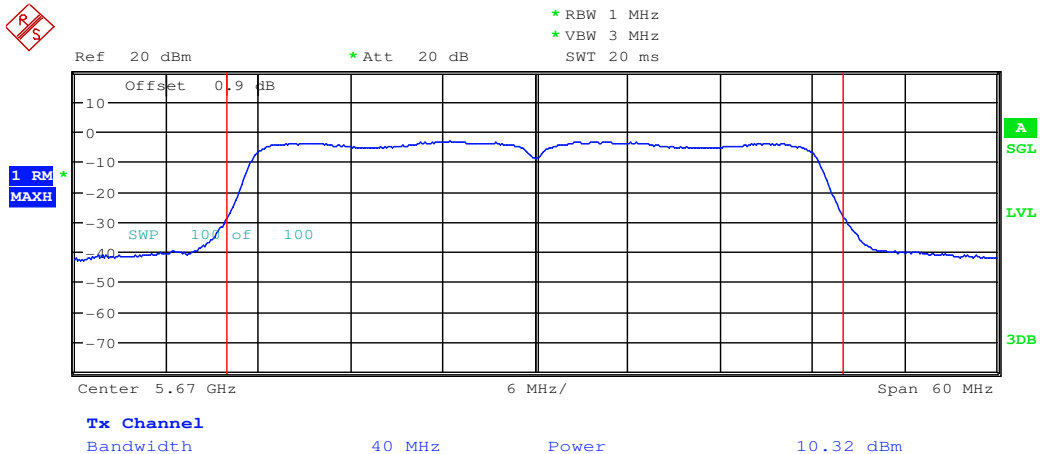
**Maximum Conduct Output Power\_11AC40\_5590\_Ant1**



**Maximum Conduct Output Power\_11AC40\_5590\_Ant2**

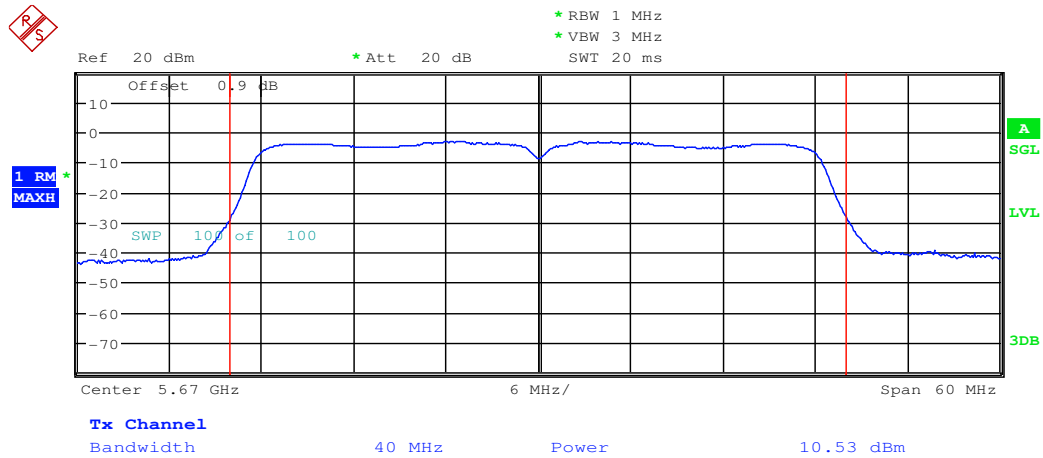


**Maximum Conduct Output Power\_11AC40\_5670\_Ant1**

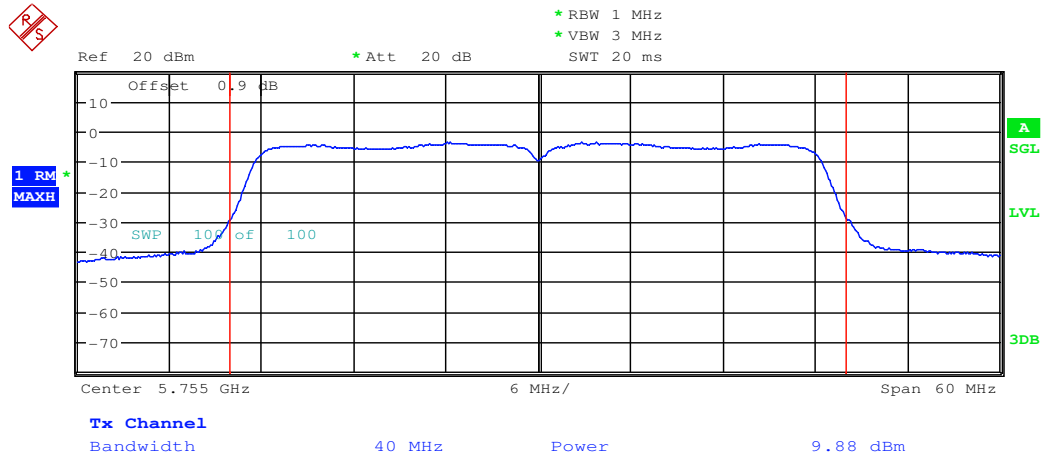




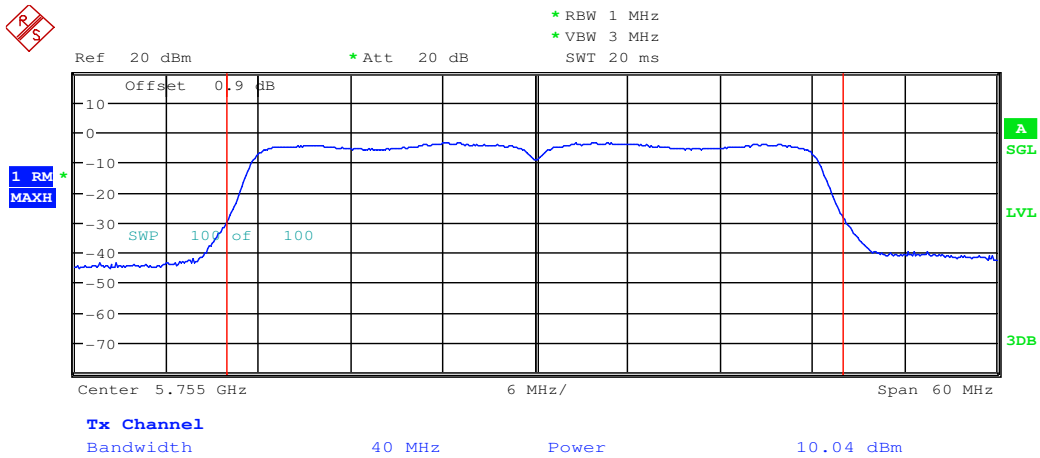
Maximum Conduct Output Power\_11AC40\_5670\_Ant2



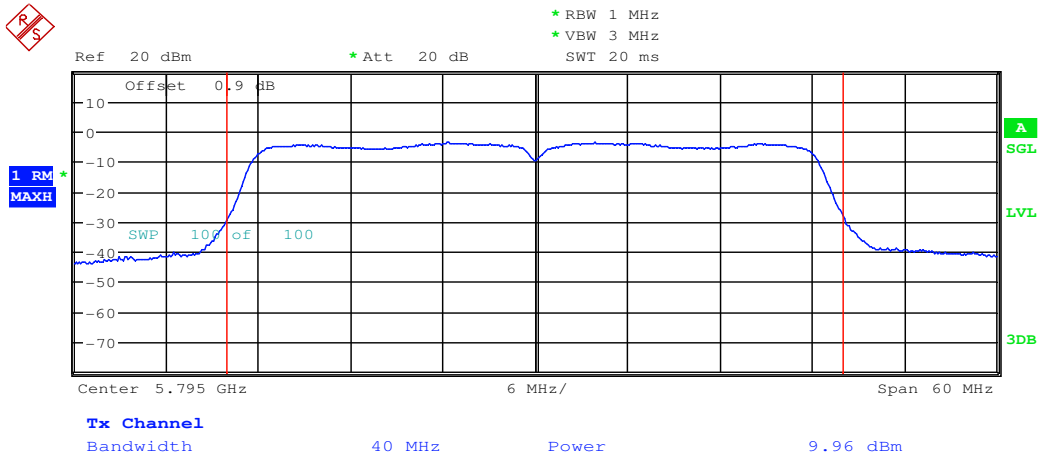
Maximum Conduct Output Power\_11AC40\_5755\_Ant1



**Maximum Conduct Output Power\_11AC40\_5755\_Ant2**

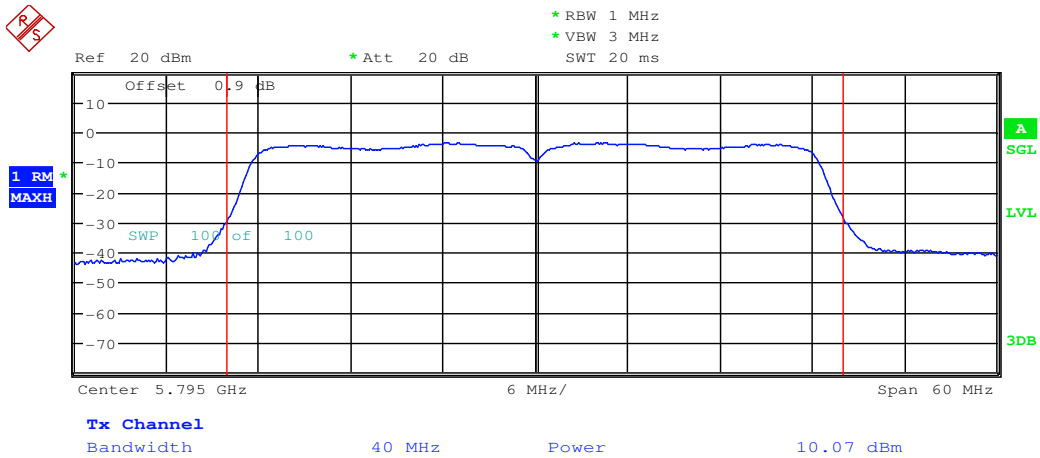


**Maximum Conduct Output Power\_11AC40\_5795\_Ant1**

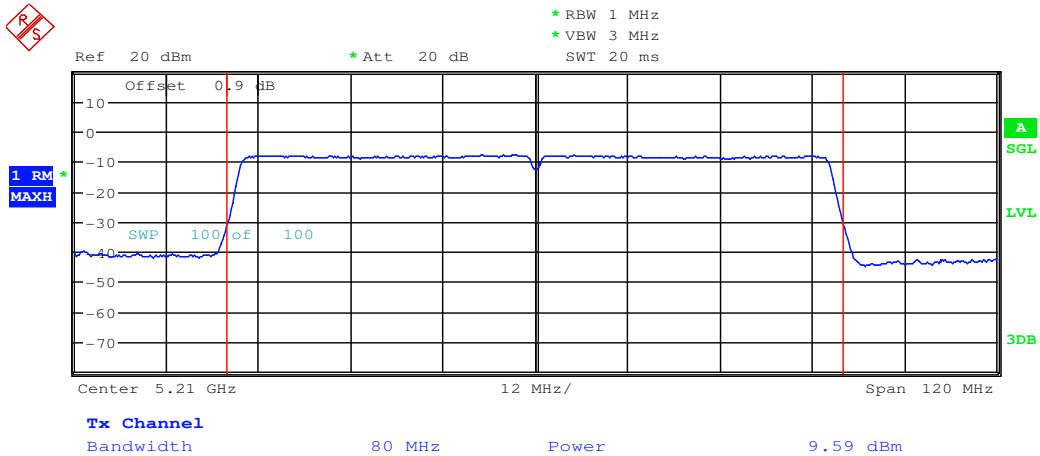




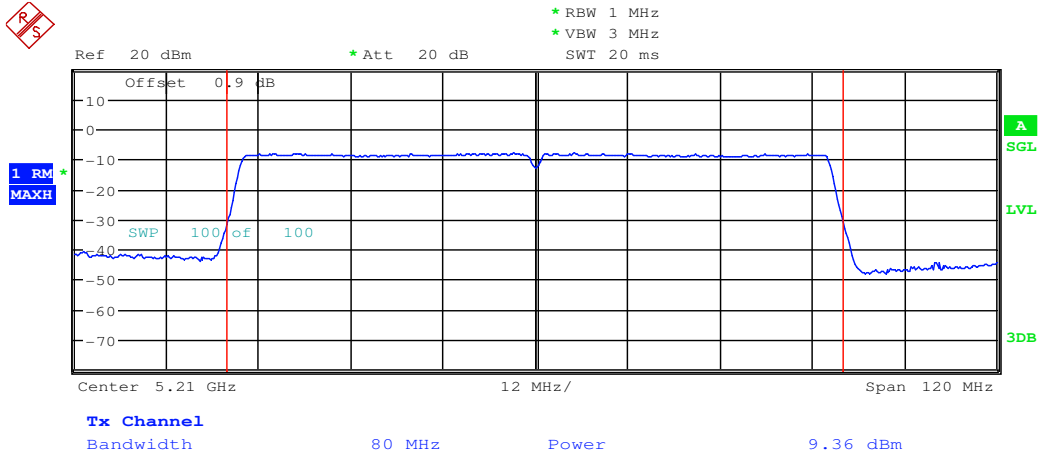
**Maximum Conduct Output Power\_11AC40\_5795\_Ant2**



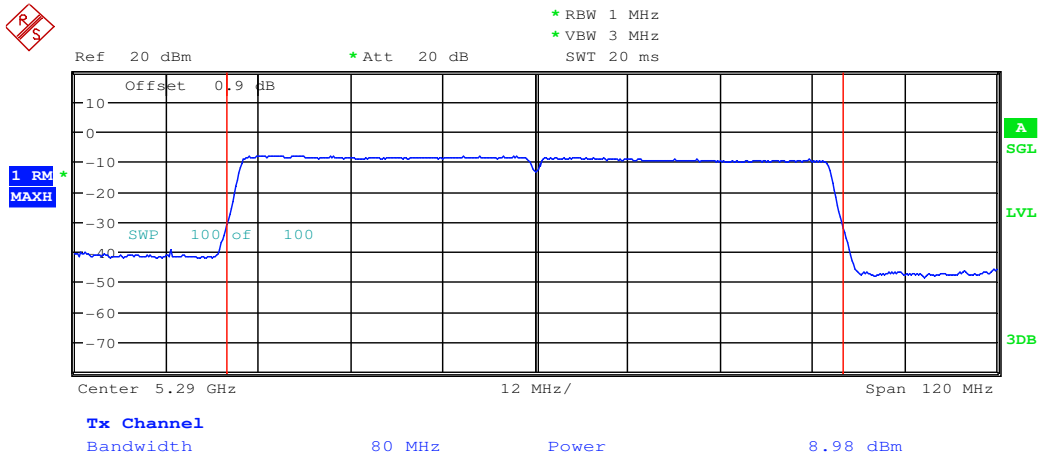
**Maximum Conduct Output Power\_11AC80\_5210\_Ant1**



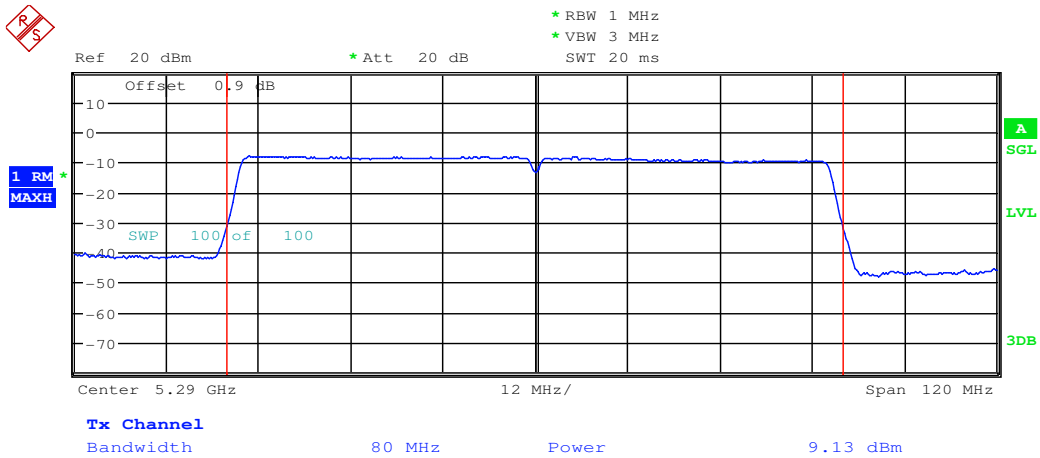
**Maximum Conduct Output Power\_11AC80\_5210\_Ant2**



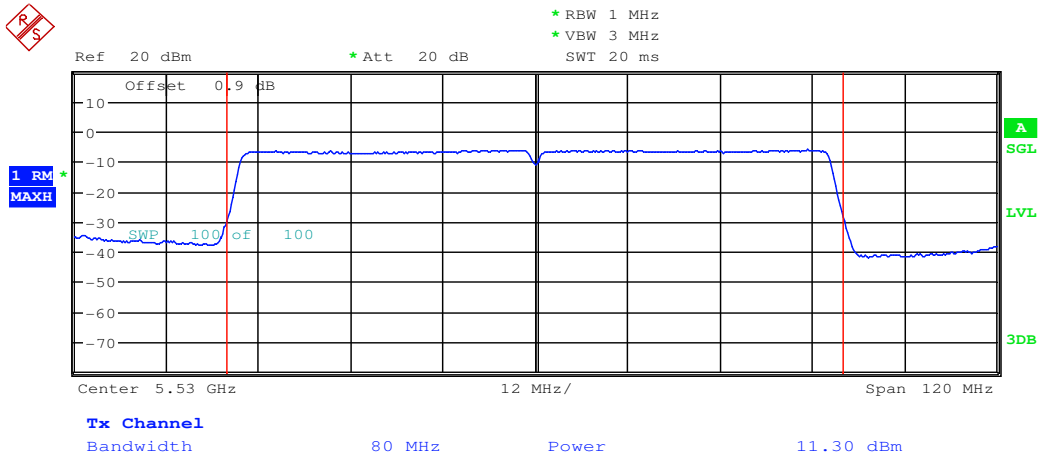
**Maximum Conduct Output Power\_11AC80\_5290\_Ant1**



**Maximum Conduct Output Power\_11AC80\_5290\_Ant2**

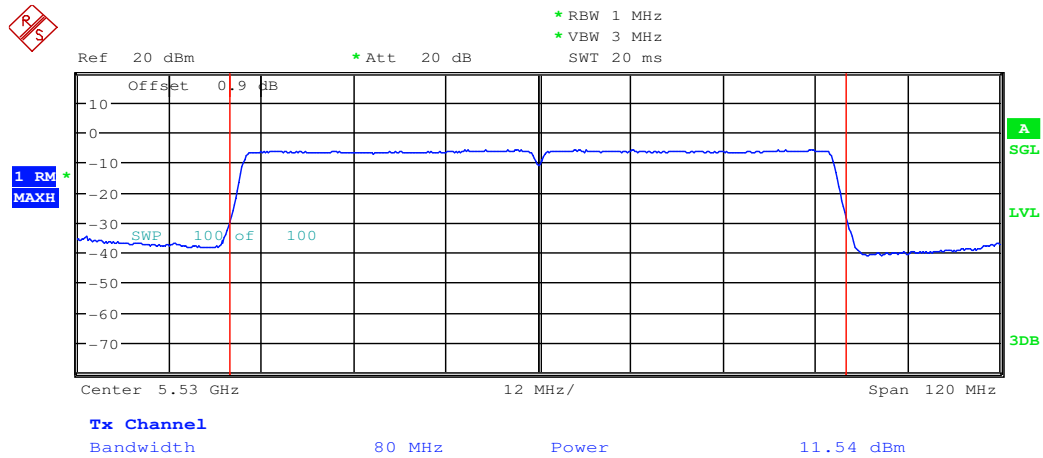


**Maximum Conduct Output Power\_11AC80\_5530\_Ant1**

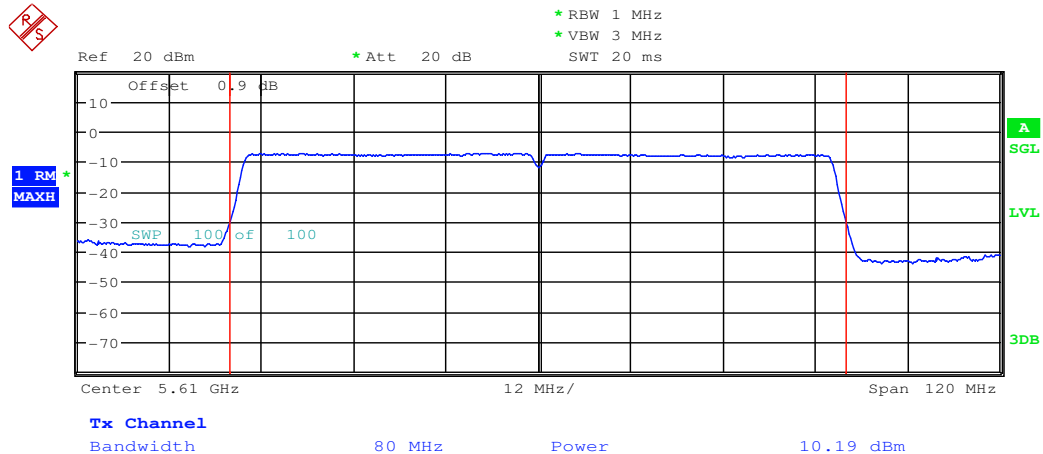




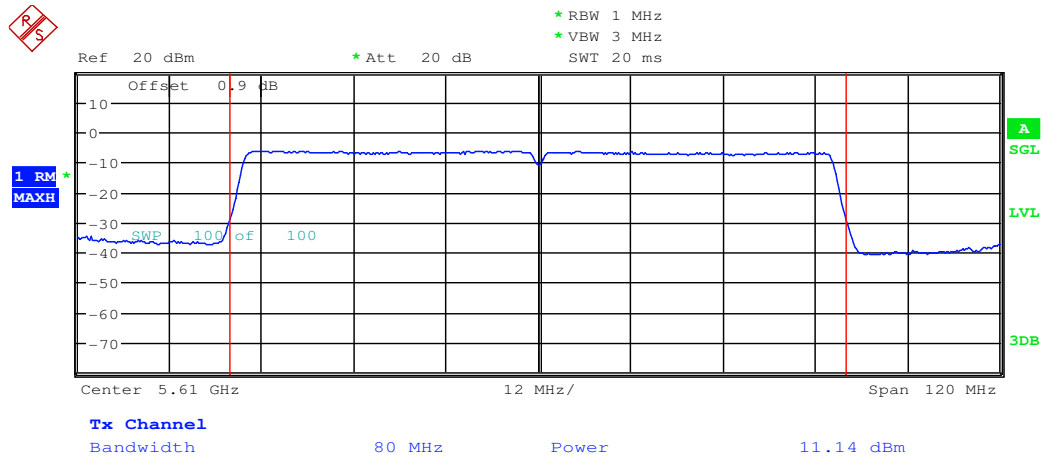
Maximum Conduct Output Power\_11AC80\_5530\_Ant2



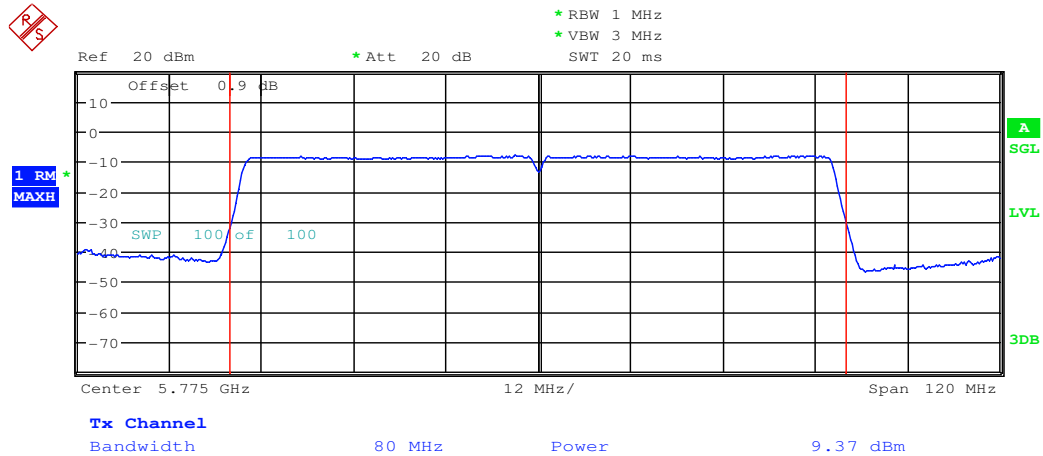
Maximum Conduct Output Power\_11AC80\_5610\_Ant1

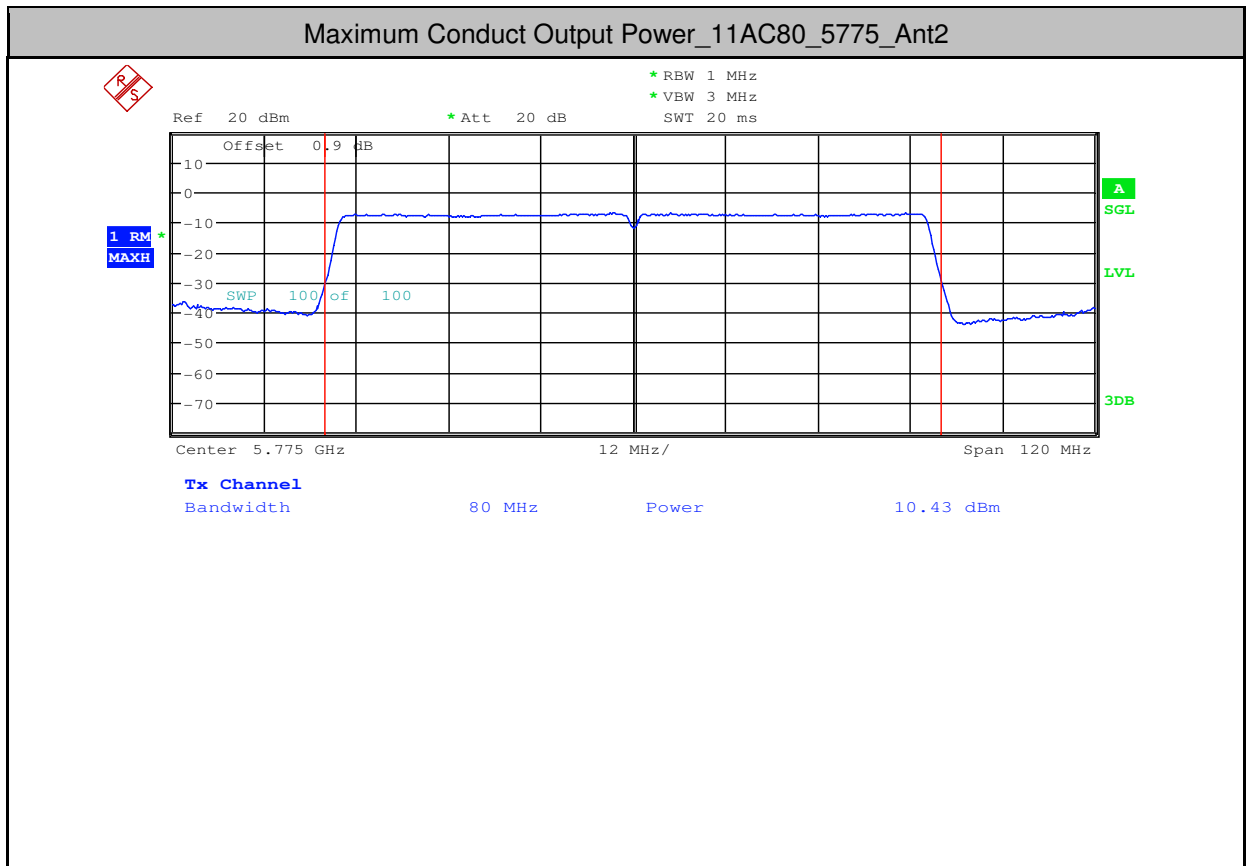


**Maximum Conduct Output Power\_11AC80\_5610\_Ant2**



**Maximum Conduct Output Power\_11AC80\_5775\_Ant1**







#### 4. Maximum Power Spectral Density

Test Mode	Test Channel	Ant	Level [dBm/MHz]	10log(1/x) Factor [dB]	PSD [dBm/MHz]	Limit [dBm/MHz]	Verdict
11A	5180	Ant1	1.83	0	1.83	<11.00	PASS
11A	5180	Ant2	0.45	0	0.45	<11.00	PASS
11A	5200	Ant1	1.89	0	1.89	<11.00	PASS
11A	5200	Ant2	0.16	0	0.16	<11.00	PASS
11A	5240	Ant1	0.75	0	0.75	<11.00	PASS
11A	5240	Ant2	0.66	0	0.66	<11.00	PASS
11A	5260	Ant1	0.63	0	0.63	<11.00	PASS
11A	5260	Ant2	1.16	0	1.16	<11.00	PASS
11A	5300	Ant1	-0.38	0	-0.38	<11.00	PASS
11A	5300	Ant2	0.21	0	0.21	<11.00	PASS
11A	5320	Ant1	-0.15	0	-0.15	<11.00	PASS
11A	5320	Ant2	-0.17	0	-0.17	<11.00	PASS
11A	5500	Ant1	0.07	0	0.07	<11.00	PASS
11A	5500	Ant2	1.74	0	1.74	<11.00	PASS
11A	5580	Ant1	0.04	0	0.04	<11.00	PASS
11A	5580	Ant2	1.89	0	1.89	<11.00	PASS
11A	5600	Ant1	-0.12	0	-0.12	<11.00	PASS
11A	5600	Ant2	1.92	0	1.92	<11.00	PASS
11A	5700	Ant1	-1.07	0	-1.07	<11.00	PASS
11A	5700	Ant2	0.83	0	0.83	<11.00	PASS
11N20	5180	Ant1	-1.05	0	-1.05	<11.00	PASS
11N20	5180	Ant2	0.6	0	0.6	<11.00	PASS
11N20	5200	Ant1	-0.11	0	-0.11	<11.00	PASS
11N20	5200	Ant2	0.47	0	0.47	<11.00	PASS
11N20	5240	Ant1	-0.4	0	-0.4	<11.00	PASS
11N20	5240	Ant2	0.07	0	0.07	<11.00	PASS
11N20	5260	Ant1	-0.4	0	-0.4	<11.00	PASS
11N20	5260	Ant2	-0.14	0	-0.14	<11.00	PASS
11N20	5300	Ant1	-1.28	0	-1.28	<11.00	PASS
11N20	5300	Ant2	-0.9	0	-0.9	<11.00	PASS
11N20	5320	Ant1	-1.66	0	-1.66	<11.00	PASS



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11N20	5320	Ant2	-1.32	0	-1.32	<11.00	PASS
11N20	5500	Ant1	1.25	0	1.25	<11.00	PASS
11N20	5500	Ant2	1.89	0	1.89	<11.00	PASS
11N20	5580	Ant1	1.43	0	1.43	<11.00	PASS
11N20	5580	Ant2	1.95	0	1.95	<11.00	PASS
11N20	5600	Ant1	1.27	0	1.27	<11.00	PASS
11N20	5600	Ant2	1.85	0	1.85	<11.00	PASS
11N20	5700	Ant1	0.32	0	0.32	<11.00	PASS
11N20	5700	Ant2	1.22	0	1.22	<11.00	PASS
11N40	5190	Ant1	-3.98	0	-3.98	<11.00	PASS
11N40	5190	Ant2	-4.07	0	-4.07	<11.00	PASS
11N40	5230	Ant1	-4.14	0	-4.14	<11.00	PASS
11N40	5230	Ant2	-4.27	0	-4.27	<11.00	PASS
11N40	5270	Ant1	-4.5	0	-4.5	<11.00	PASS
11N40	5270	Ant2	-4.63	0	-4.63	<11.00	PASS
11N40	5310	Ant1	-5.27	0	-5.27	<11.00	PASS
11N40	5310	Ant2	-5.36	0	-5.36	<11.00	PASS
11N40	5510	Ant1	-2.55	0	-2.55	<11.00	PASS
11N40	5510	Ant2	-2.35	0	-2.35	<11.00	PASS
11N40	5550	Ant1	-2.43	0	-2.43	<11.00	PASS
11N40	5550	Ant2	-2.31	0	-2.31	<11.00	PASS
11N40	5590	Ant1	-2.21	0	-2.21	<11.00	PASS
11N40	5590	Ant2	-2.3	0	-2.3	<11.00	PASS
11N40	5670	Ant1	-2.9	0	-2.9	<11.00	PASS
11N40	5670	Ant2	-2.91	0	-2.91	<11.00	PASS
11AC20	5180	Ant1	0.1	0	0.1	<11.00	PASS
11AC20	5200	Ant1	0.78	0	0.78	<11.00	PASS
11AC20	5200	Ant2	0.11	0	0.11	<11.00	PASS
11AC20	5240	Ant1	-0.08	0	-0.08	<11.00	PASS
11AC20	5240	Ant2	-0.04	0	-0.04	<11.00	PASS
11AC20	5260	Ant1	0.01	0	0.01	<11.00	PASS
11AC20	5260	Ant2	1.05	0	1.05	<11.00	PASS
11AC20	5300	Ant1	-1.42	0	-1.42	<11.00	PASS
11AC20	5300	Ant2	-0.24	0	-0.24	<11.00	PASS

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11AC20	5320	Ant1	-1.82	0	-1.82	<11.00	PASS
11AC20	5320	Ant2	-0.24	0	-0.24	<11.00	PASS
11AC20	5500	Ant1	-0.67	0	-0.67	<11.00	PASS
11AC20	5500	Ant2	1.86	0	1.86	<11.00	PASS
11AC20	5580	Ant1	-0.54	0	-0.54	<11.00	PASS
11AC20	5580	Ant2	2.13	0	2.13	<11.00	PASS
11AC20	5600	Ant1	-0.86	0	-0.86	<11.00	PASS
11AC20	5600	Ant2	1.97	0	1.97	<11.00	PASS
11AC20	5700	Ant1	-1.65	0	-1.65	<11.00	PASS
11AC20	5700	Ant2	0.55	0	0.55	<11.00	PASS
11AC40	5190	Ant1	-3.78	0	-3.78	<11.00	PASS
11AC40	5190	Ant2	-3.99	0	-3.99	<11.00	PASS
11AC40	5230	Ant1	-3.86	0	-3.86	<11.00	PASS
11AC40	5230	Ant2	-4.17	0	-4.17	<11.00	PASS
11AC40	5270	Ant1	-4.33	0	-4.33	<11.00	PASS
11AC40	5270	Ant2	-4.34	0	-4.34	<11.00	PASS
11AC40	5310	Ant1	-5.1	0	-5.1	<11.00	PASS
11AC40	5310	Ant2	-5.35	0	-5.35	<11.00	PASS
11AC40	5510	Ant1	-2.33	0	-2.33	<11.00	PASS
11AC40	5510	Ant2	-2.19	0	-2.19	<11.00	PASS
11AC40	5550	Ant1	-2.15	0	-2.15	<11.00	PASS
11AC40	5550	Ant2	-2.23	0	-2.23	<11.00	PASS
11AC40	5590	Ant1	-2.48	0	-2.48	<11.00	PASS
11AC40	5590	Ant2	-2.02	0	-2.02	<11.00	PASS
11AC40	5670	Ant1	-3.04	0	-3.04	<11.00	PASS
11AC40	5670	Ant2	-2.88	0	-2.88	<11.00	PASS
11AC80	5210	Ant1	-7.33	0	-7.33	<11.00	PASS
11AC80	5210	Ant2	-7.45	0	-7.45	<11.00	PASS
11AC80	5290	Ant1	-7.41	0	-7.41	<11.00	PASS
11AC80	5290	Ant2	-7.58	0	-7.58	<11.00	PASS
11AC80	5530	Ant1	-5.46	0	-5.46	<11.00	PASS
11AC80	5530	Ant2	-5.16	0	-5.16	<11.00	PASS
11AC80	5610	Ant1	-6.62	0	-6.62	<11.00	PASS
11AC80	5610	Ant2	-5.77	0	-5.77	<11.00	PASS

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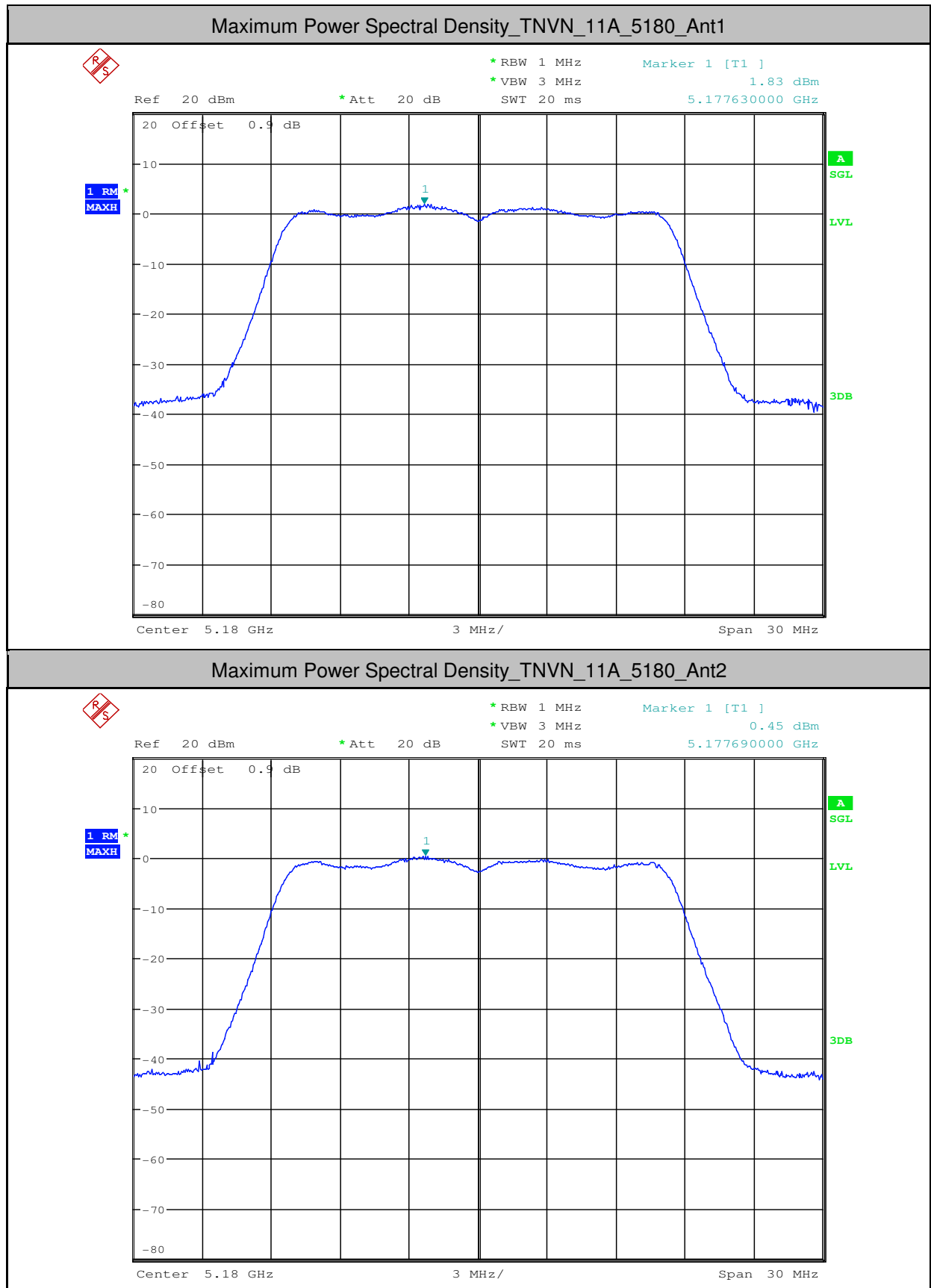


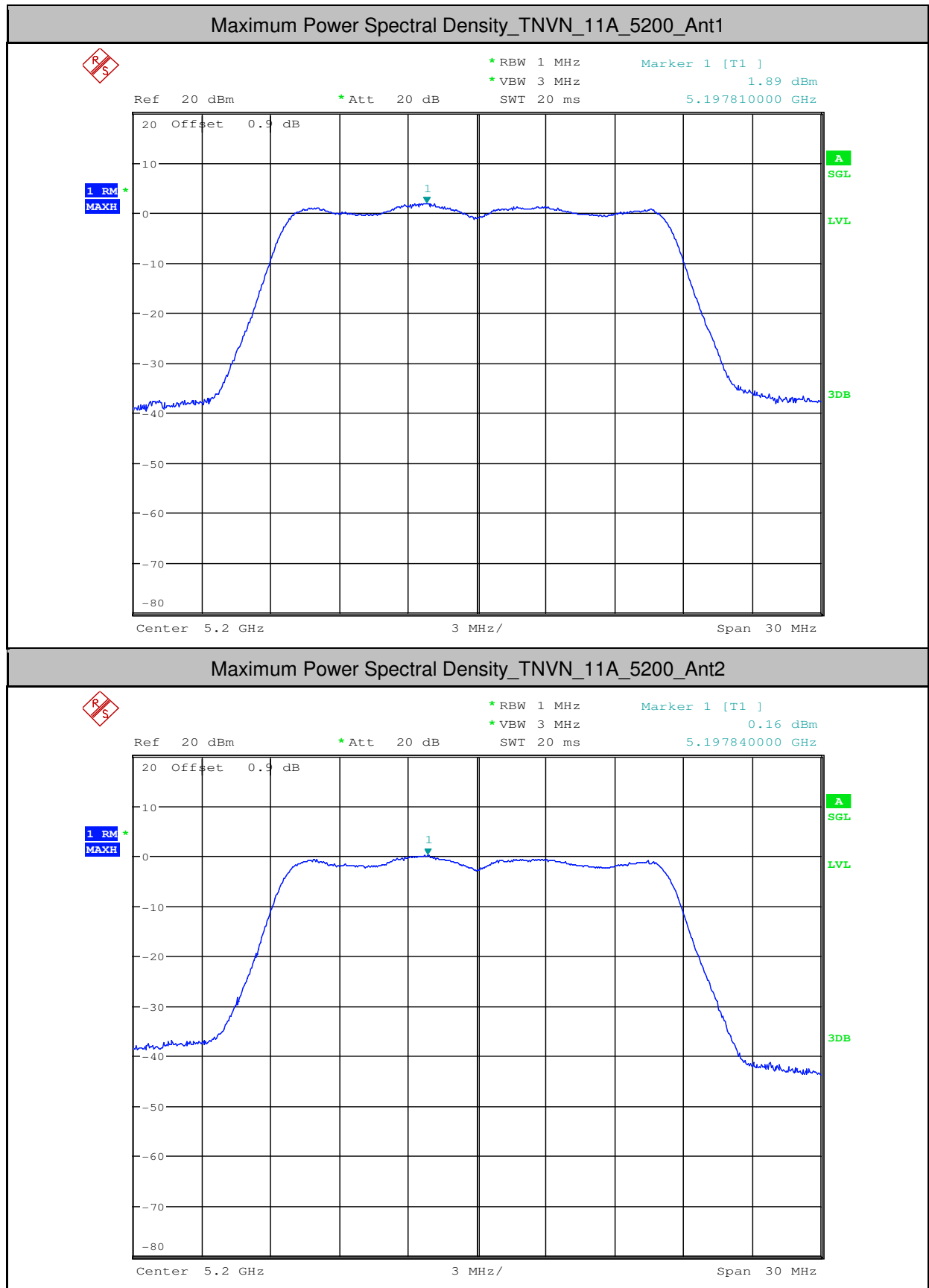
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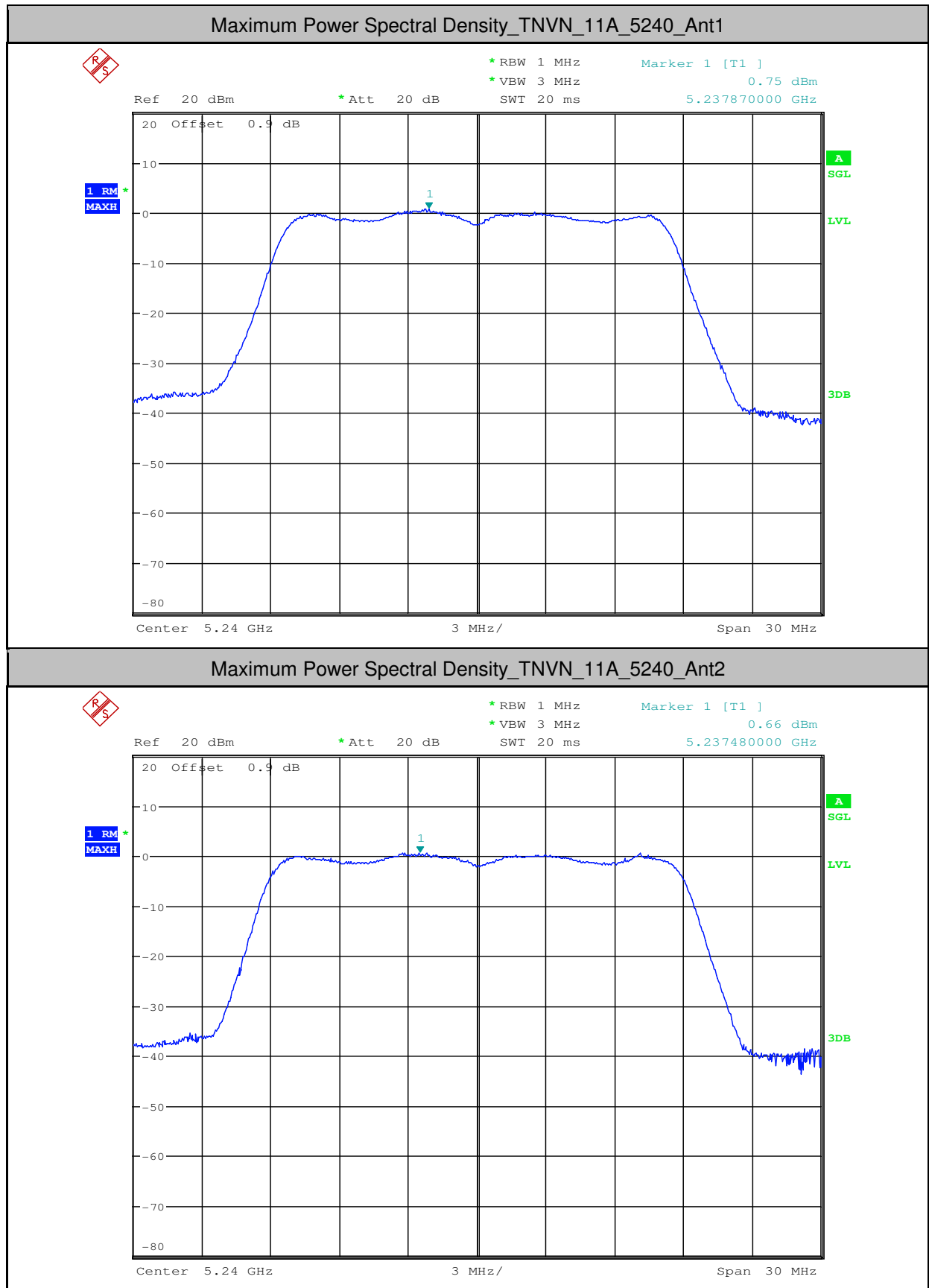
Report No.: SZEM170700767504

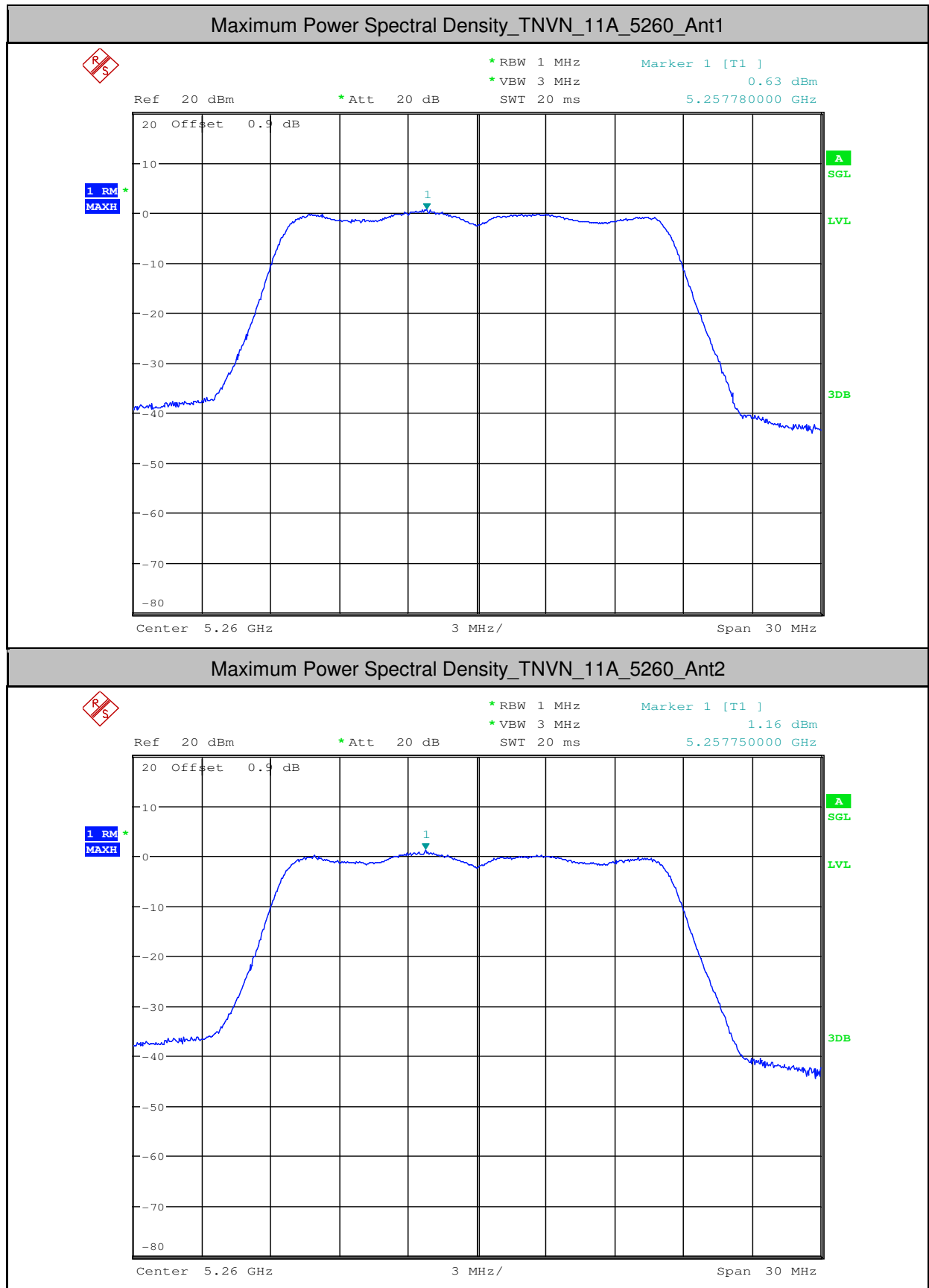
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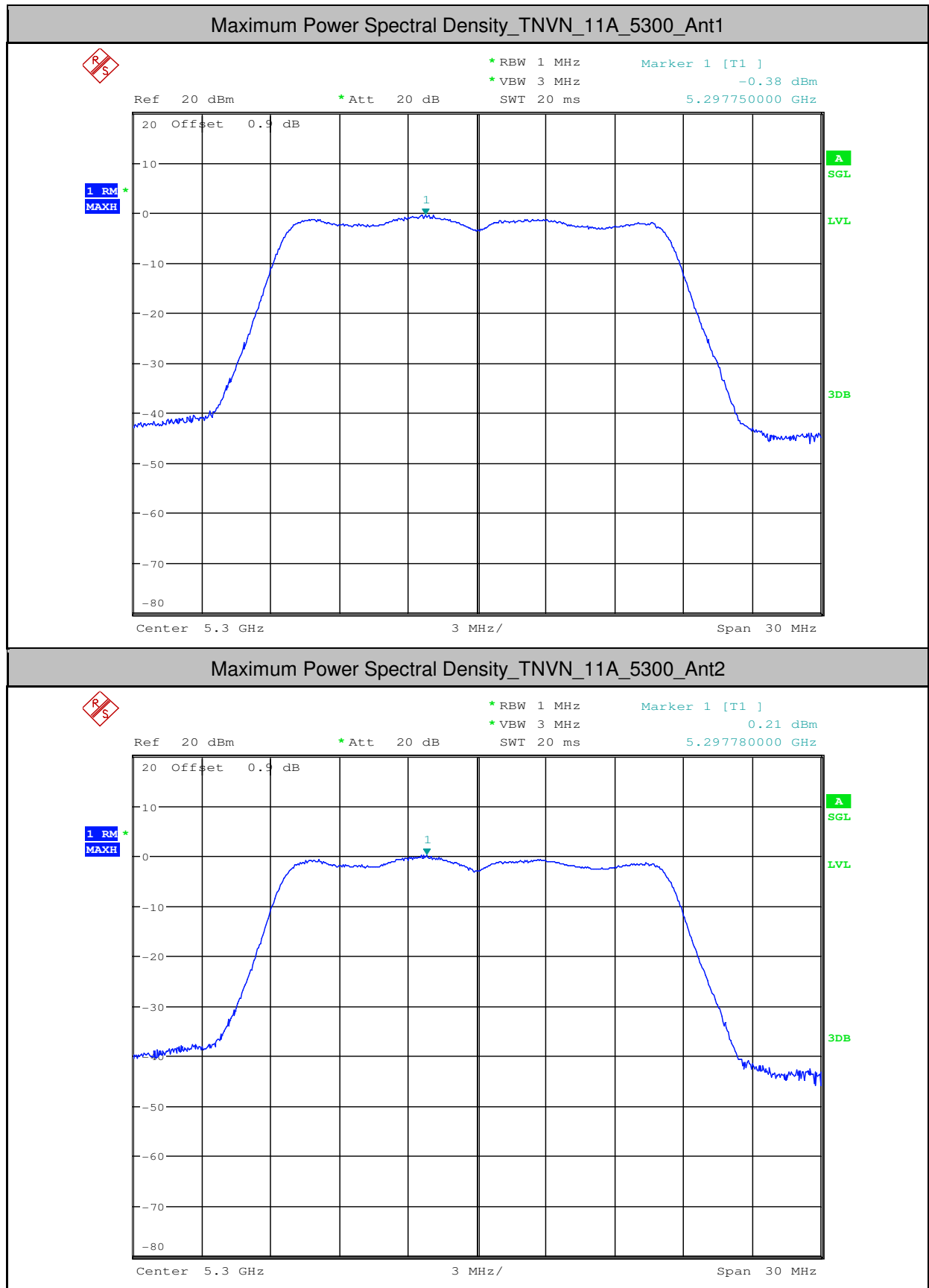
Test Mode	Test Channel	Ant	Level [dBm/500kHz]	10log(1/x) Factor[dB]	10log(500kHz/RBW) Factor [dB]	PSD [dBm/500kHz]	Limit [dBm/500kHz]	Verdict
11A	5745	Ant1	-4.12	0	0	-4.12	<17.00	PASS
11A	5745	Ant2	-1.52	0	0	-1.52	<17.00	PASS
11A	5785	Ant1	-4.49	0	0	-4.49	<17.00	PASS
11A	5785	Ant2	-1.64	0	0	-1.64	<17.00	PASS
11A	5825	Ant1	-4.27	0	0	-4.27	<17.00	PASS
11A	5825	Ant2	-1.52	0	0	-1.52	<17.00	PASS
11N20	5745	Ant1	-1.52	0	0	-1.52	<17.00	PASS
11N20	5745	Ant2	-1.47	0	0	-1.47	<17.00	PASS
11N20	5785	Ant1	-1.59	0	0	-1.59	<17.00	PASS
11N20	5785	Ant2	-1.64	0	0	-1.64	<17.00	PASS
11N20	5825	Ant1	-1.27	0	0	-1.27	<17.00	PASS
11N20	5825	Ant2	-1.24	0	0	-1.24	<17.00	PASS
11N40	5755	Ant1	-5.69	0	0	-5.69	<17.00	PASS
11N40	5755	Ant2	-5.51	0	0	-5.51	<17.00	PASS
11N40	5795	Ant1	-5.4	0	0	-5.4	<17.00	PASS
11N40	5795	Ant2	-5.48	0	0	-5.48	<17.00	PASS
11AC20	5745	Ant1	-4.45	0	0	-4.45	<17.00	PASS
11AC20	5745	Ant2	-1.45	0	0	-1.45	<17.00	PASS
11AC20	5785	Ant1	-4.39	0	0	-4.39	<17.00	PASS
11AC20	5785	Ant2	-1.58	0	0	-1.58	<17.00	PASS
11AC20	5825	Ant1	-5.13	0	0	-5.13	<17.00	PASS
11AC20	5825	Ant2	-1.26	0	0	-1.26	<17.00	PASS
11AC40	5755	Ant1	-5.38	0	0	-5.38	<17.00	PASS
11AC40	5755	Ant2	-5.44	0	0	-5.44	<17.00	PASS
11AC40	5795	Ant1	-5.39	0	0	-5.39	<17.00	PASS
11AC40	5795	Ant2	-5.47	0	0	-5.47	<17.00	PASS
11AC80	5775	Ant1	-9.31	0	0	-9.31	<17.00	PASS
11AC80	5775	Ant2	-8.42	0	0	-8.42	<17.00	PASS

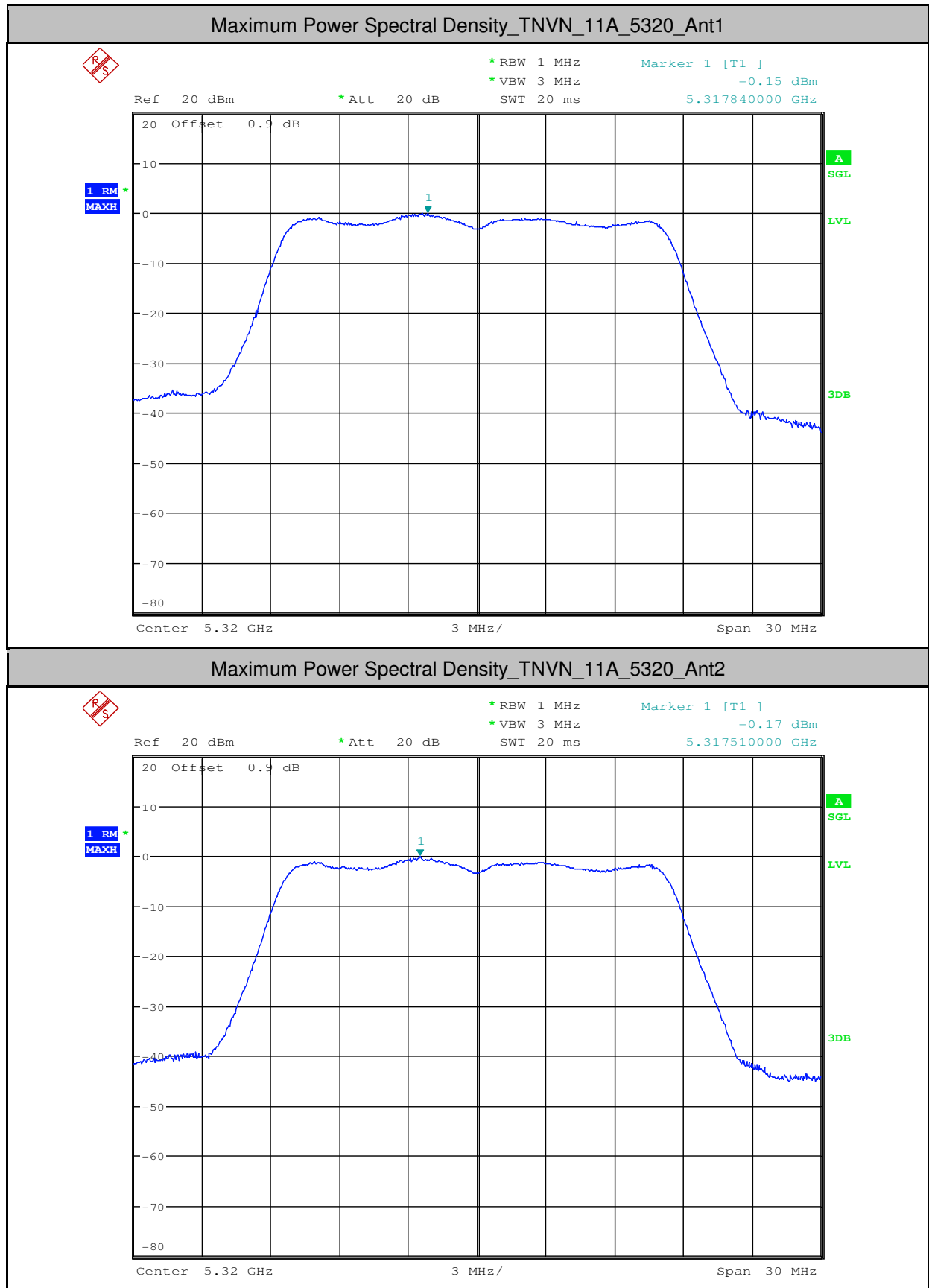




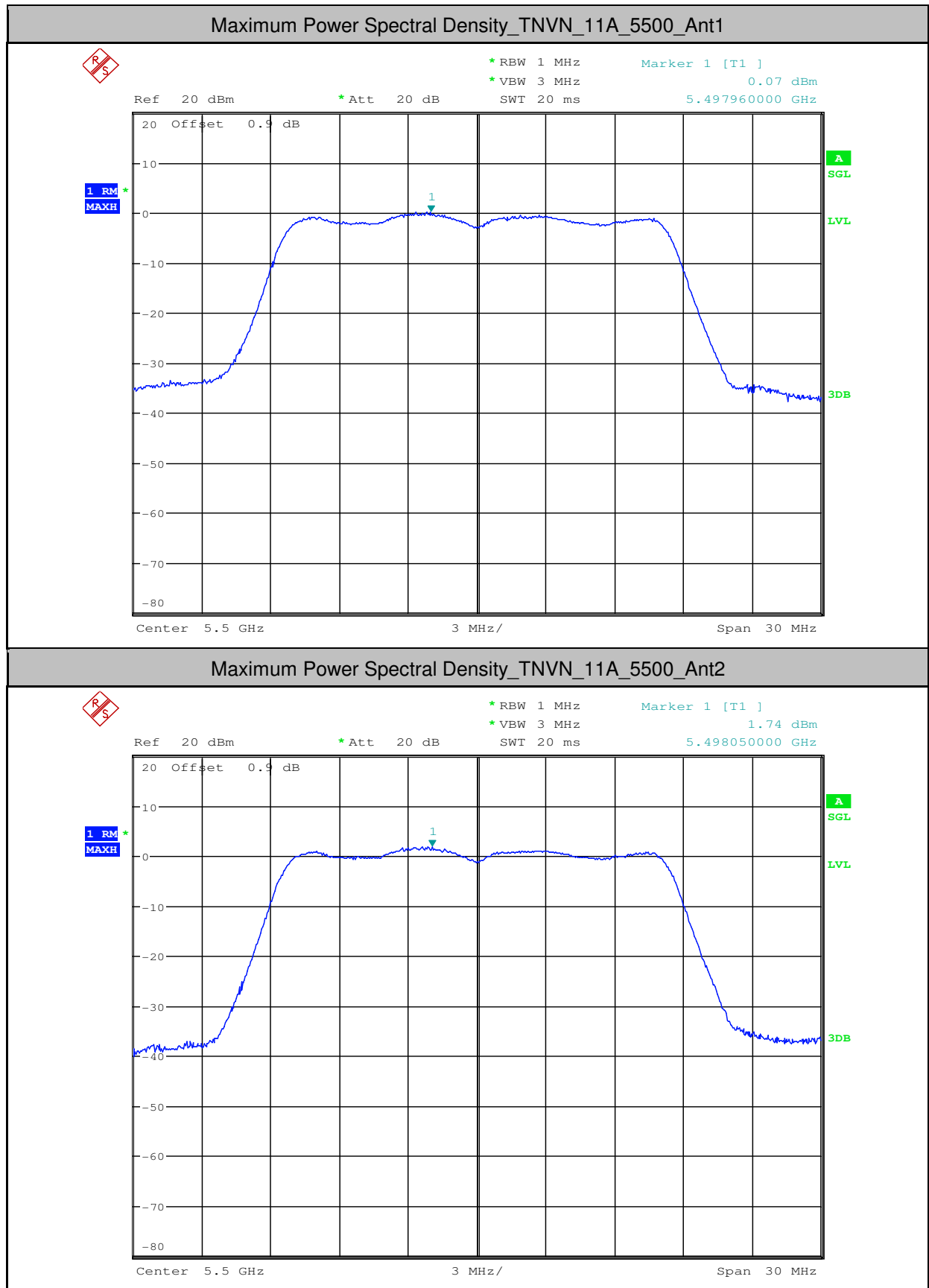


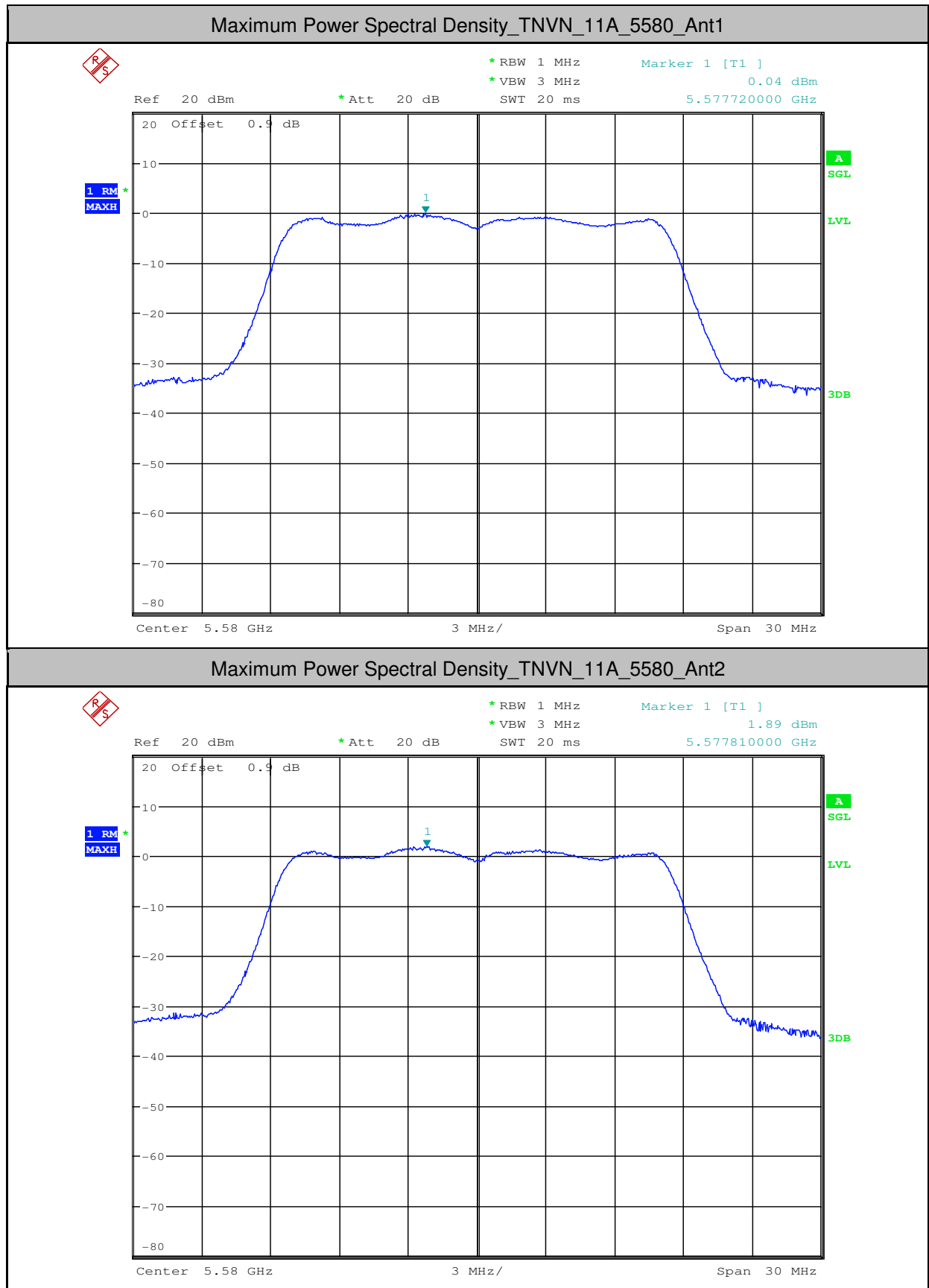


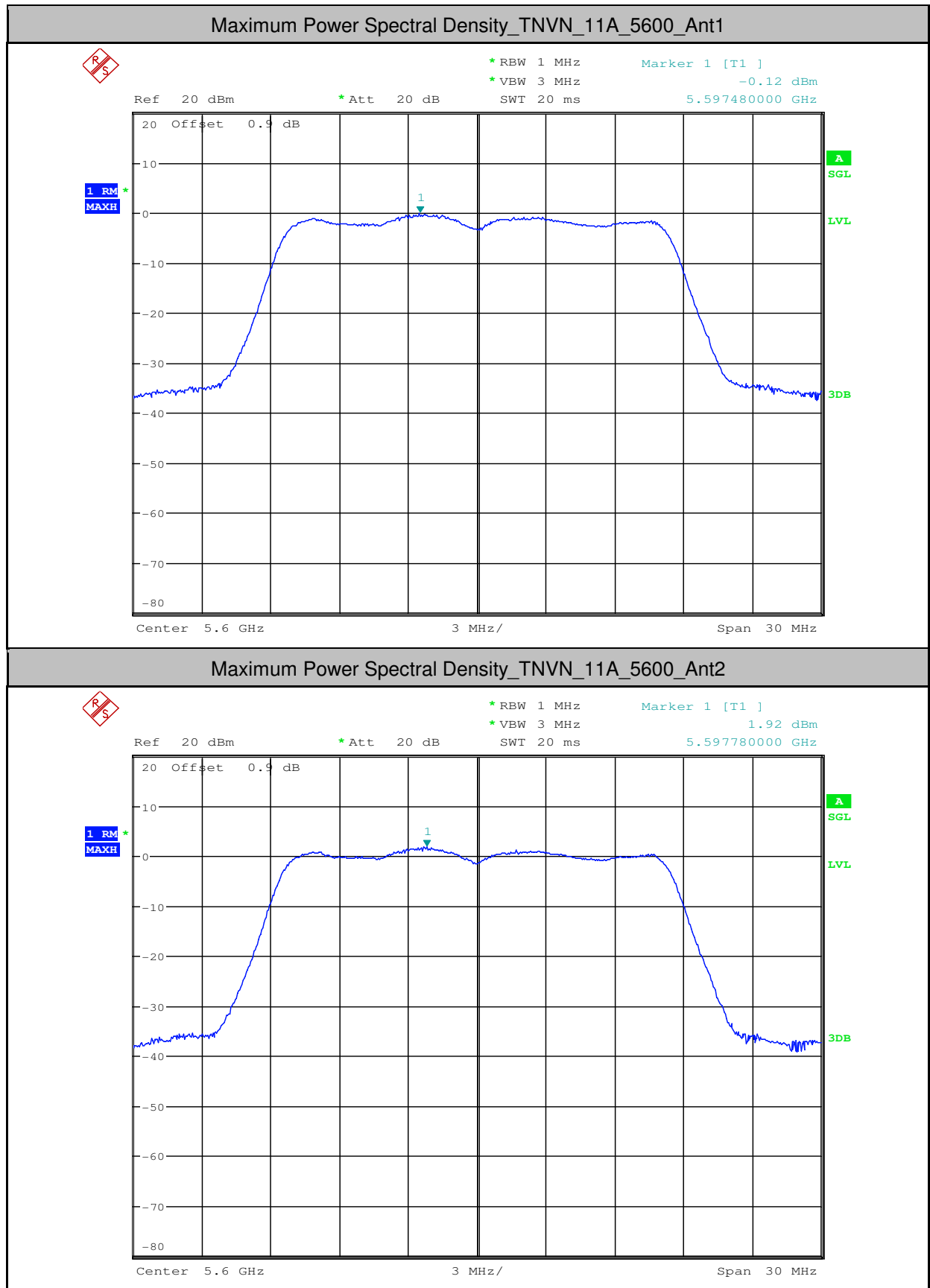


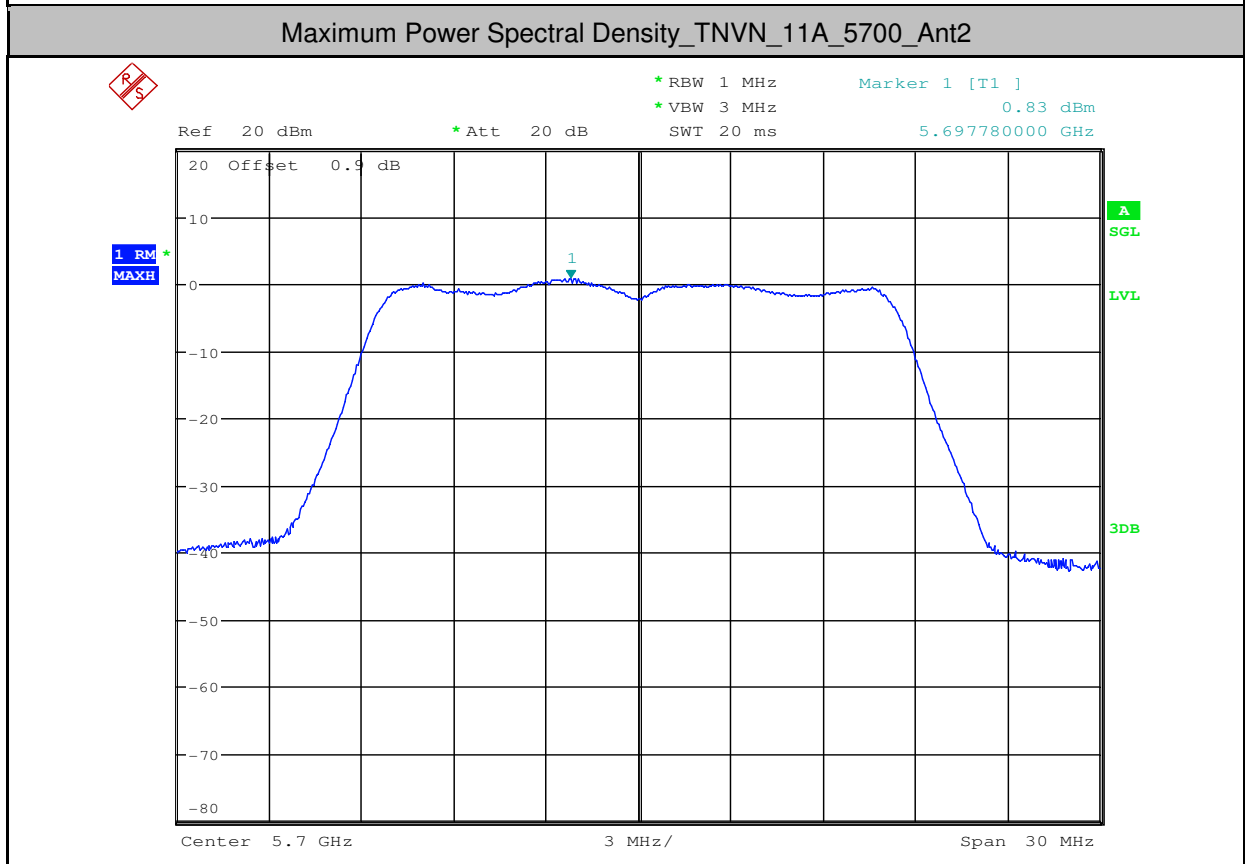
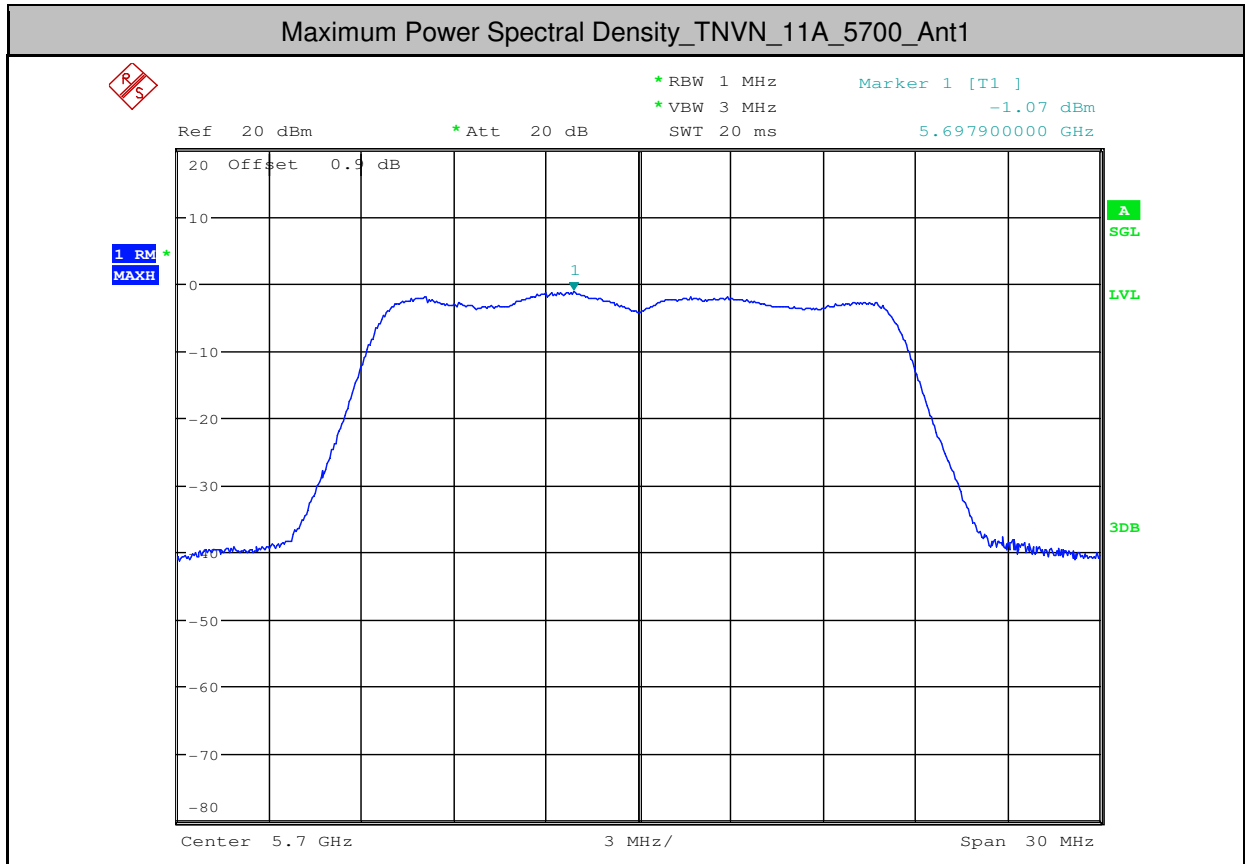


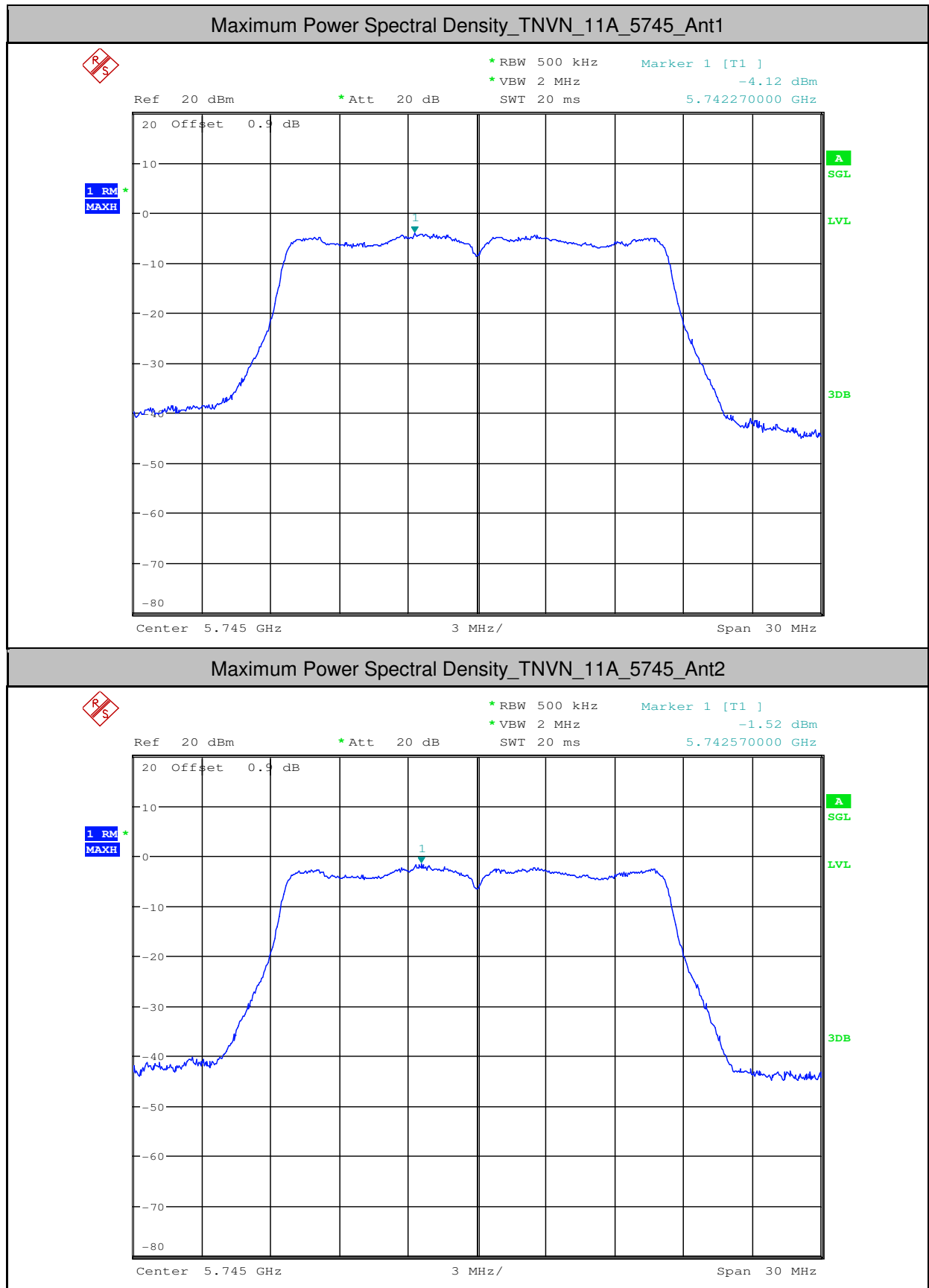


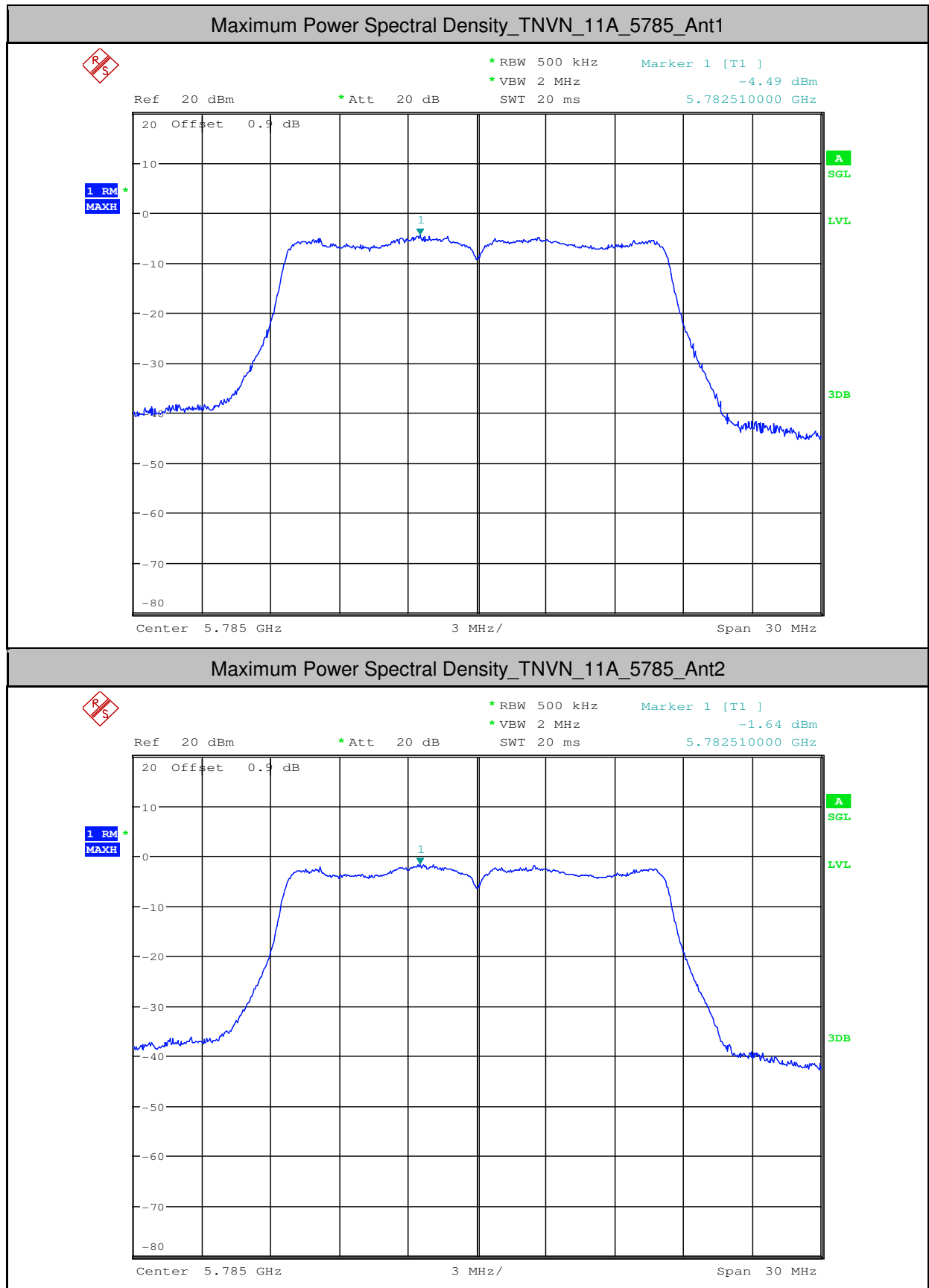


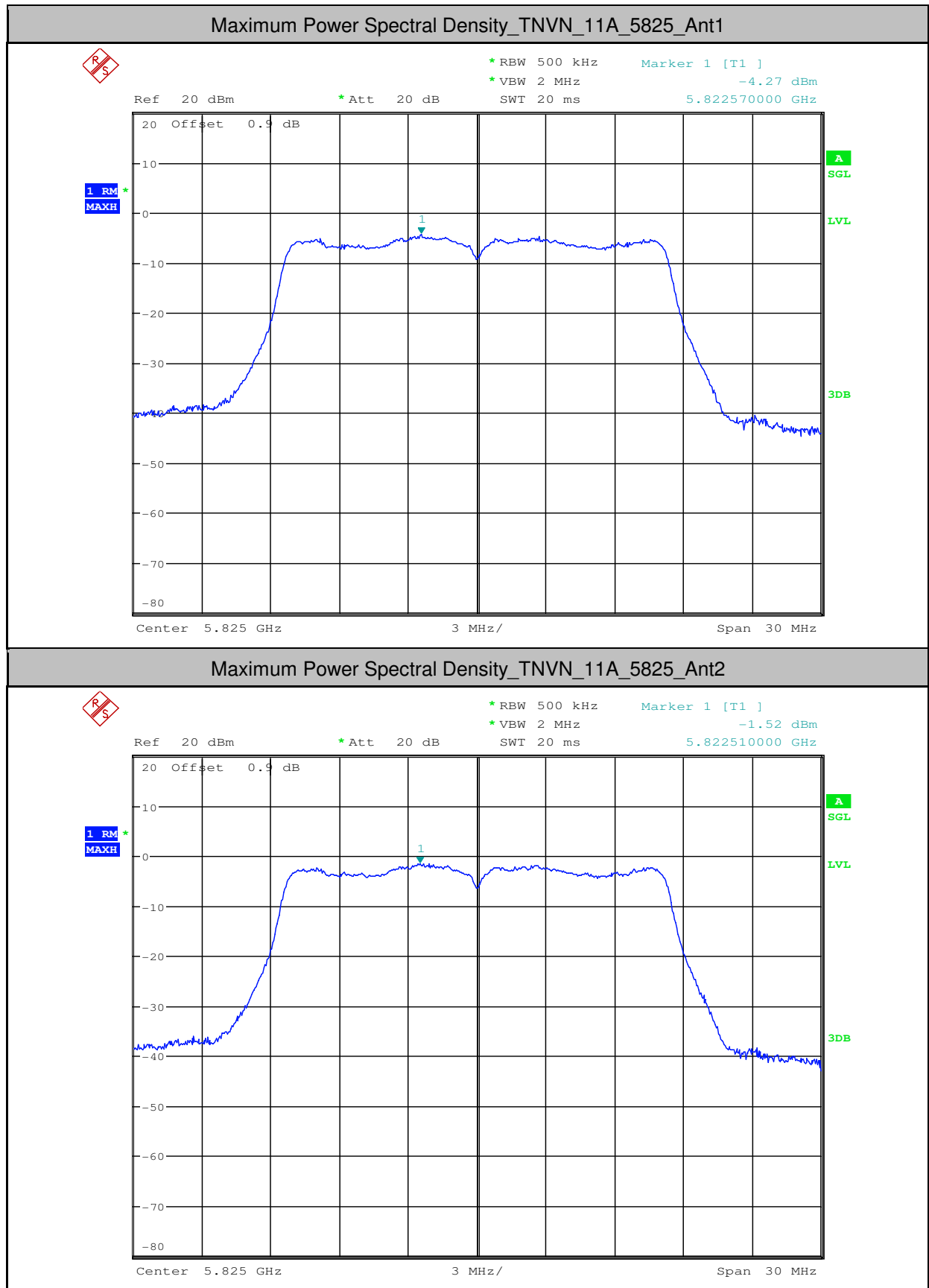


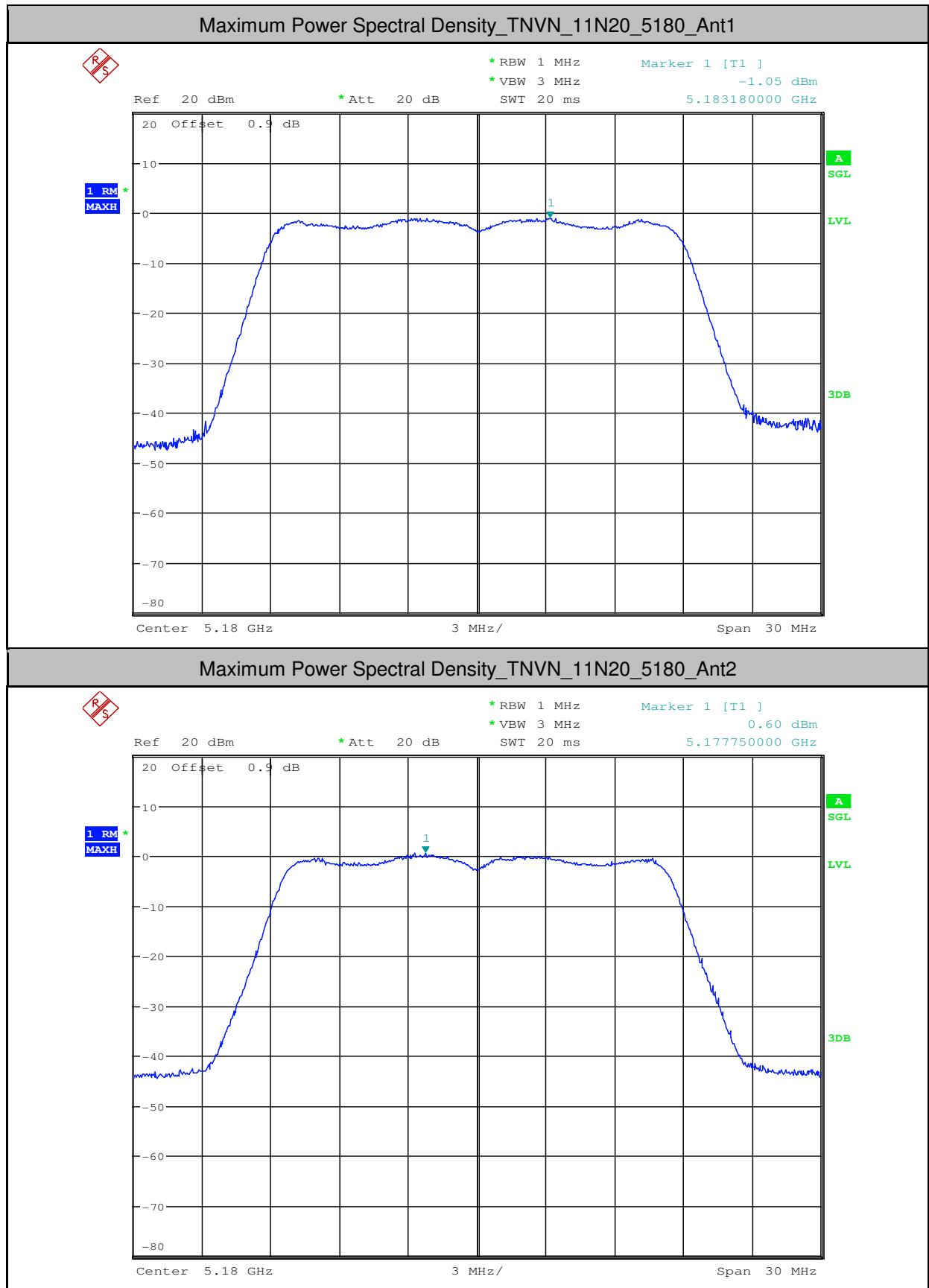




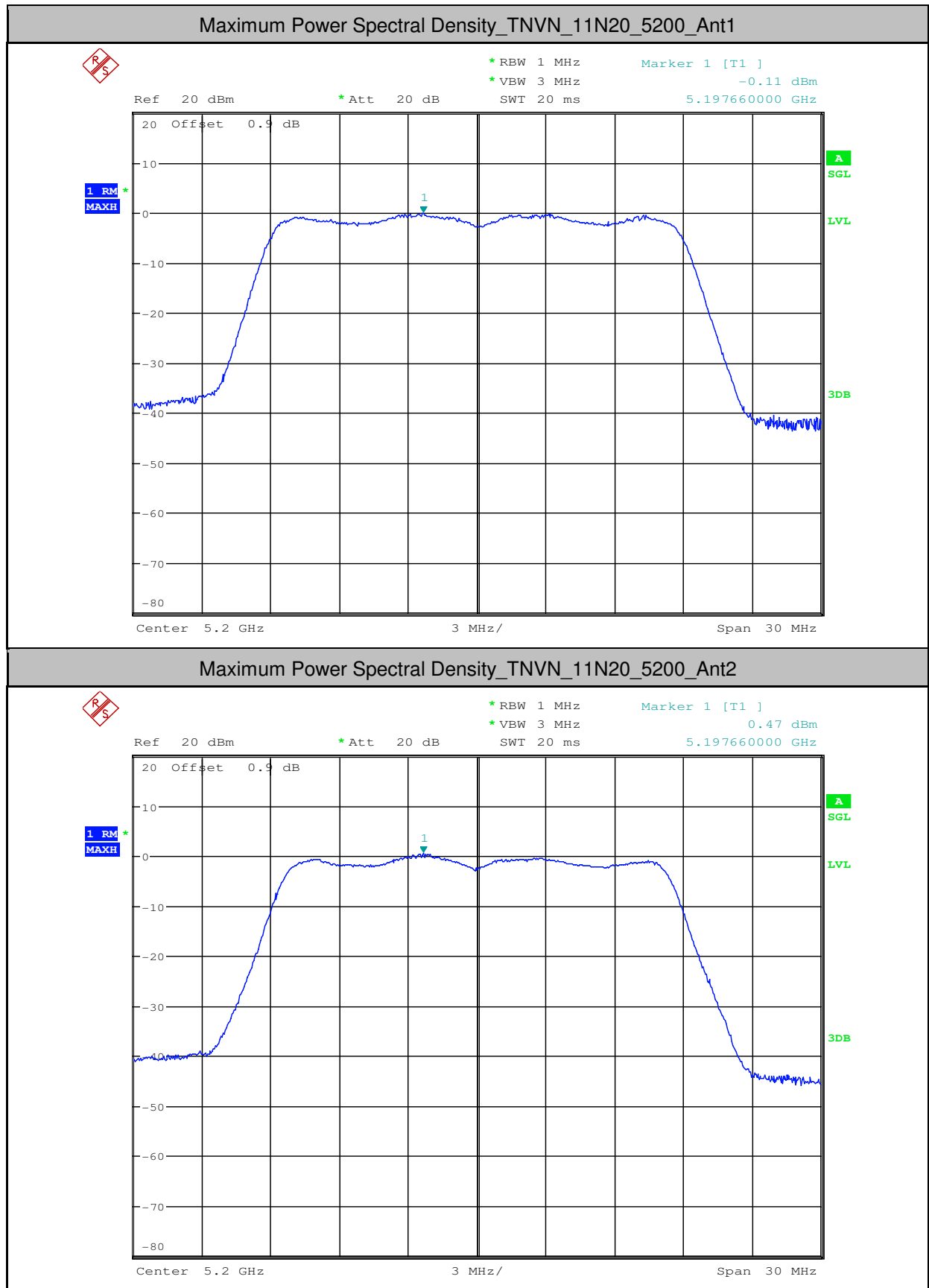


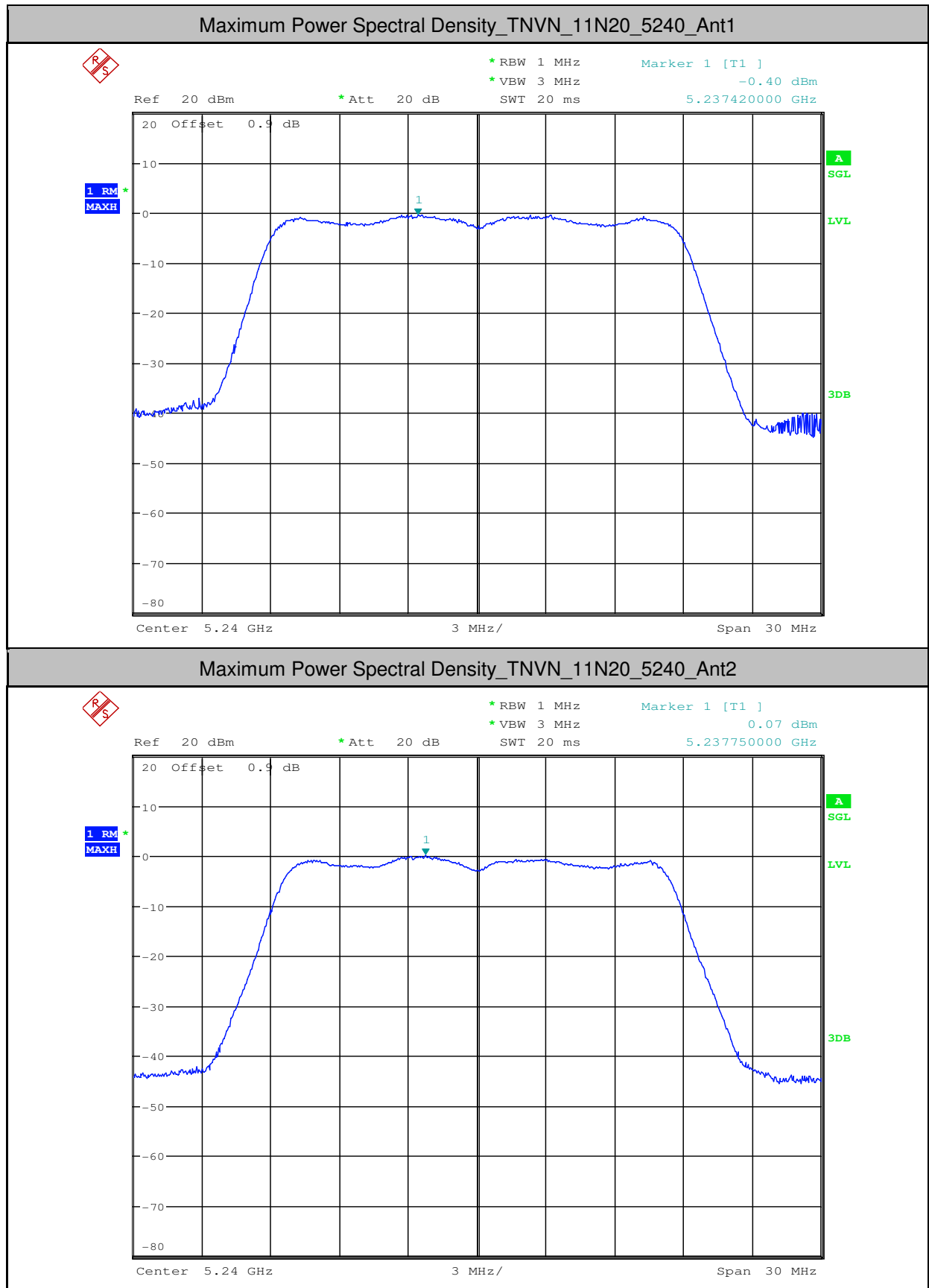


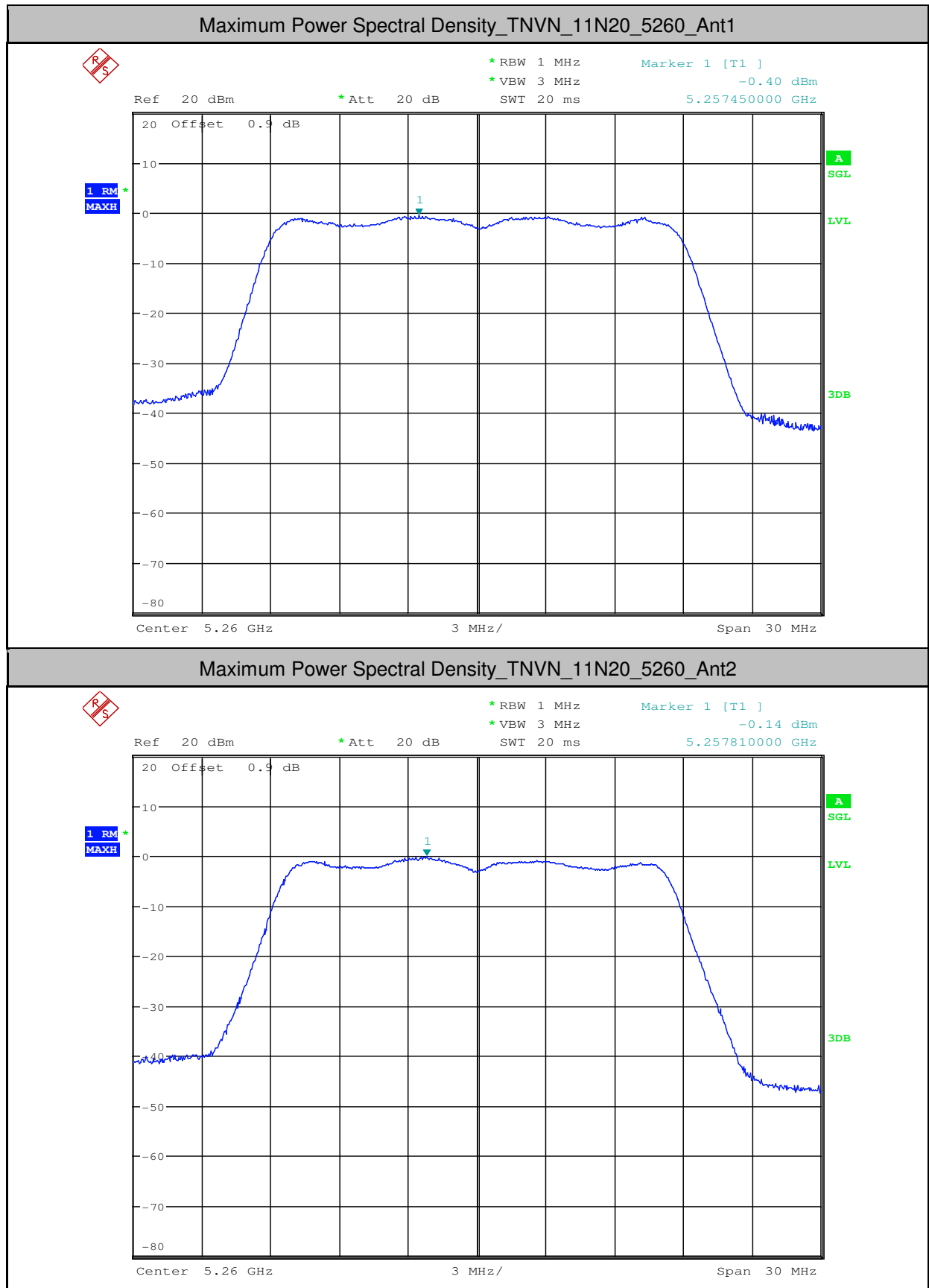


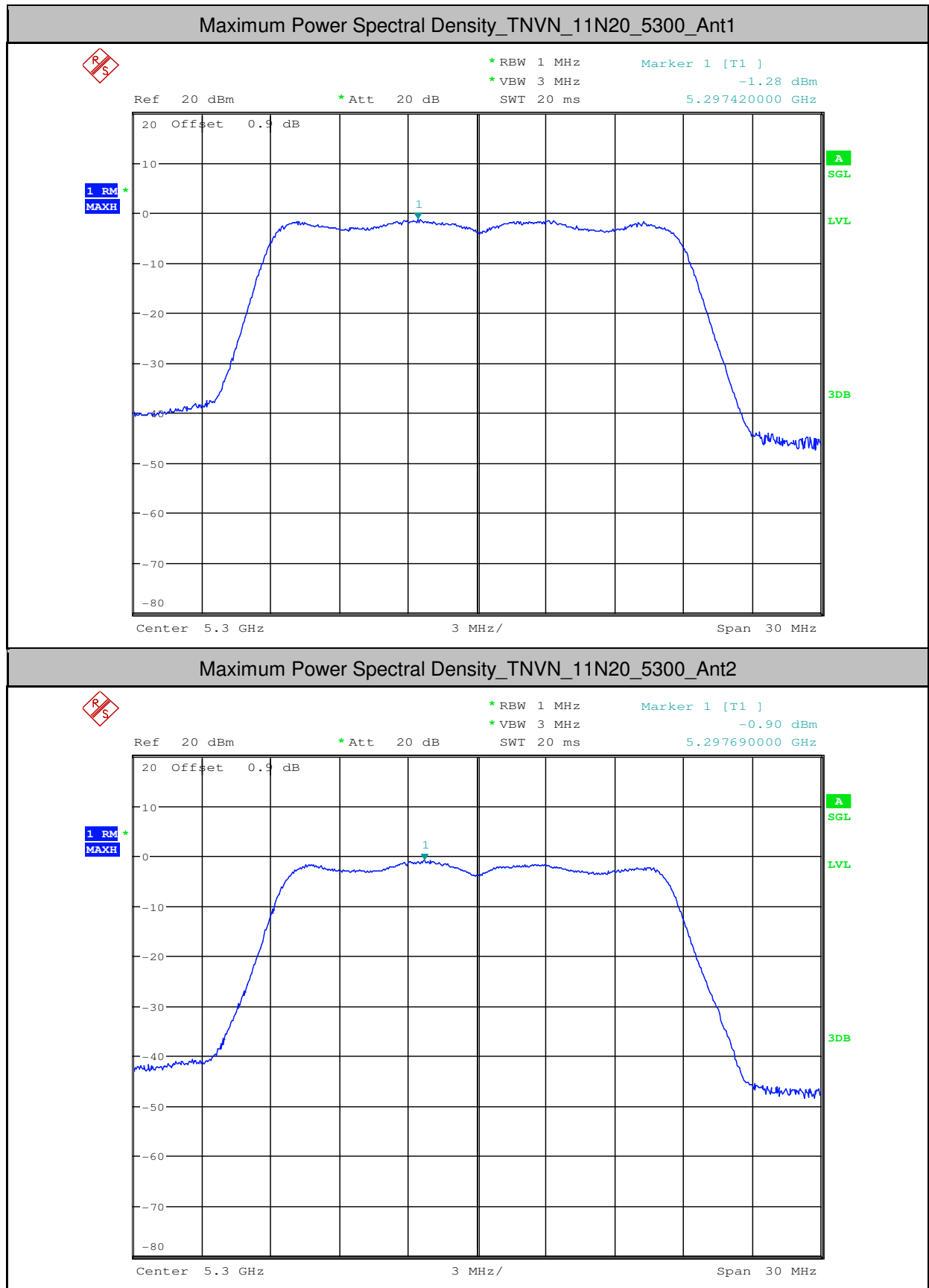


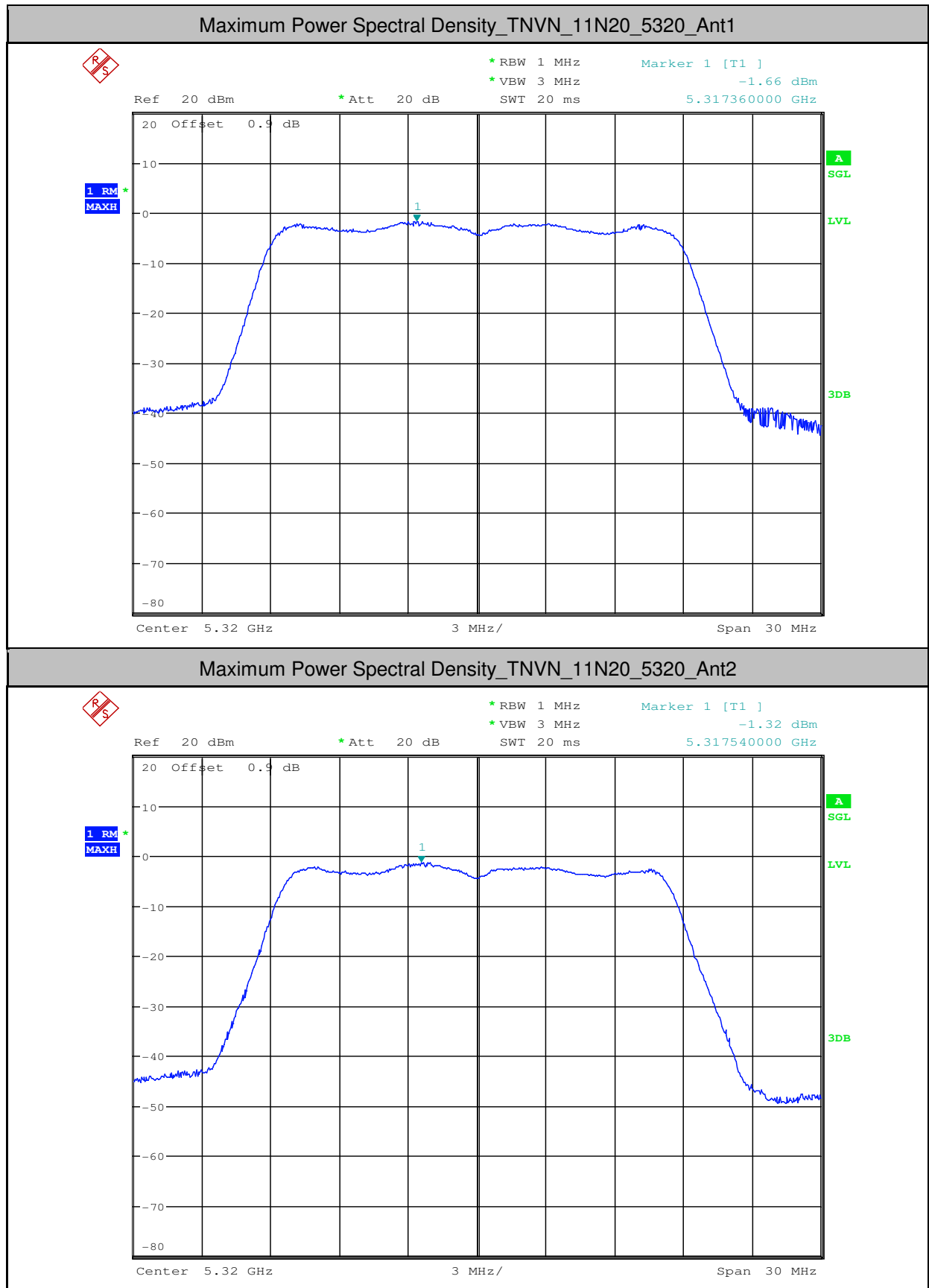


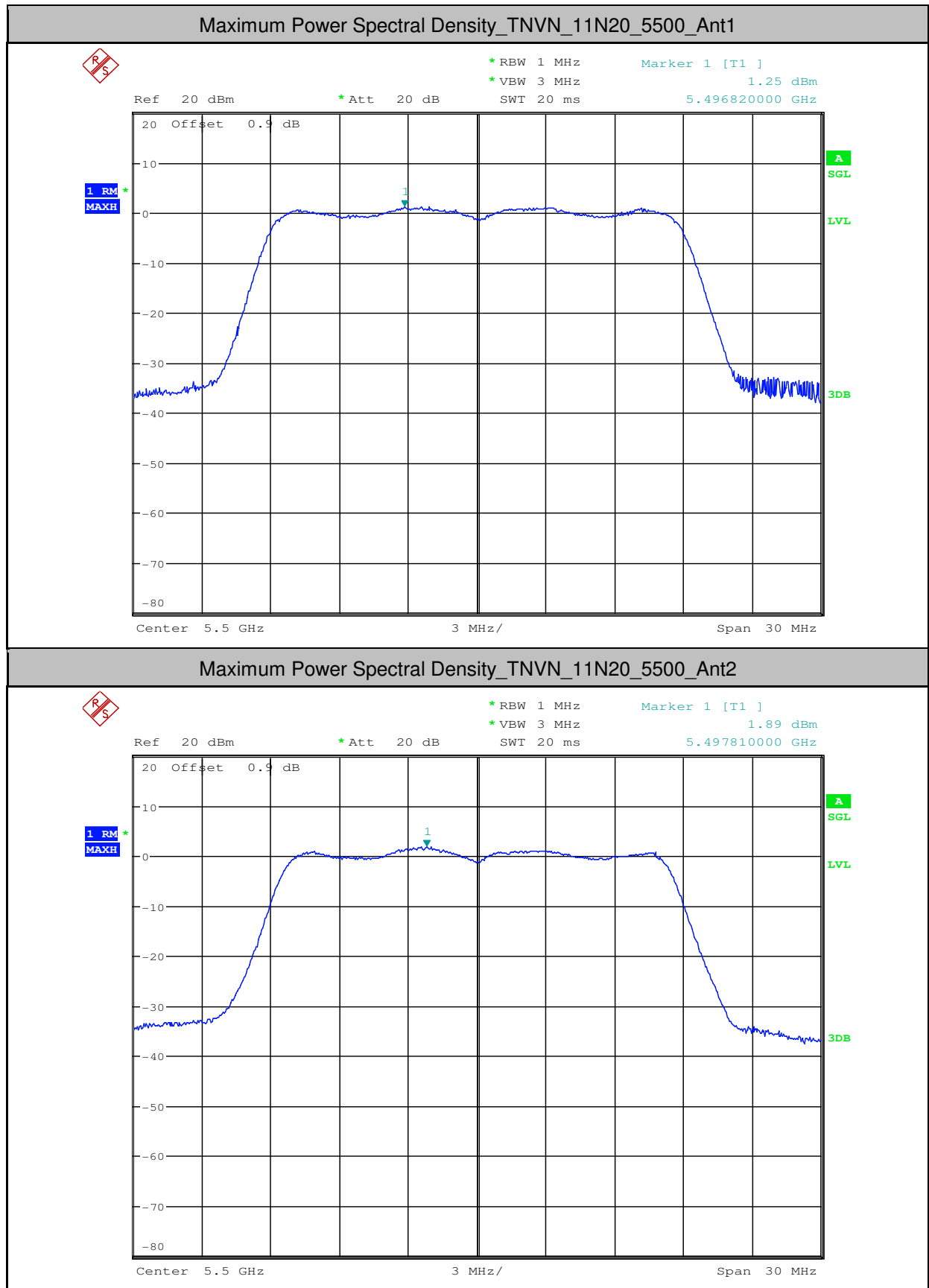


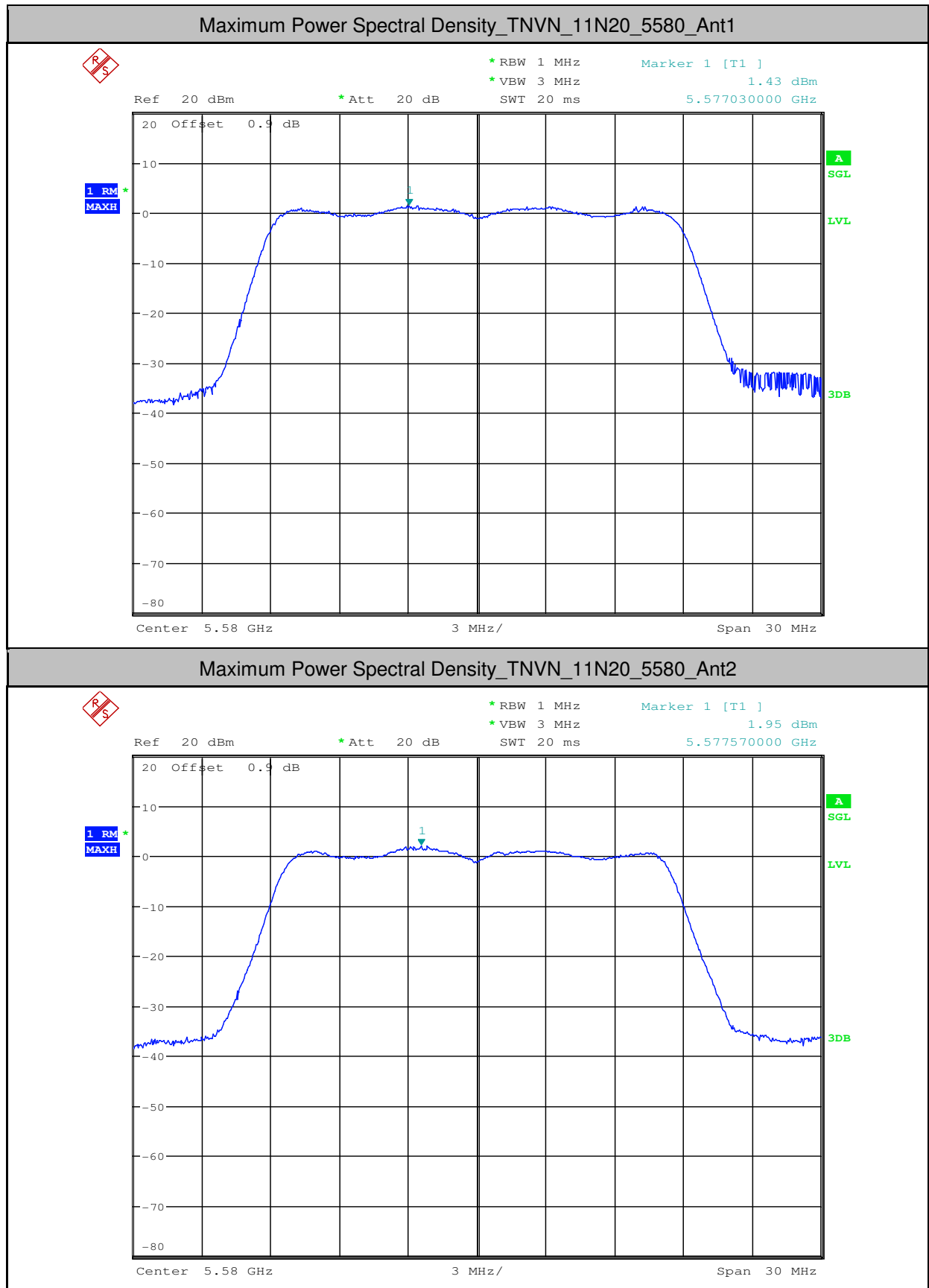






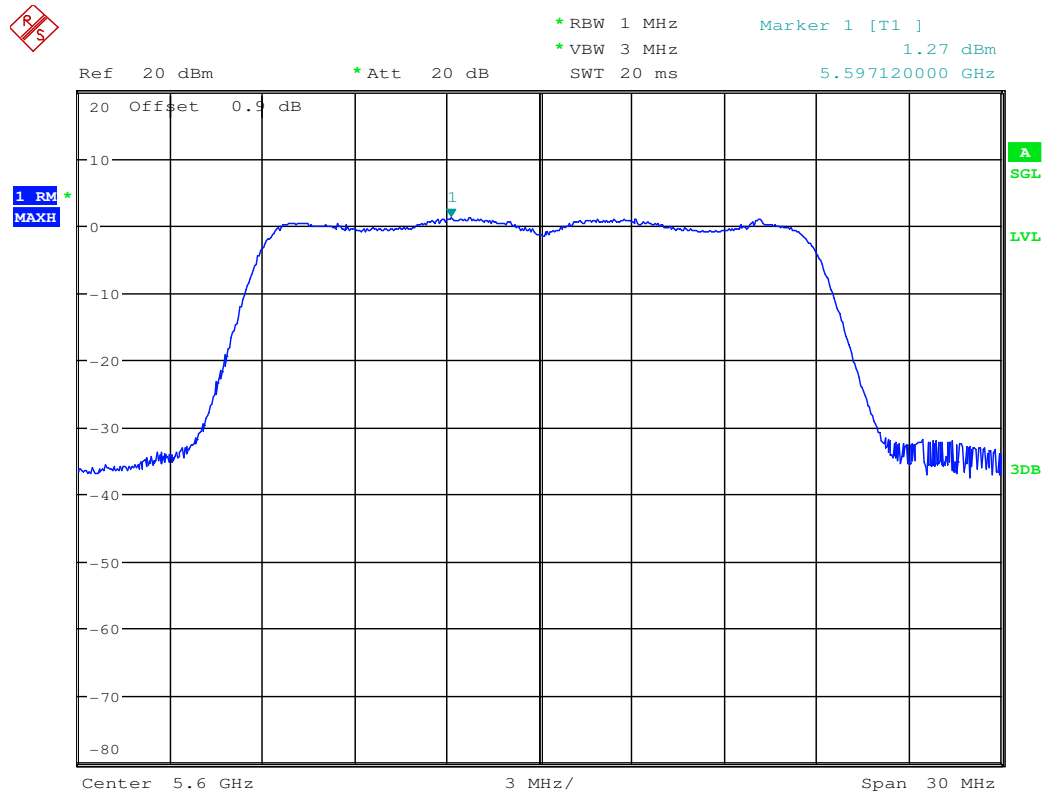




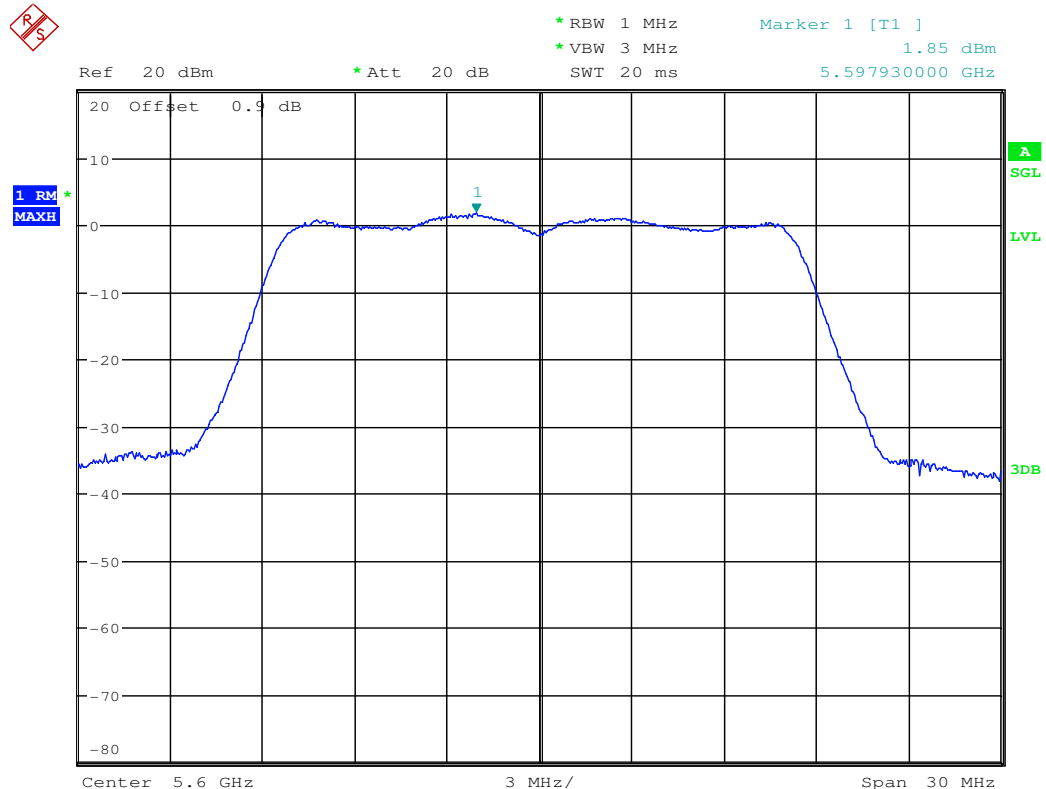




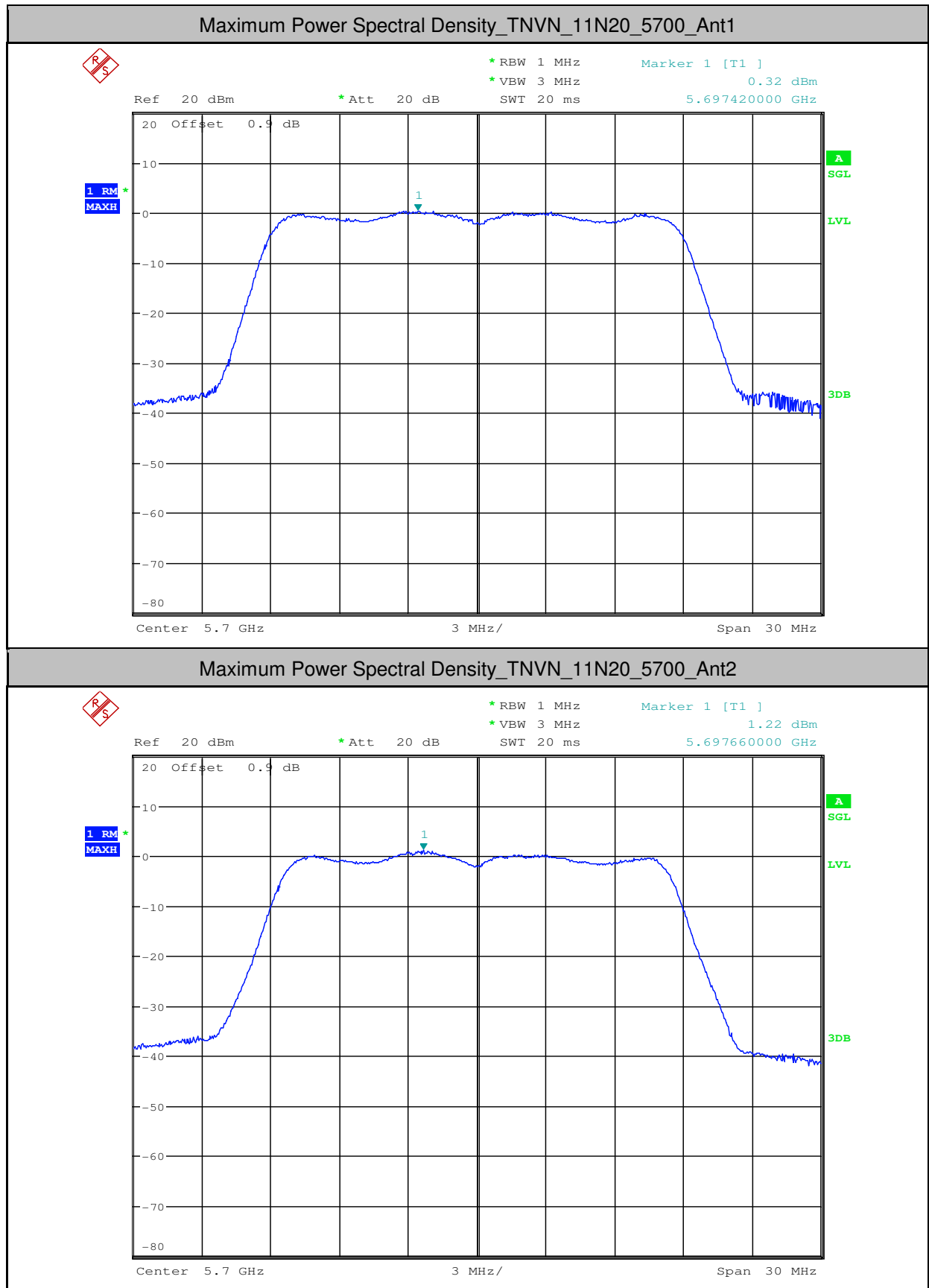
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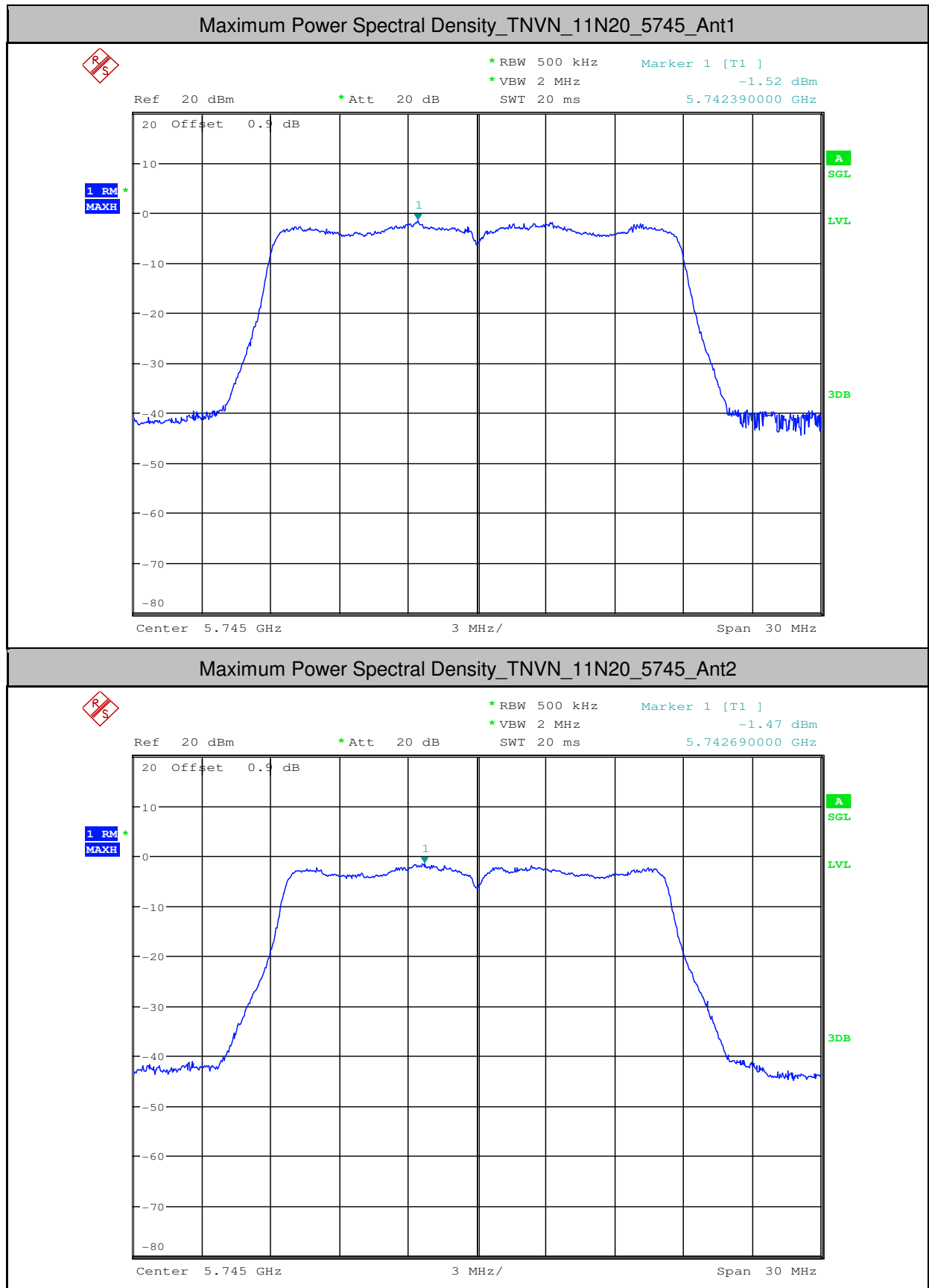


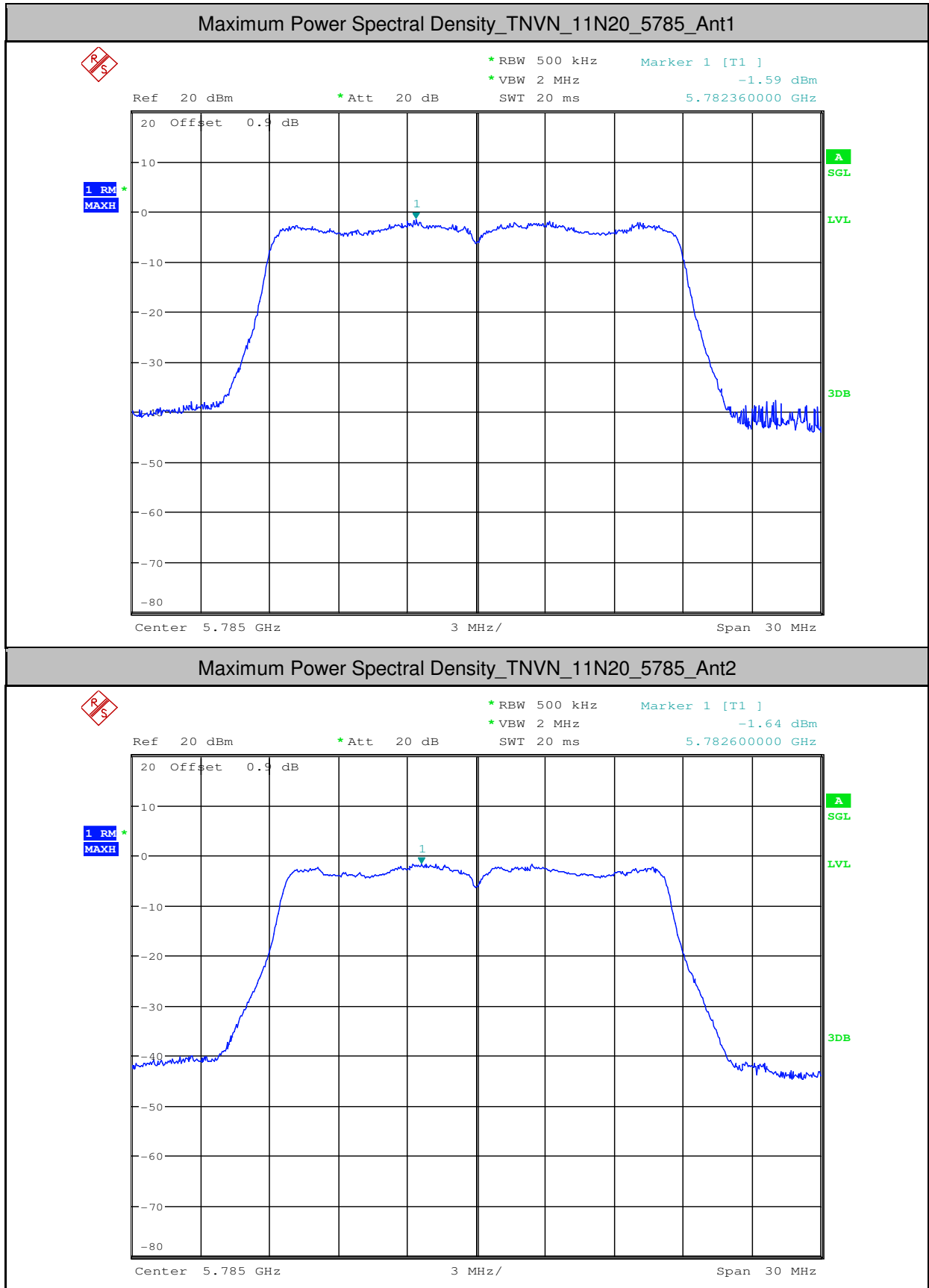
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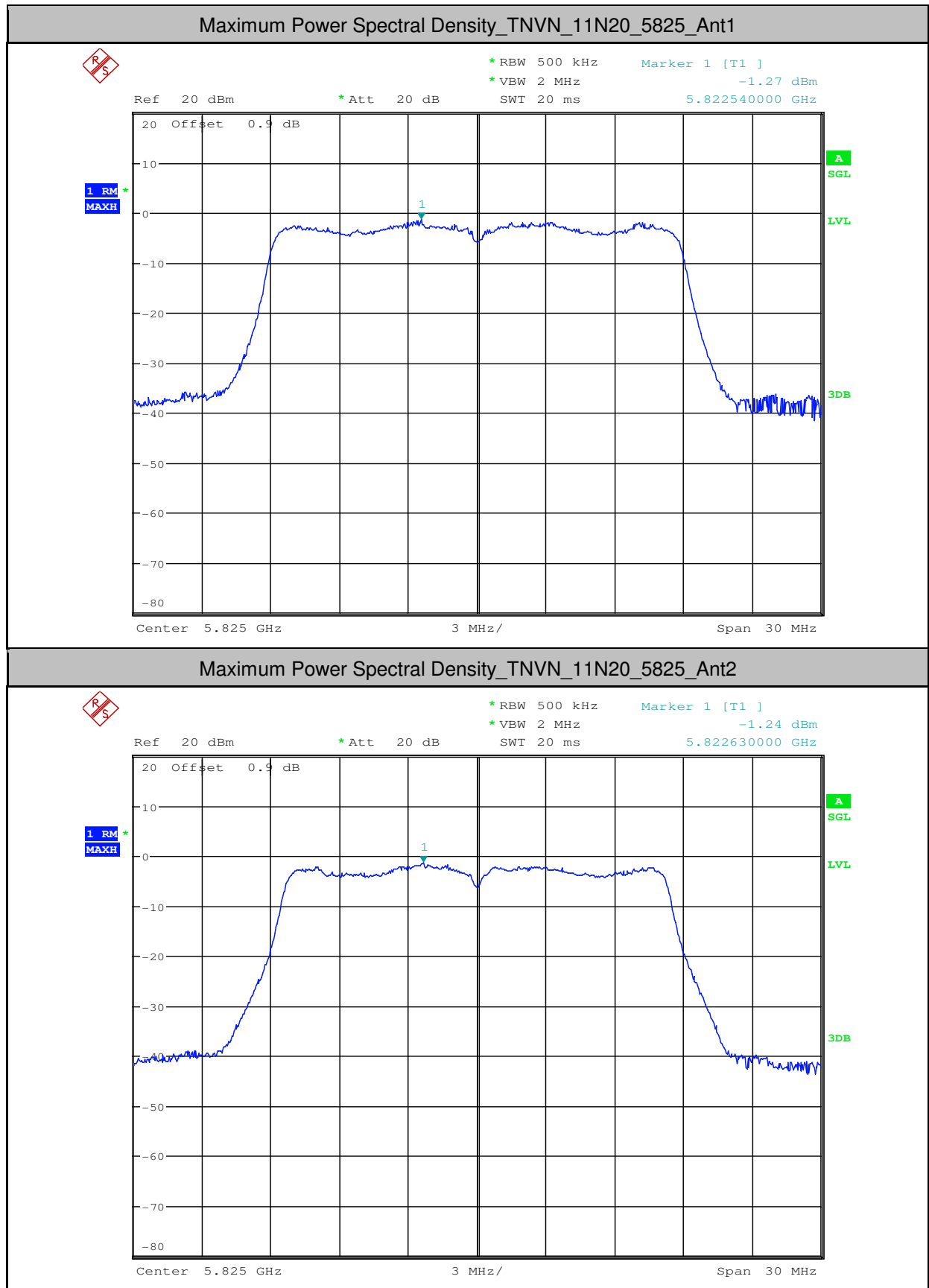


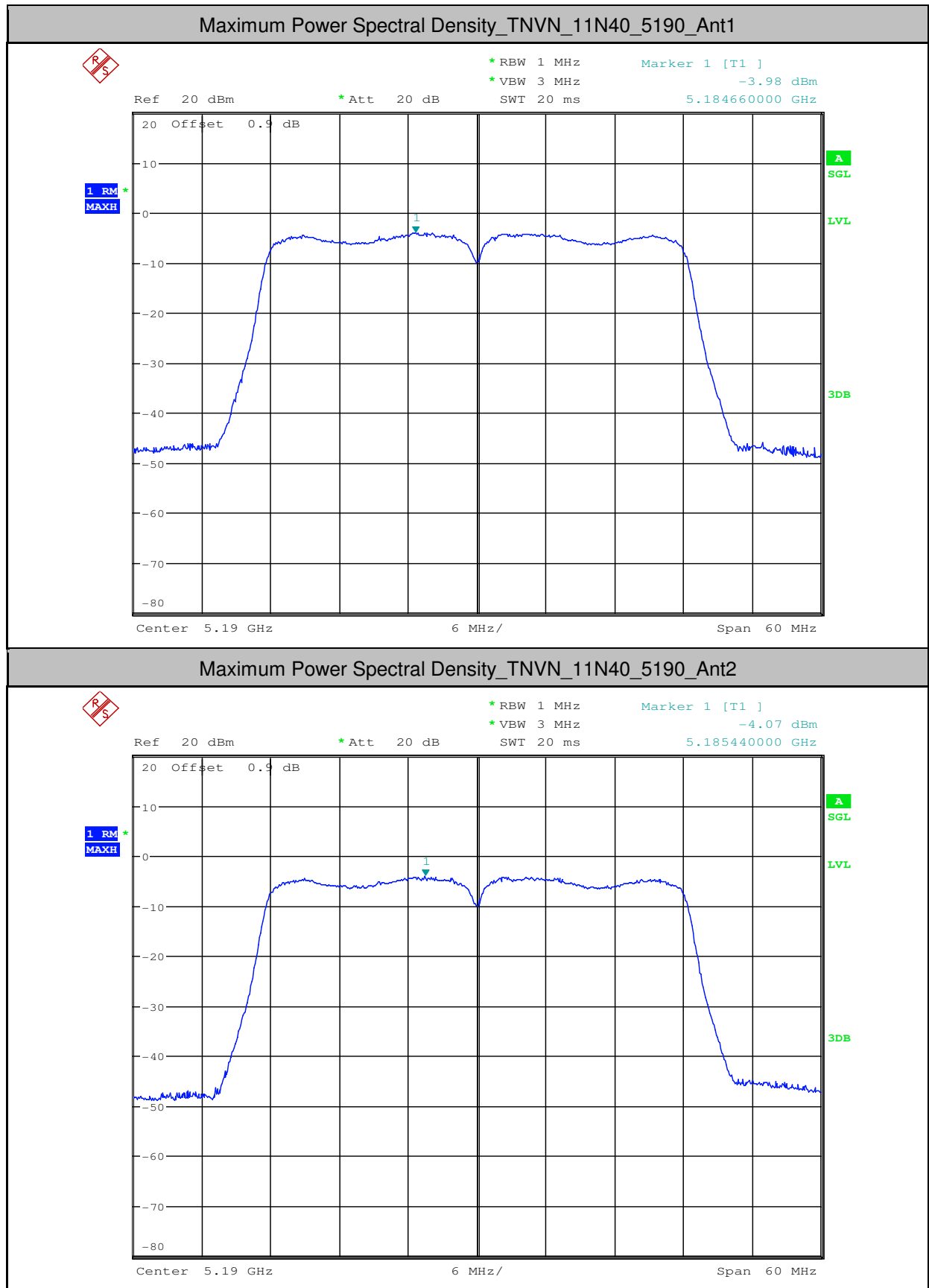


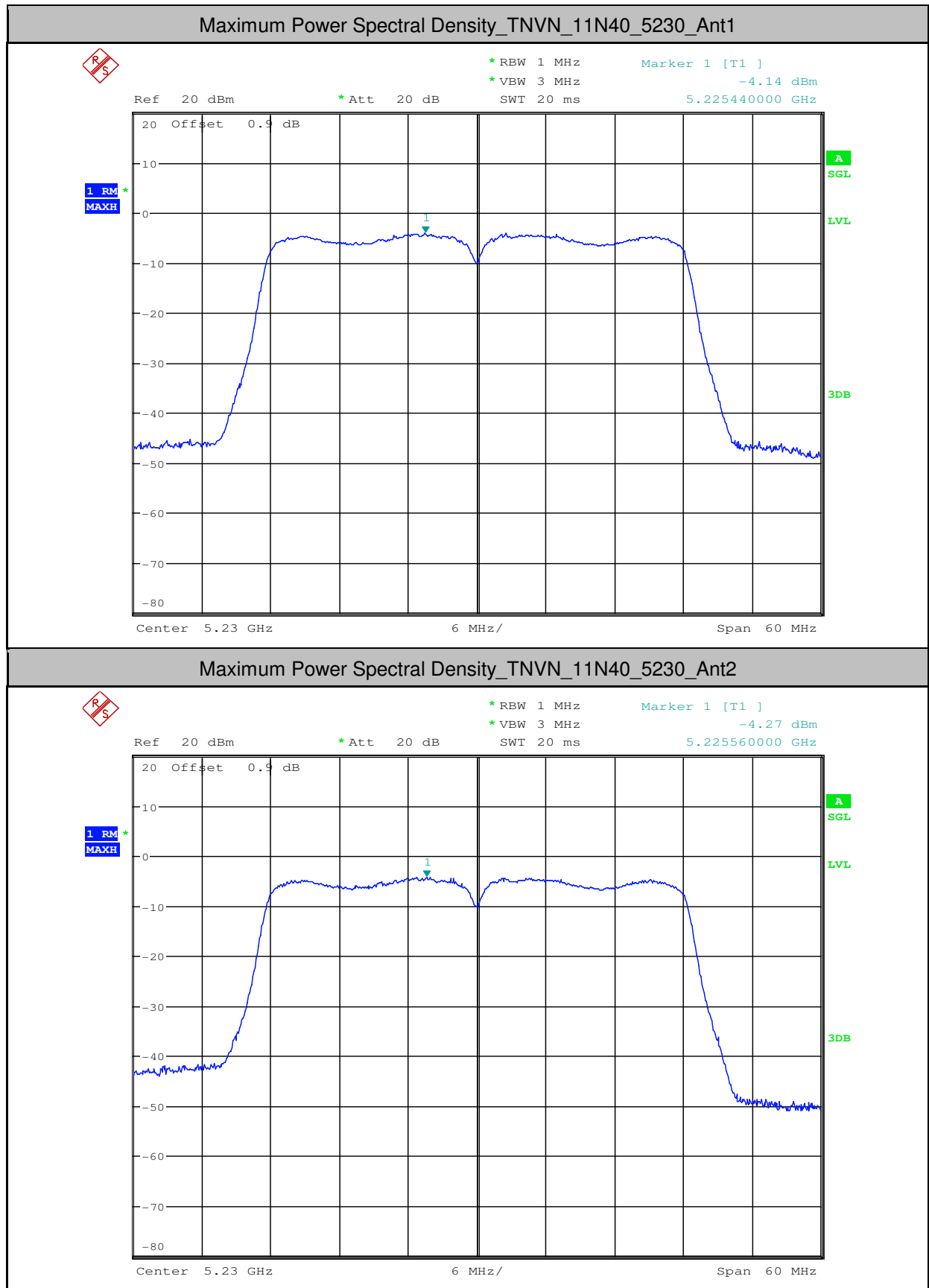


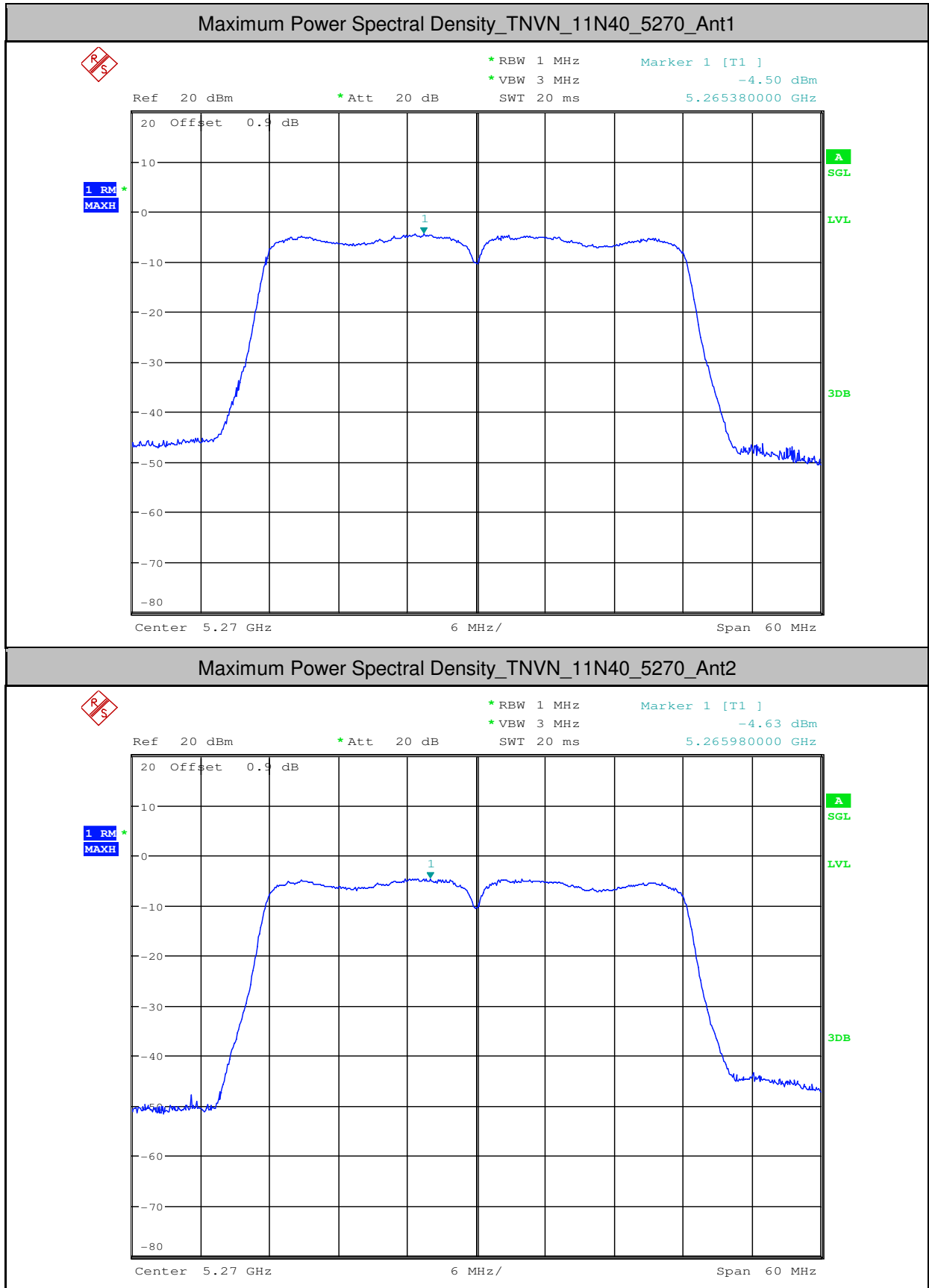


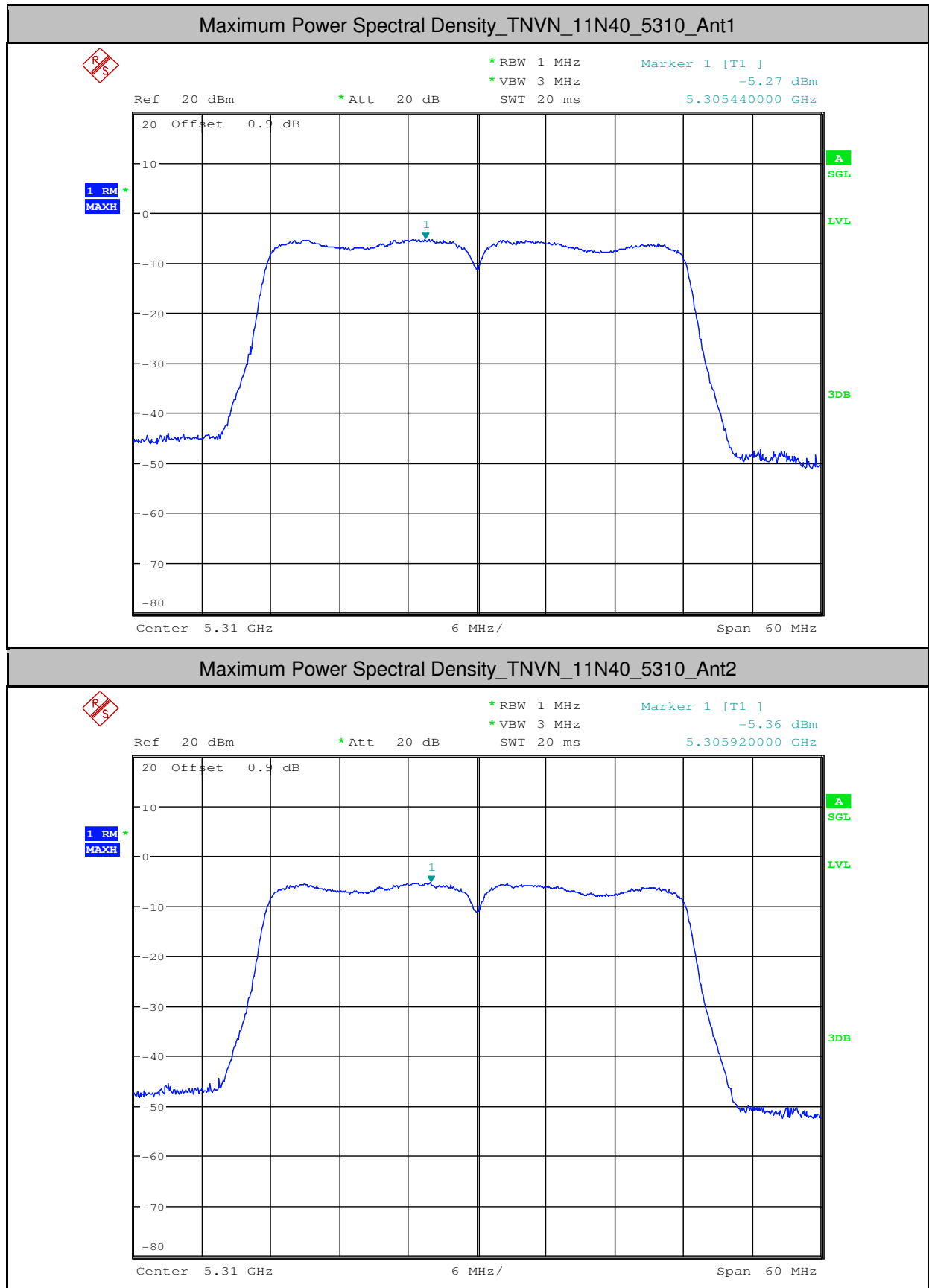




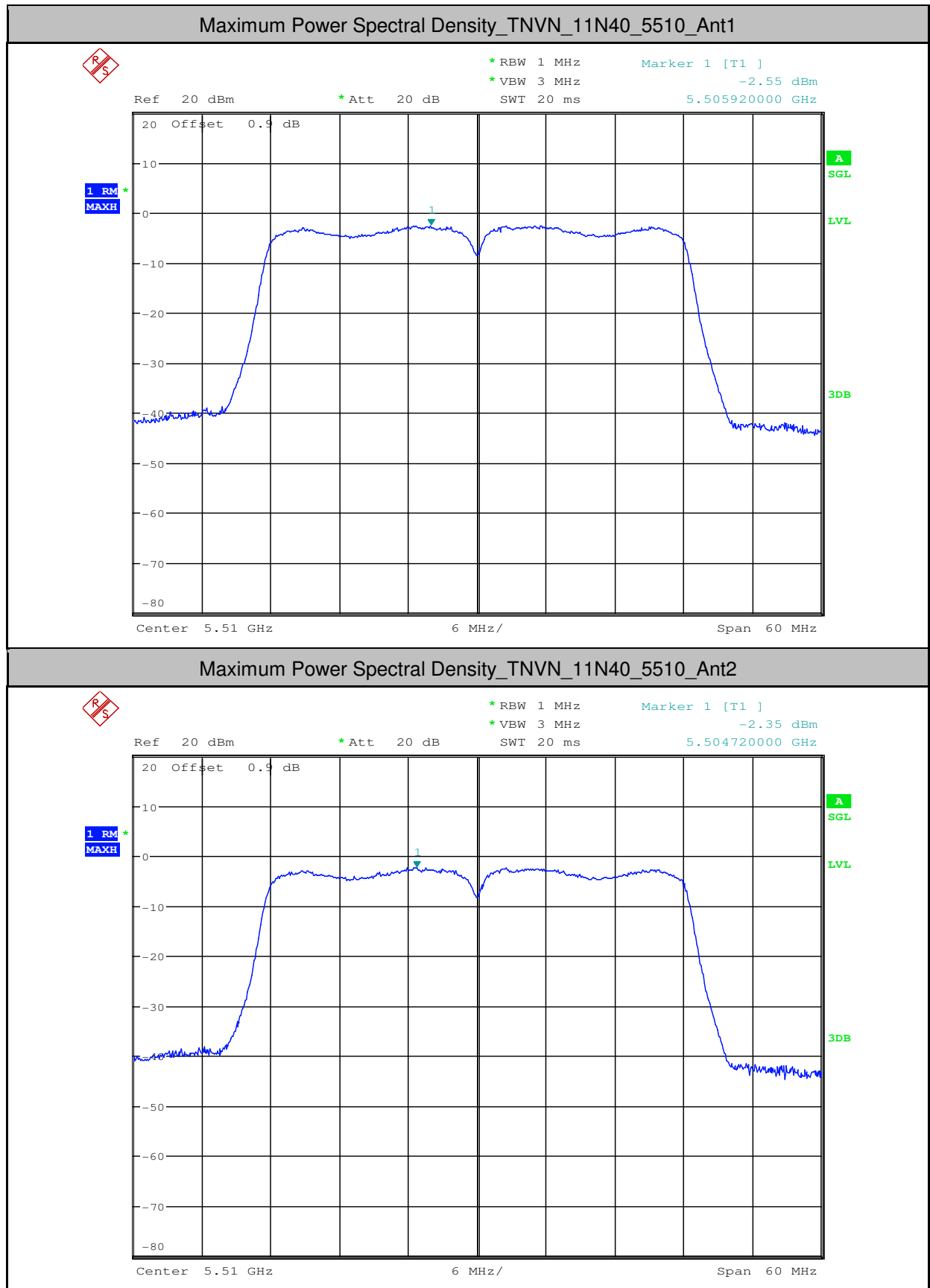


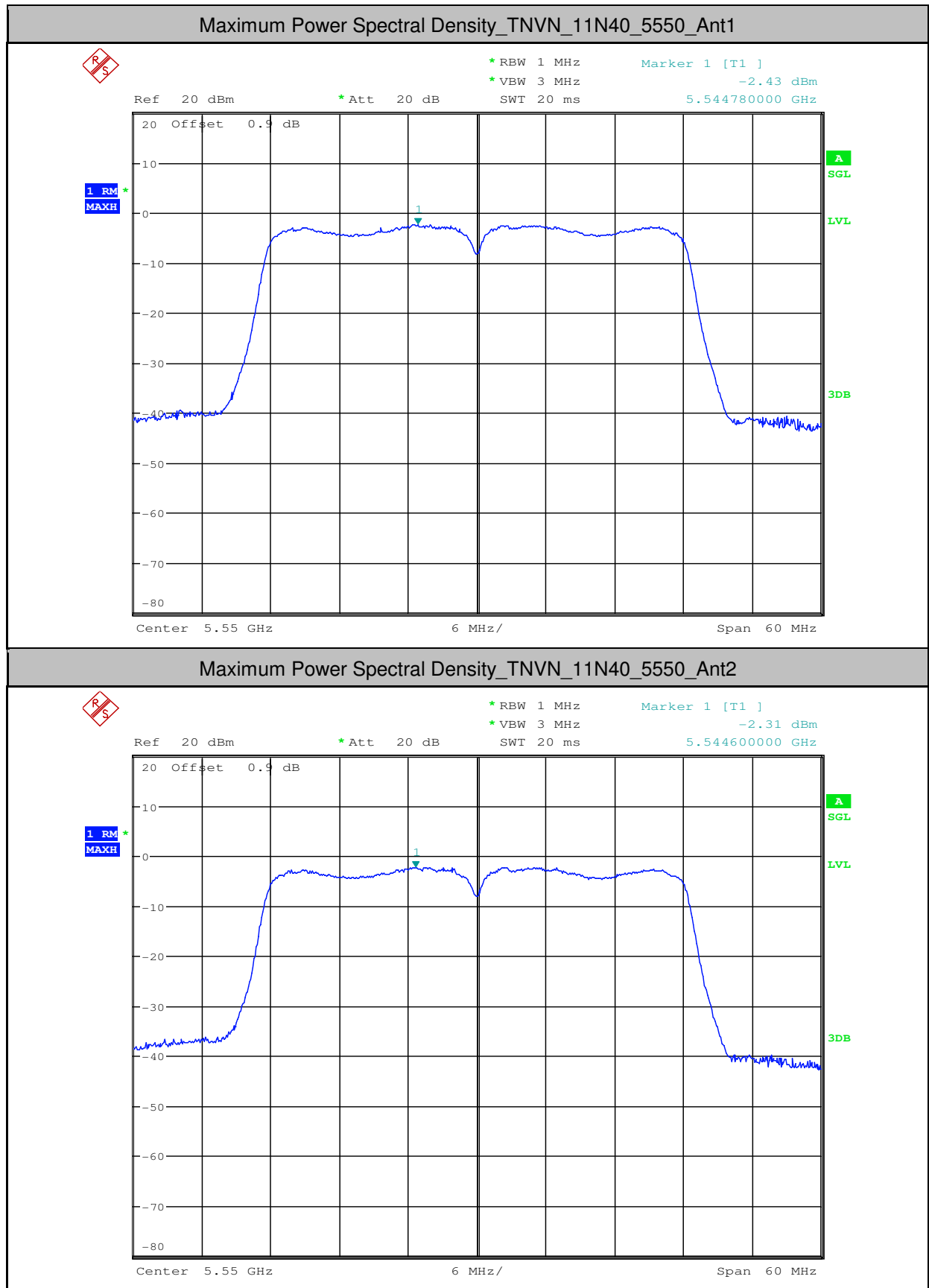


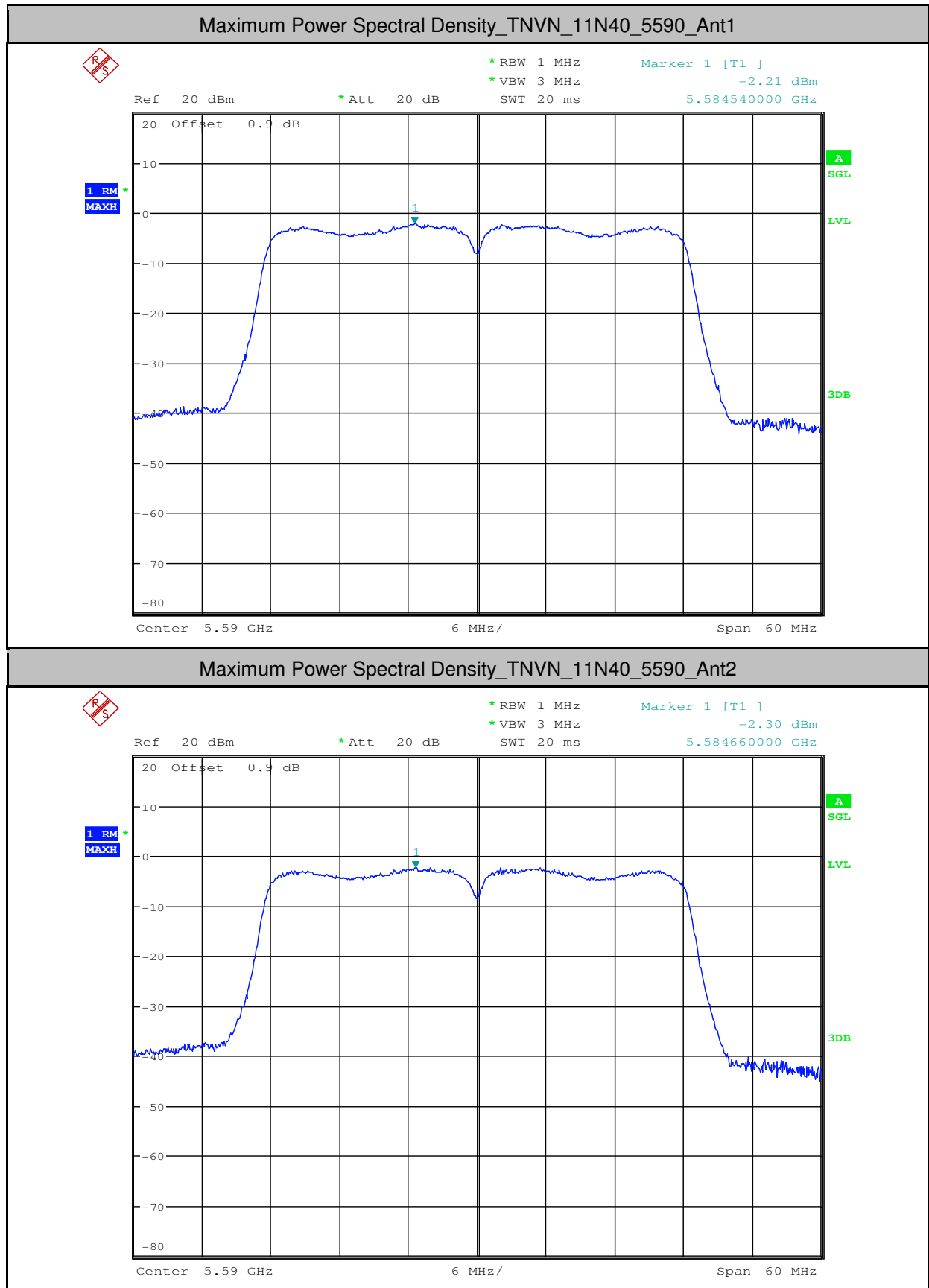


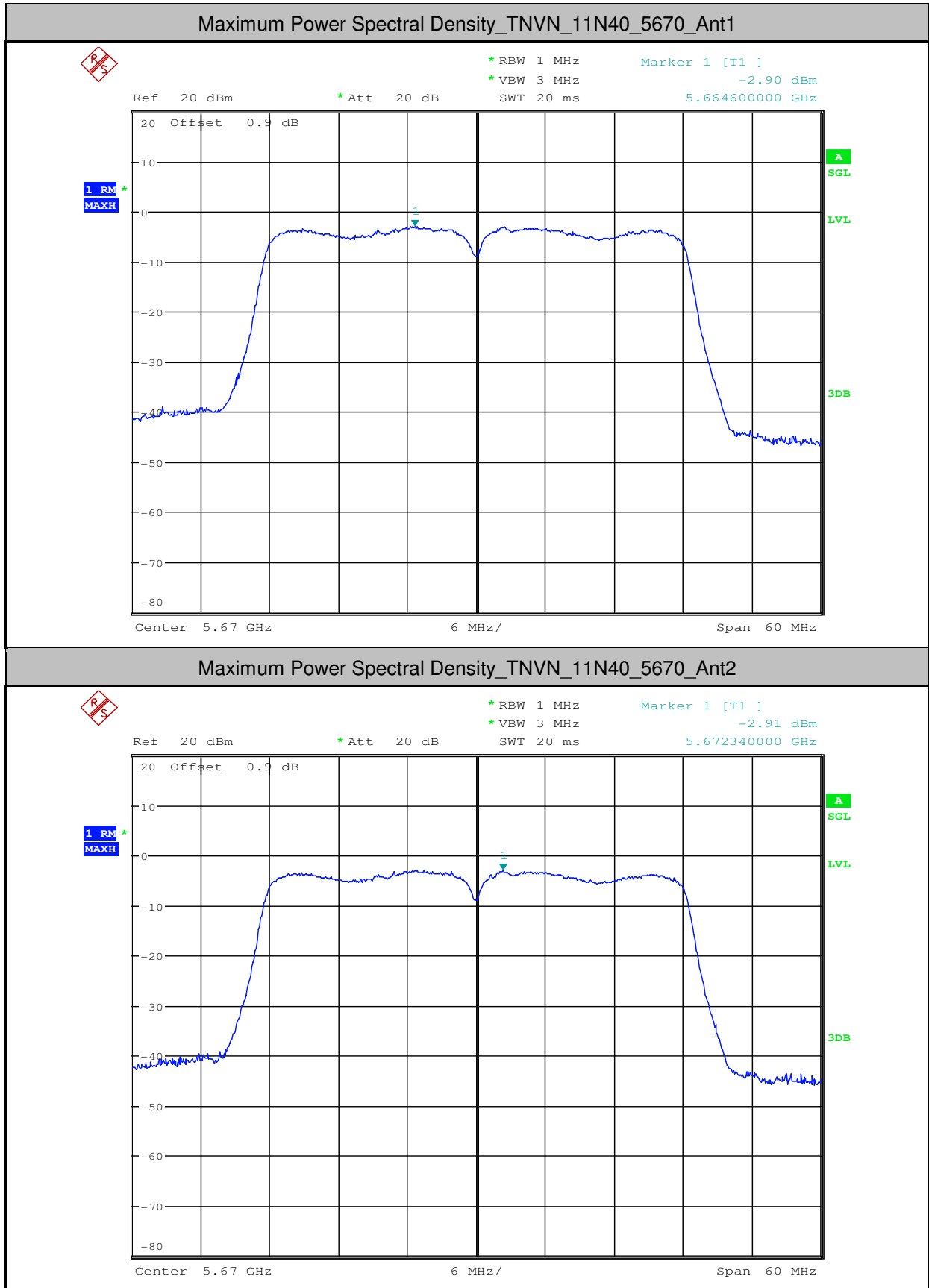


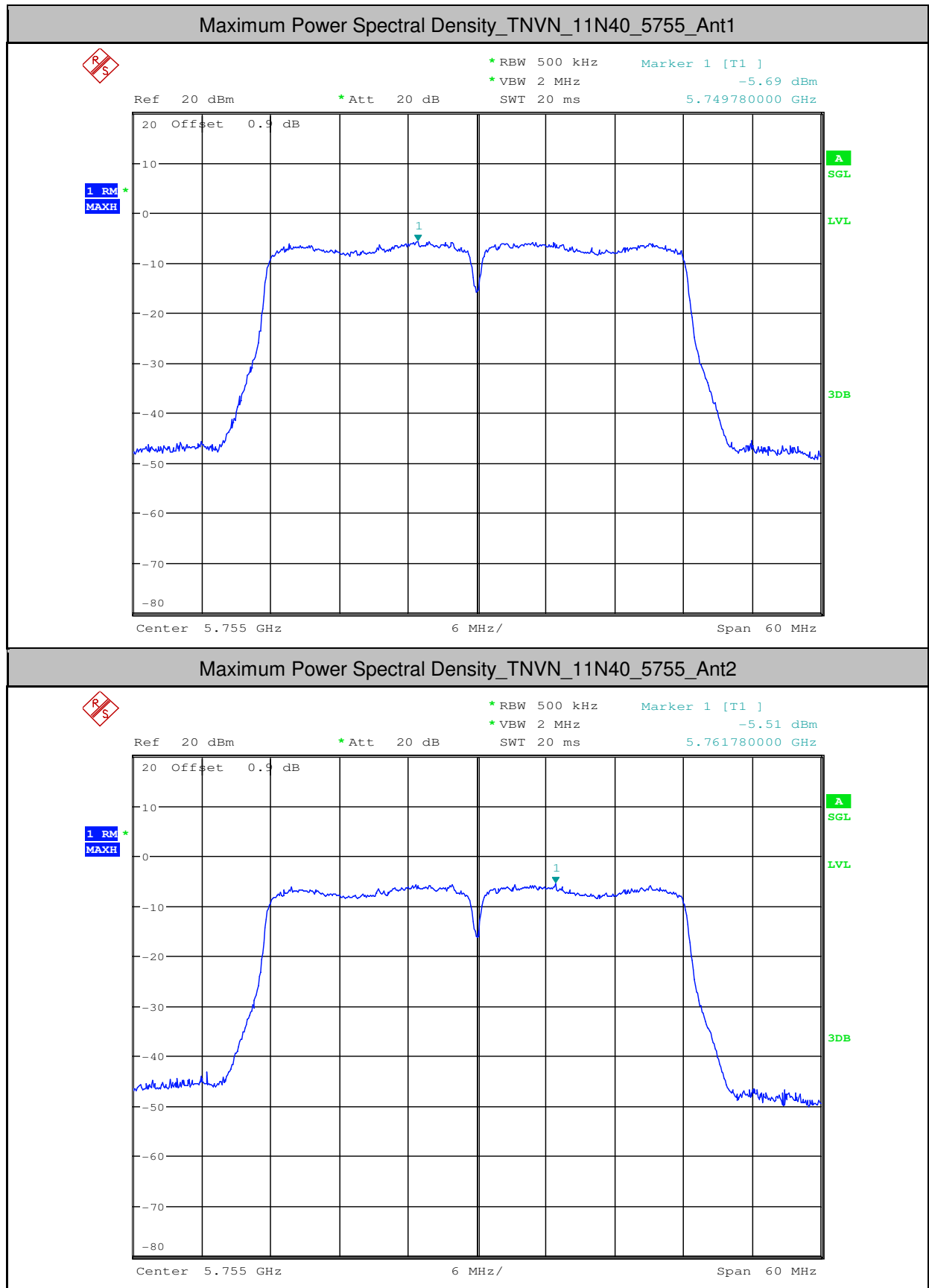


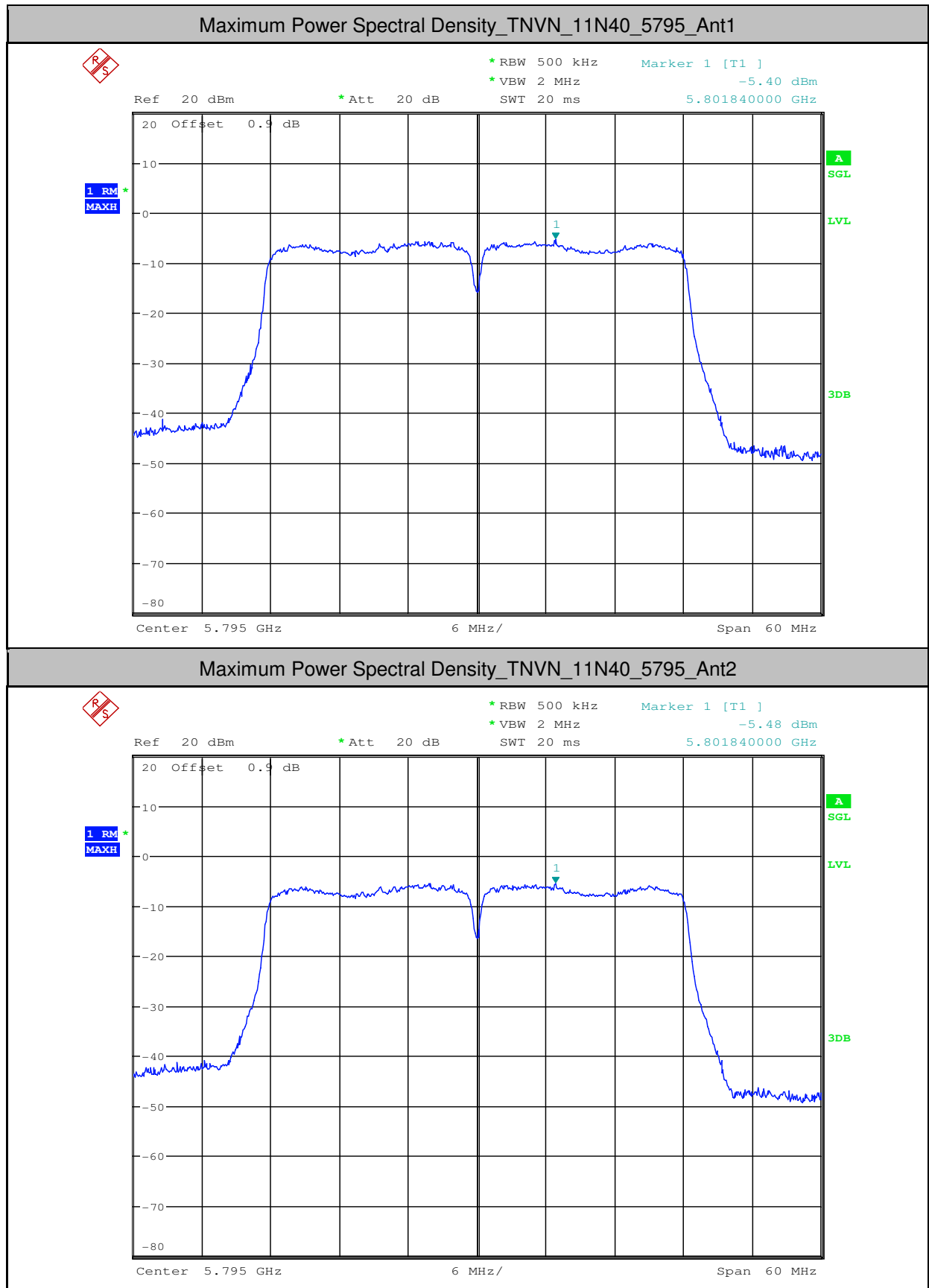


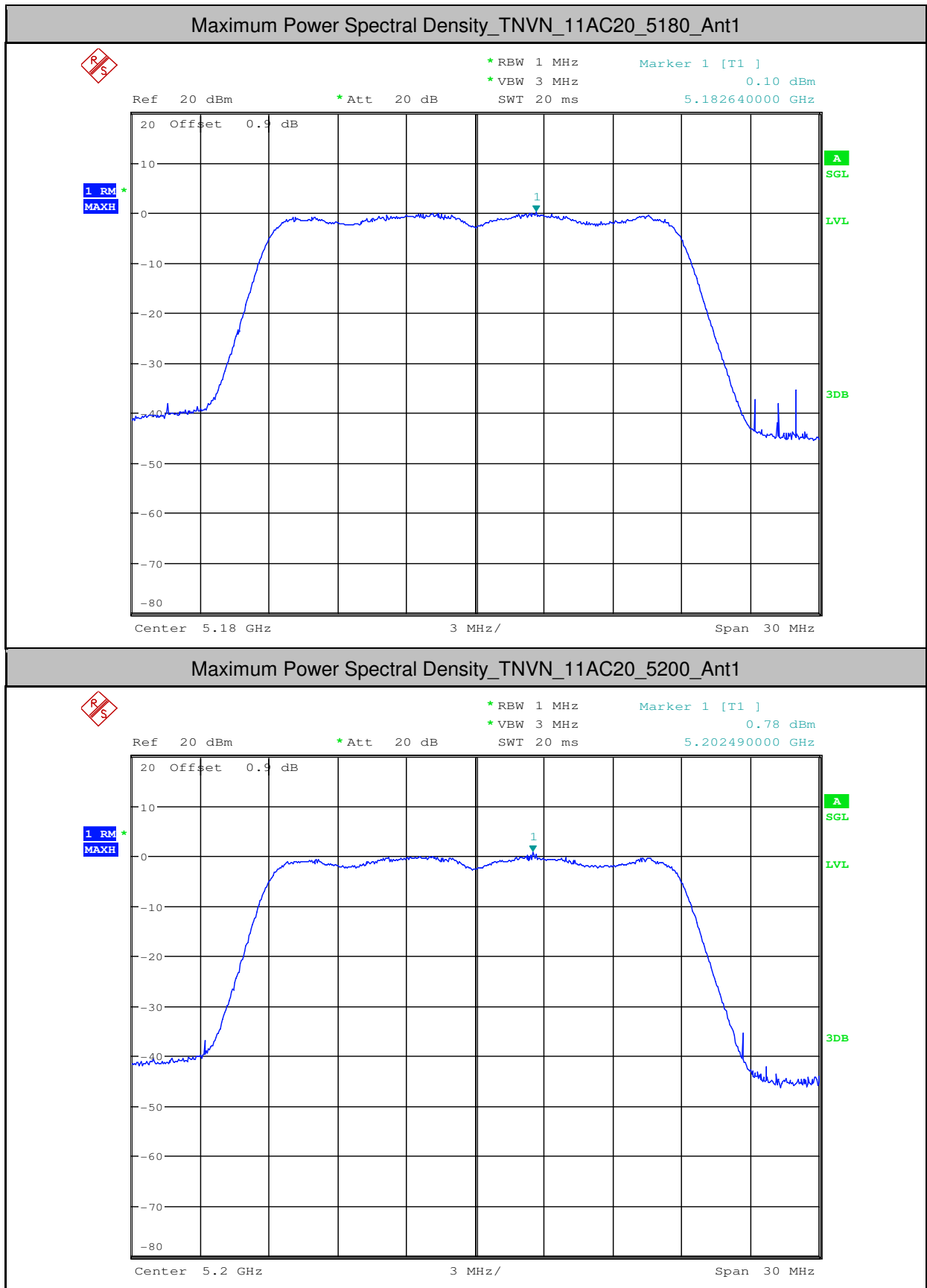


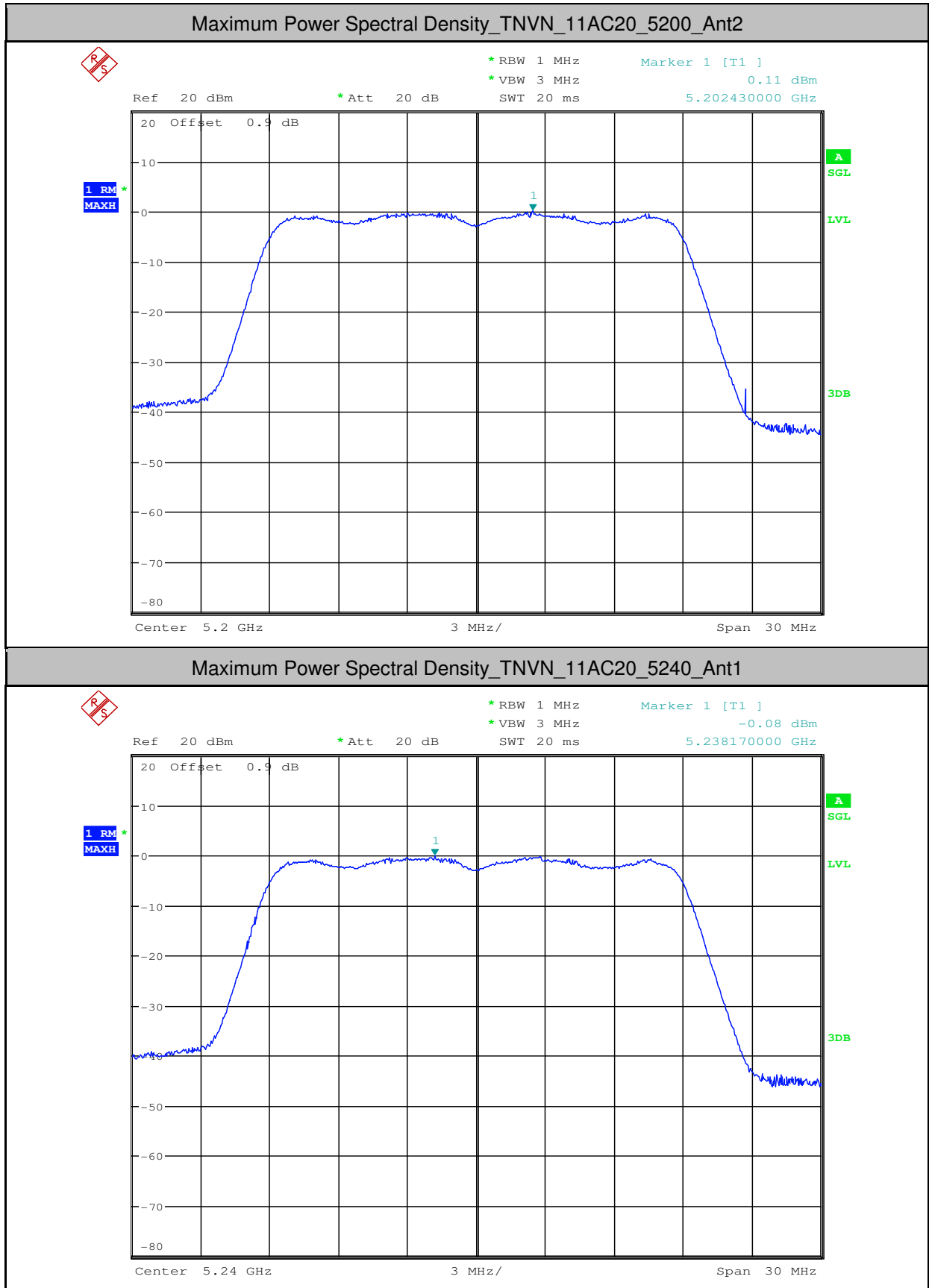




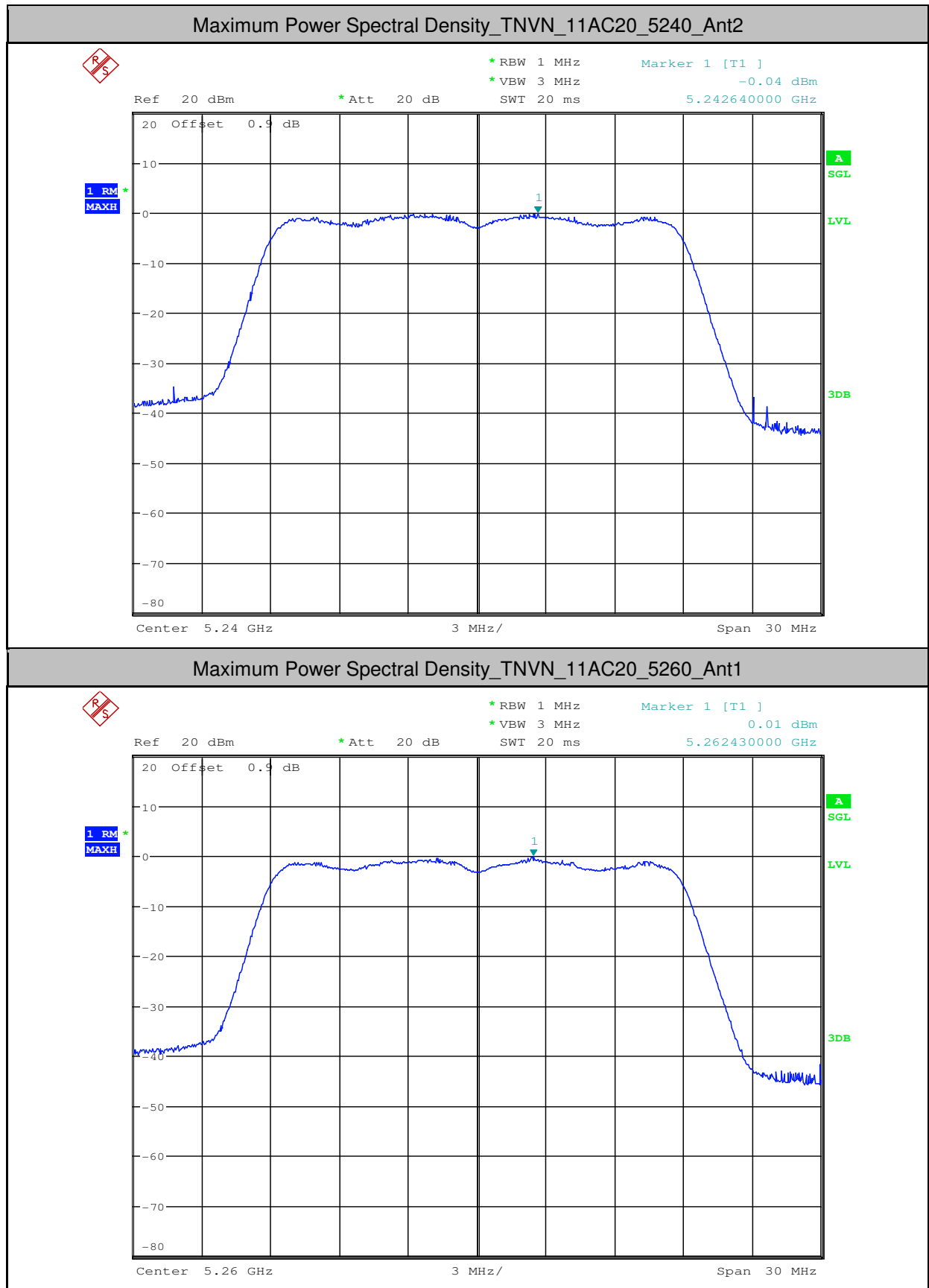


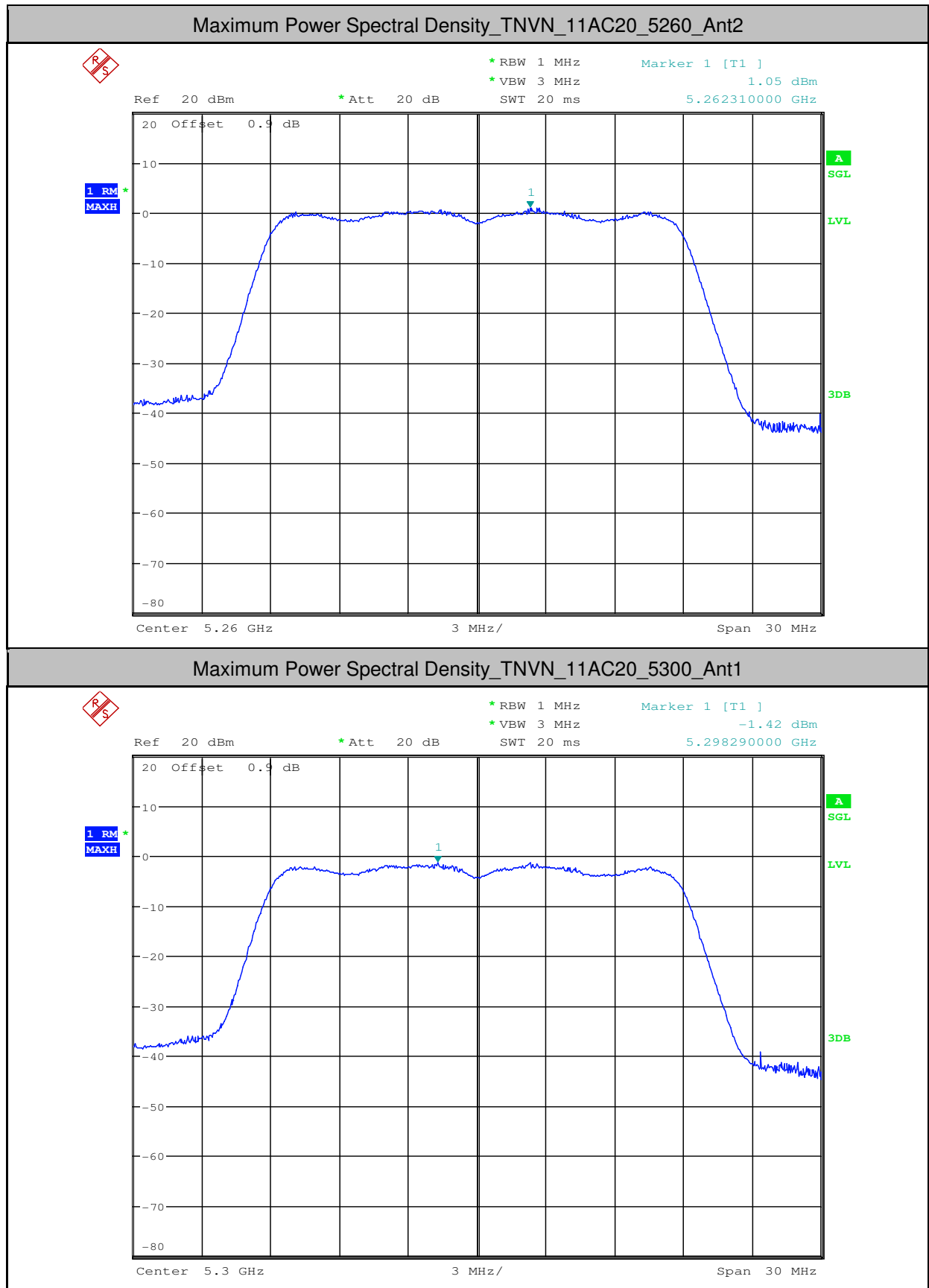


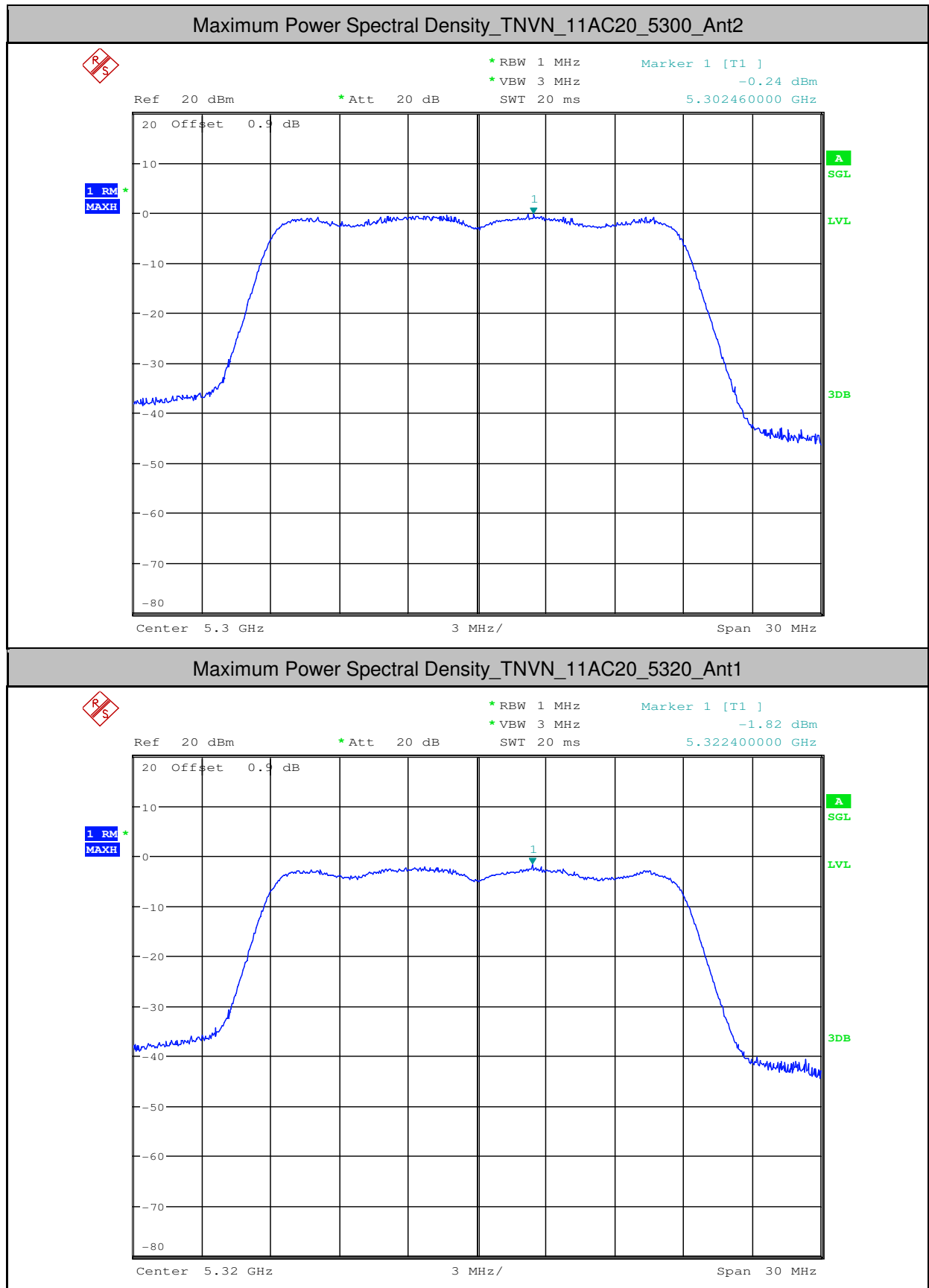


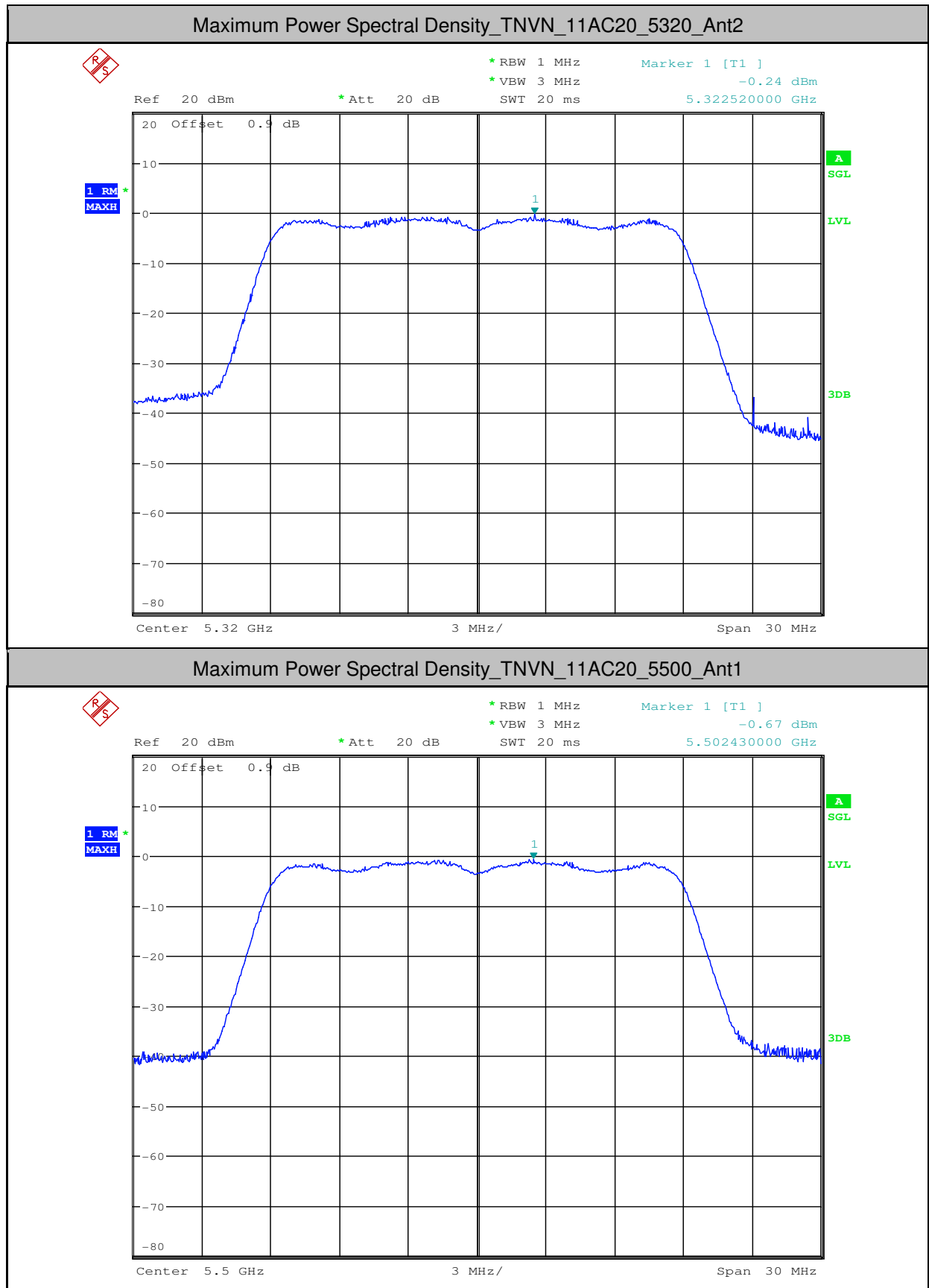


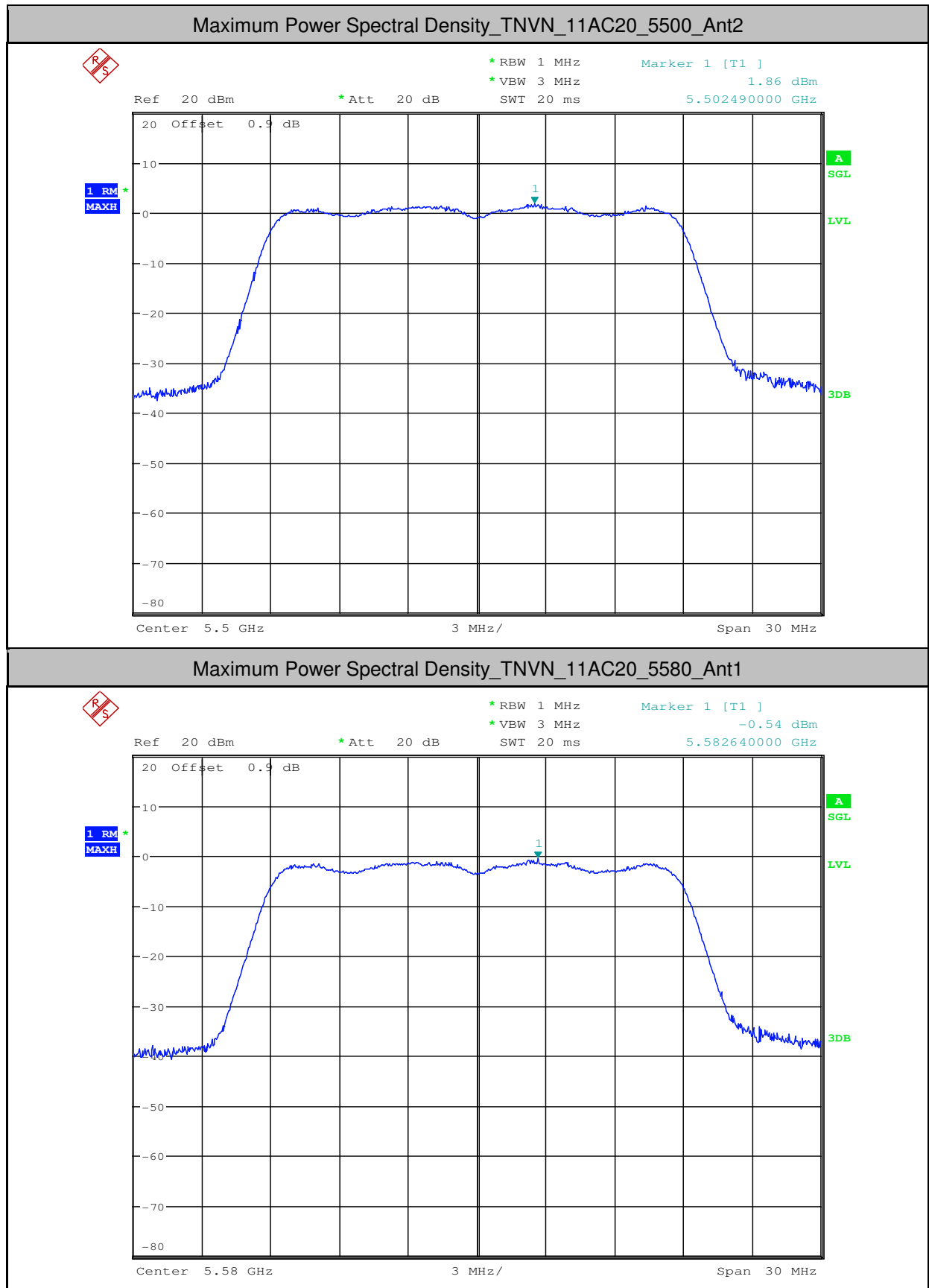


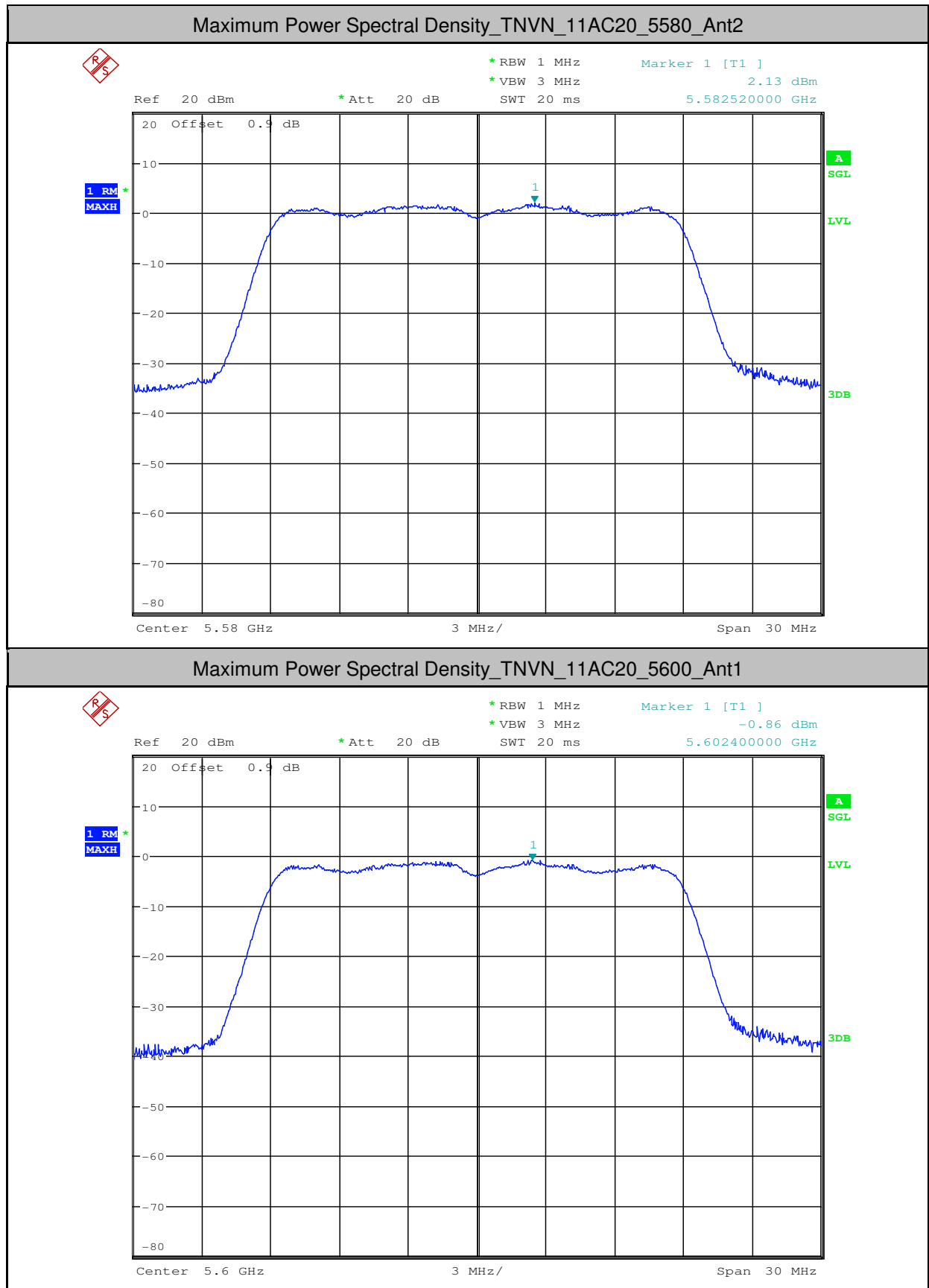


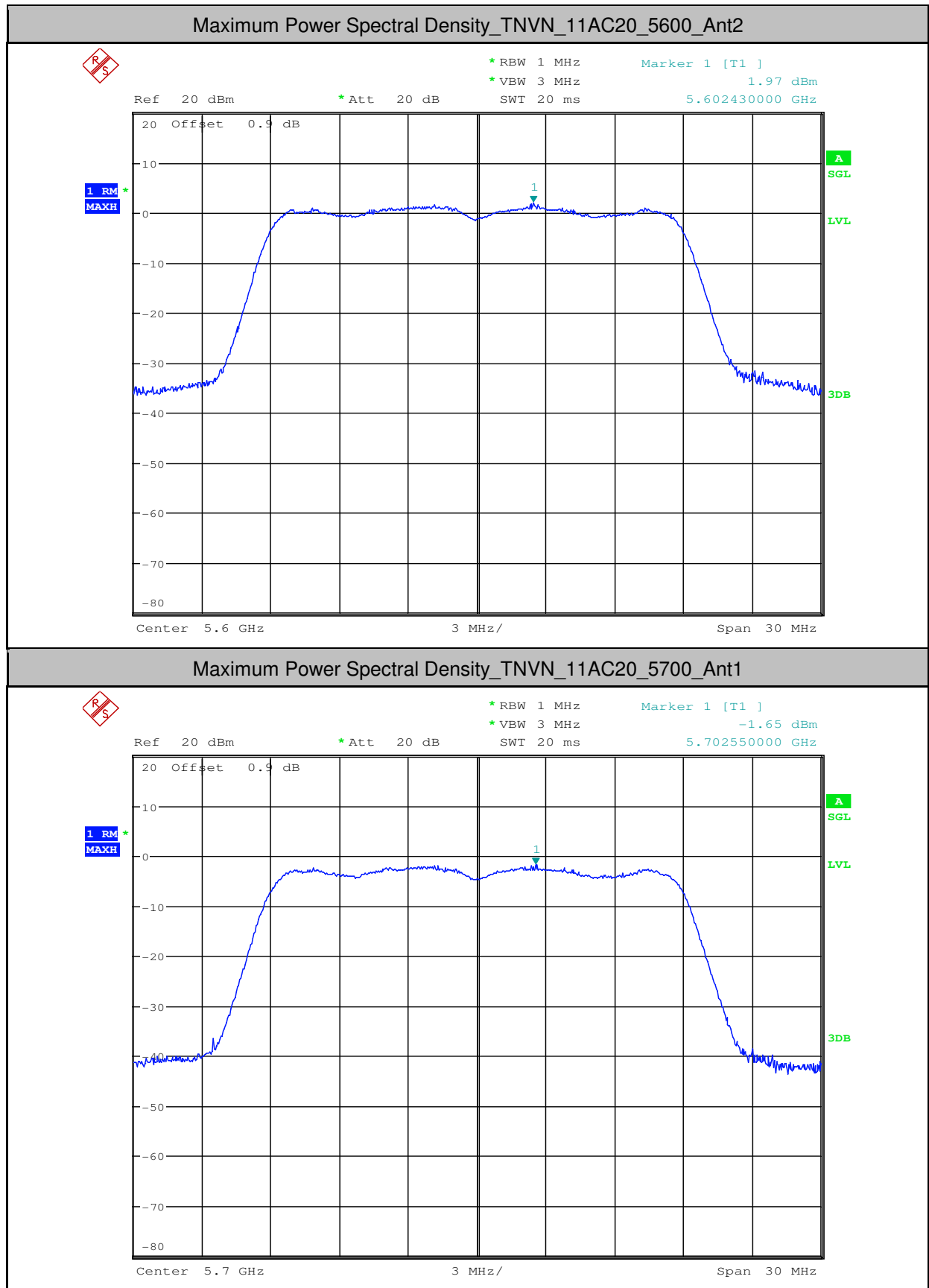


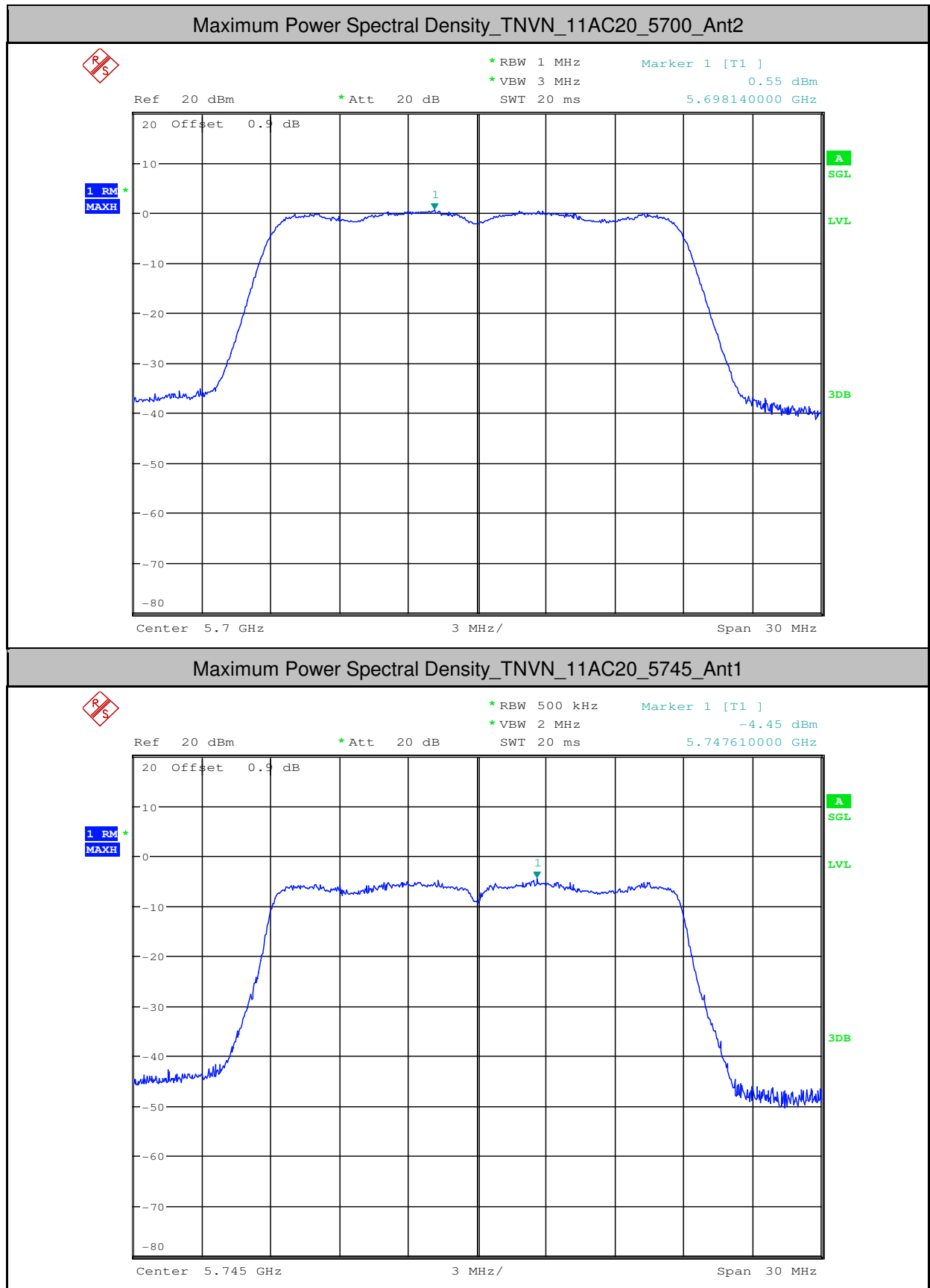




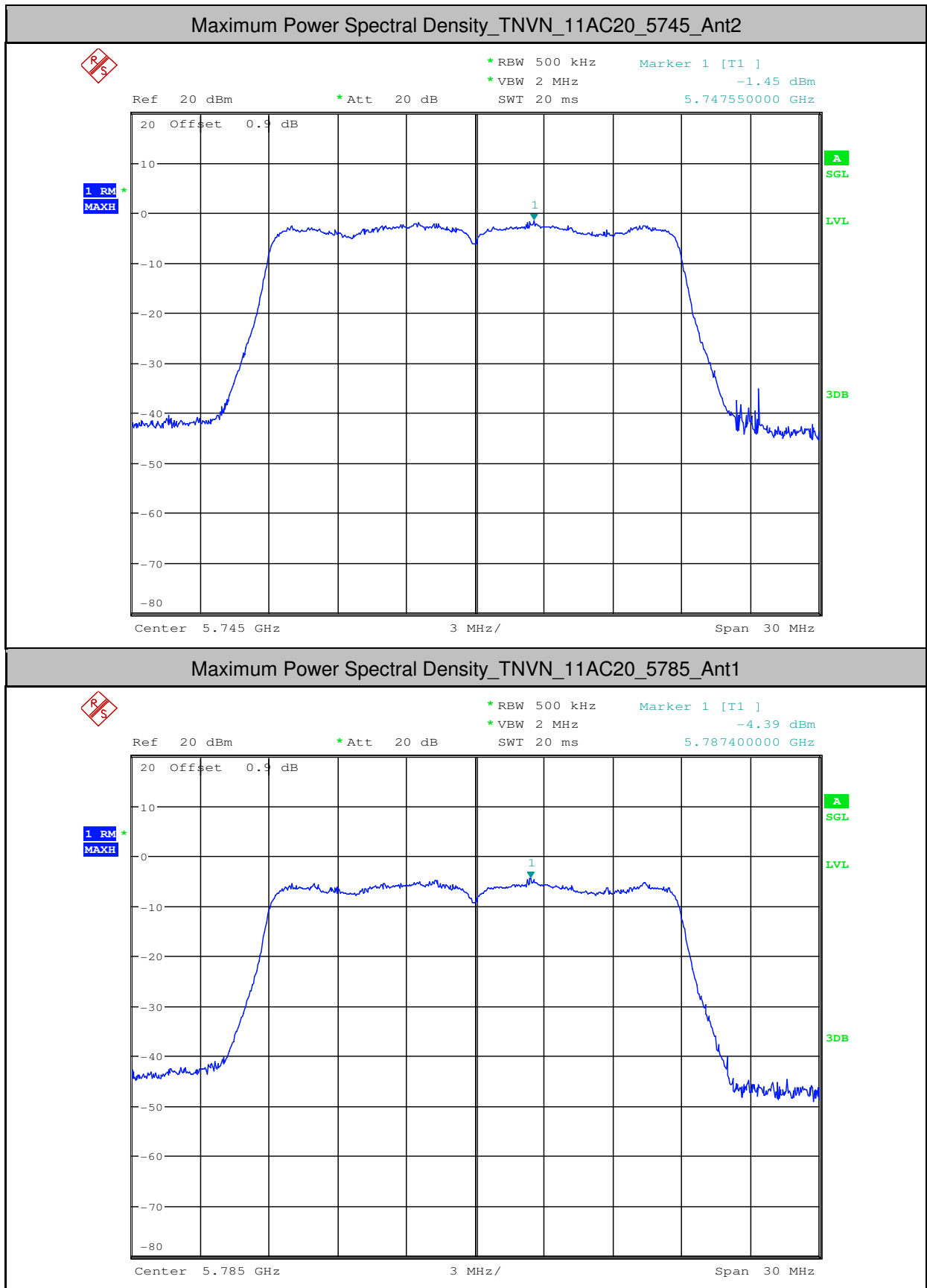


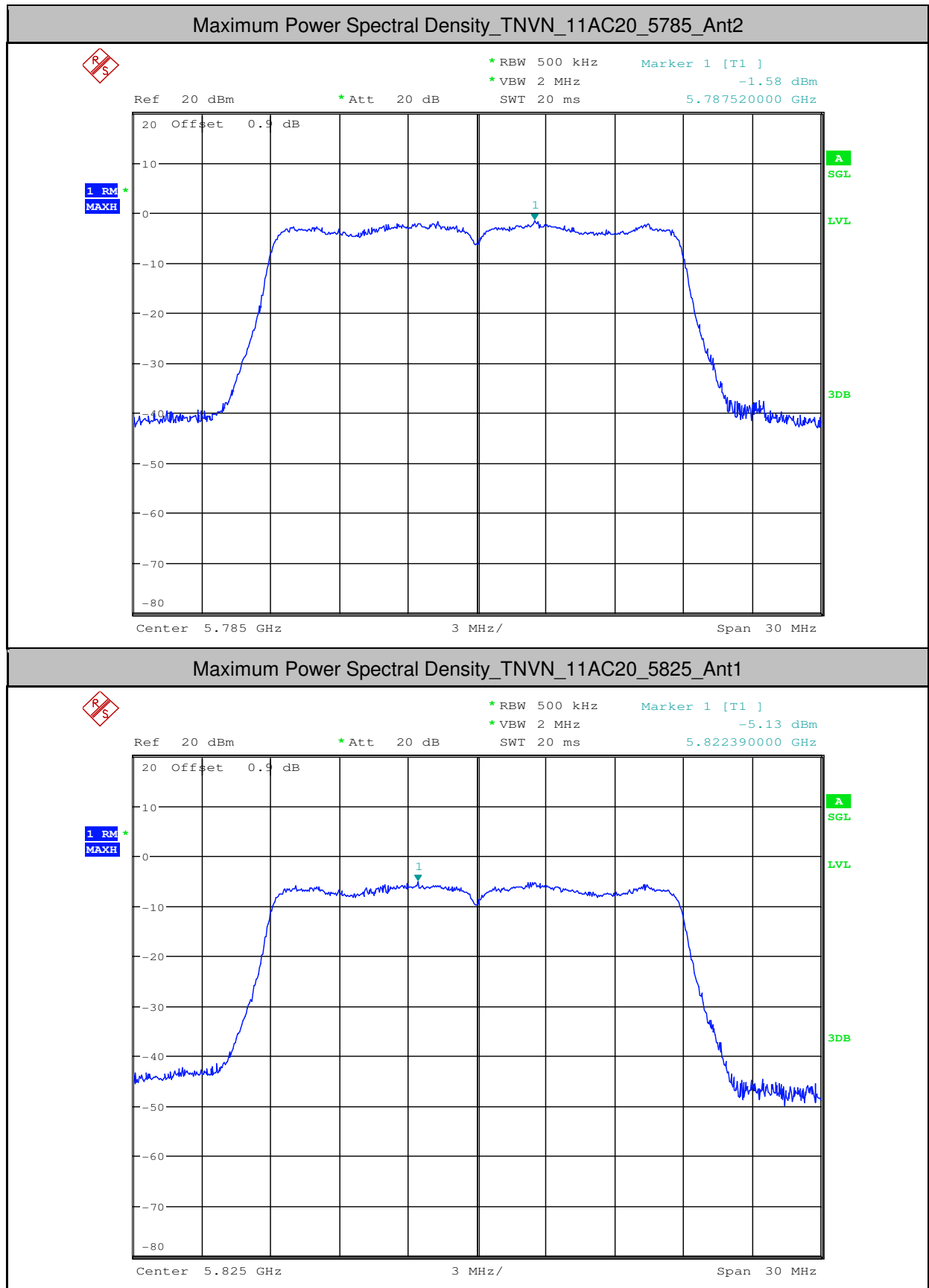


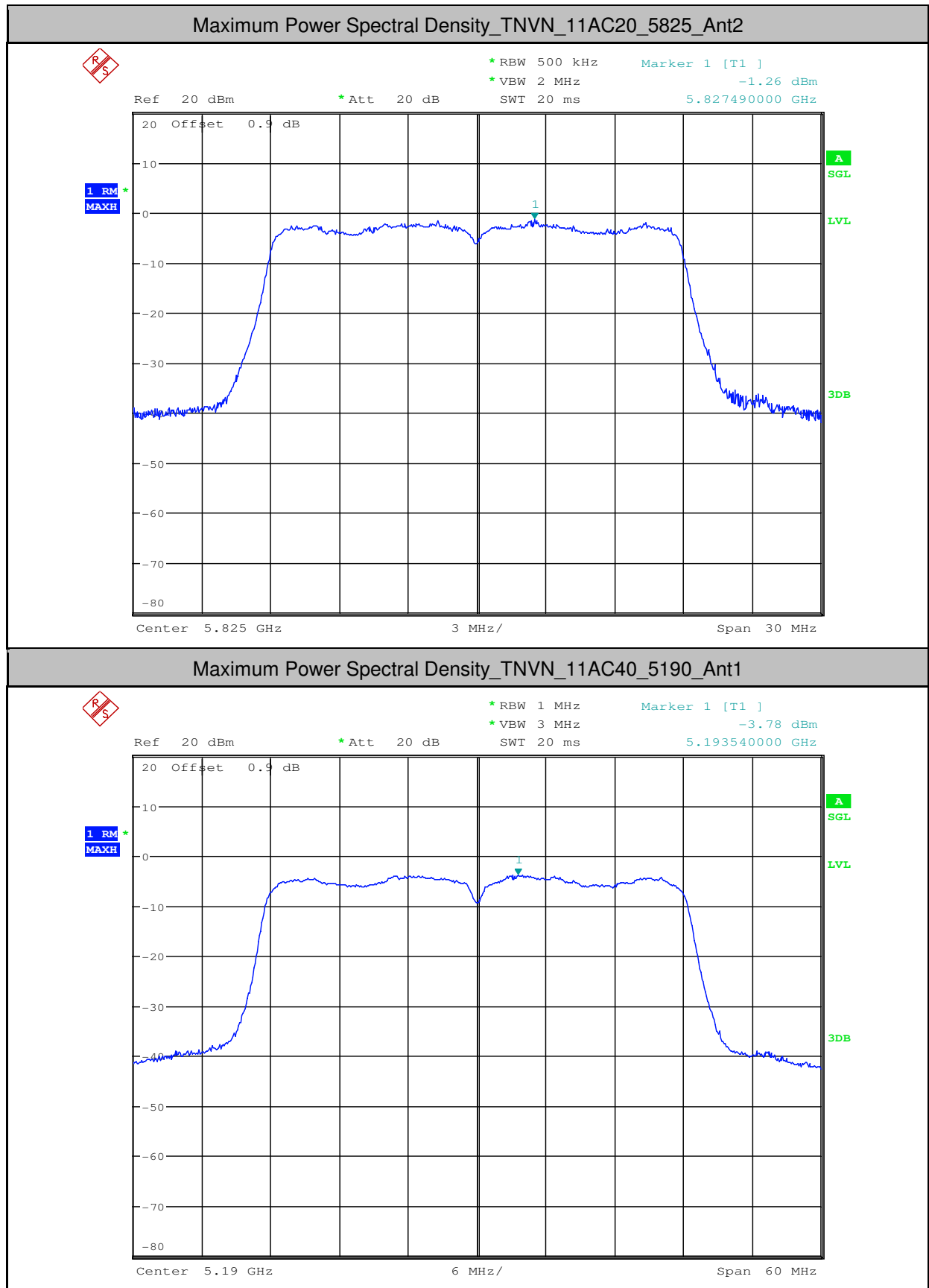


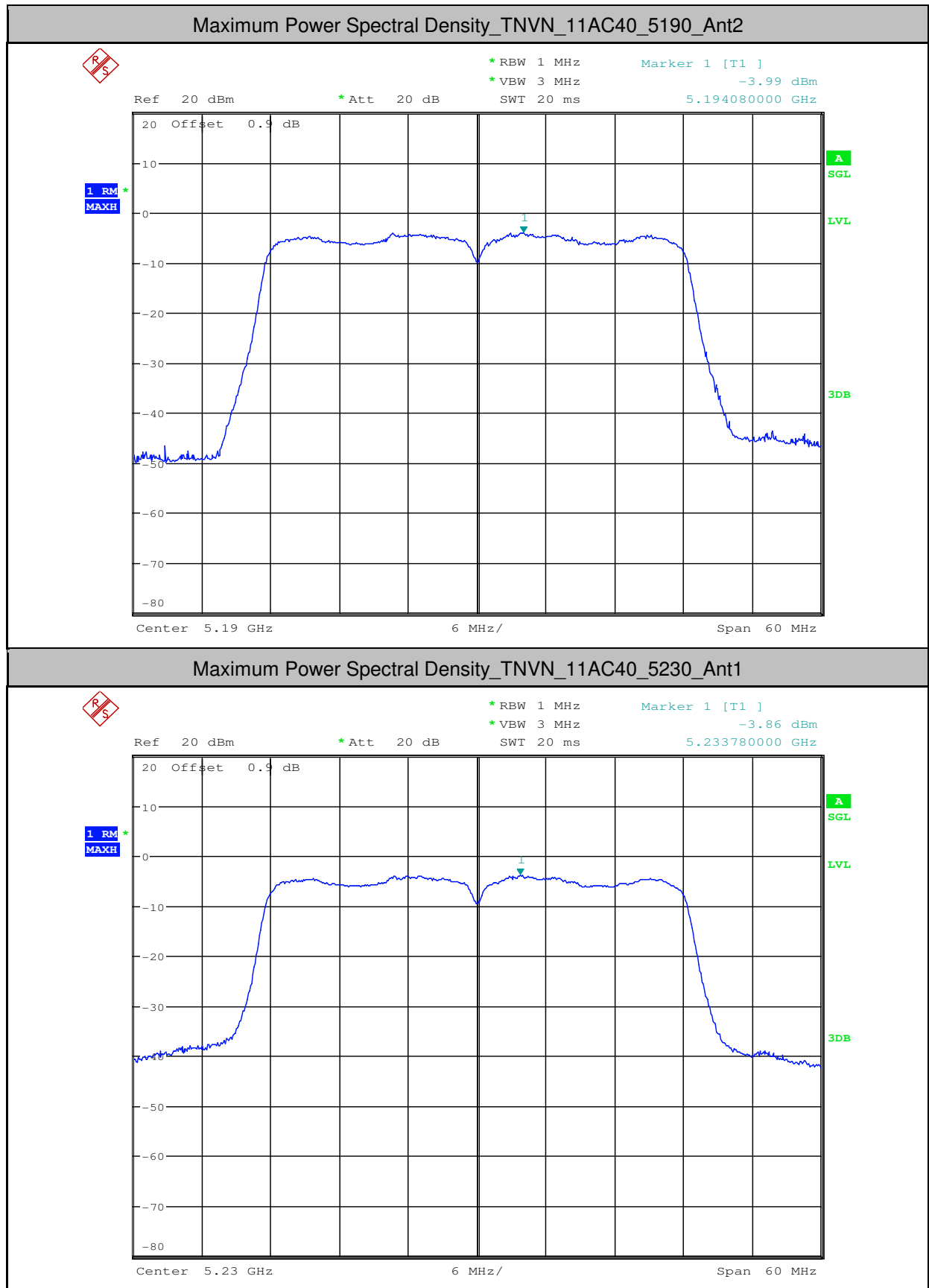


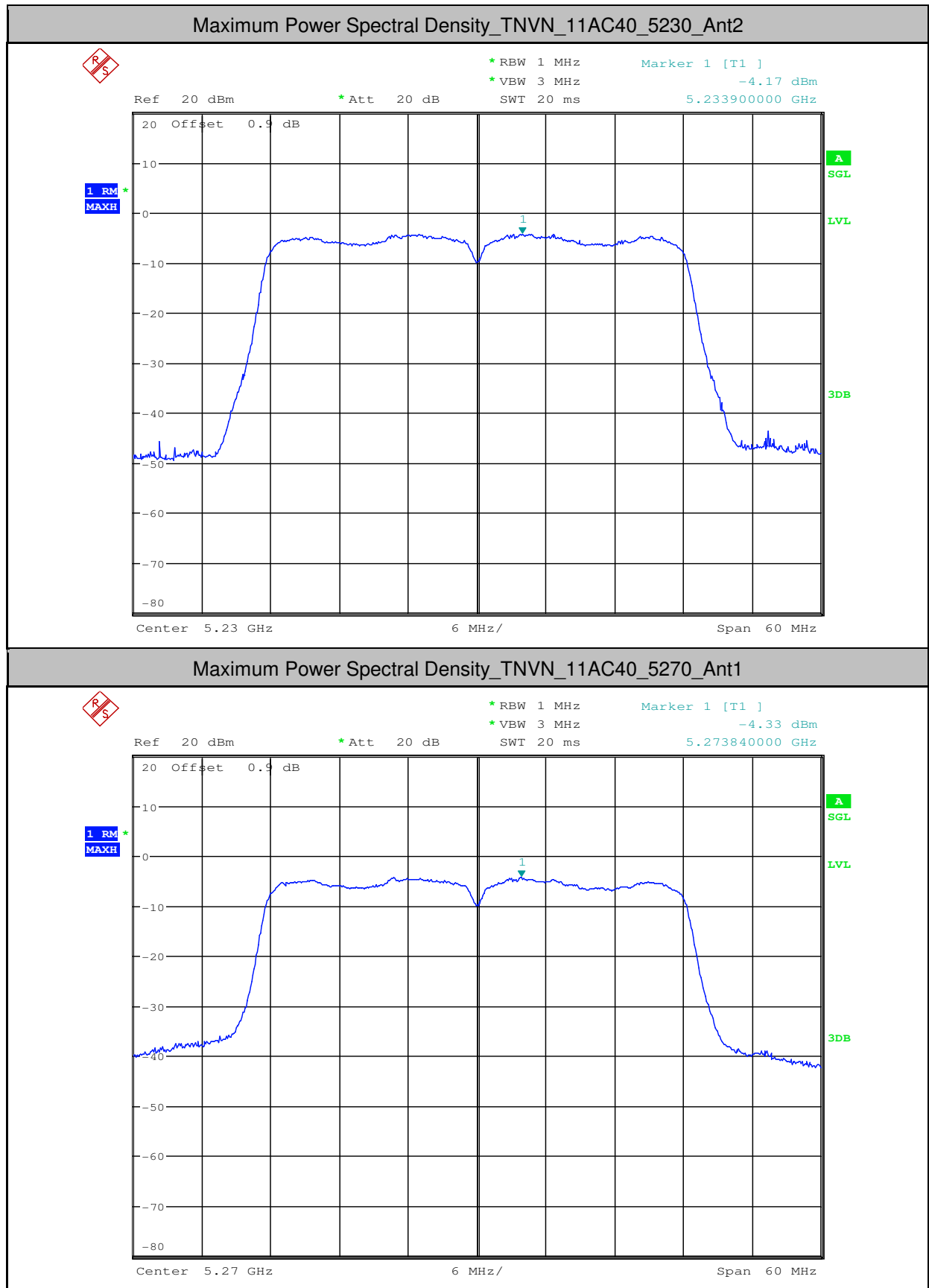


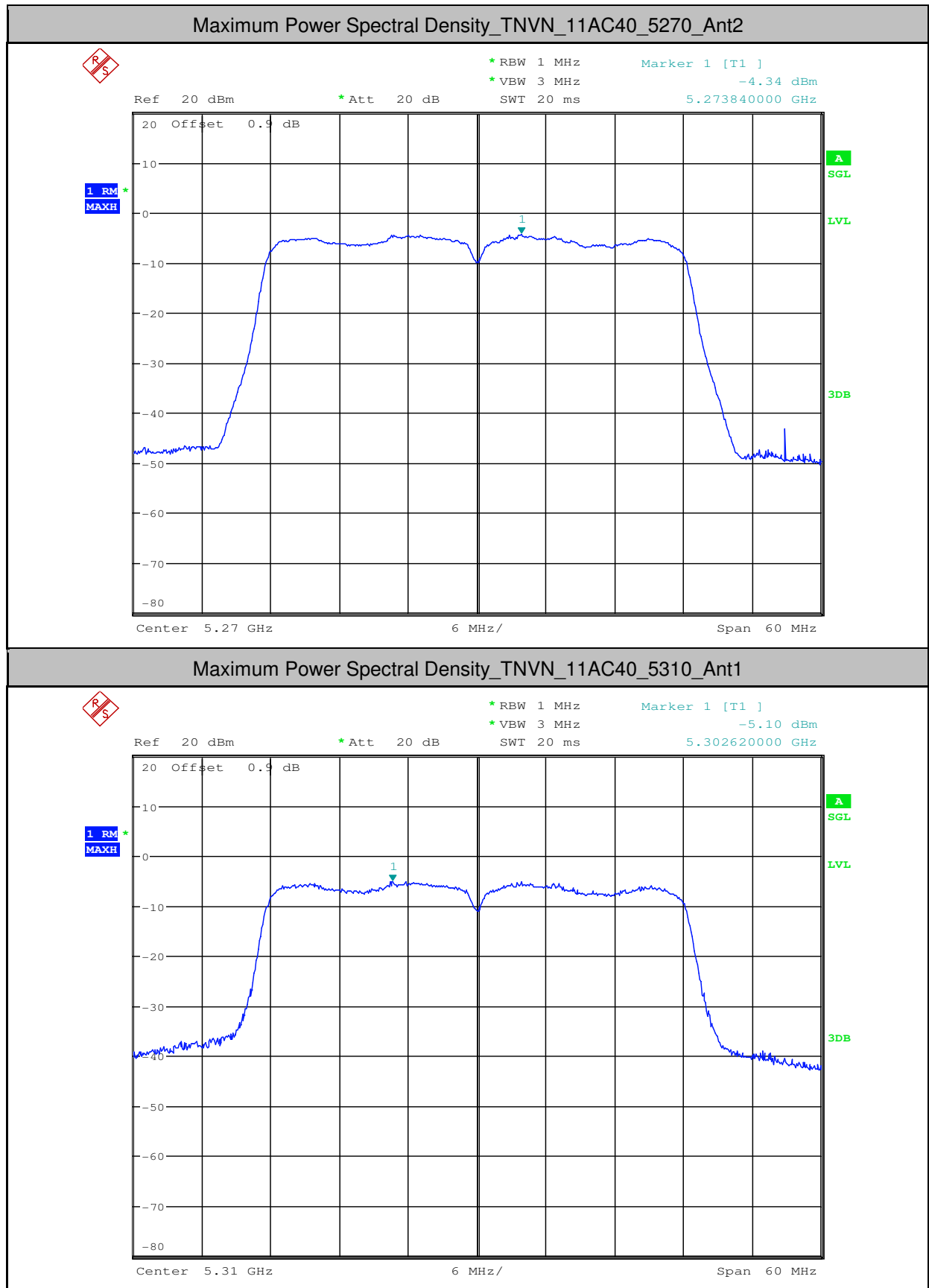


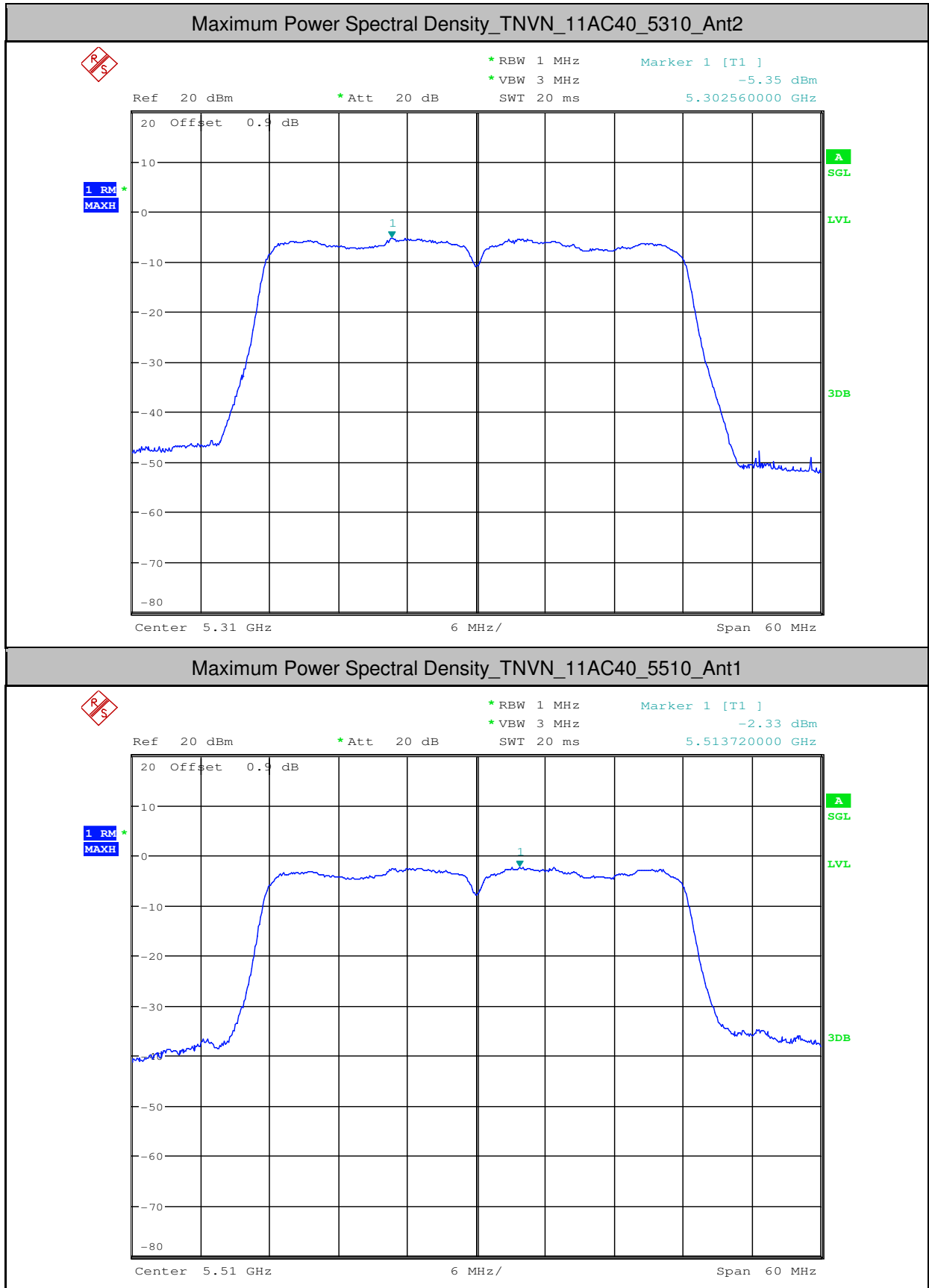


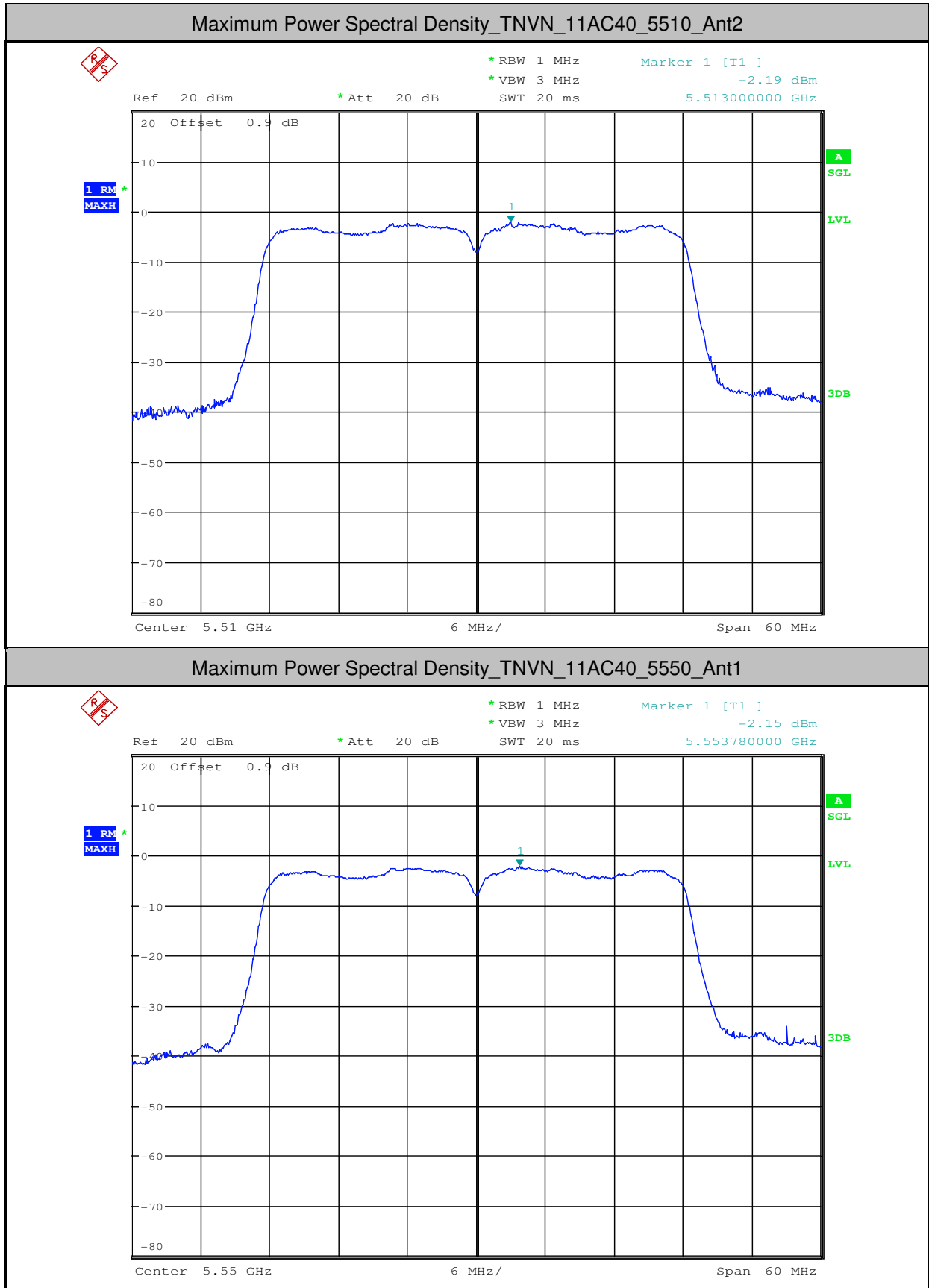




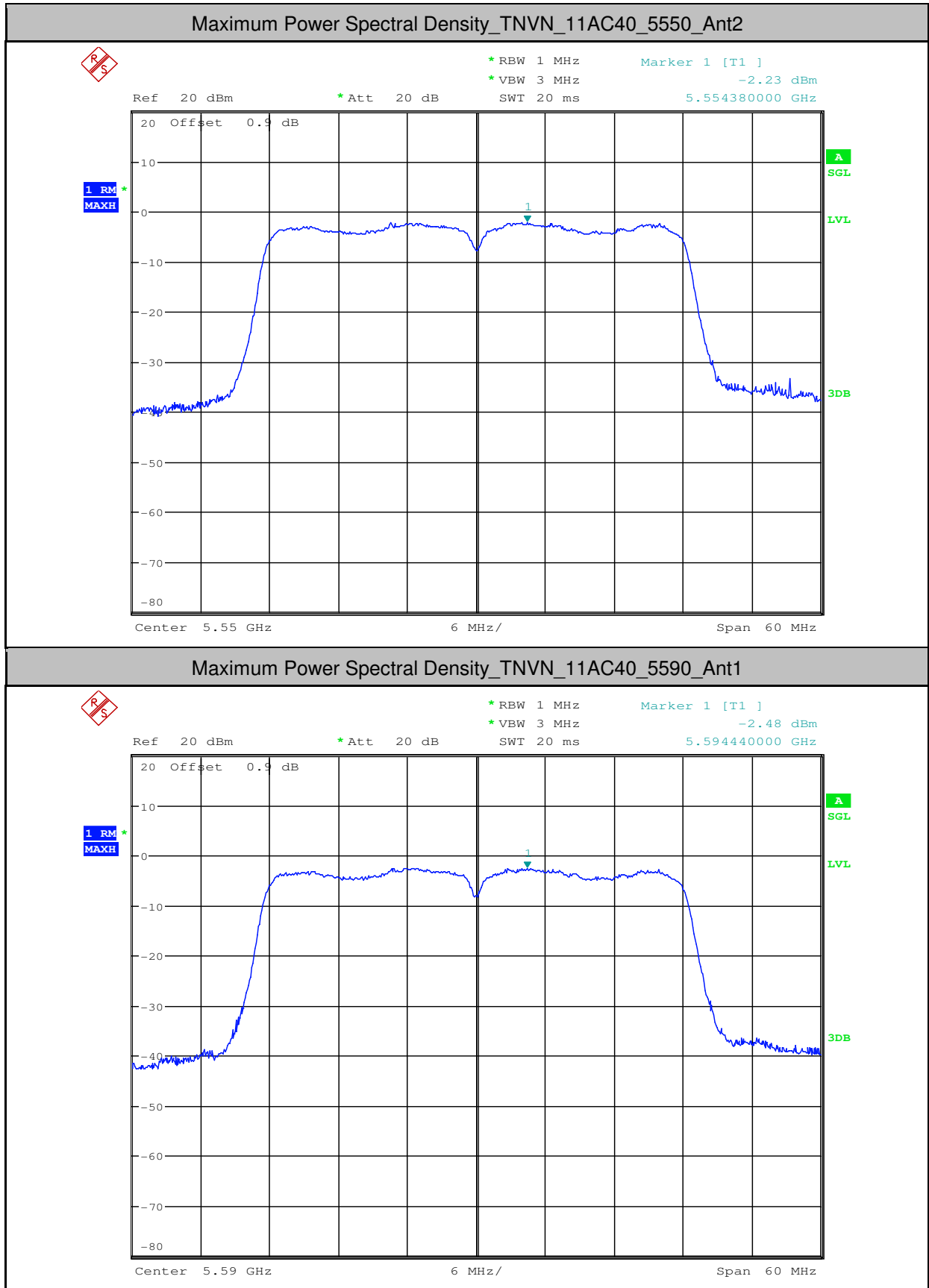


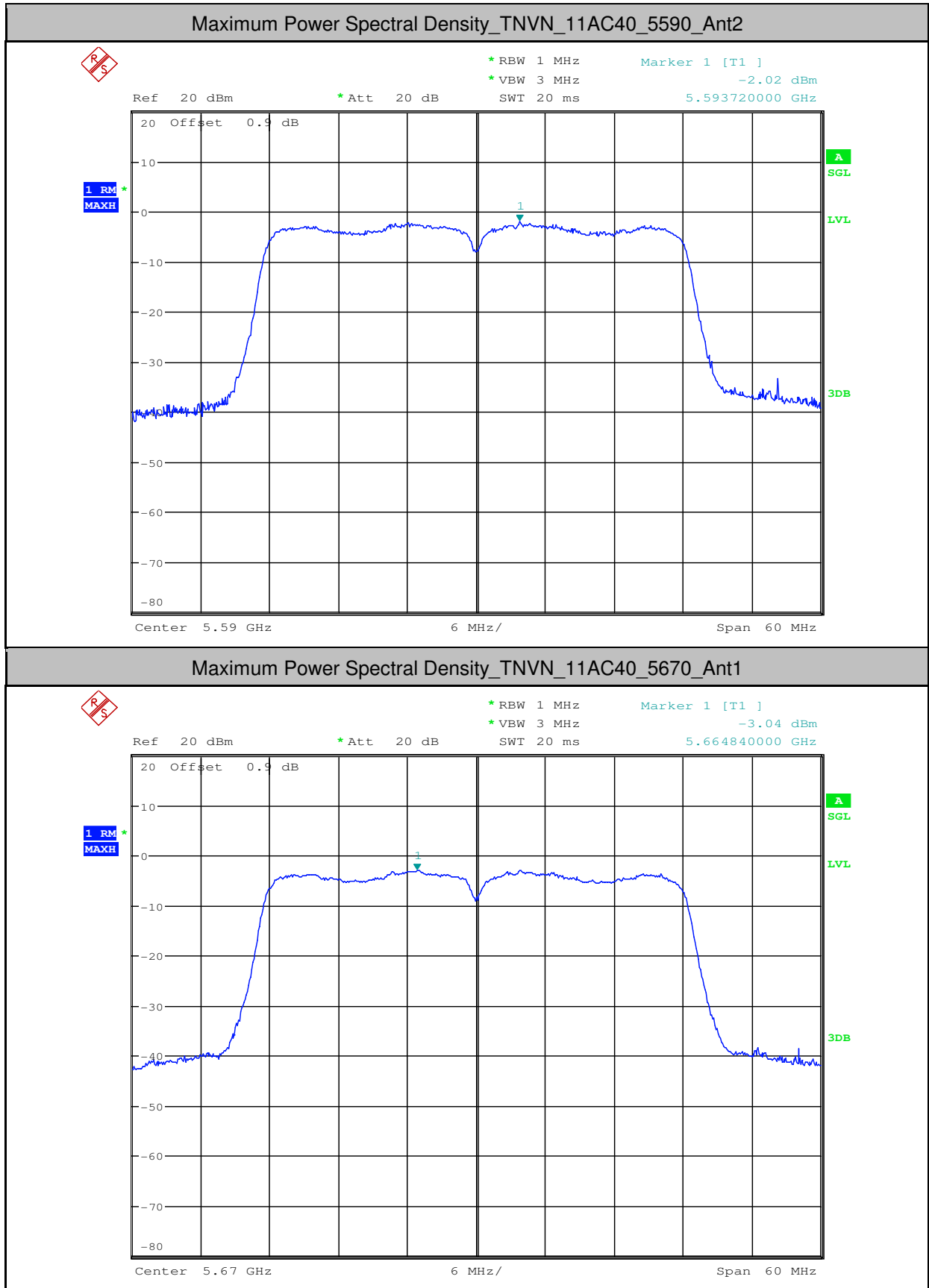


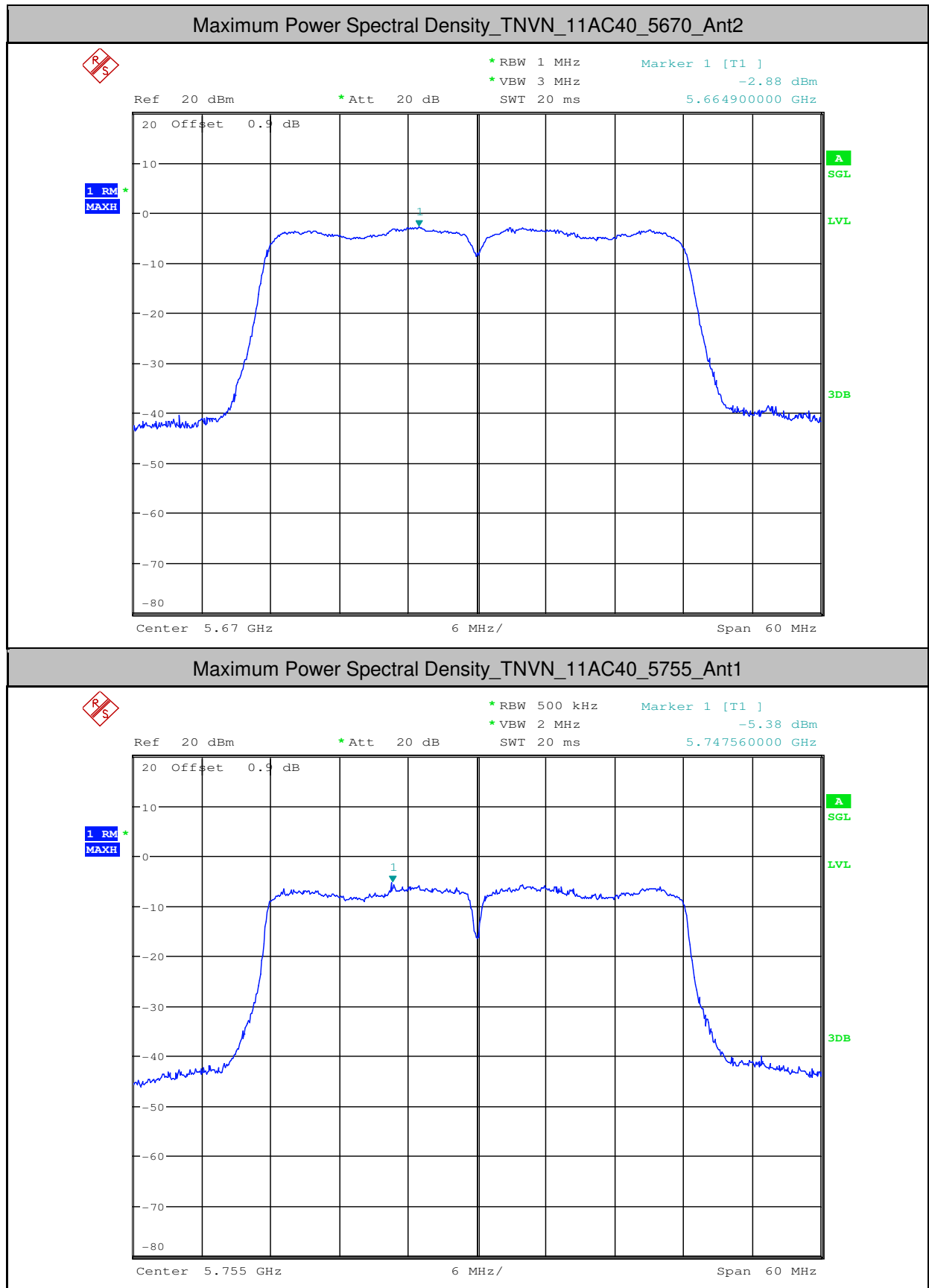


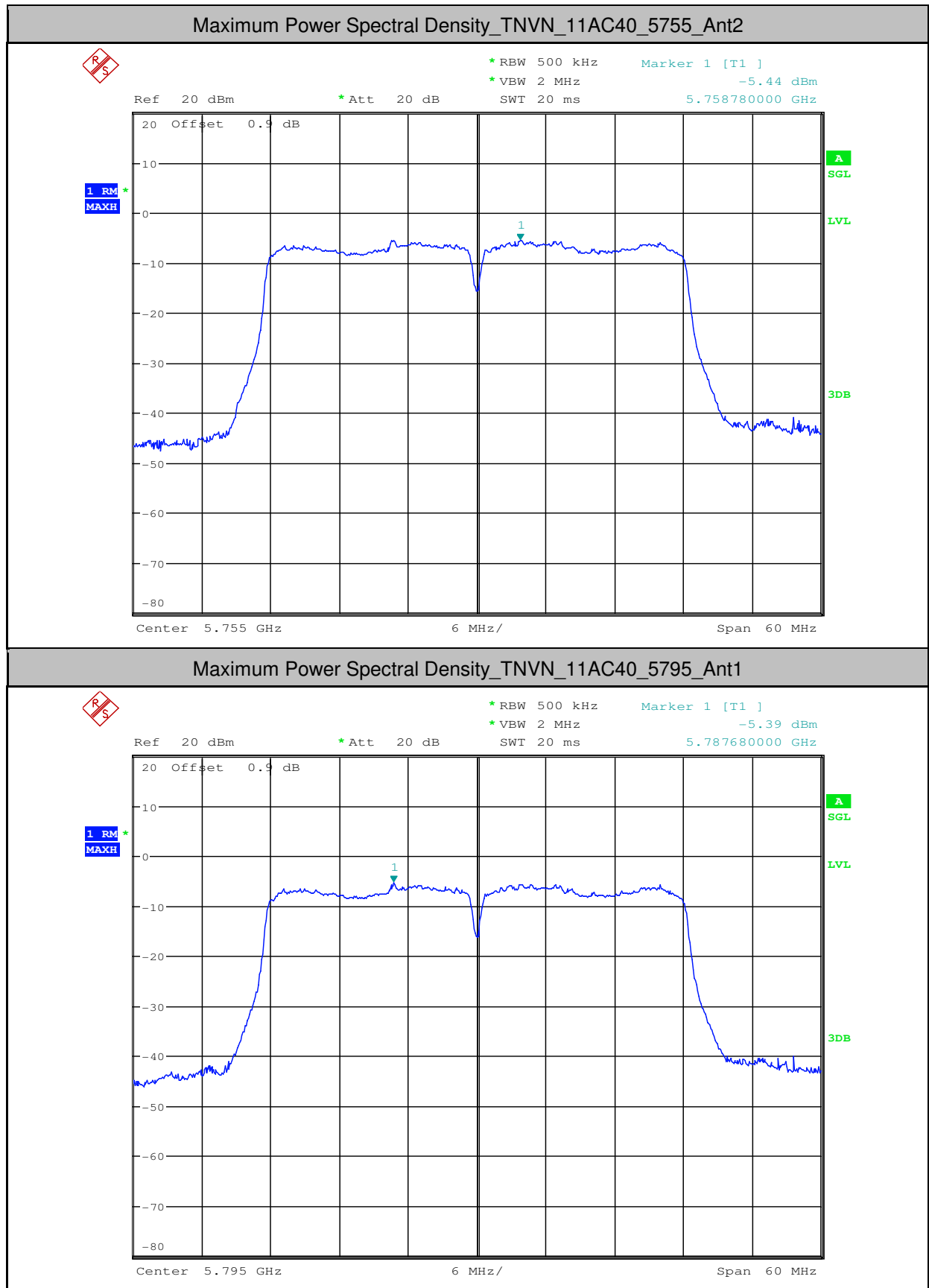


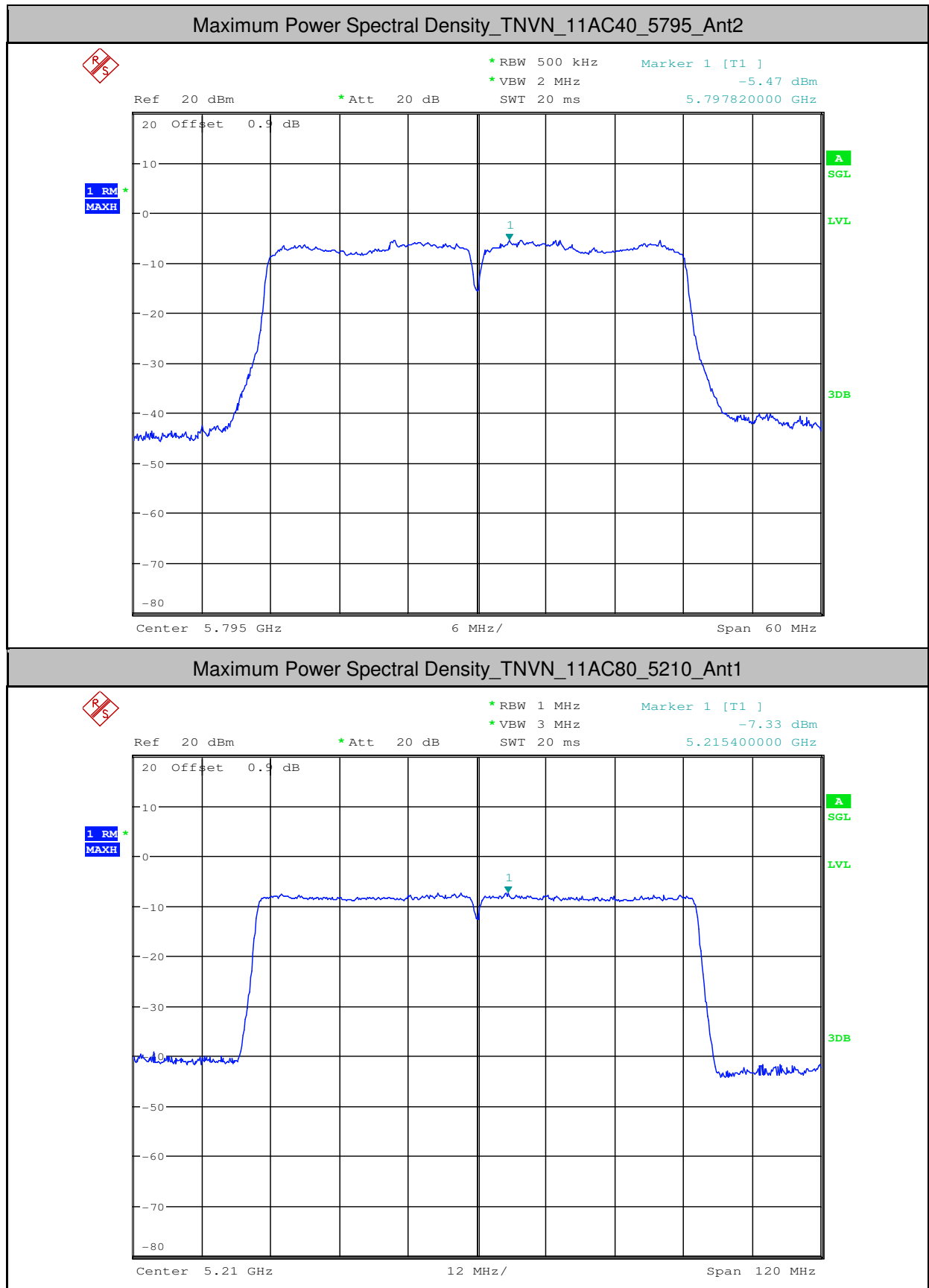


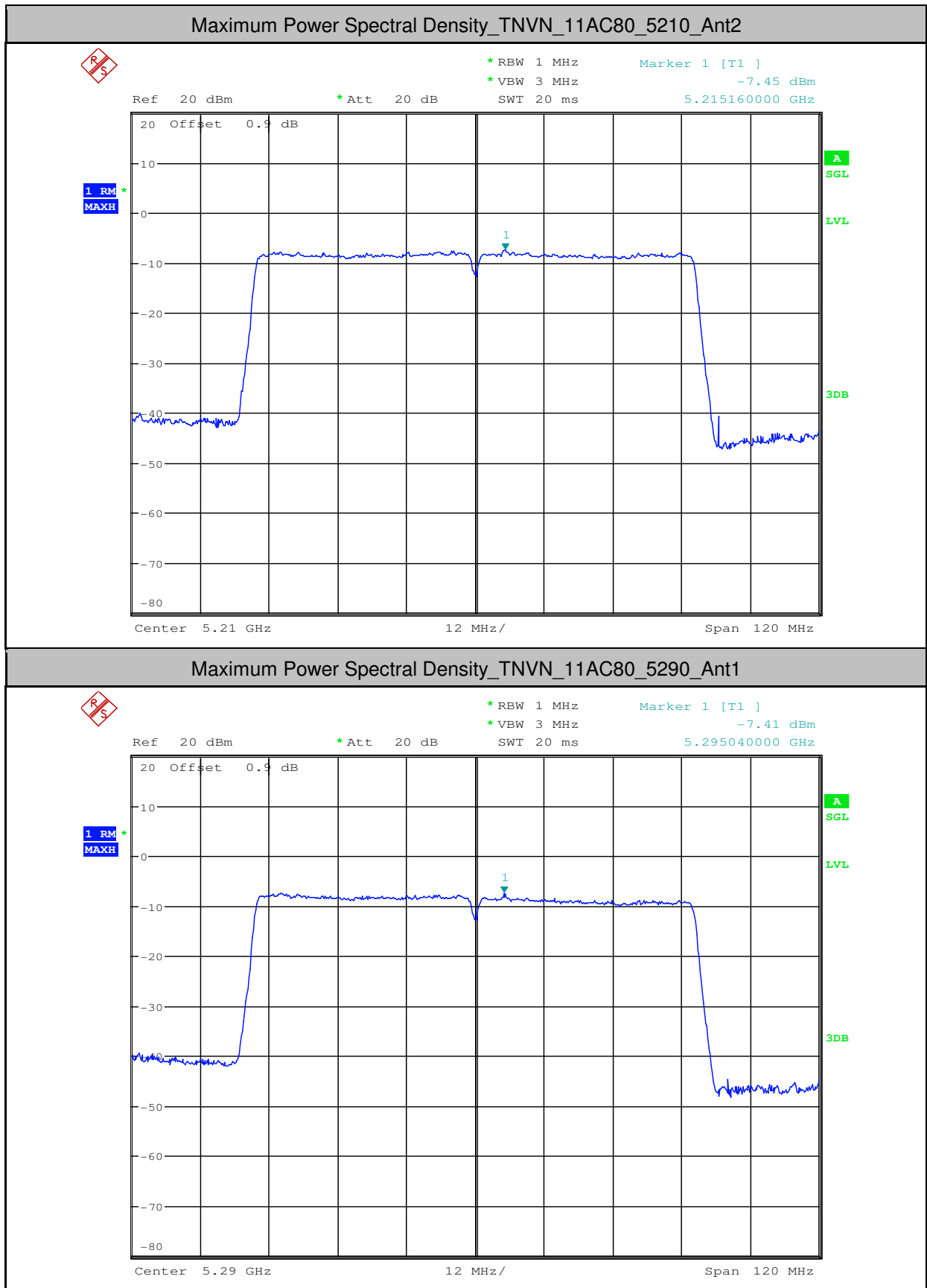


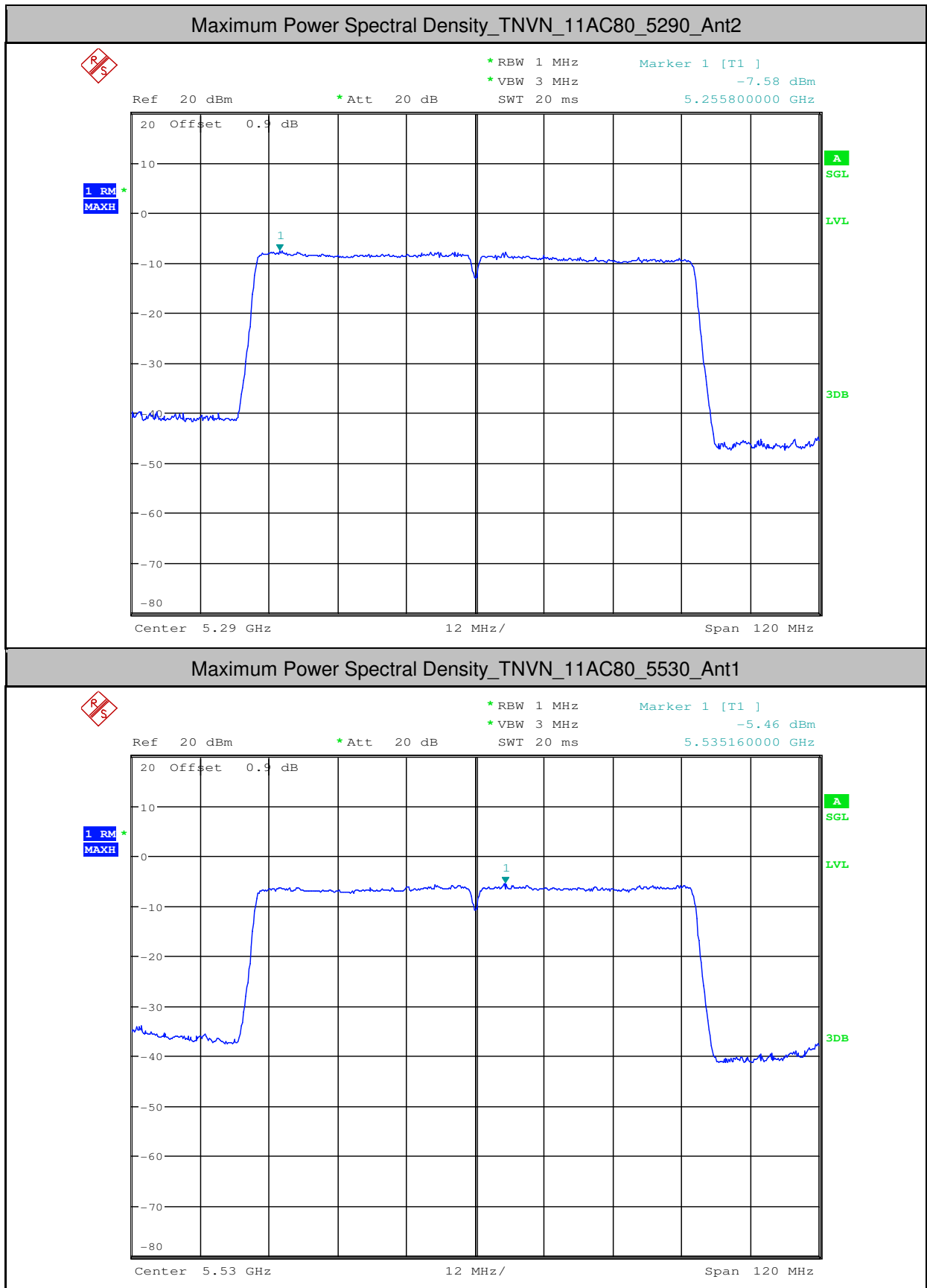


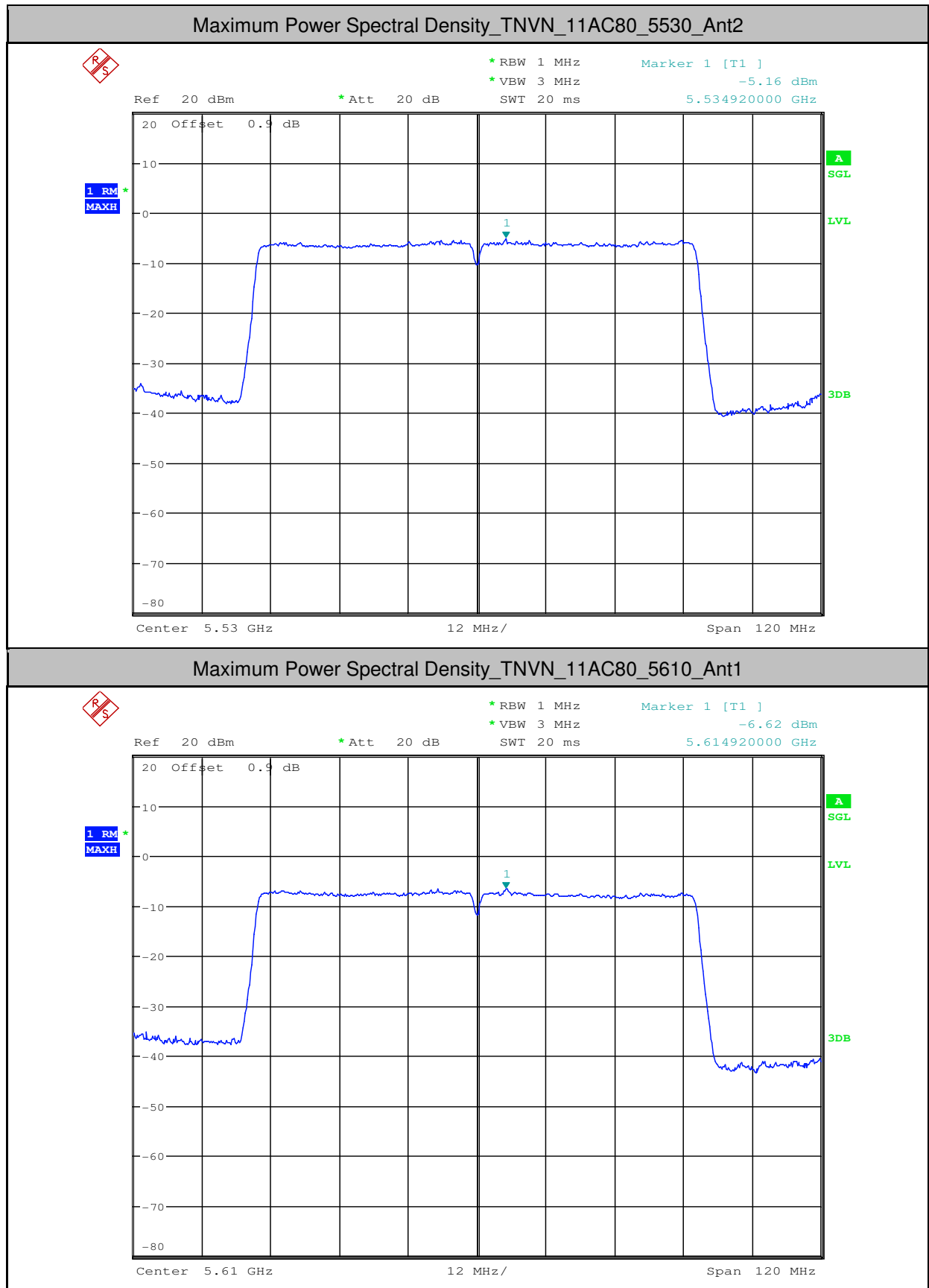




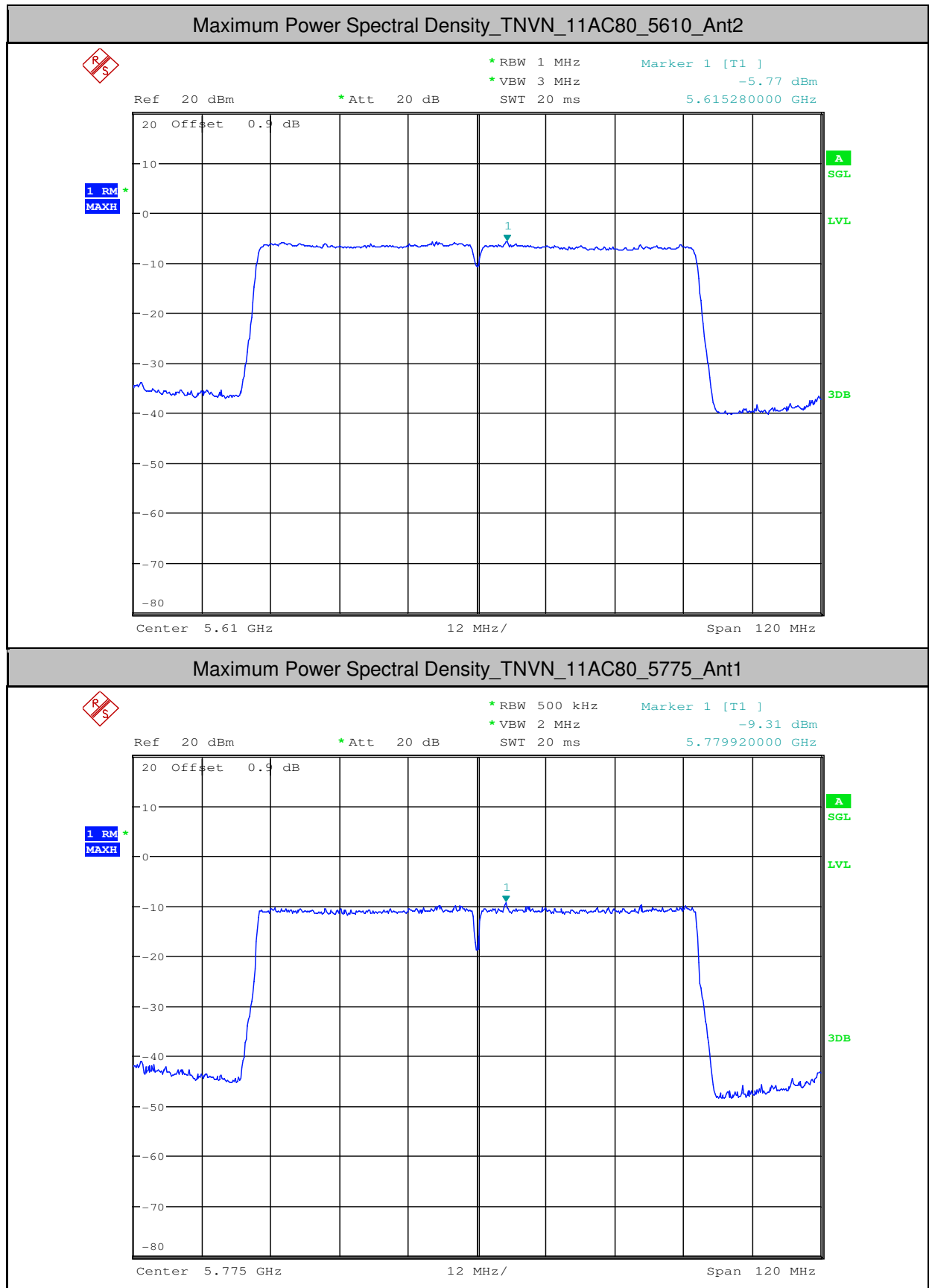


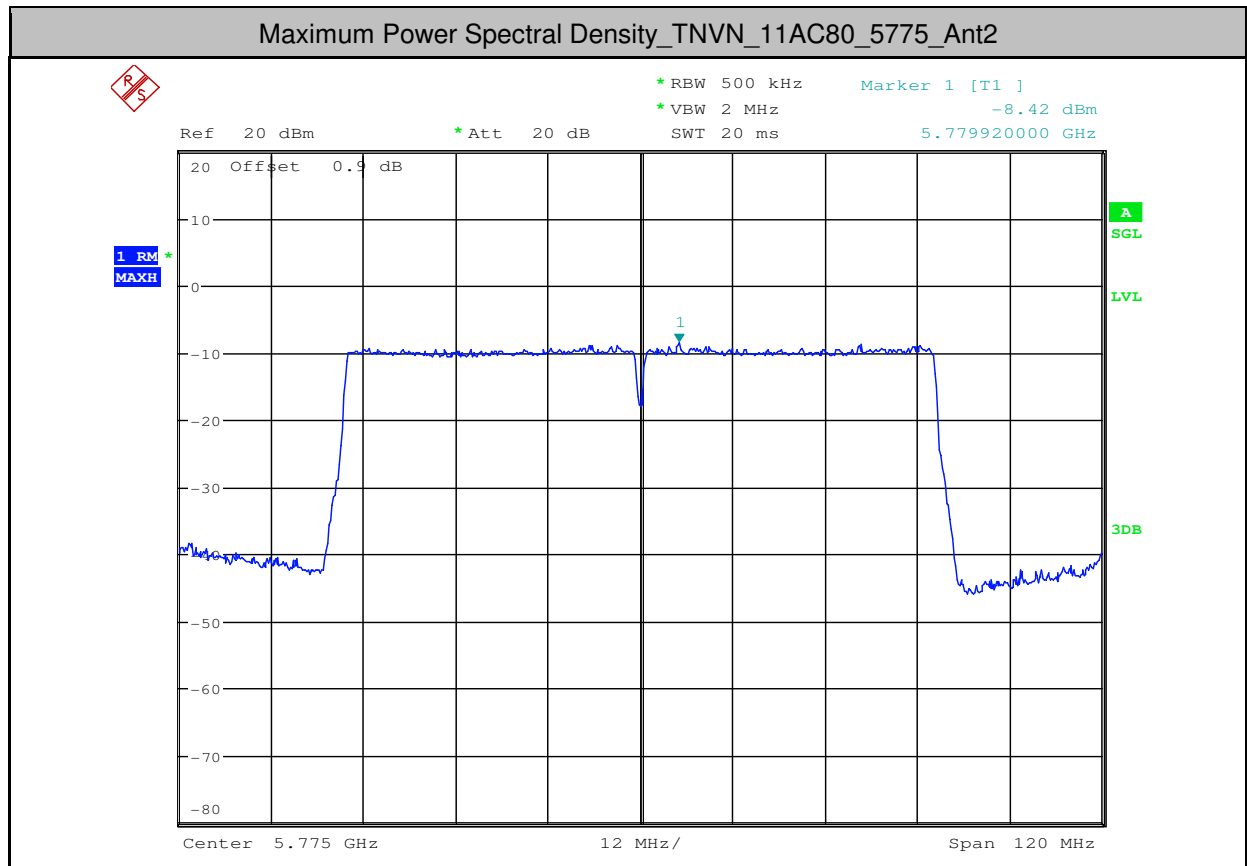














5.Duty Cycle (x)

Test Mode	Test Channel	Ant	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	5180	Ant1	100	0
11A	5180	Ant2	100	0
11A	5200	Ant1	100	0
11A	5200	Ant2	100	0
11A	5240	Ant1	100	0
11A	5240	Ant2	100	0
11A	5260	Ant1	100	0
11A	5260	Ant2	100	0
11A	5300	Ant1	100	0
11A	5300	Ant2	100	0
11A	5320	Ant1	100	0
11A	5320	Ant2	100	0
11A	5500	Ant1	100	0
11A	5500	Ant2	100	0
11A	5580	Ant1	100	0
11A	5580	Ant2	100	0
11A	5600	Ant1	100	0
11A	5600	Ant2	100	0
11A	5700	Ant1	100	0
11A	5700	Ant2	100	0
11A	5745	Ant1	100	0
11A	5745	Ant2	100	0
11A	5785	Ant1	100	0
11A	5785	Ant2	100	0
11A	5825	Ant1	100	0
11A	5825	Ant2	100	0
11N20	5180	Ant1	100	0
11N20	5180	Ant2	100	0
11N20	5200	Ant1	100	0
11N20	5200	Ant2	100	0
11N20	5240	Ant1	100	0



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11N20	5240	Ant2	100	0
11N20	5260	Ant1	100	0
11N20	5260	Ant2	100	0
11N20	5300	Ant1	100	0
11N20	5300	Ant2	100	0
11N20	5320	Ant1	100	0
11N20	5320	Ant2	100	0
11N20	5500	Ant1	100	0
11N20	5500	Ant2	100	0
11N20	5580	Ant1	100	0
11N20	5580	Ant2	100	0
11N20	5600	Ant1	100	0
11N20	5600	Ant2	100	0
11N20	5700	Ant1	100	0
11N20	5700	Ant2	100	0
11N20	5745	Ant1	100	0
11N20	5745	Ant2	100	0
11N20	5785	Ant1	100	0
11N20	5785	Ant2	100	0
11N20	5825	Ant1	100	0
11N20	5825	Ant2	100	0
11N40	5190	Ant1	100	0
11N40	5190	Ant2	100	0
11N40	5230	Ant1	100	0
11N40	5230	Ant2	100	0
11N40	5270	Ant1	100	0
11N40	5270	Ant2	100	0
11N40	5310	Ant1	100	0
11N40	5310	Ant2	100	0
11N40	5510	Ant1	100	0
11N40	5510	Ant2	100	0
11N40	5550	Ant1	100	0
11N40	5550	Ant2	100	0
11N40	5590	Ant1	100	0

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11N40	5590	Ant2	100	0
11N40	5670	Ant1	100	0
11N40	5670	Ant2	100	0
11N40	5755	Ant1	100	0
11N40	5755	Ant2	100	0
11N40	5795	Ant1	100	0
11N40	5795	Ant2	100	0
11AC20	5180	Ant1	100	0
11AC20	5200	Ant1	100	0
11AC20	5200	Ant2	100	0
11AC20	5240	Ant1	100	0
11AC20	5240	Ant2	100	0
11AC20	5260	Ant1	100	0
11AC20	5260	Ant2	100	0
11AC20	5300	Ant1	100	0
11AC20	5300	Ant2	100	0
11AC20	5320	Ant1	100	0
11AC20	5320	Ant2	100	0
11AC20	5500	Ant1	100	0
11AC20	5500	Ant2	100	0
11AC20	5580	Ant1	100	0
11AC20	5580	Ant2	100	0
11AC20	5600	Ant1	100	0
11AC20	5600	Ant2	100	0
11AC20	5700	Ant1	100	0
11AC20	5700	Ant2	100	0
11AC20	5745	Ant1	100	0
11AC20	5745	Ant2	100	0
11AC20	5785	Ant1	100	0
11AC20	5785	Ant2	100	0
11AC20	5825	Ant1	100	0
11AC20	5825	Ant2	100	0
11AC40	5190	Ant1	100	0
11AC40	5190	Ant2	100	0

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11AC40	5230	Ant1	100	0
11AC40	5230	Ant2	100	0
11AC40	5270	Ant1	100	0
11AC40	5270	Ant2	100	0
11AC40	5310	Ant1	100	0
11AC40	5310	Ant2	100	0
11AC40	5510	Ant1	100	0
11AC40	5510	Ant2	100	0
11AC40	5550	Ant1	100	0
11AC40	5550	Ant2	100	0
11AC40	5590	Ant1	100	0
11AC40	5590	Ant2	100	0
11AC40	5670	Ant1	100	0
11AC40	5670	Ant2	100	0
11AC40	5755	Ant1	100	0
11AC40	5755	Ant2	100	0
11AC40	5795	Ant1	100	0
11AC40	5795	Ant2	100	0
11AC80	5210	Ant1	100	0
11AC80	5210	Ant2	100	0
11AC80	5290	Ant1	100	0
11AC80	5290	Ant2	100	0
11AC80	5530	Ant1	100	0
11AC80	5530	Ant2	100	0
11AC80	5610	Ant1	100	0
11AC80	5610	Ant2	100	0
11AC80	5775	Ant1	100	0
11AC80	5775	Ant2	100	0





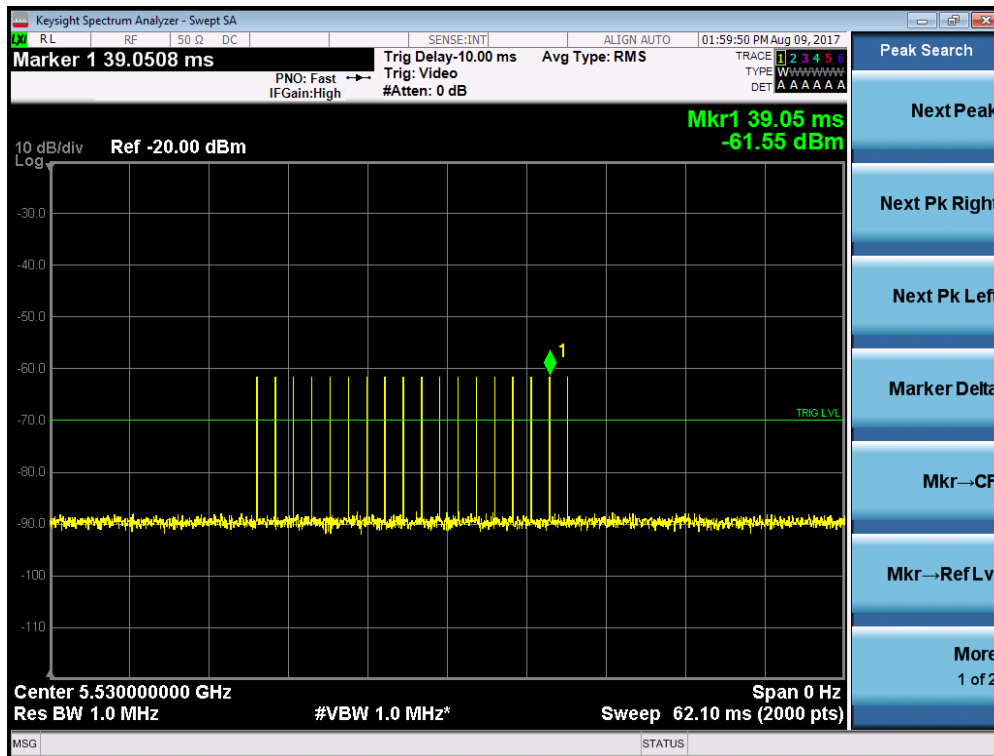
6. (DFS: Channel Move Time; DFS: Channel Closing Transmission Time)

Test plots as follows:

Remark: Only the data of Ant.2 is recorded.

Radar Waveform Calibration Result

Radar Type 0 (20MHz / 5500MHz)





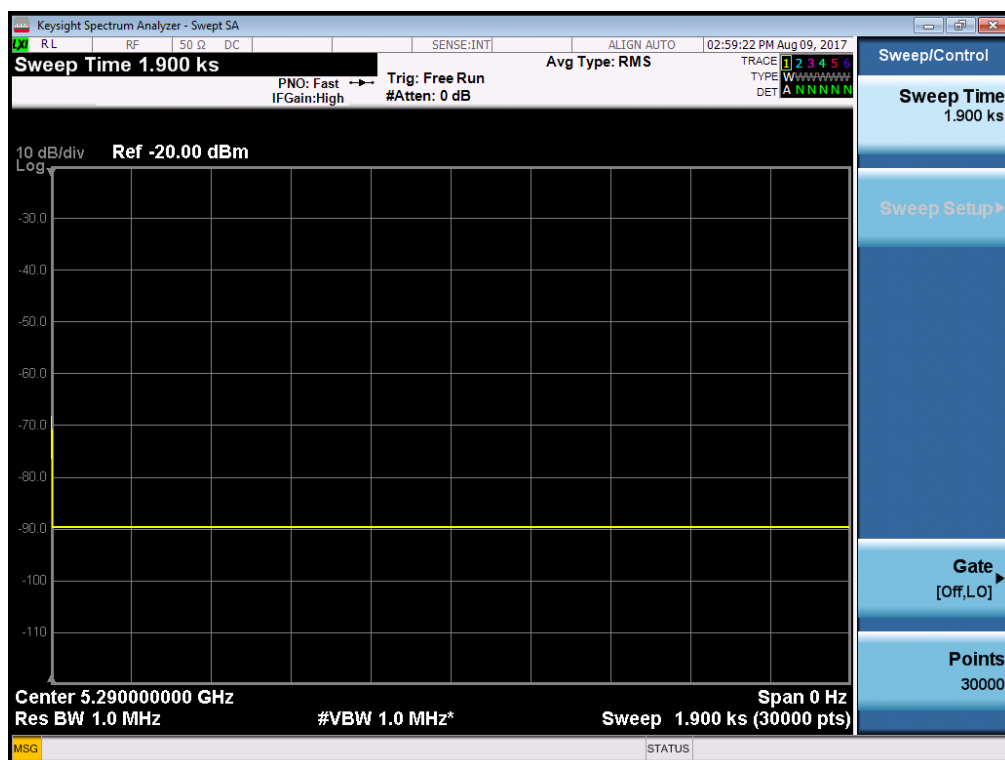


Test Data: Antenna 2

BW/Channel	Test Item	Test Result	Limit	Results
80MHz/5290MHz	Channel Move Time	0.494	<10 s	Pass
	Channel Closing Transmission Time	2.5	<60ms	Pass
80MHz/5530MHz	Channel Move Time	0.471	<10 s	Pass
	Channel Closing Transmission Time	2.5	<60ms	Pass

Test plots as follows:

80MHz/5290MHz

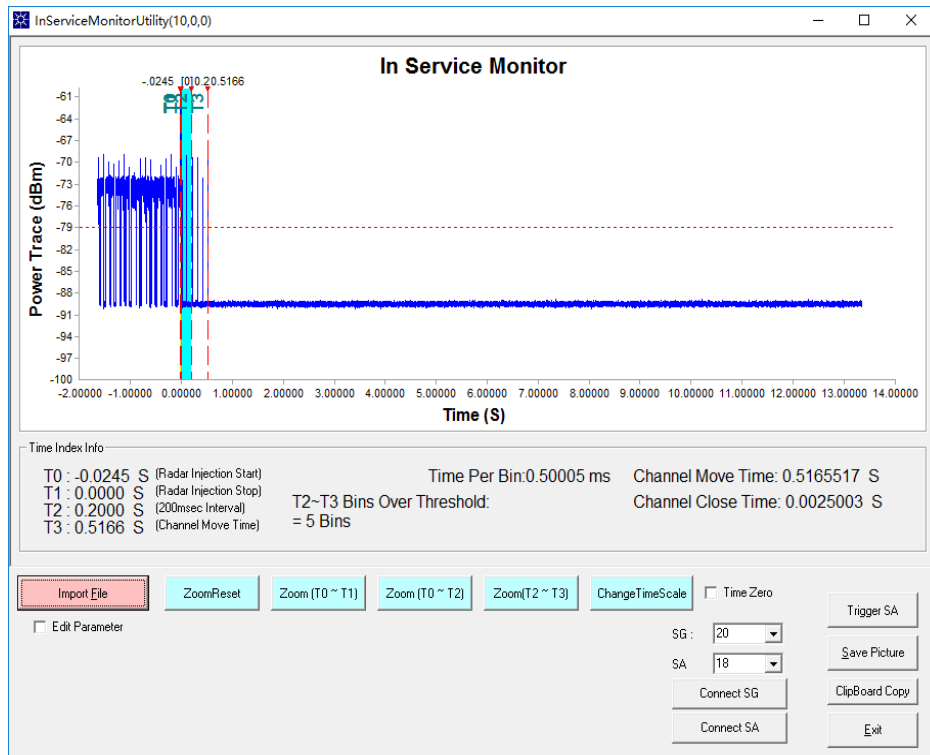
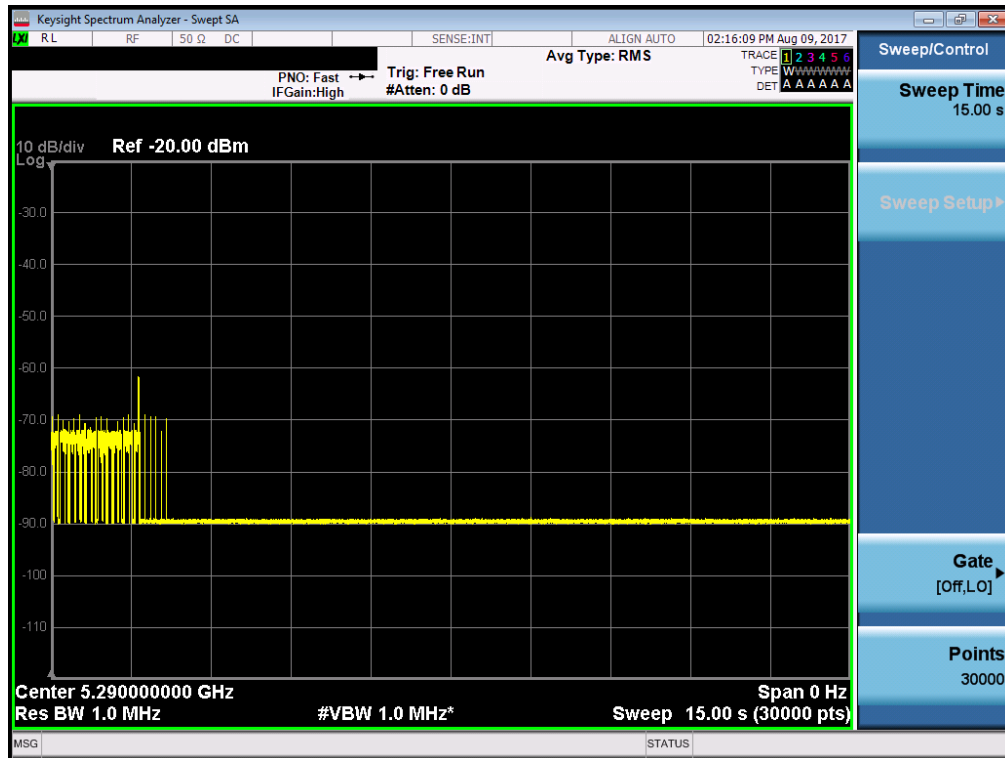




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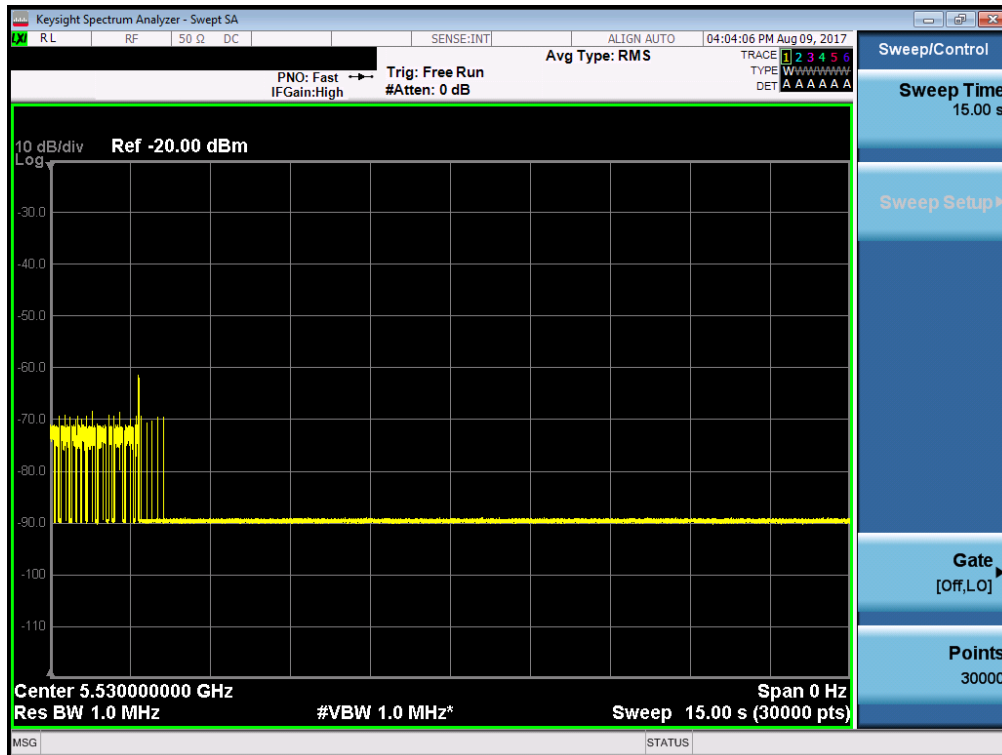
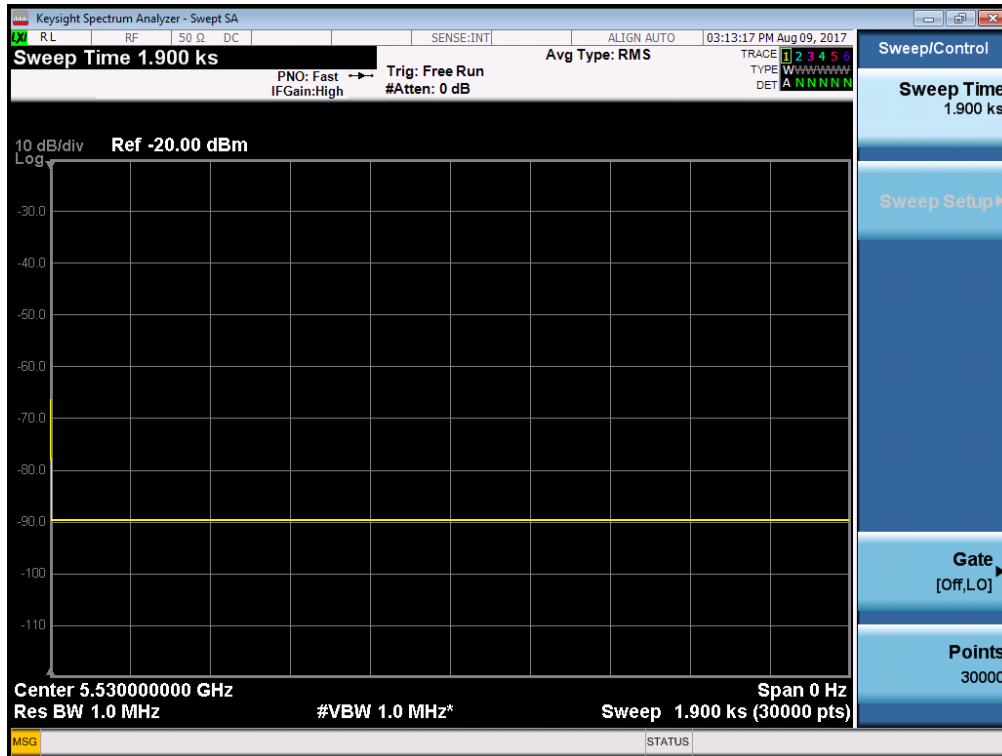




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80MHz/5530MHz





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