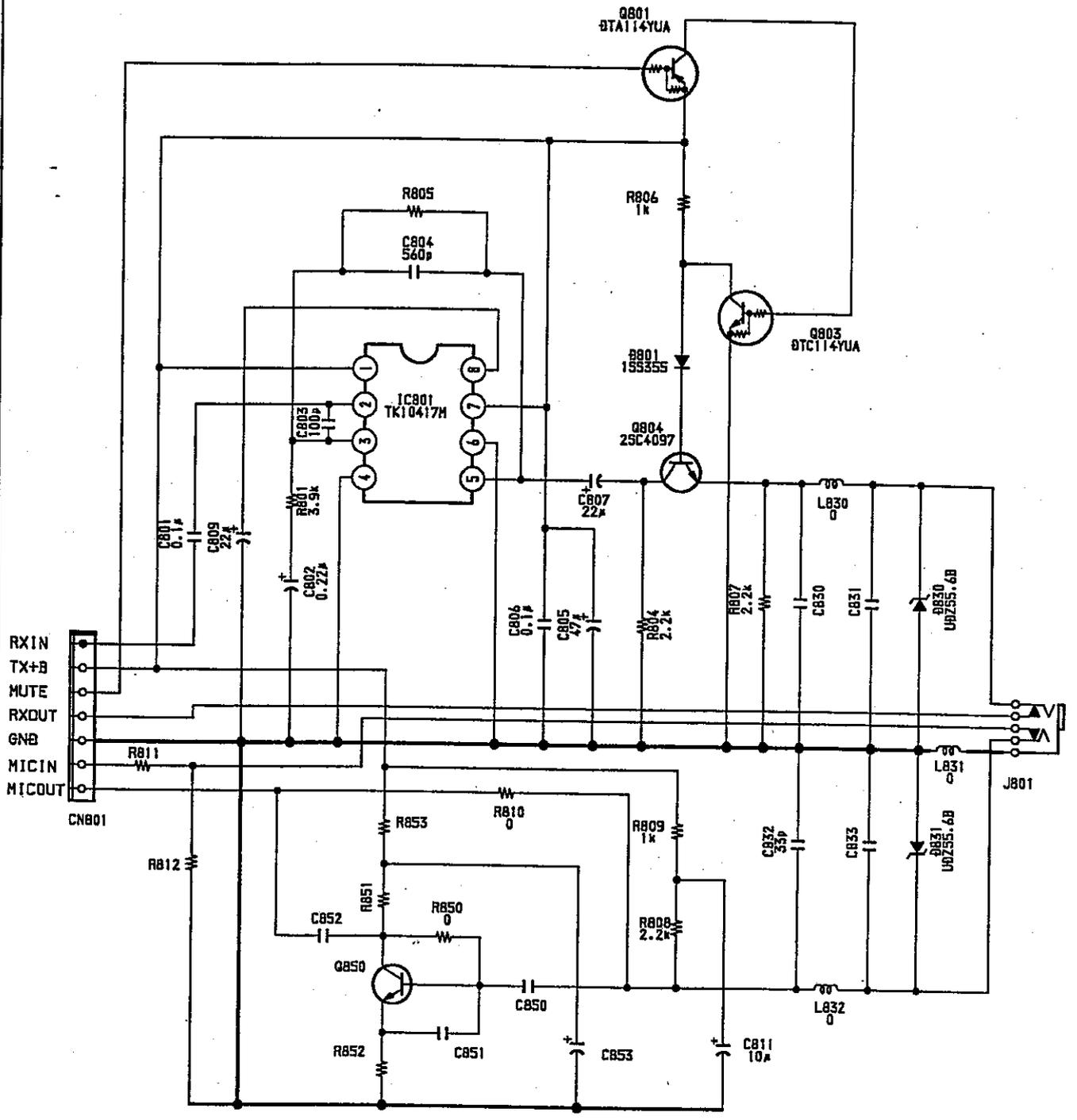


EXHIBIT D

Circuit Diagram

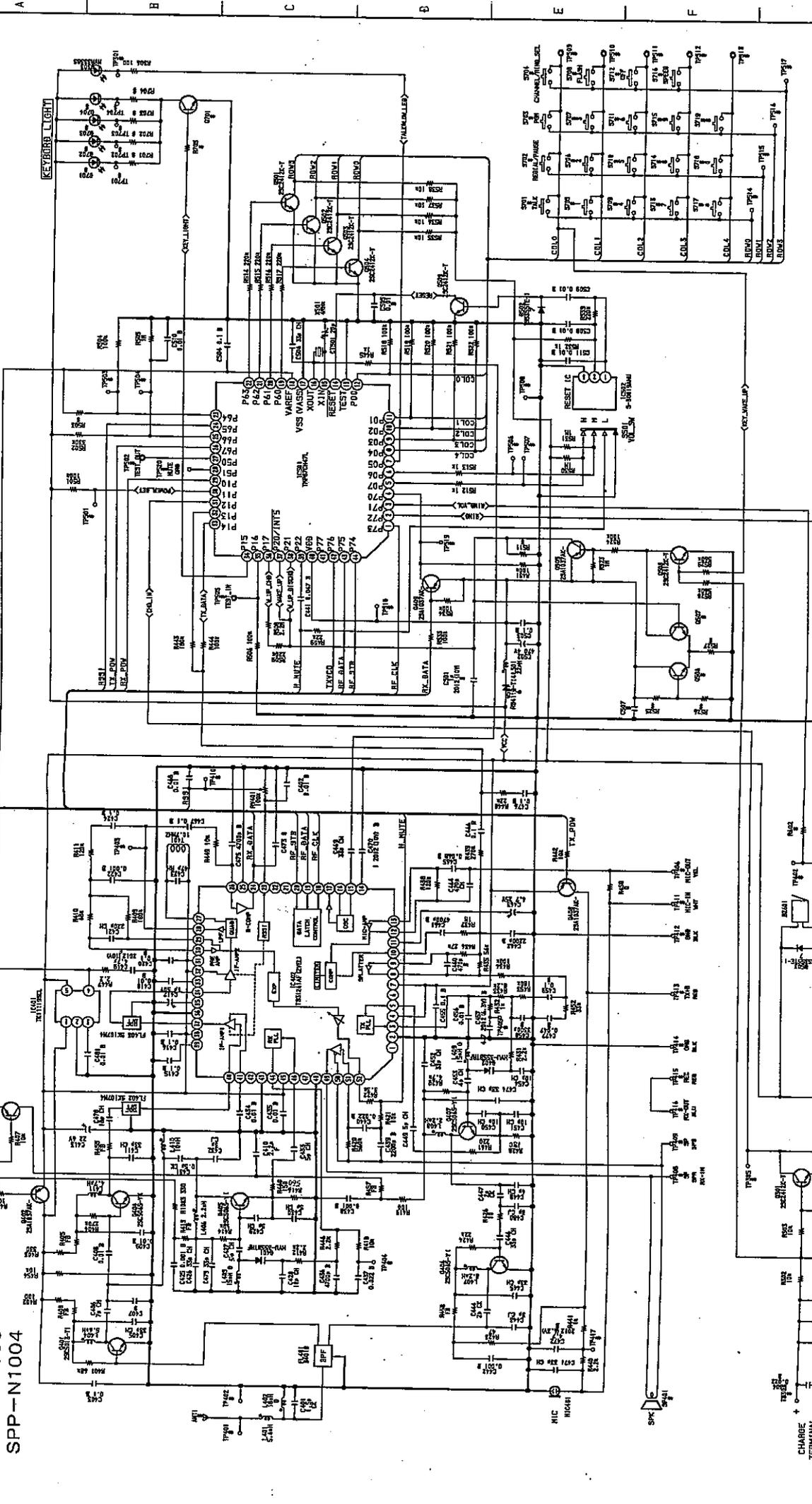
A
B
C
D
E
F



E	△x					UNIT	TOLERANCE	USED ON	RANK
	△x					ANGLE			FAMILY
	△x					SCALE		ORIGINAL MODEL	
	△x					MATERIAL (COLOR)		FINISH (COLOR)	
	△x					DESCRIPTION	HAND-SUB		
	△x				(E)				
F	HISTORY	SUFFIX	ECN-NO.	REVISION		(L) SPP-N1003 HAND SUB 回路図 PART NO.			
	X COUNT	REPL.	DATE	SIGN.					
	DRAWN BY	PLANNED BY	CHECKED BY	APPROVED BY	TENTATIVE				
					MODEL	SPP-N1003			
					PART NO.				
						SHEET			

SPP-N1000
 SPP-N1001
 SPP-N1003
 SPP-N1004

1 2 3 4 5 6 7 8 9 10 11 12



REV	DATE	DESCRIPTION	DESIGNED BY	CHECKED BY	DATE
ΔX					

REV	DATE	DESCRIPTION	DESIGNED BY	CHECKED BY	DATE
ΔX					

REV	DATE	DESCRIPTION	DESIGNED BY	CHECKED BY	DATE
ΔX					

REV	DATE	DESCRIPTION	DESIGNED BY	CHECKED BY	DATE
ΔX					

900MHZ BASIC HAND

