

Appendix C.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	11.19	78.95	1.03	12.22	≤23.98	PASS
		5280	10.68	75.00	1.25	11.93	≤23.98	PASS
		5320	10.47	75.00	1.25	11.72	≤23.98	PASS
11N20SISO	Ant0	5260	11.30	77.27	1.12	12.42	≤23.98	PASS
		5280	10.89	78.26	1.06	11.95	≤23.98	PASS
		5320	10.55	78.26	1.06	11.61	≤23.98	PASS
11N40SISO	Ant0	5270	11.14	78.26	1.06	12.20	≤23.98	PASS
		5310	10.51	78.26	1.06	11.57	≤23.98	PASS
11AC20SISO	Ant0	5260	11.25	76.19	1.18	12.43	≤23.98	PASS
		5280	10.81	76.19	1.18	11.99	≤23.98	PASS
		5320	10.55	76.19	1.18	11.73	≤23.98	PASS
11AC40SISO	Ant0	5270	10.98	75.00	1.25	12.23	≤23.98	PASS
		5310	10.40	75.00	1.25	11.65	≤23.98	PASS
11AC80SISO	Ant0	5290	10.84	76.19	1.18	12.02	≤23.98	PASS
11AX20SISO	Ant0	5260	12.08	77.27	1.12	13.20	≤23.98	PASS
		5280	11.64	78.26	1.06	12.70	≤23.98	PASS
		5320	11.35	78.26	1.06	12.41	≤23.98	PASS
11AX40SISO	Ant0	5270	11.82	77.27	1.12	12.94	≤23.98	PASS
		5310	11.22	77.27	1.12	12.34	≤23.98	PASS
11AX80SISO	Ant0	5290	11.53	78.26	1.06	12.59	≤23.98	PASS

Appendix C.5: Maximum power spectral density

Test Result

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5260	1.41	≤11.00	PASS
		5280	1.23	≤11.00	PASS
		5320	1.12	≤11.00	PASS
11N20SISO	Ant0	5260	1.62	≤11.00	PASS
		5280	1.14	≤11.00	PASS
		5320	0.90	≤11.00	PASS
11N40SISO	Ant0	5270	-1.79	≤11.00	PASS
		5310	-2.53	≤11.00	PASS
11AC20SISO	Ant0	5260	1.58	≤11.00	PASS
		5280	1.15	≤11.00	PASS
		5320	0.96	≤11.00	PASS
11AC40SISO	Ant0	5270	-1.91	≤11.00	PASS
		5310	-2.52	≤11.00	PASS
11AC80SISO	Ant0	5290	-5.14	≤11.00	PASS
11AX20SISO	Ant0	5260	2.26	≤11.00	PASS
		5280	1.56	≤11.00	PASS
		5320	1.37	≤11.00	PASS
11AX40SISO	Ant0	5270	-1.31	≤11.00	PASS
		5310	-1.94	≤11.00	PASS
11AX80SISO	Ant0	5290	-4.81	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

11A_Ant0_5260



11A_Ant0_5280



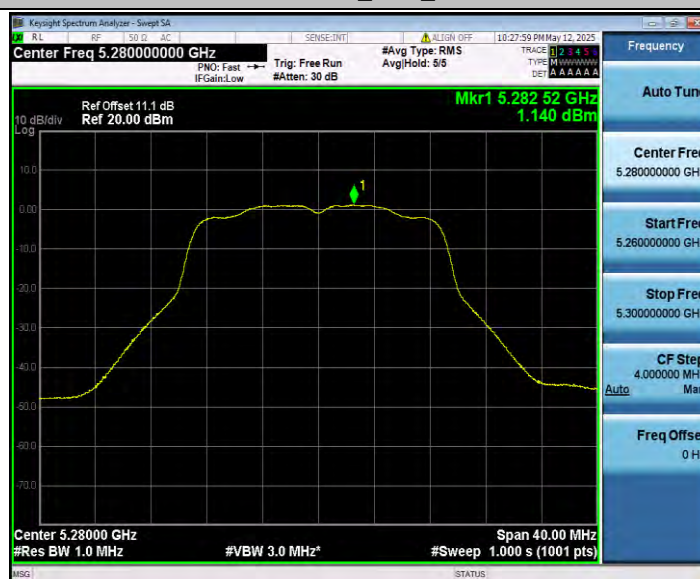
11A_Ant0_5320



11N20SISO_Ant0_5260



11N20SISO_Ant0_5280



11N20SISO_Ant0_5320



11N40SISO_Ant0_5270



11N40SISO_Ant0_5310



11AC20SISO_Ant0_5260



11AC20SISO_Ant0_5280



11AC20SISO_Ant0_5320



11AC40SISO_Ant0_5270



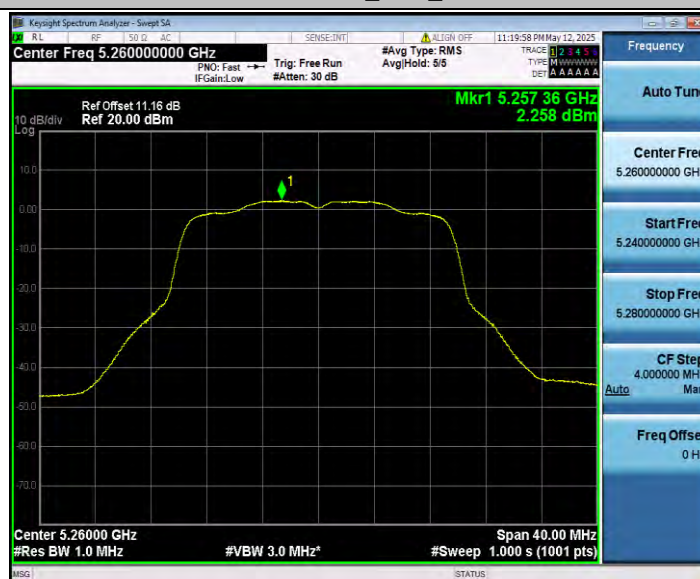
11AC40SISO_Ant0_5310



11AC80SISO_Ant0_5290



11AX20SISO_Ant0_5260



11AX20SISO_Ant0_5280



11AX20SISO_Ant0_5320



11AX40SISO_Ant0_5270



11AX40SISO_Ant0_5310



11AX80SISO_Ant0_5290

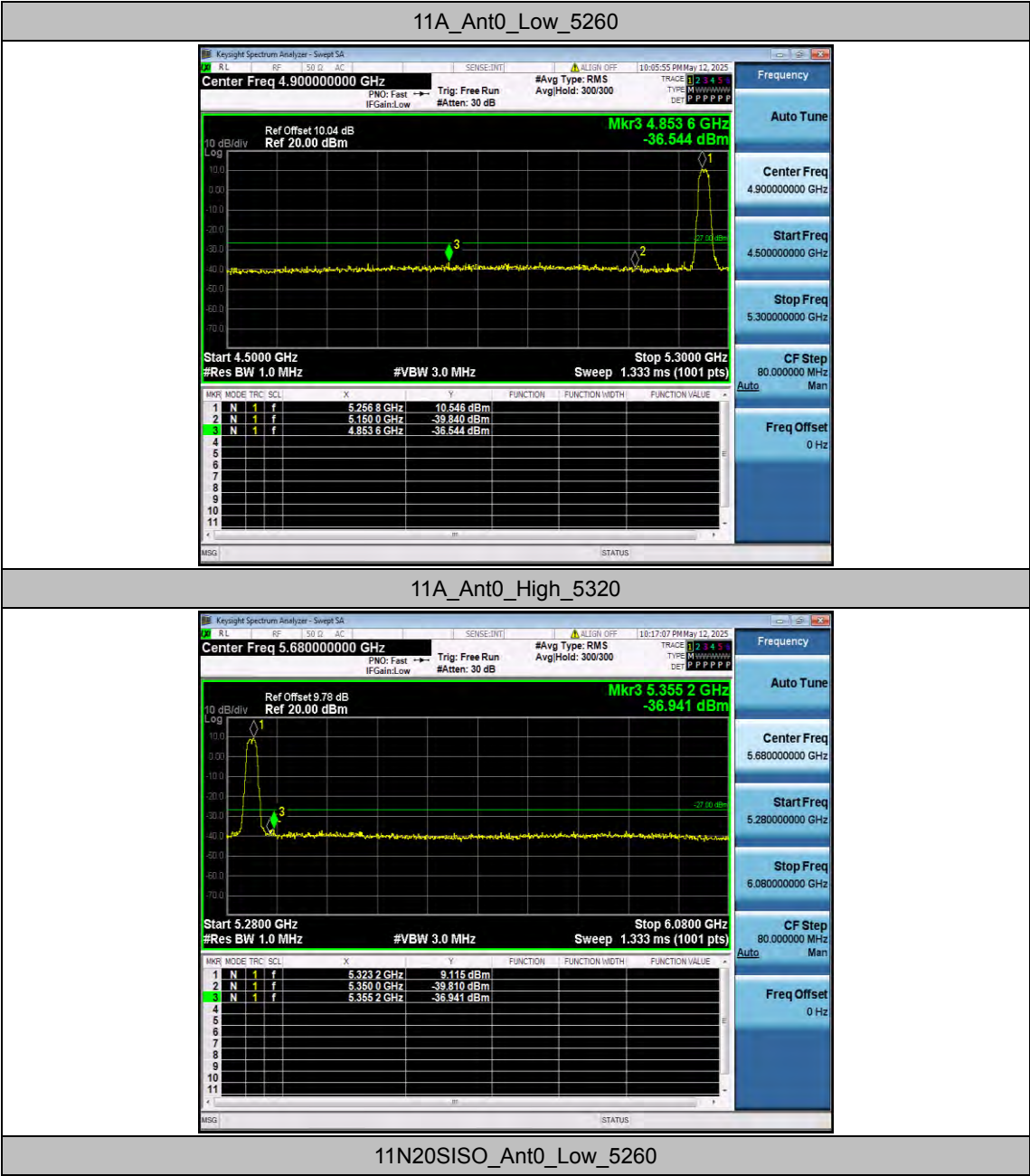


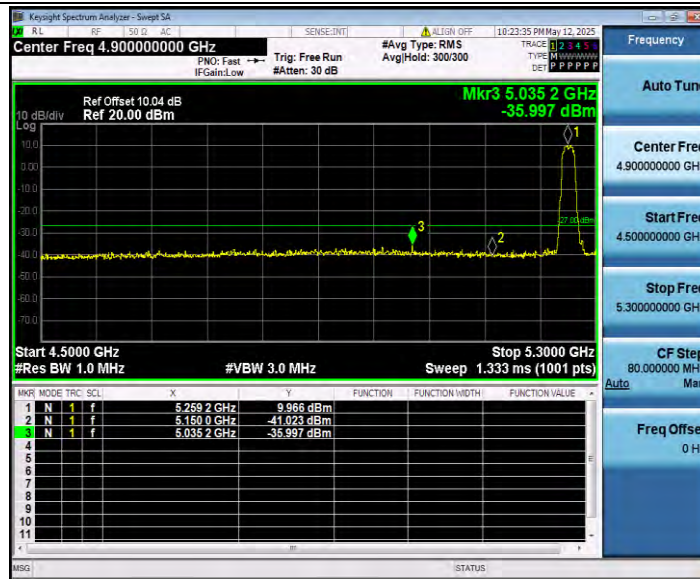
Appendix C.6: Band edge measurements

Test Result B2

TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5260	-36.54	≤ -27	PASS
		High	5320	-36.94	≤ -27	PASS
11N20SISO	Ant0	Low	5260	-36	≤ -27	PASS
		High	5320	-37.94	≤ -27	PASS
11N40SISO	Ant0	Low	5270	-36.77	≤ -27	PASS
		High	5310	-35.83	≤ -27	PASS
11AC20SISO	Ant0	Low	5260	-36.77	≤ -27	PASS
		High	5320	-37.06	≤ -27	PASS
11AC40SISO	Ant0	Low	5270	-35.91	≤ -27	PASS
		High	5310	-36.69	≤ -27	PASS
11AC80SISO	Ant0	Low	5290	-37.5	≤ -27	PASS
		High	5290	-34.36	≤ -27	PASS
11AX20SISO	Ant0	Low	5260	-36.54	≤ -27	PASS
		High	5320	-36.52	≤ -27	PASS
11AX40SISO	Ant0	Low	5270	-37.05	≤ -27	PASS
		High	5310	-32.69	≤ -27	PASS
11AX80SISO	Ant0	Low	5290	-37.24	≤ -27	PASS
		High	5290	-32.66	≤ -27	PASS

Test Graphs B2

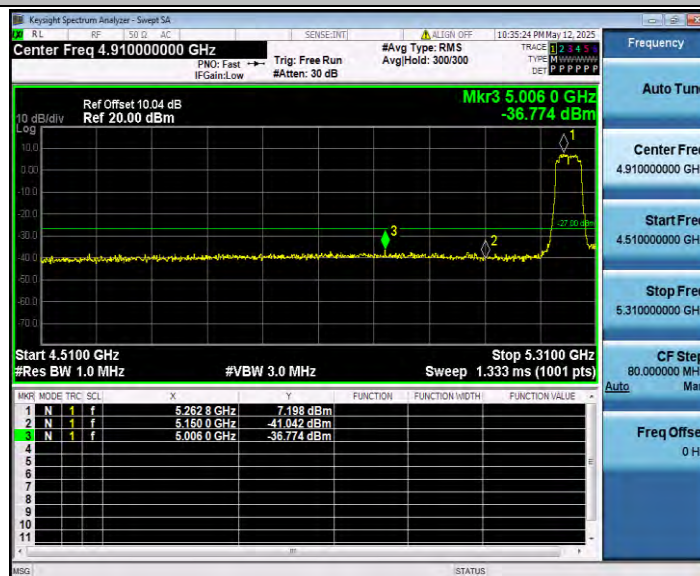




11N20SISO_Ant0_High_5320



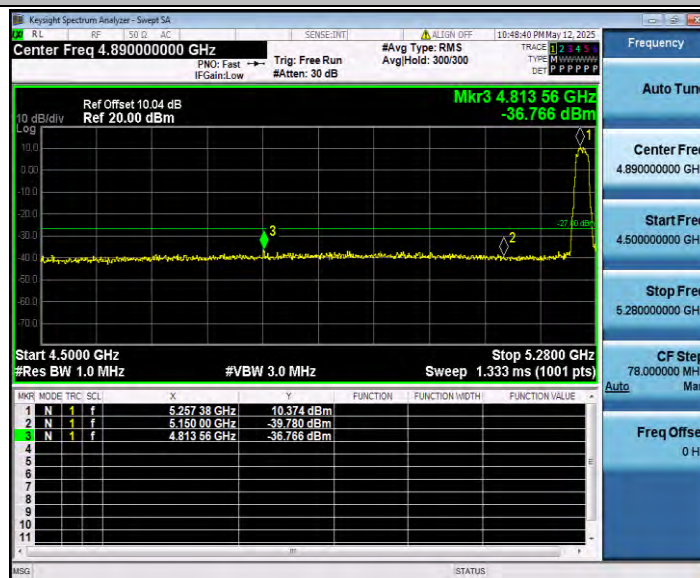
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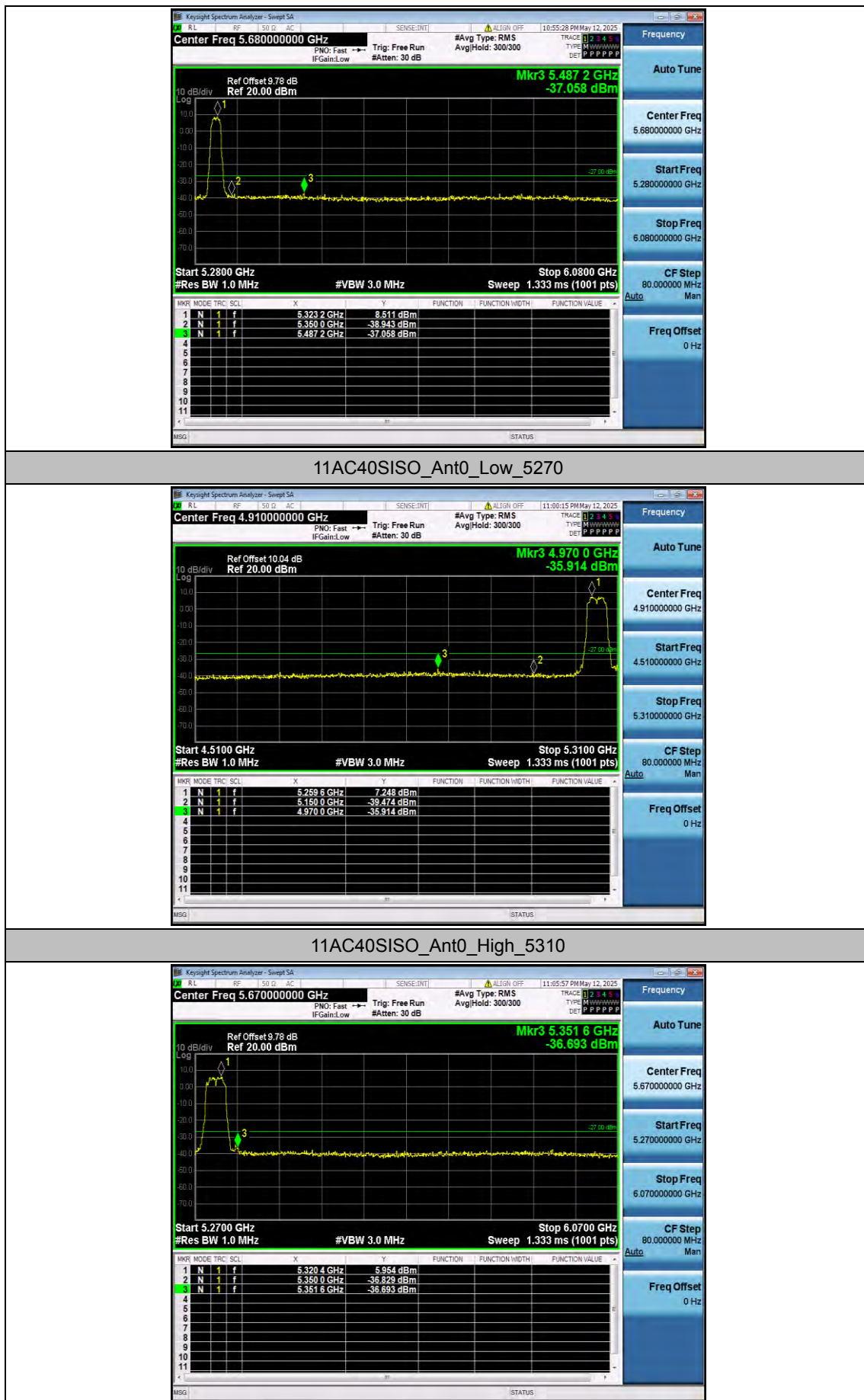
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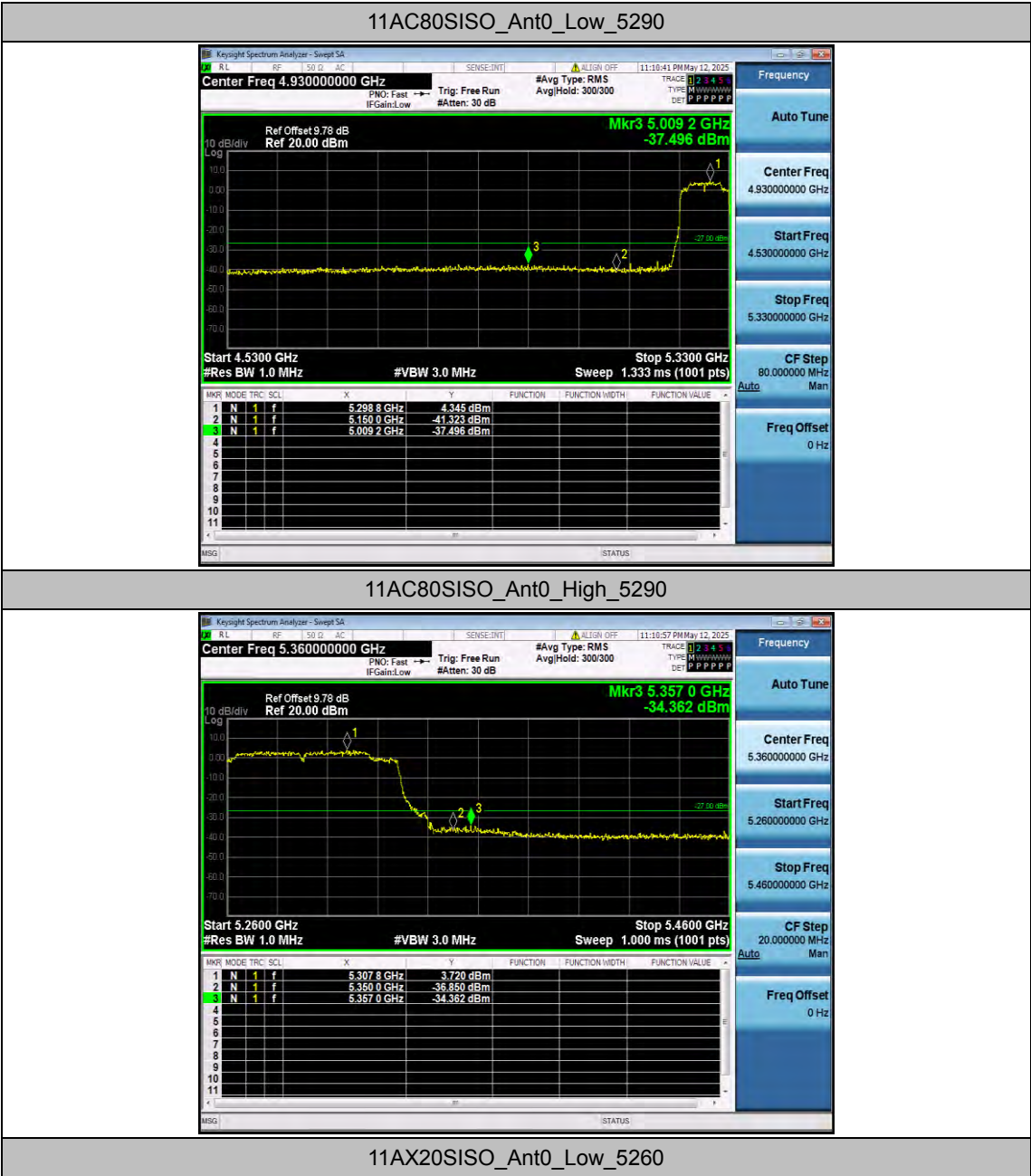


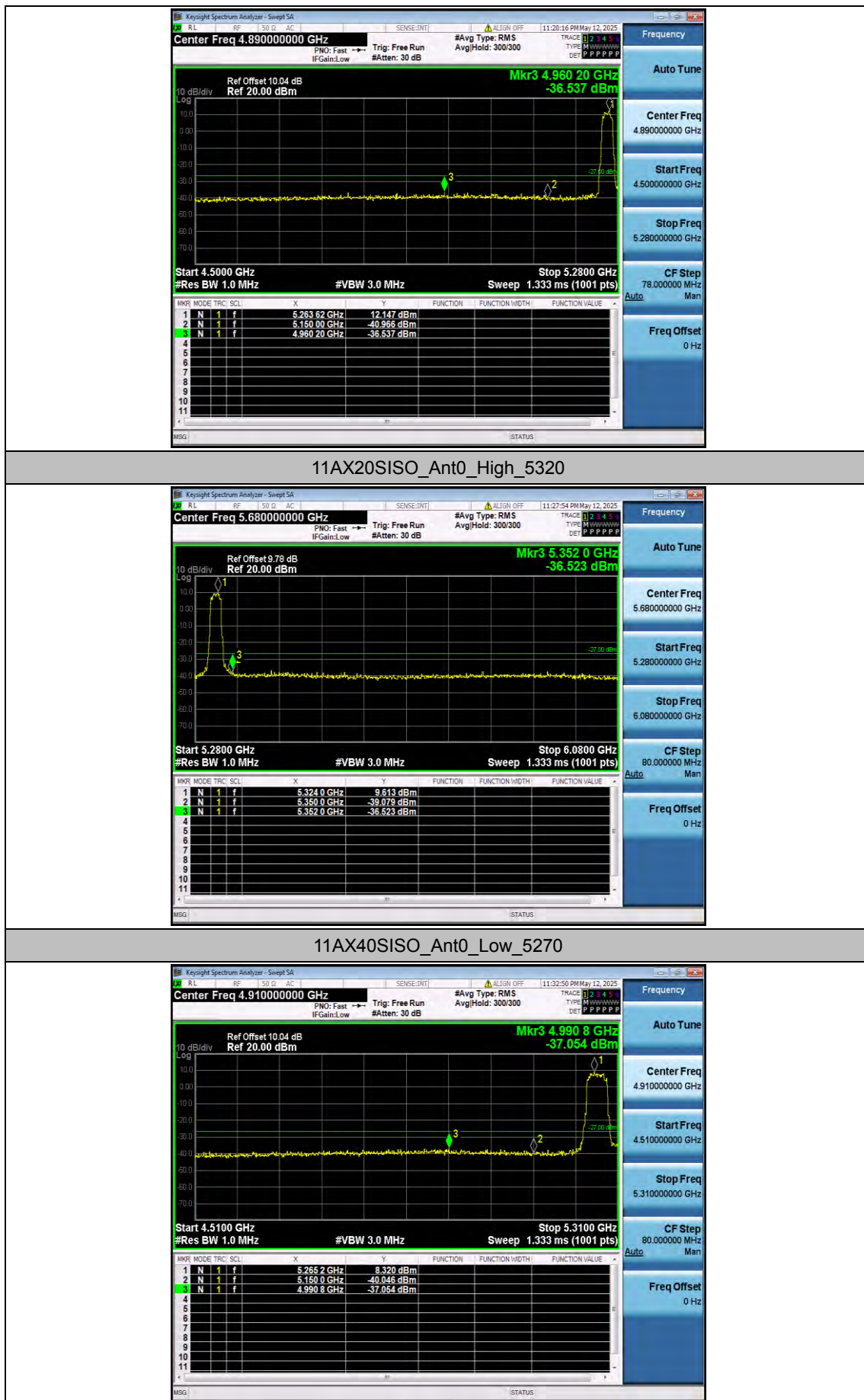
11AC20SISO_Ant0_Low_5260



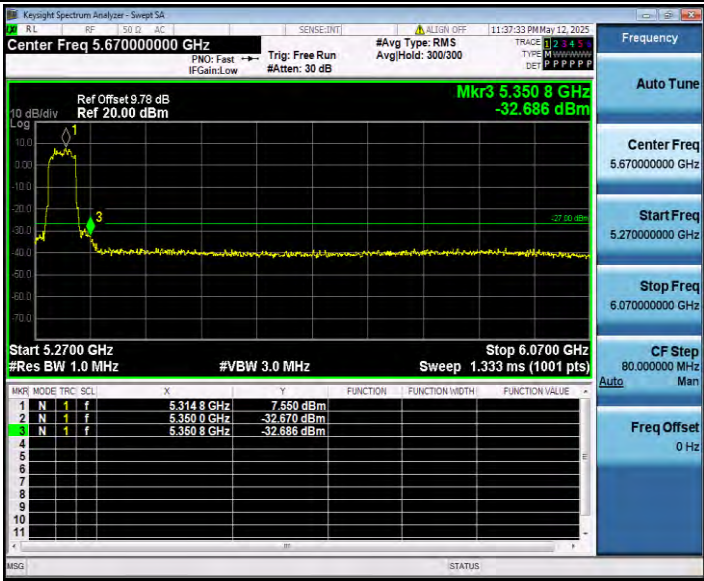
11AC20SISO_Ant0_High_5320



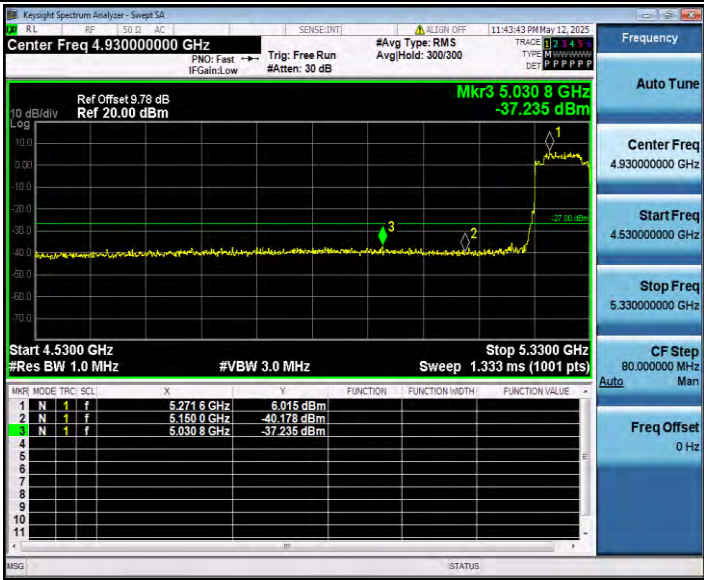




11AX40SISO_Ant0_High_5310



11AX80SISO_Ant0_Low_5290



11AX80SISO_Ant0_High_5290



Appendix C.7: Conducted Spurious Emission

Test Result

TestMode	Antenna	Freq(MHz)	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	30~5140	3039.62	-46.3	≤-27	PASS
			5360~27000	24280.57	-37.16	≤-27	PASS
		5280	30~5140	3171.97	-46.24	≤-27	PASS
			5360~27000	26559.27	-37.26	≤-27	PASS
		5320	30~5140	5134.72	-46.05	≤-27	PASS
			5360~27000	26800.19	-37.05	≤-27	PASS
11N20SISO	Ant0	5260	30~5140	4982.78	-45.82	≤-27	PASS
			5360~27000	24239.46	-36.44	≤-27	PASS
		5280	30~5140	2645.13	-45.34	≤-27	PASS
			5360~27000	24261.82	-36.9	≤-27	PASS
		5320	30~5140	3050.52	-46.88	≤-27	PASS
			5360~27000	26786.49	-37.18	≤-27	PASS
11N40SISO	Ant0	5270	30~5140	4960.81	-45.38	≤-27	PASS
			5360~27000	24276.25	-37.02	≤-27	PASS
		5310	30~5140	4943.09	-46.22	≤-27	PASS
			5360~27000	26546.28	-36.24	≤-27	PASS
11AC20SISO	Ant0	5260	30~5140	5099.97	-44.12	≤-27	PASS
			5360~27000	24822.29	-36.82	≤-27	PASS
		5280	30~5140	5086	-45.33	≤-27	PASS
			5360~27000	24287.79	-36.64	≤-27	PASS
		5320	30~5140	2633.89	-46.5	≤-27	PASS
			5360~27000	24887.21	-37.44	≤-27	PASS
11AC40SISO	Ant0	5270	30~5140	5077.32	-45.55	≤-27	PASS
			5360~27000	26003.12	-36.56	≤-27	PASS
		5310	30~5140	3037.58	-46.65	≤-27	PASS
			5360~27000	23497.93	-37	≤-27	PASS
11AC80SISO	Ant0	5290	30~5140	5129.95	-43.53	≤-27	PASS
			5360~27000	5362.89	-37.53	≤-27	PASS
11AX20SISO	Ant0	5260	30~5140	5097.76	-45.77	≤-27	PASS
			5360~27000	26502.28	-37.16	≤-27	PASS
		5280	30~5140	3283.54	-45.97	≤-27	PASS
			5360~27000	24815.8	-37	≤-27	PASS
		5320	30~5140	4964.56	-46.26	≤-27	PASS
			5360~27000	25164.21	-37.29	≤-27	PASS
11AX40SISO	Ant0	5270	30~5140	5102.53	-44.91	≤-27	PASS
			5360~27000	24194.73	-36.82	≤-27	PASS

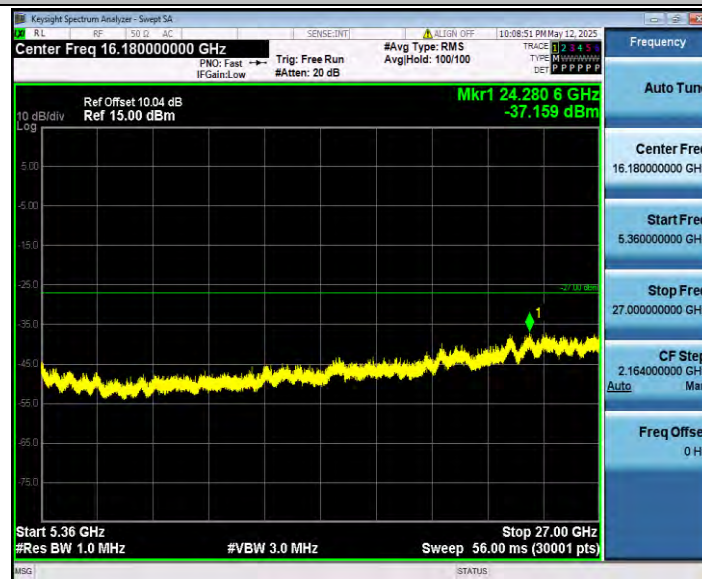
		5310	30~5140	5049.04	-45.03	≤ -27	PASS
			5360~27000	26042.07	-37.18	≤ -27	PASS
11AX80SISO	Ant0	5290	30~5140	5138.64	-44.85	≤ -27	PASS
			5360~27000	24277.69	-36.02	≤ -27	PASS

Test Graphs

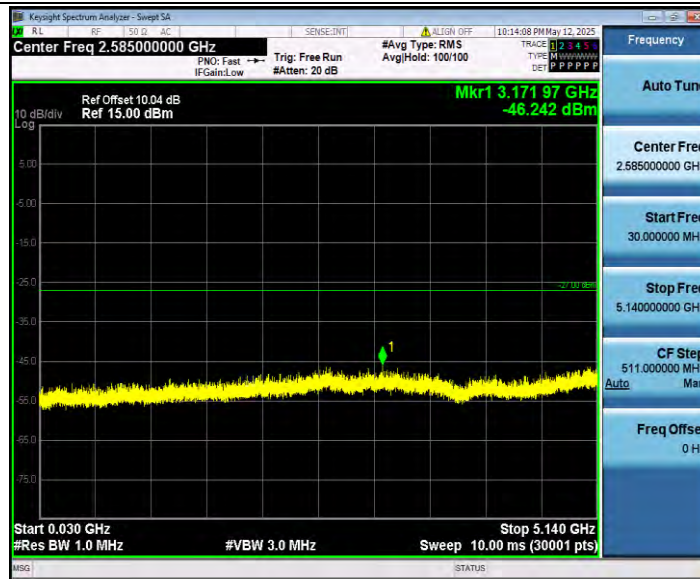
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11A_Ant0_5260_5360~27000



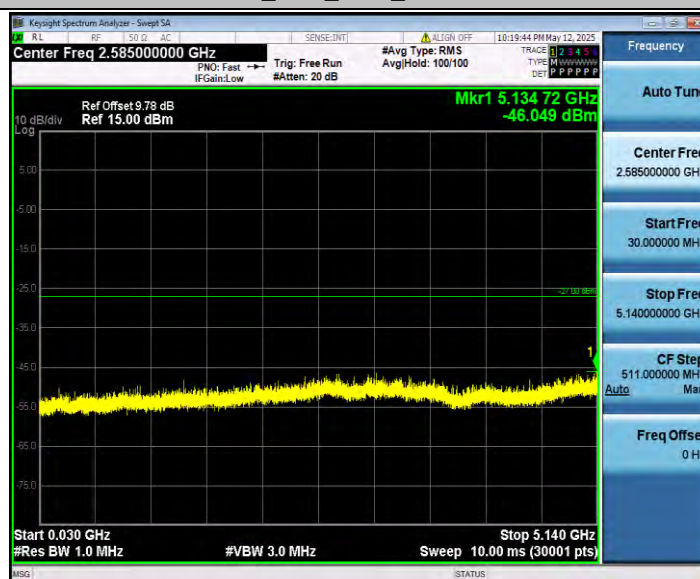
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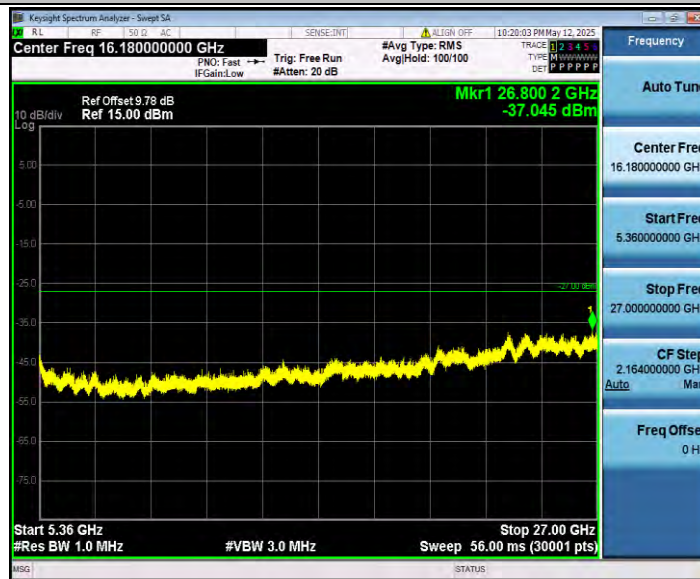
11A_Ant0_5280_5360~27000



11A_Ant0_5320_30~5140



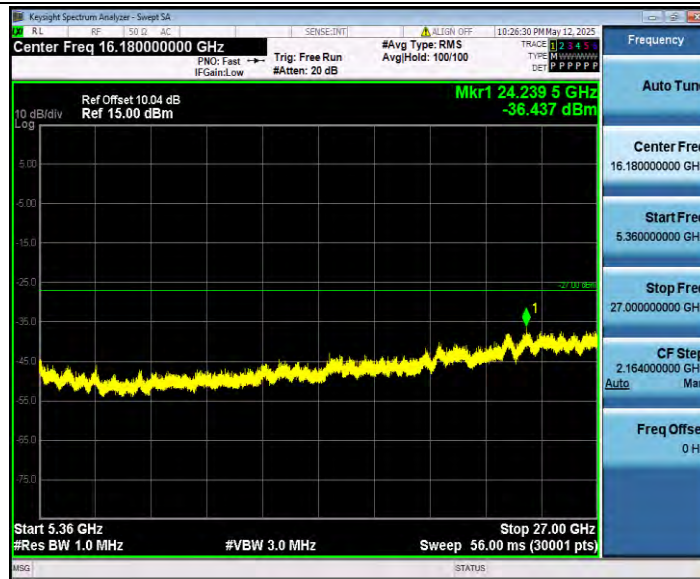
11A_Ant0_5320_5360~27000



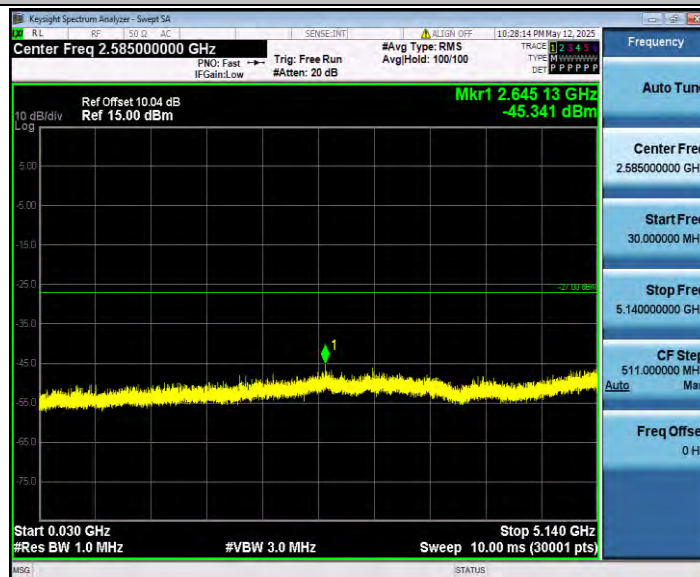
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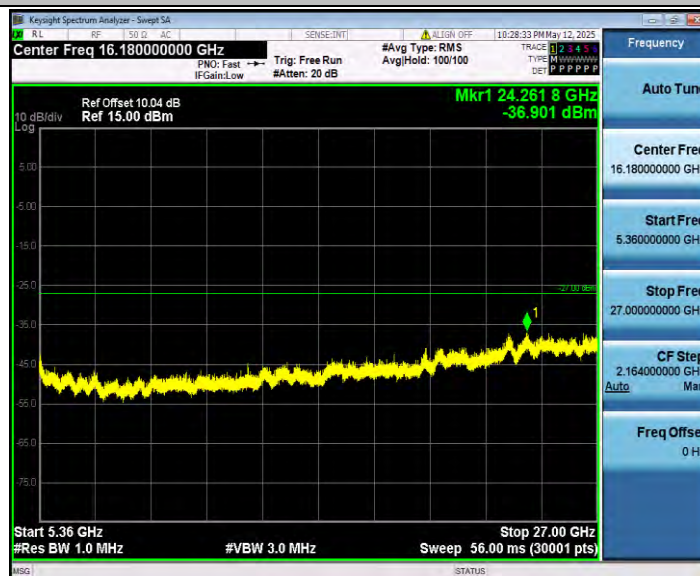
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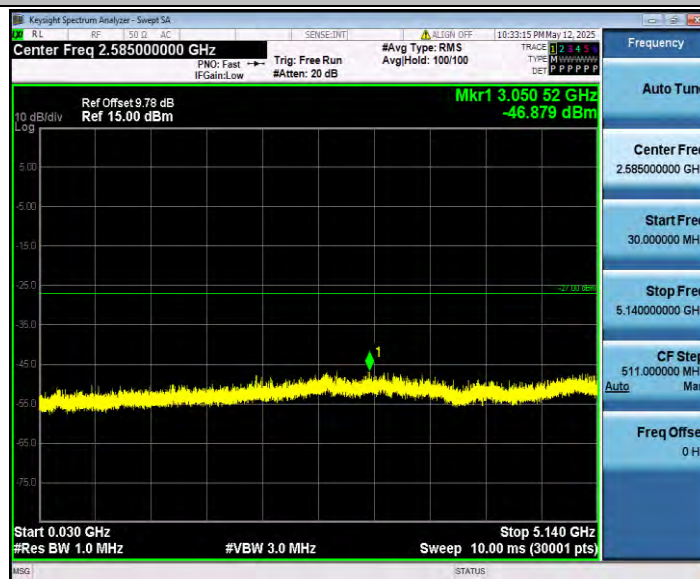
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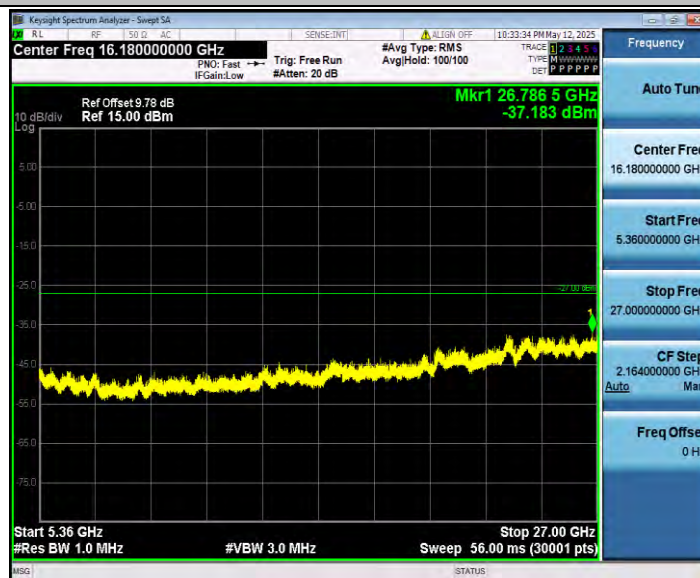
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11N20SISO_Ant0_5320_30~5140



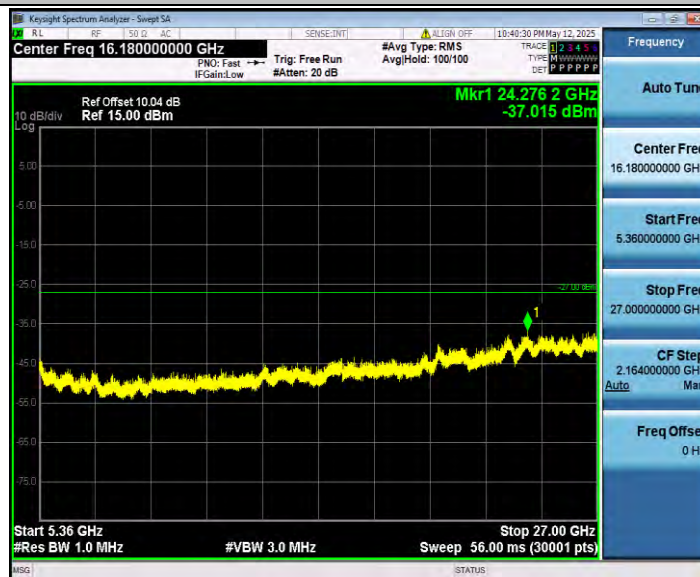
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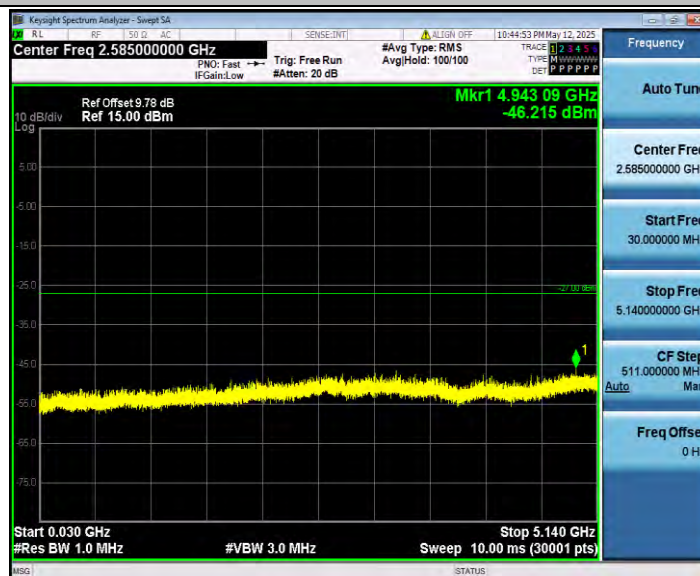
11N40SISO_Ant0_5270_30~5140



11N40SISO_Ant0_5270_5360~27000



11N40SISO_Ant0_5310_30~5140



11N40SISO_Ant0_5310_5360~27000



11AC20SISO_Ant0_5260_30~5140



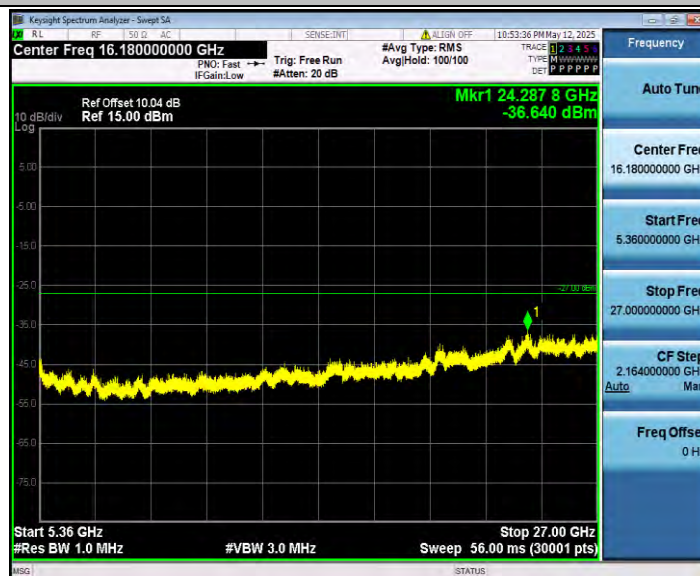
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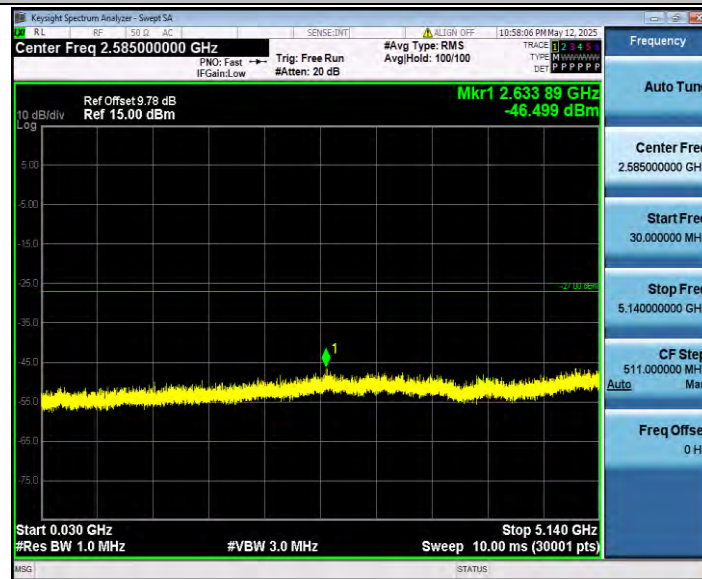
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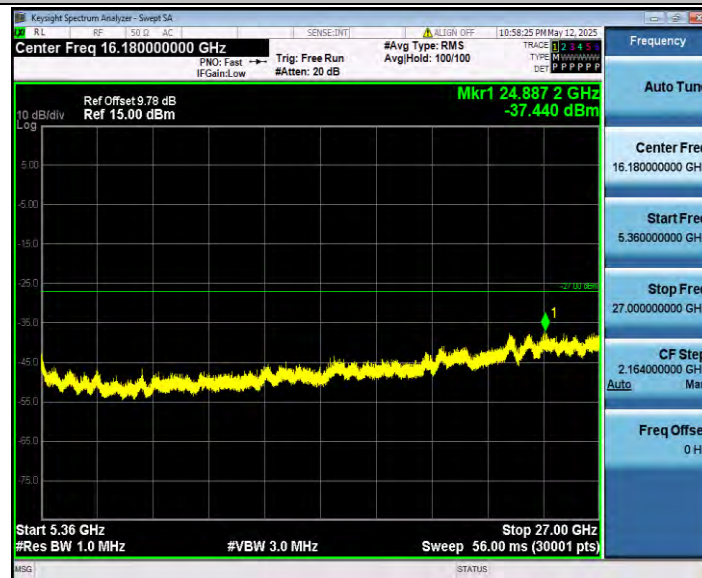
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11AC20SISO_Ant0_5320_30~5140



11AC20SISO_Ant0_5320_5360~27000



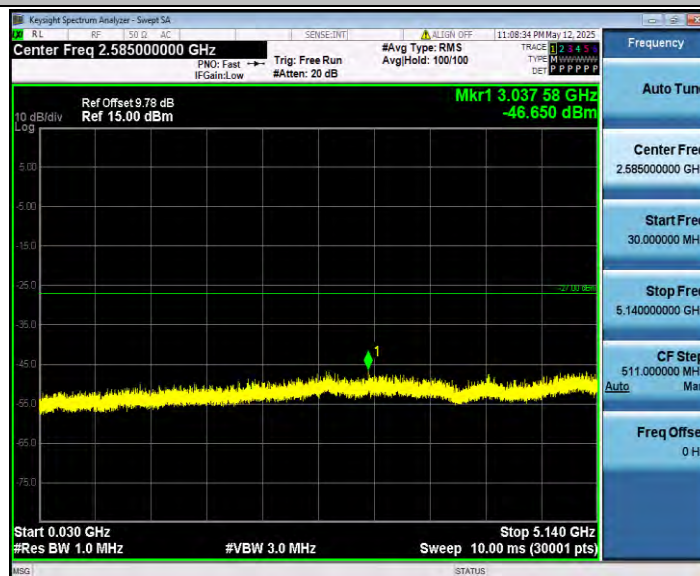
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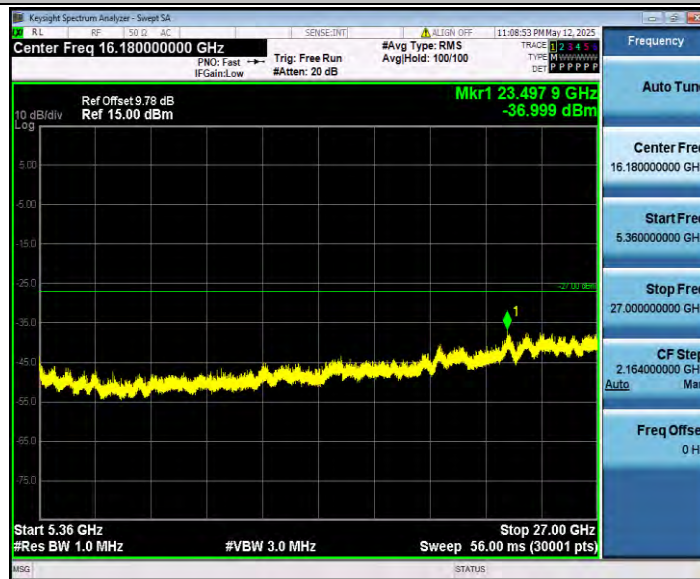
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11AC40SISO_Ant0_5310_30~5140



11AC40SISO_Ant0_5310_5360~27000



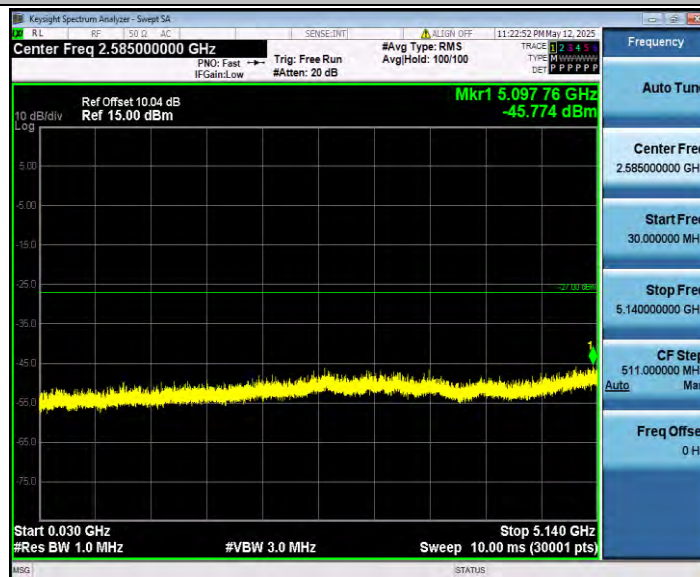
11AC80SISO_Ant0_5290_30~5140



11AC80SISO_Ant0_5290_5360~27000



11AX20SISO_Ant0_5260_30~5140



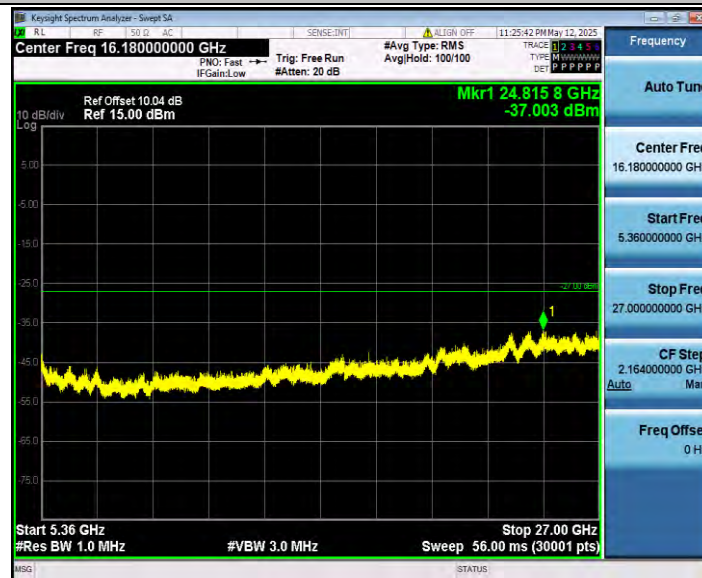
11AX20SISO_Ant0_5260_5360~27000



11AX20SISO_Ant0_5280_30~5140



11AX20SISO_Ant0_5280_5360~27000



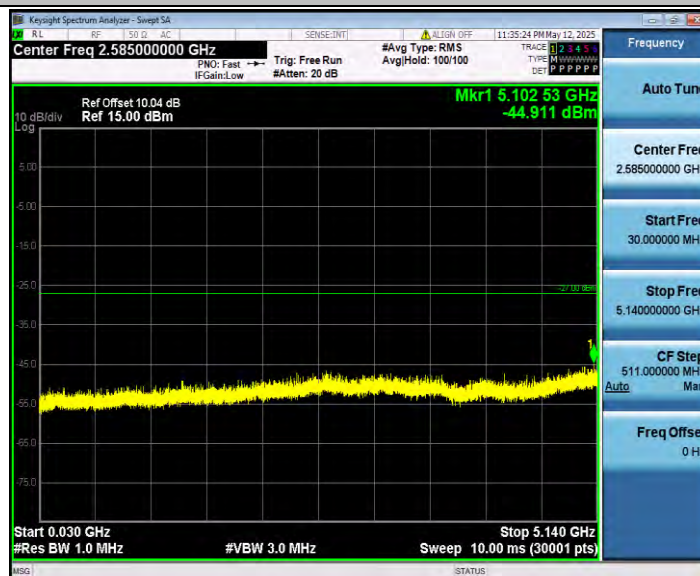
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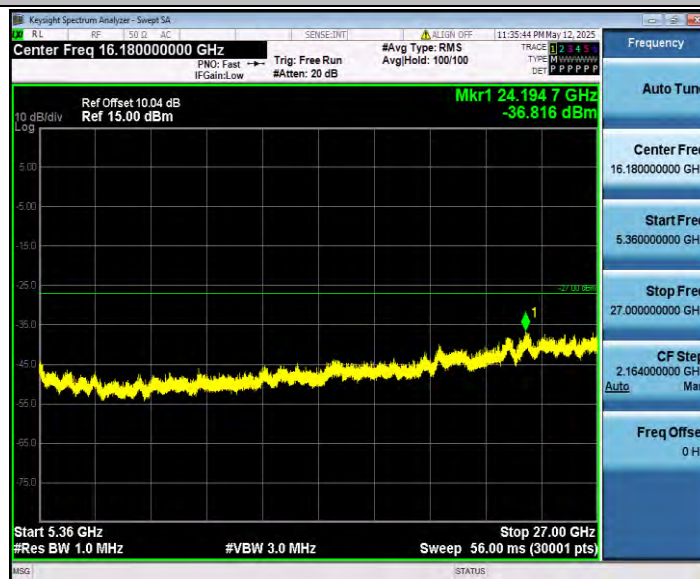
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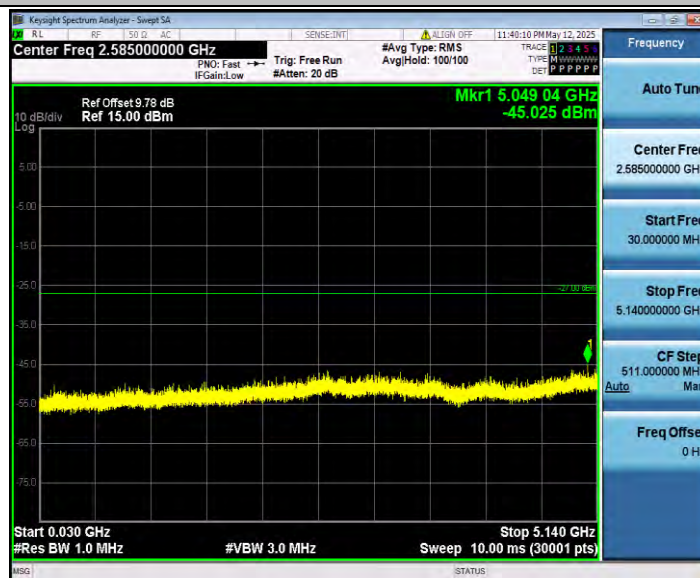
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11AX40SISO_Ant0_5270_5360~27000



11AX40SISO_Ant0_5310_30~5140



11AX40SISO_Ant0_5310_5360~27000



11AX80SISO_Ant0_5290_30~5140



11AX80SISO_Ant0_5290_5360~27000

