

ANNEX D TEST DATA

For

Project No.:	8331EU120308W
Client:	SimplyTech Electronics Inc.
Product Name:	Wireless earphone
Model No.:	CLIP-OWS-BLACK
FCC ID:	2BKTL-ELVT01
Technology:	Bluetooth BDR&EDR
Test Engineer:	<i>Mikey zhu</i>
Test Date:	2025-06-13

Test Summary

Item	Result
Duty Cycle	Pass
Bandwidth	Pass
Maximum Conducted Output Power	Pass
Carrier Frequency Separation	Pass
Number of Hopping Frequencies	Pass
Time of Occupancy (Dwell Time)	Pass
Unwanted Emissions In Non-restricted Frequency Bands	Pass

1. Duty Cycle

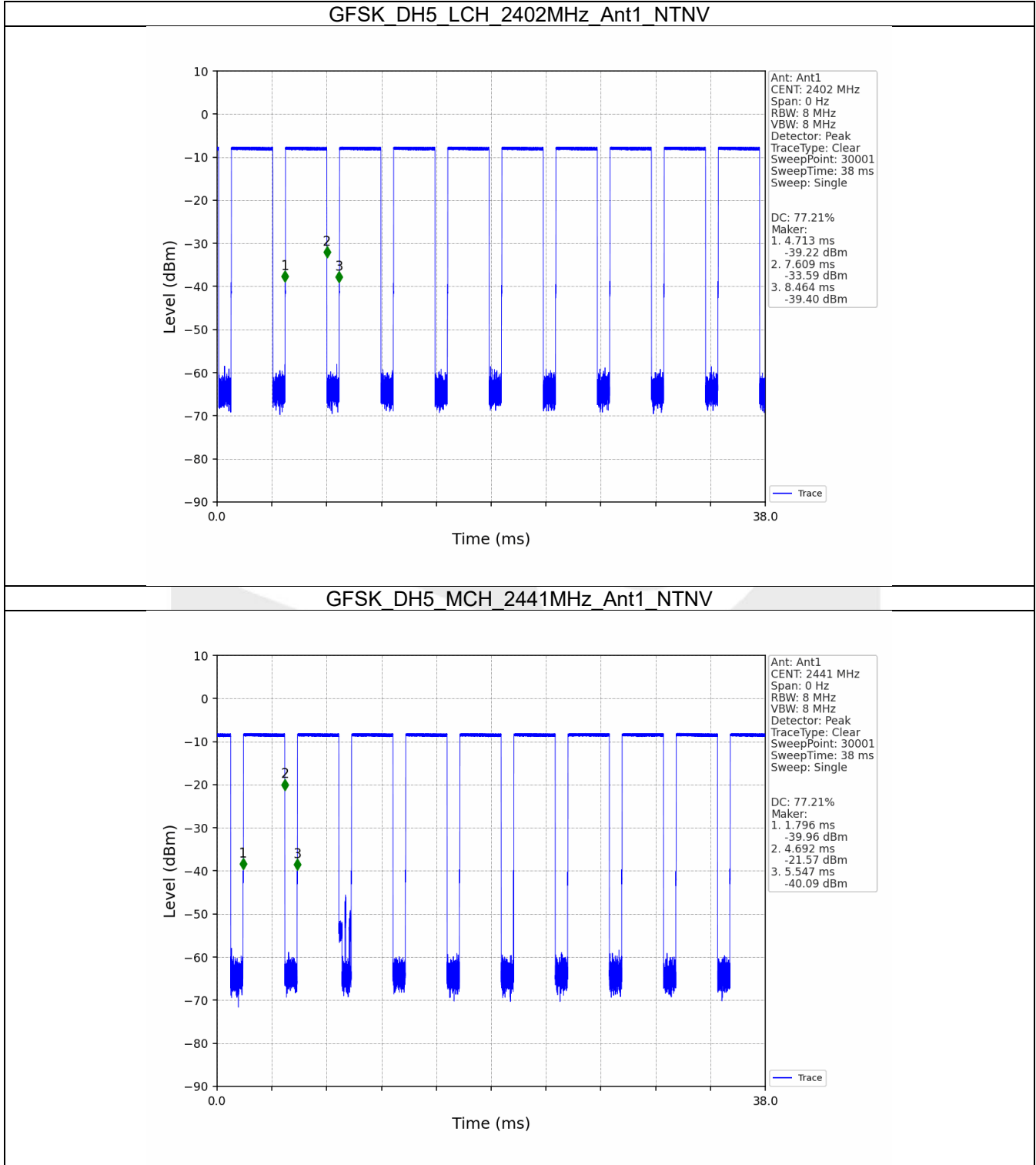
1.1 Test Result

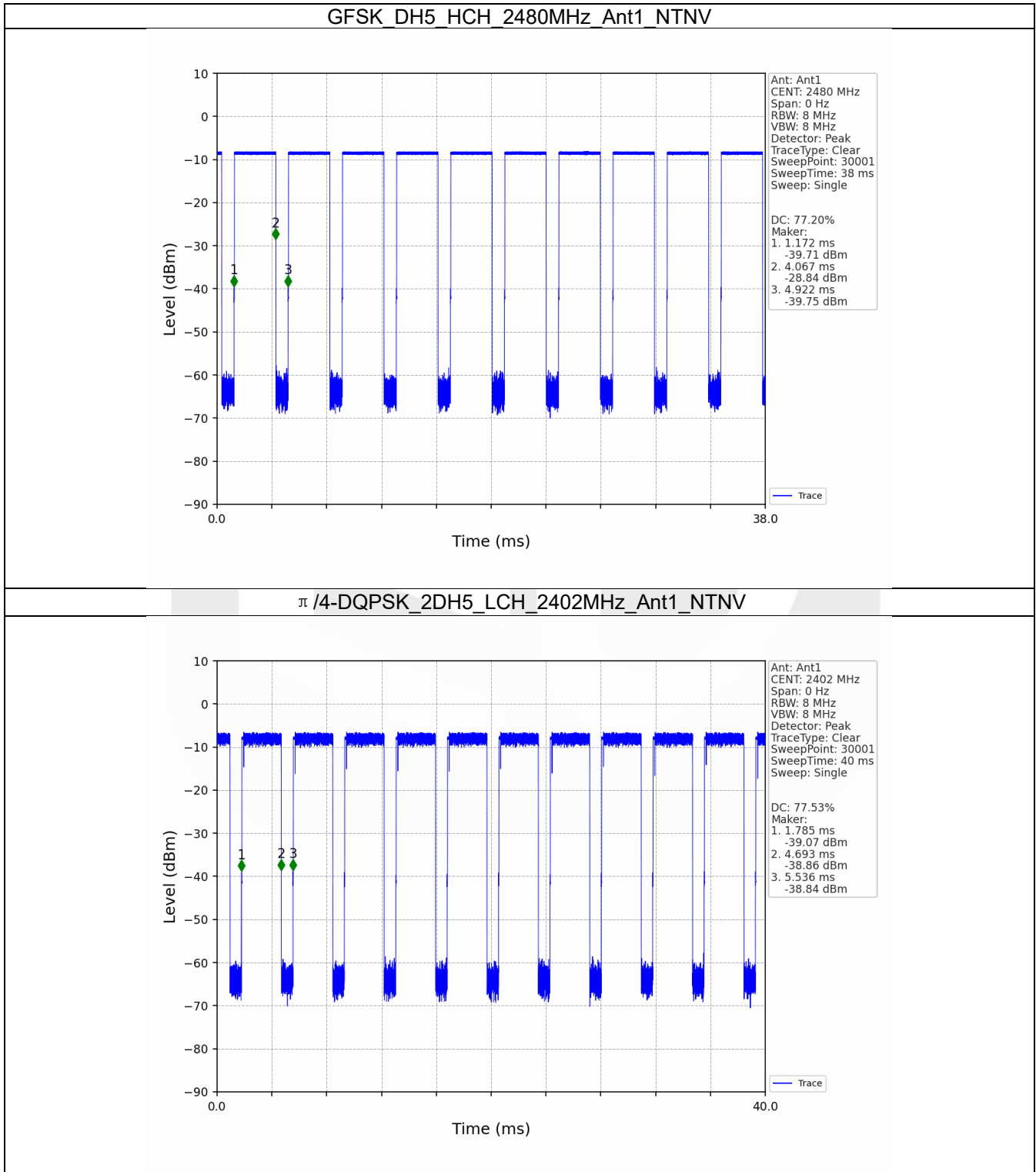
1.1.1 Ant1

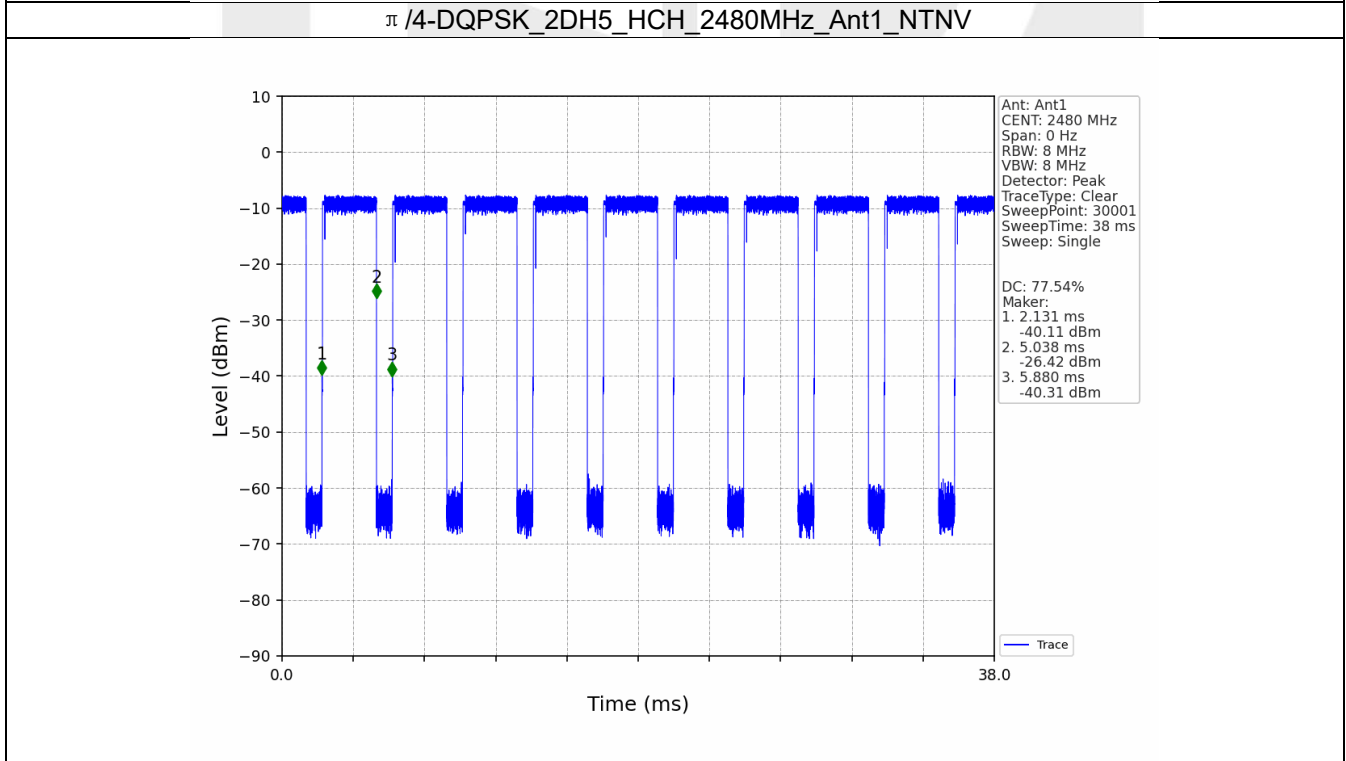
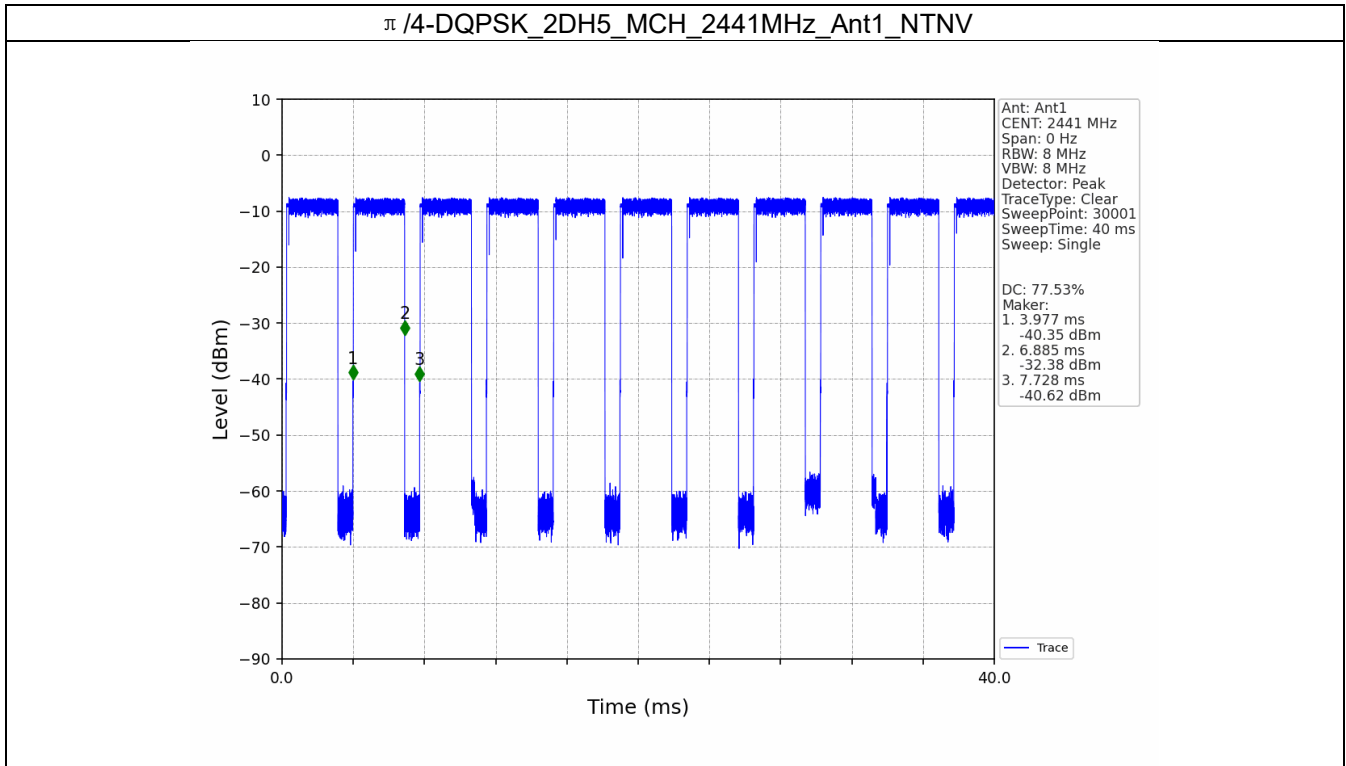
Ant1								
Mode	TX Type	Frequency (MHz)	Packet Type	T_on (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	Max. DC Variation (%)
GFSK	SISO	2402	DH5	2.896	3.751	77.21	1.12	0.03
		2441	DH5	2.896	3.751	77.21	1.12	0.03
		2480	DH5	2.895	3.750	77.20	1.12	0.03
$\pi/4$ -DQPSK	SISO	2402	2DH5	2.908	3.751	77.53	1.11	0.03
		2441	2DH5	2.908	3.751	77.53	1.11	0.03
		2480	2DH5	2.907	3.749	77.54	1.10	0.03
8DPSK	SISO	2402	3DH5	2.908	3.751	77.53	1.11	0.03
		2441	3DH5	2.908	3.749	77.57	1.10	0.01
		2480	3DH5	2.909	3.750	77.57	1.10	0.01

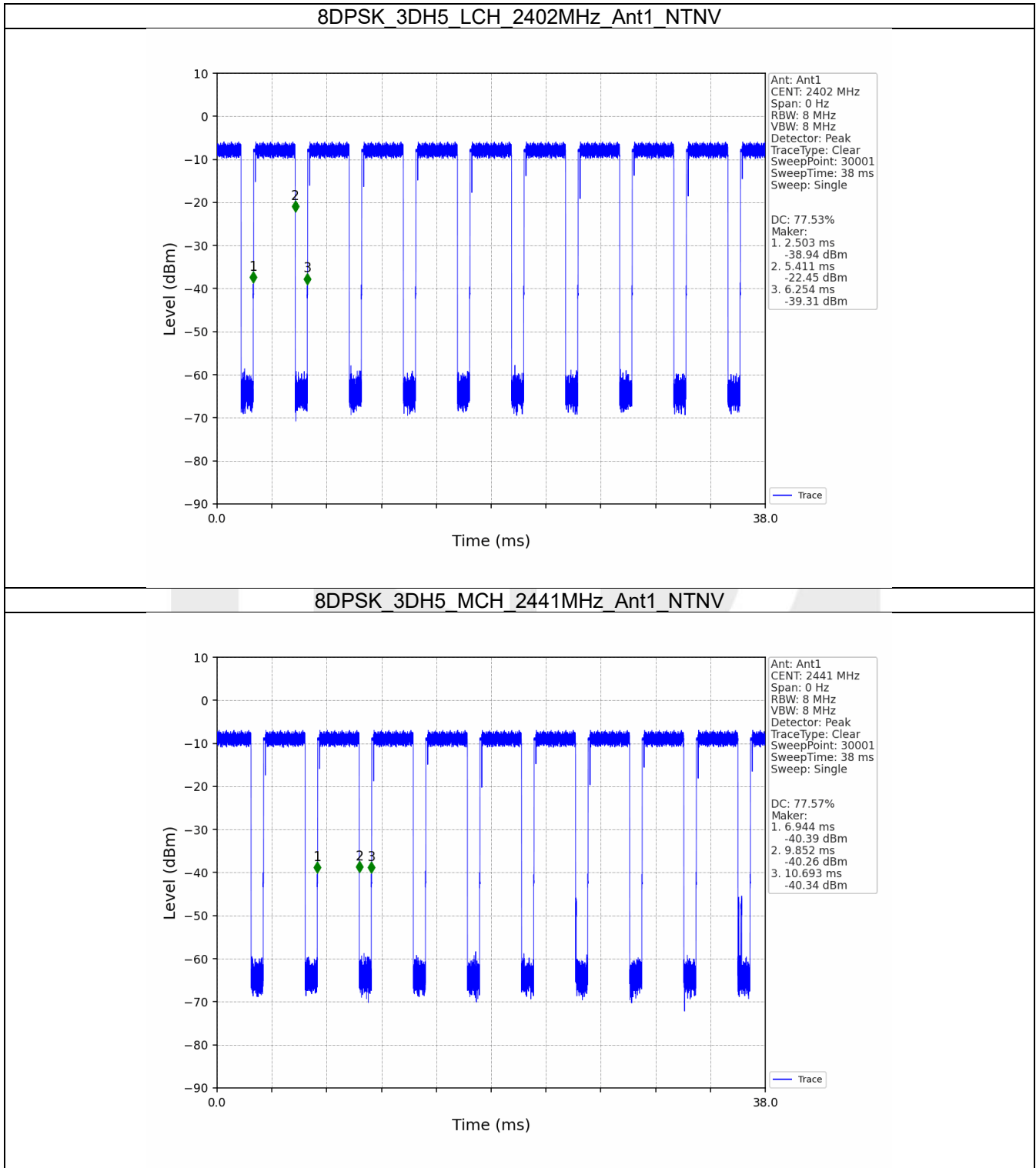
1.2 Test Graph

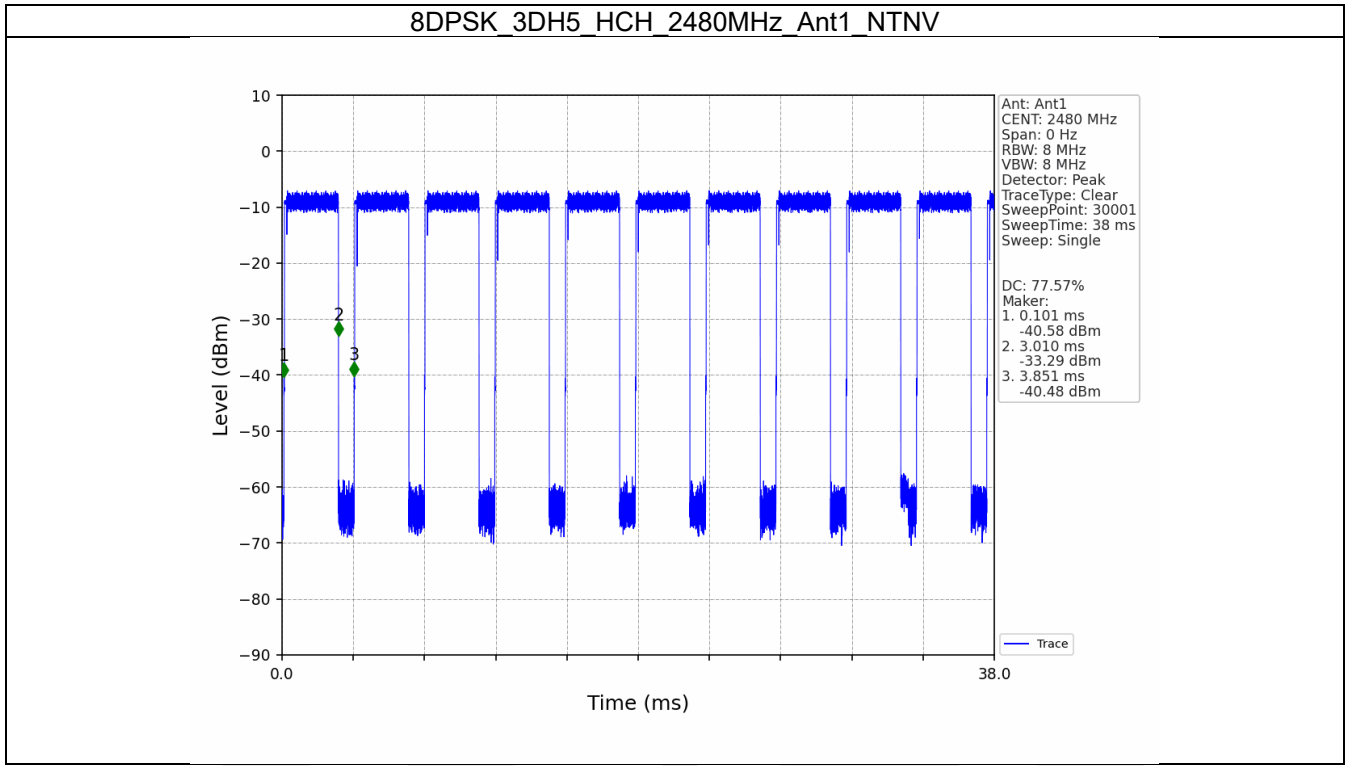
1.2.1 Ant1











2. Bandwidth

2.1 Test Result

2.1.1 OBW

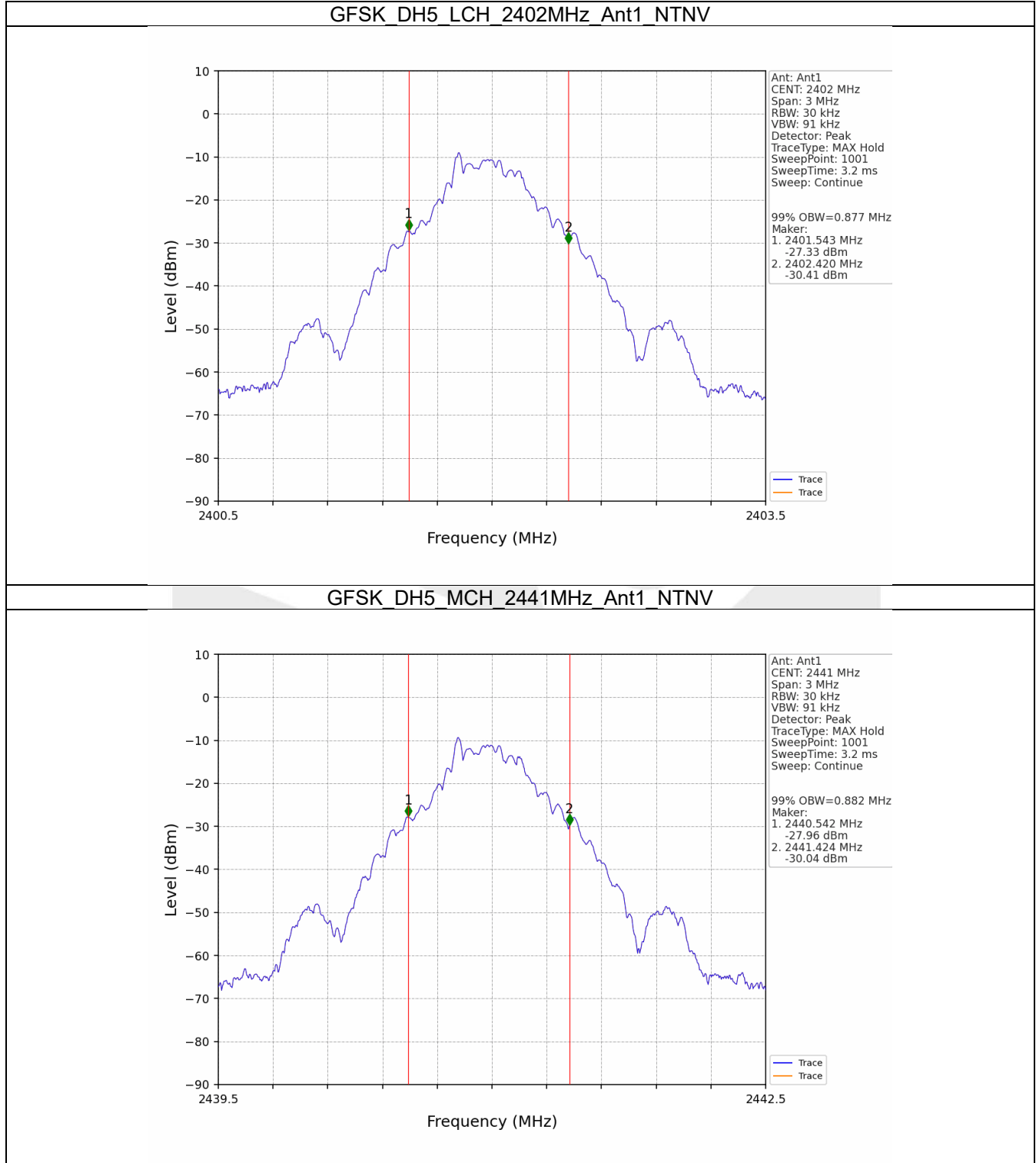
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	99% Occupied Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.877	/	Pass
		2441	DH5	1	0.882	/	Pass
		2480	DH5	1	0.882	/	Pass
π /4-DQPSK	SISO	2402	2DH5	1	1.163	/	Pass
		2441	2DH5	1	1.163	/	Pass
		2480	2DH5	1	1.165	/	Pass
8DPSK	SISO	2402	3DH5	1	1.156	/	Pass
		2441	3DH5	1	1.157	/	Pass
		2480	3DH5	1	1.157	/	Pass

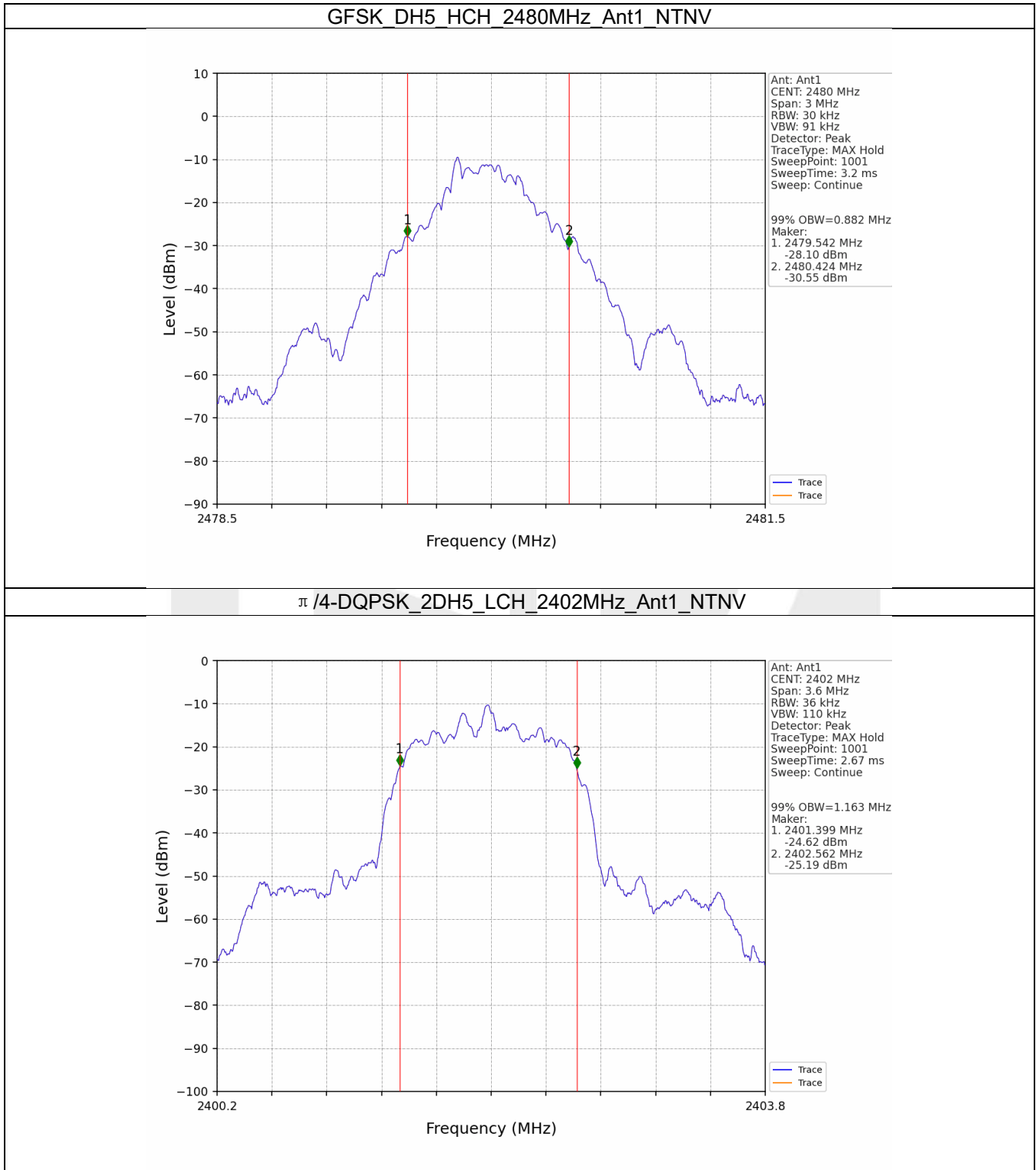
2.1.2 20dB BW

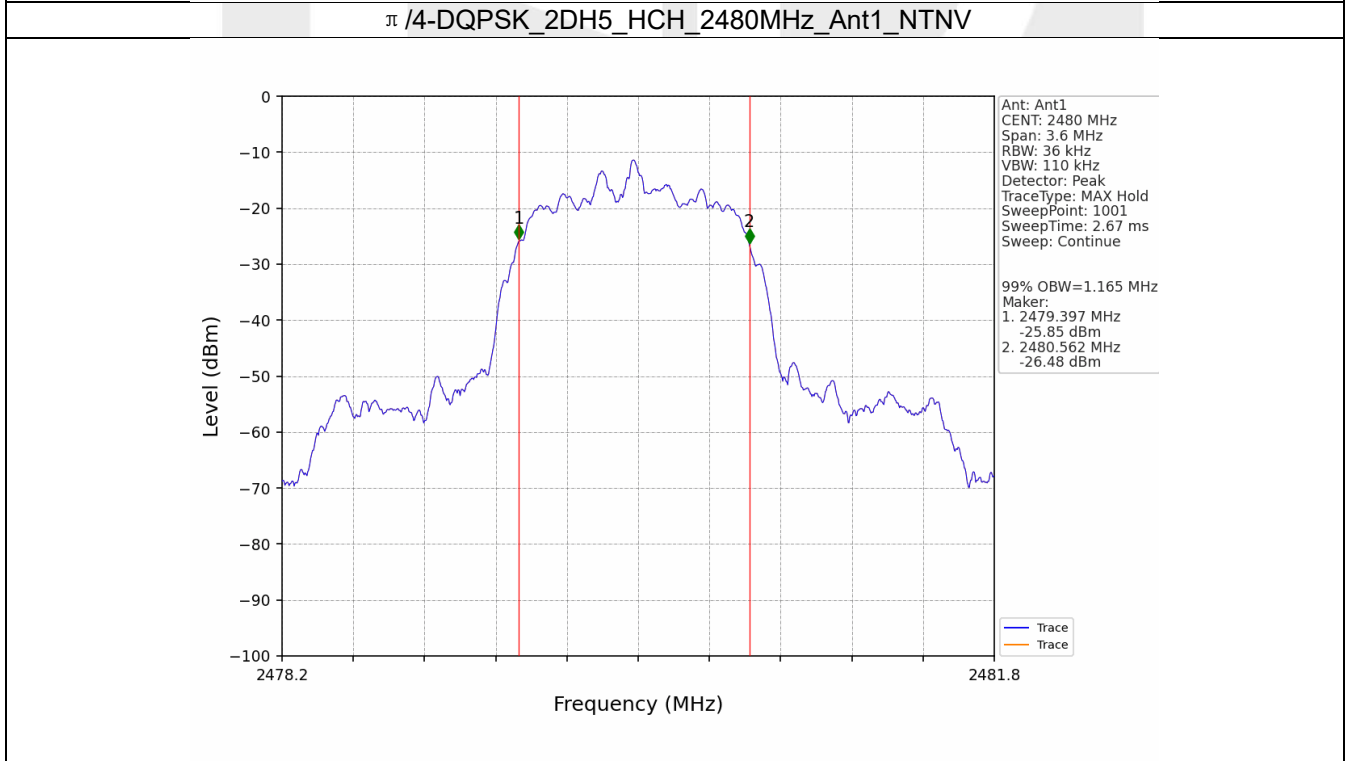
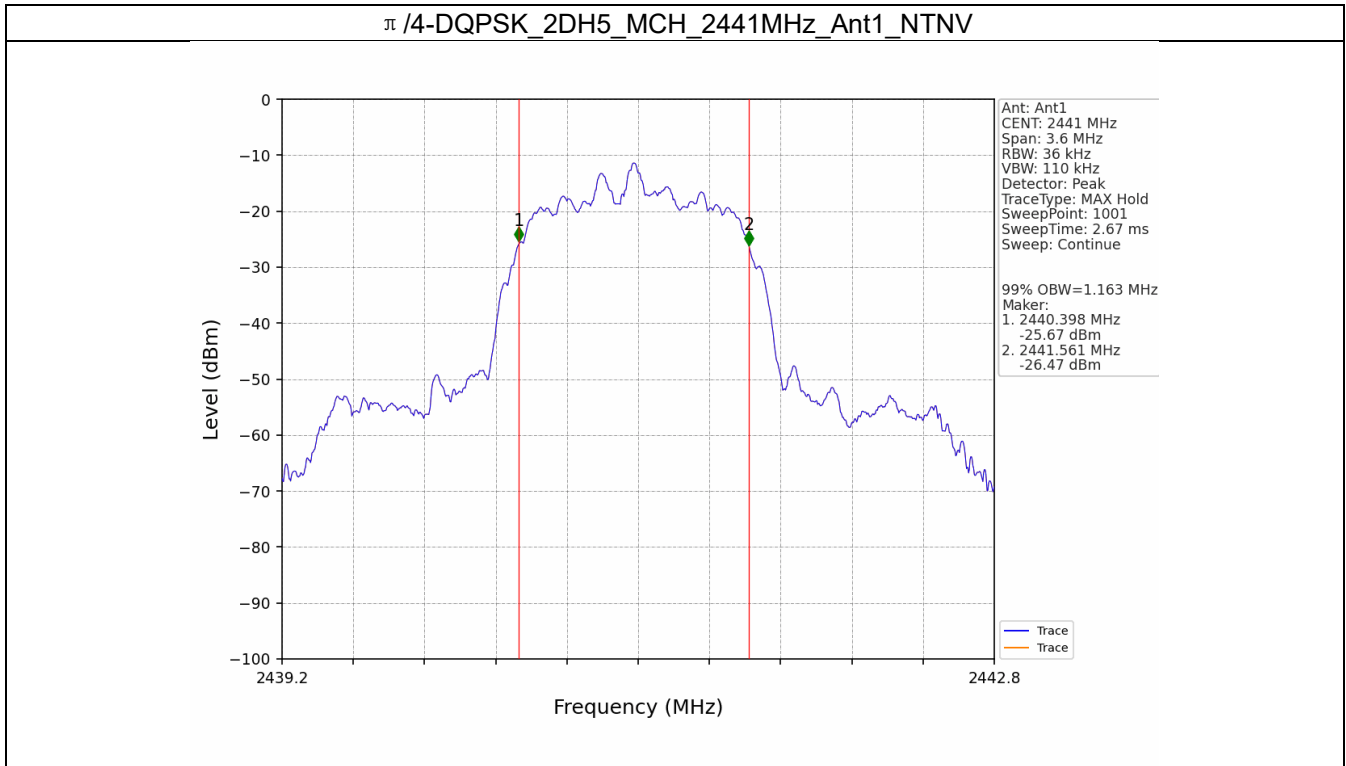
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	20dB Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.947	/	Pass
		2441	DH5	1	0.951	/	Pass
		2480	DH5	1	0.949	/	Pass
π /4-DQPSK	SISO	2402	2DH5	1	1.282	/	Pass
		2441	2DH5	1	1.281	/	Pass
		2480	2DH5	1	1.284	/	Pass
8DPSK	SISO	2402	3DH5	1	1.263	/	Pass
		2441	3DH5	1	1.266	/	Pass
		2480	3DH5	1	1.266	/	Pass

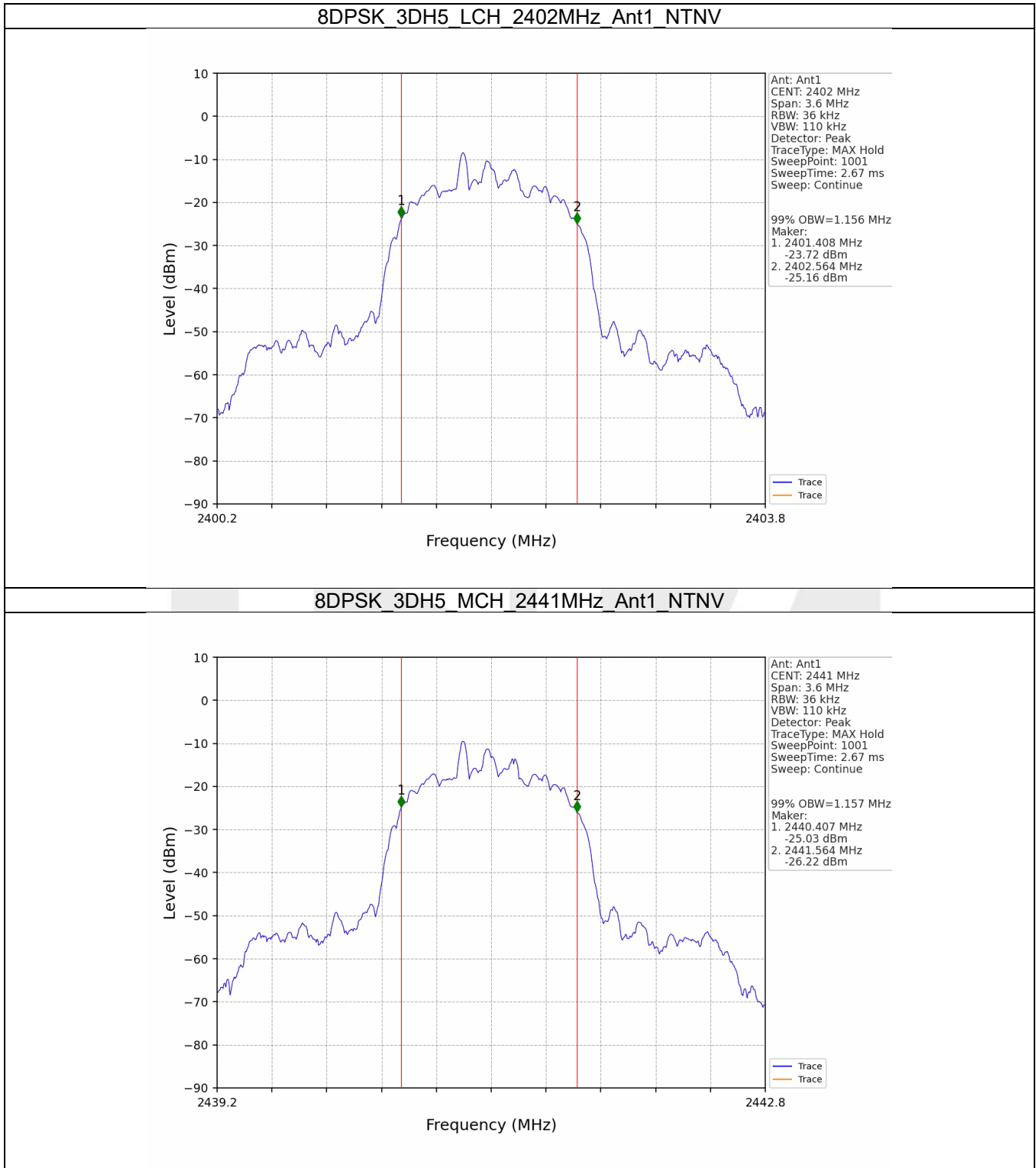
2.2 Test Graph

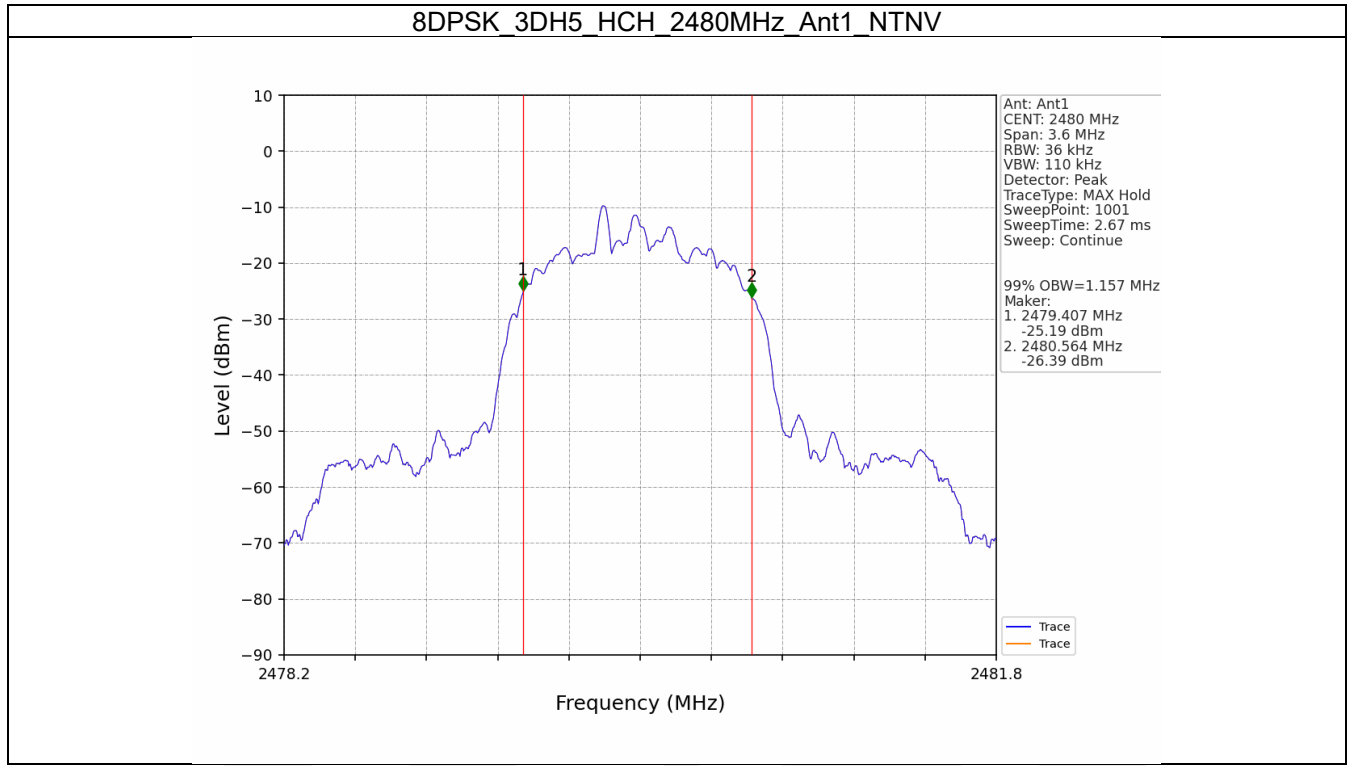
2.2.1 OBW



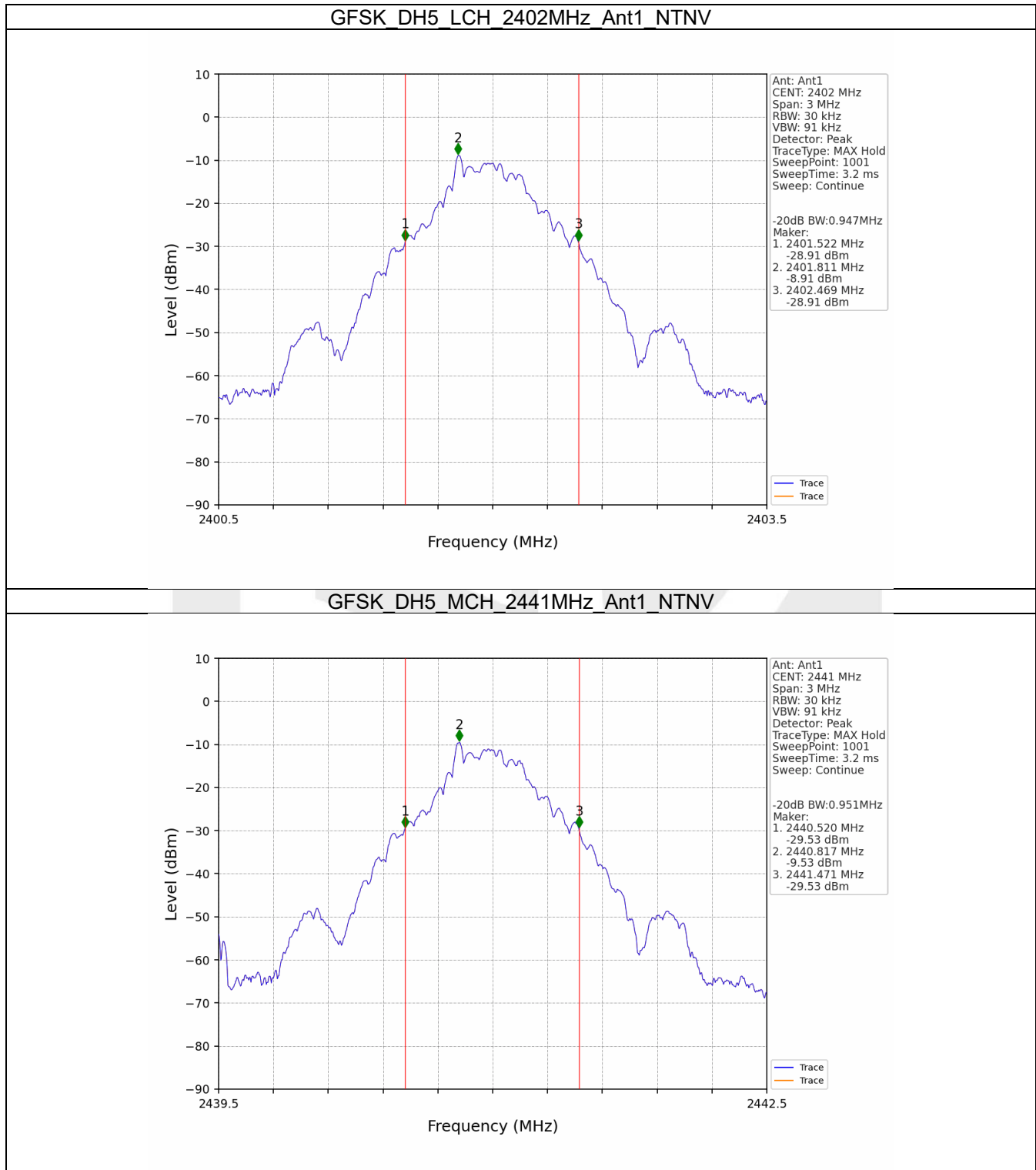


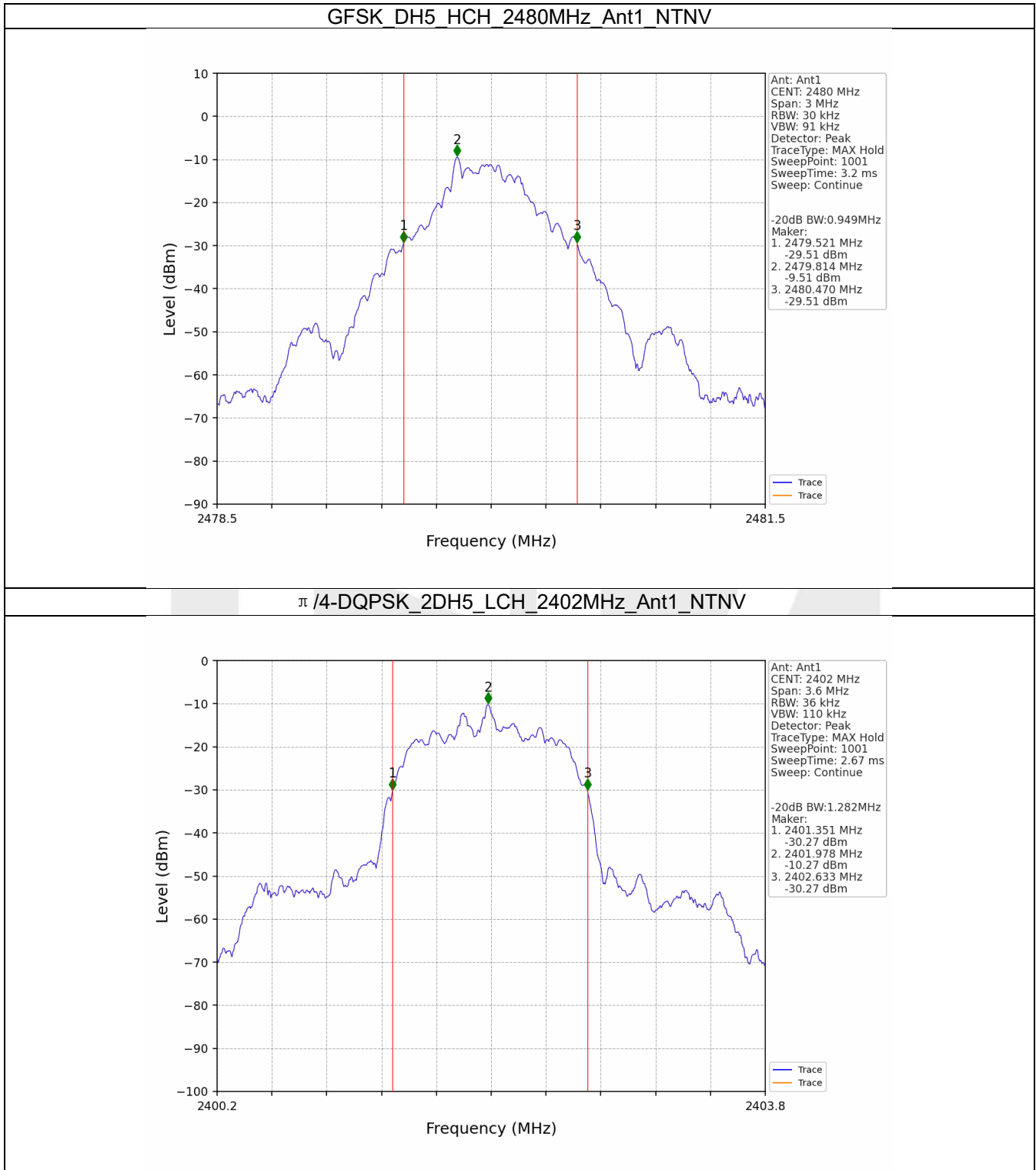


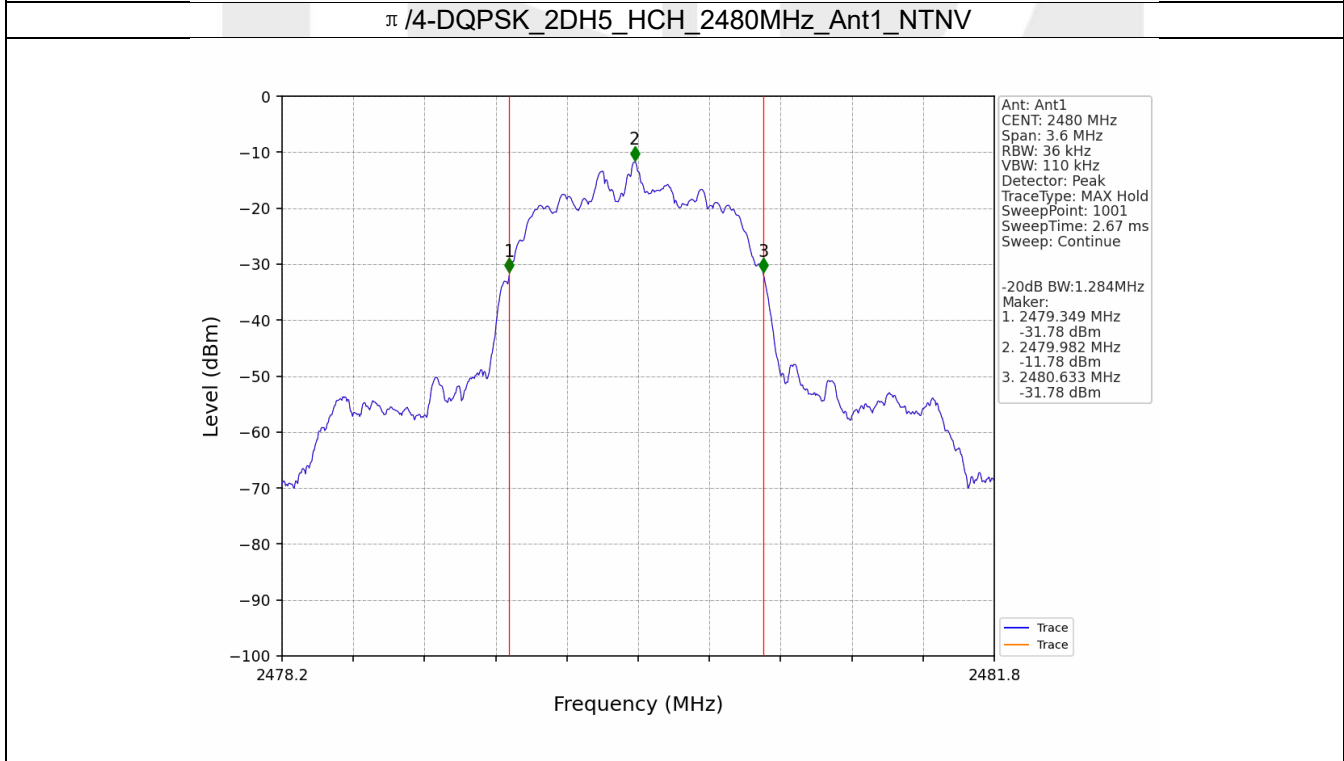
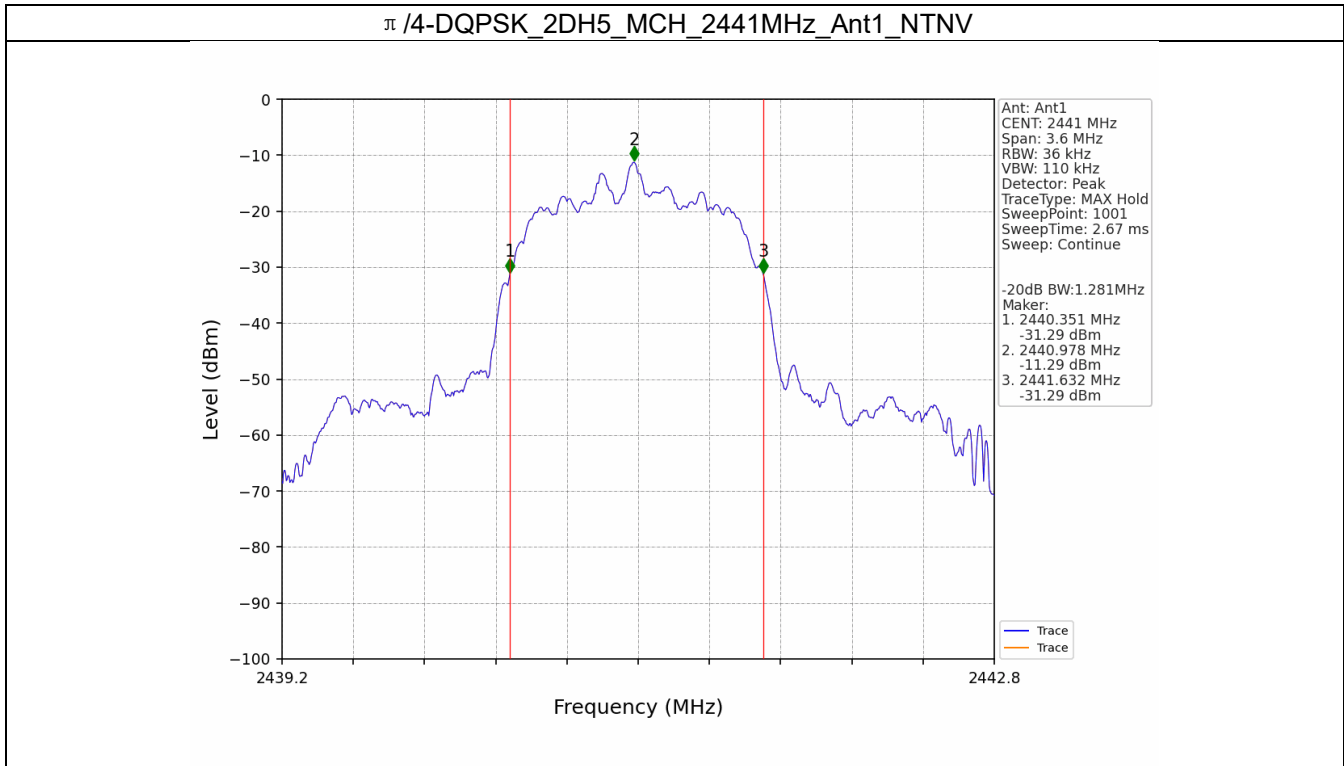


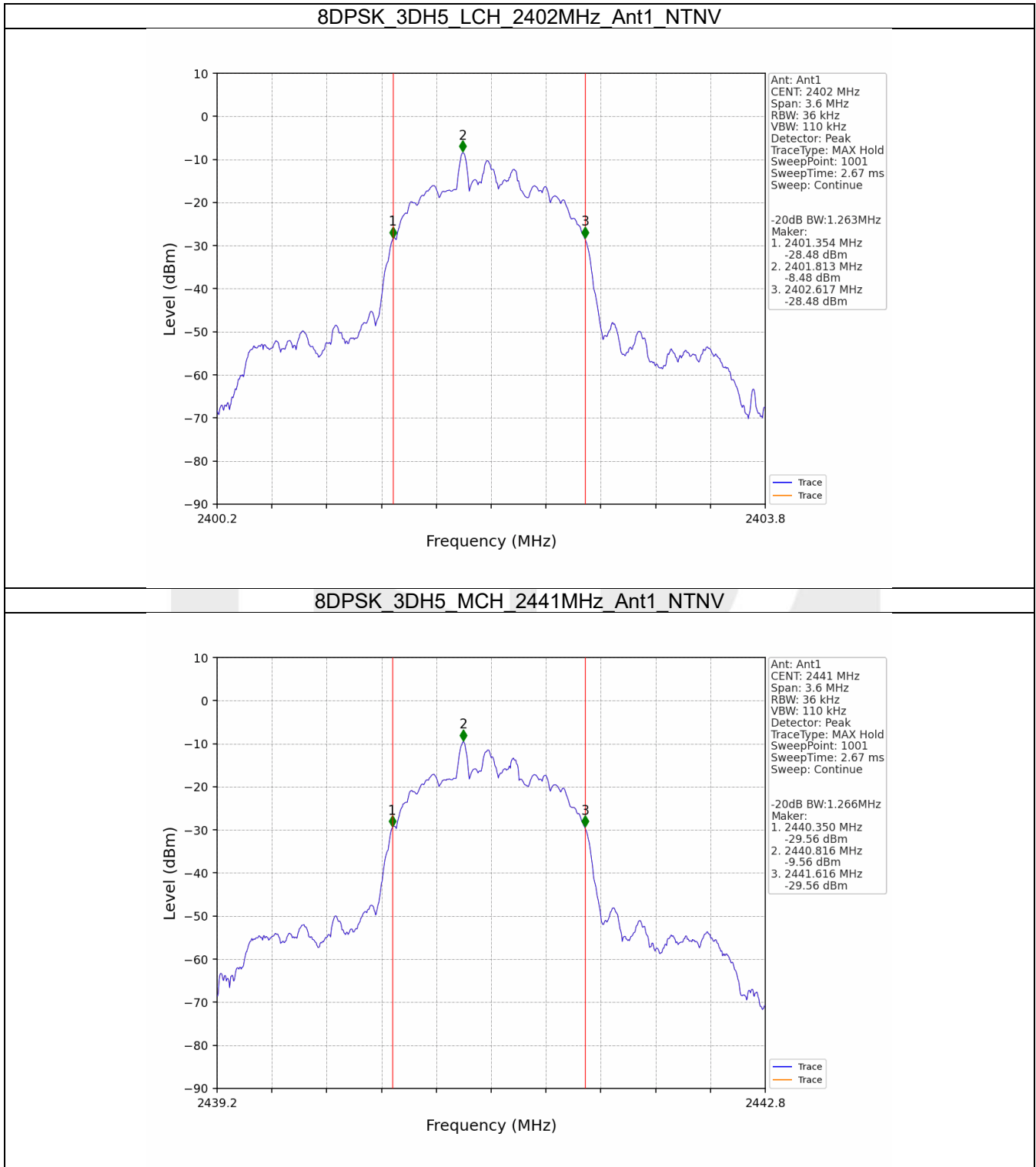


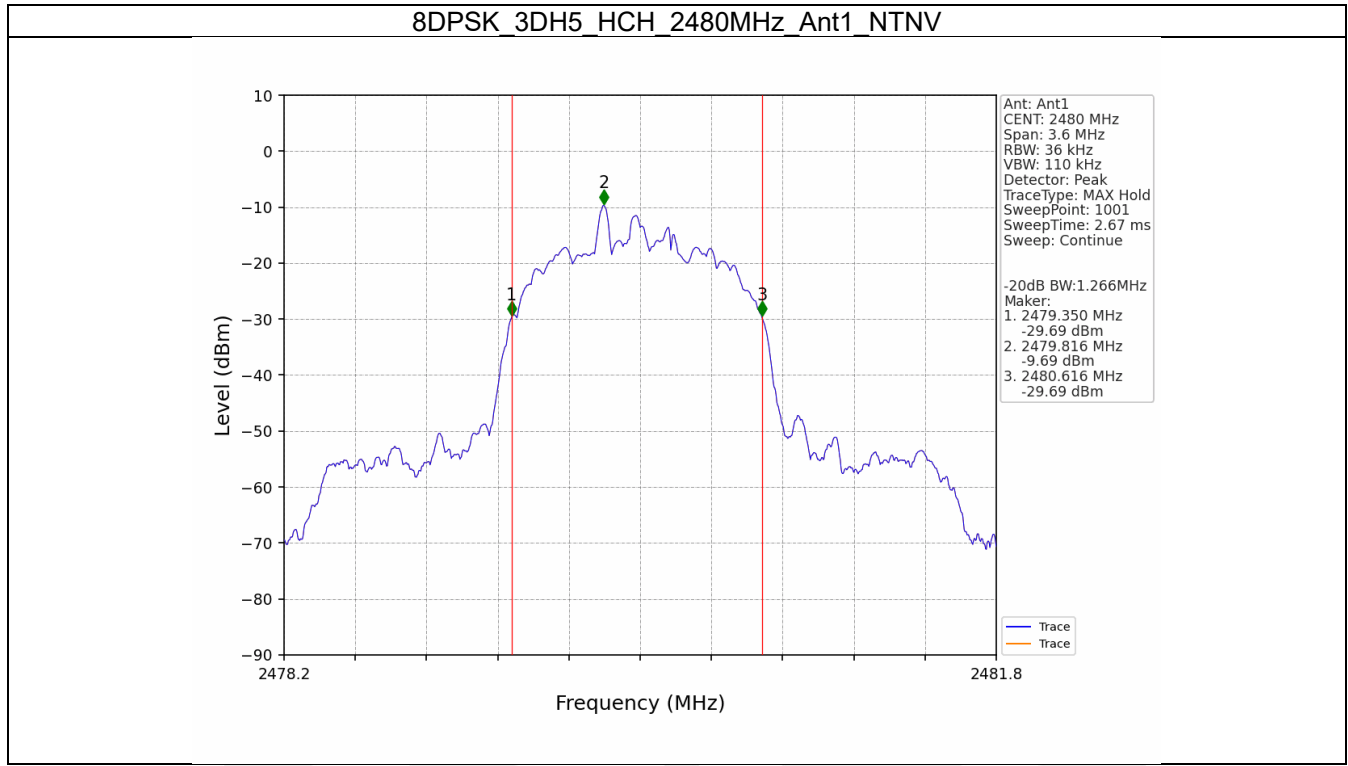
2.2.2 20dB BW











3. Maximum Conducted Output Power

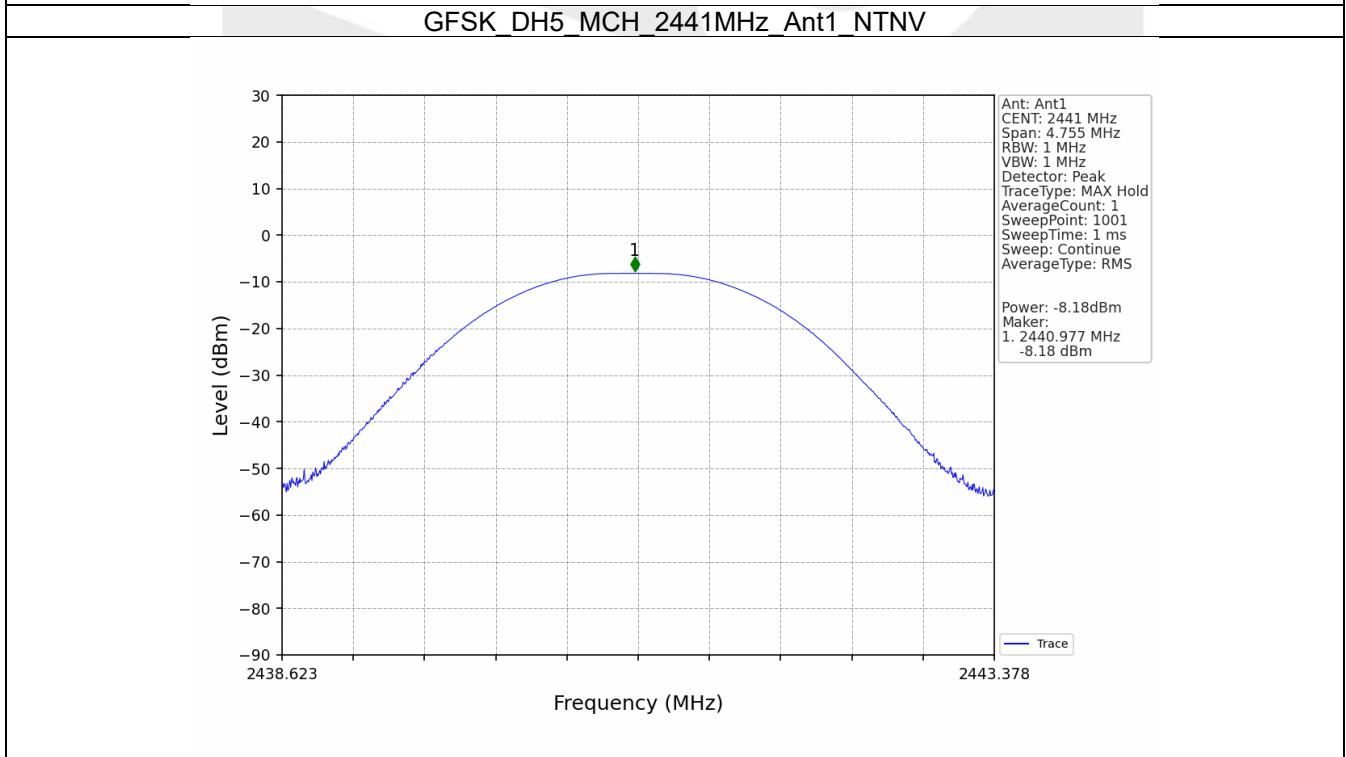
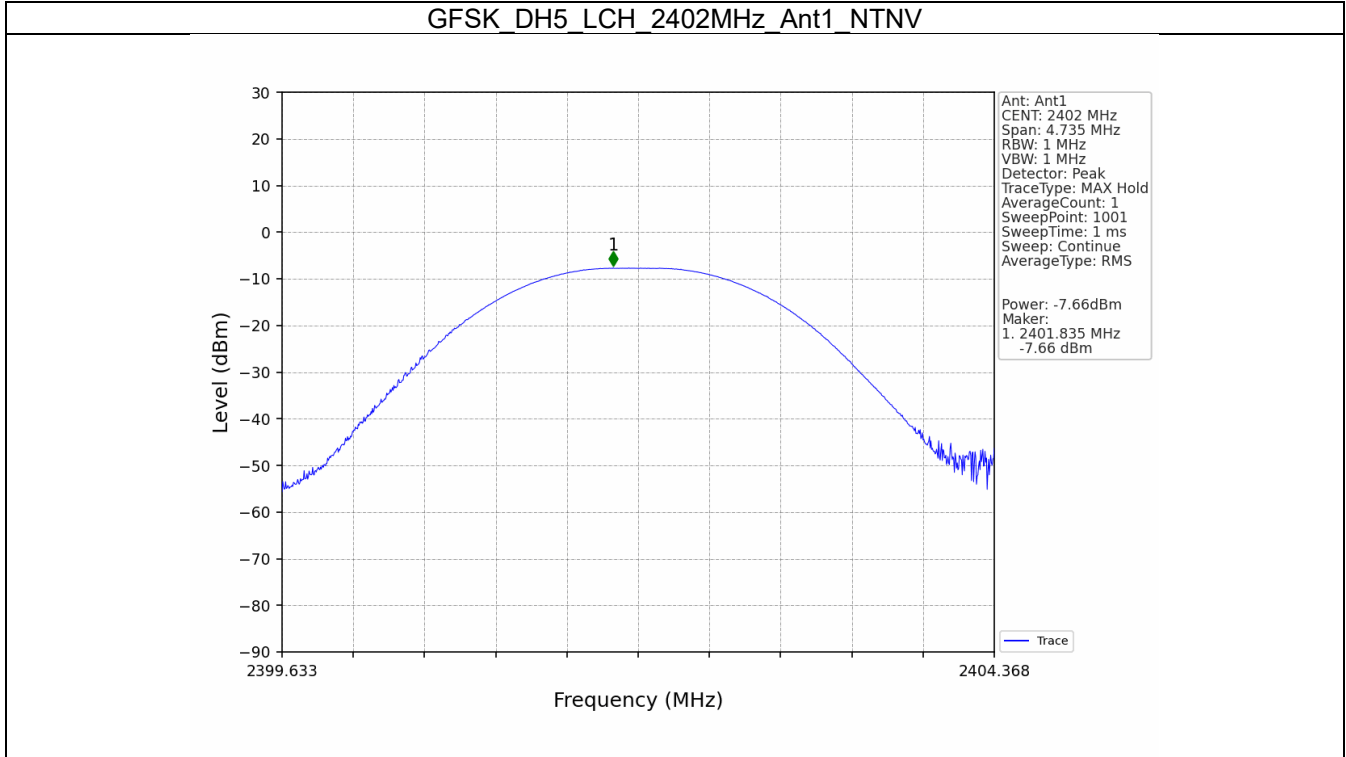
3.1 Test Result

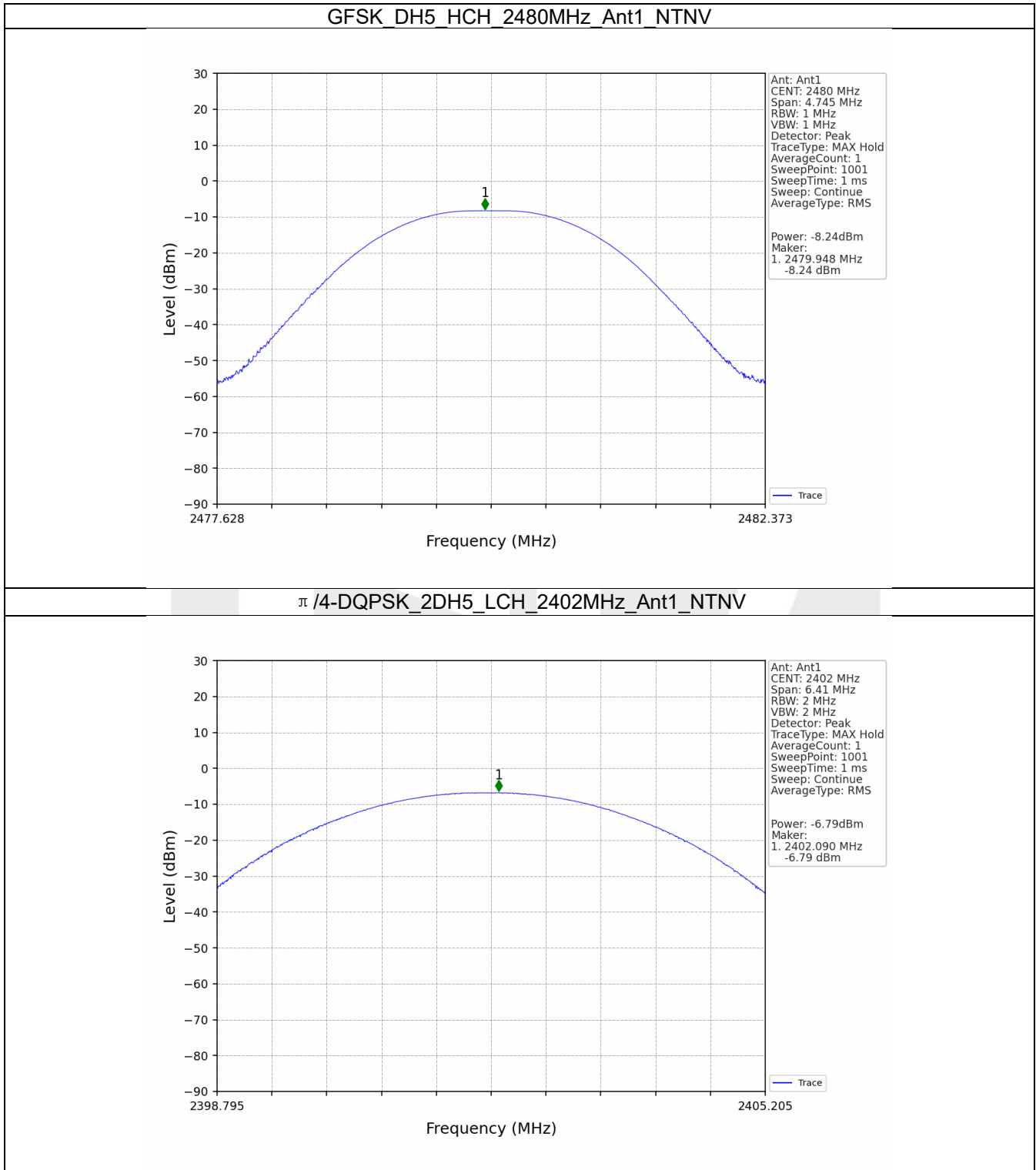
3.1.1 Power

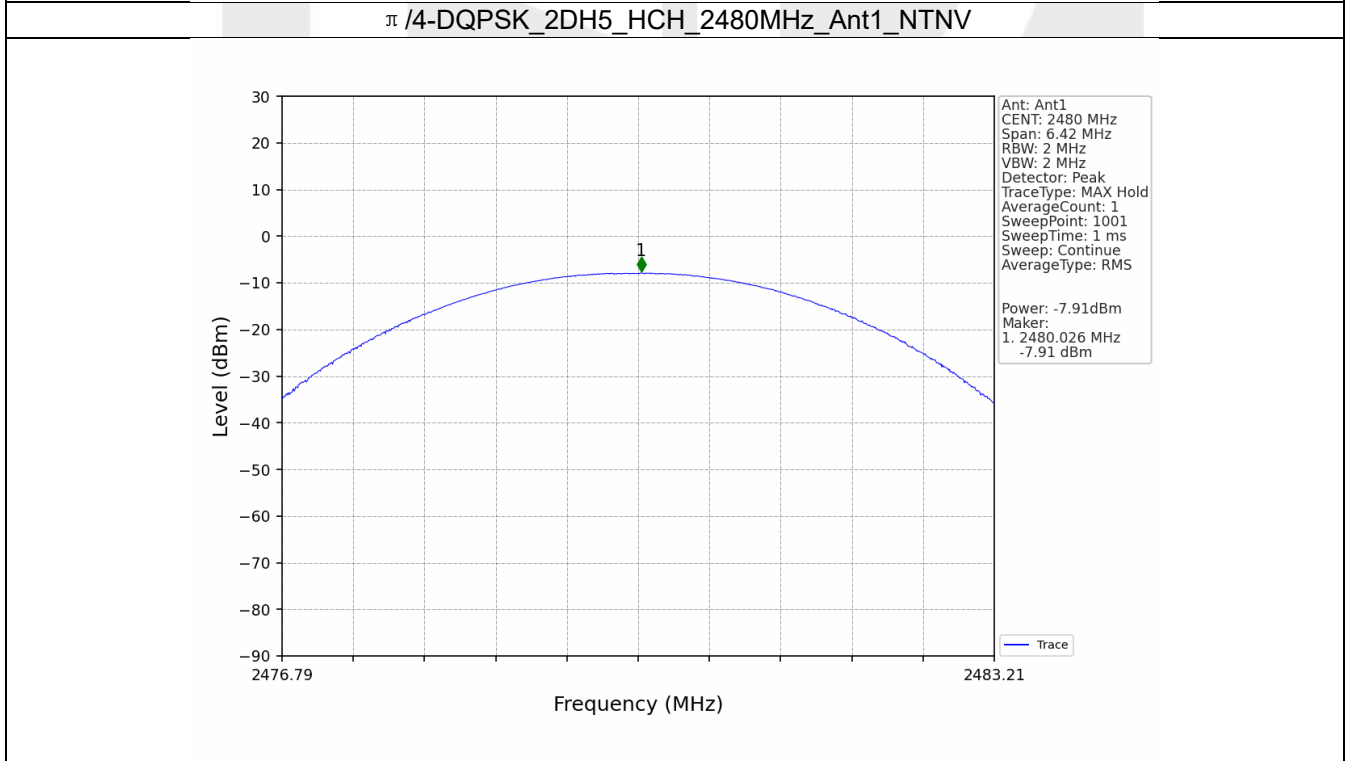
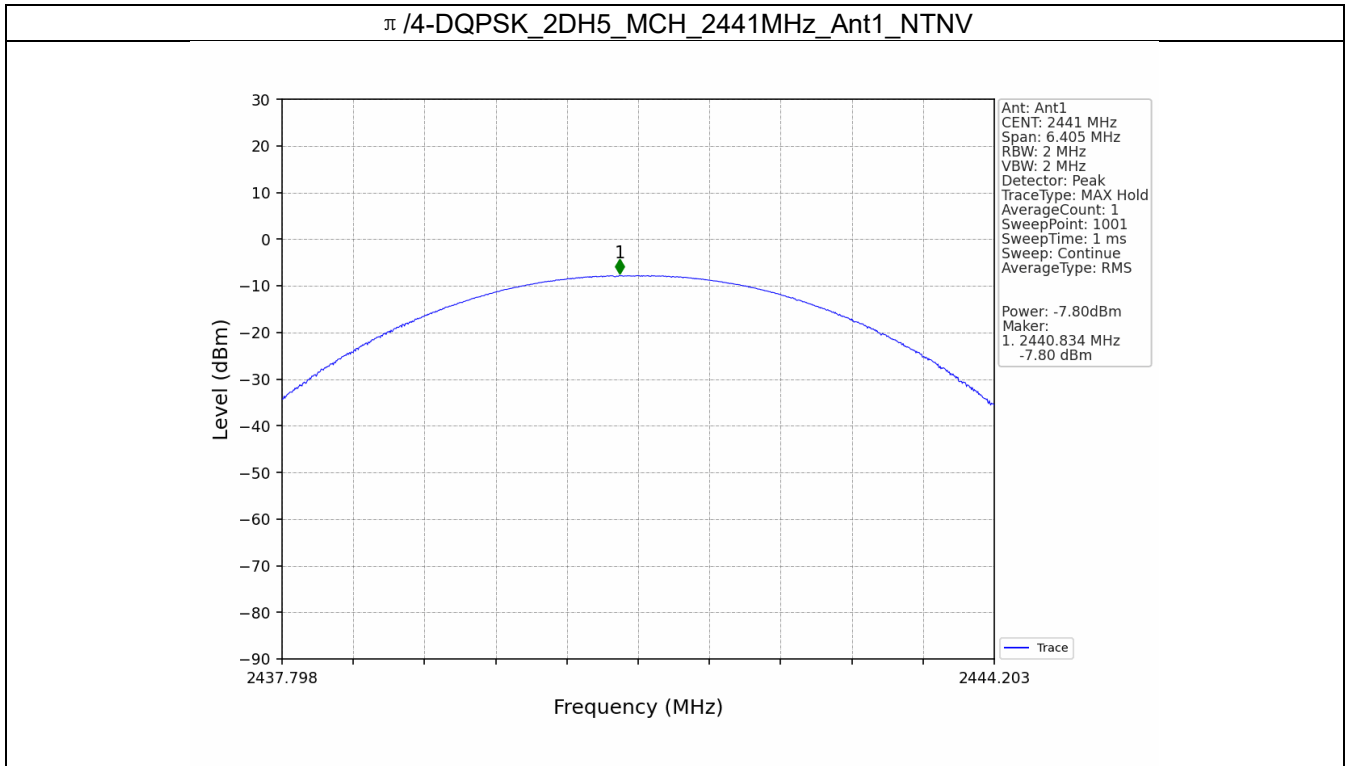
Mode	TX Type	Frequency (MHz)	Packet Type	Maximum Peak Conducted Output Power (dBm)		Verdict
				ANT1	Limit	
GFSK	SISO	2402	DH5	-7.66	<=30	Pass
		2441	DH5	-8.18	<=30	Pass
		2480	DH5	-8.24	<=30	Pass
$\pi/4$ -DQPSK	SISO	2402	2DH5	-6.79	<=20.97	Pass
		2441	2DH5	-7.80	<=20.97	Pass
		2480	2DH5	-7.91	<=20.97	Pass
8DPSK	SISO	2402	3DH5	-6.09	<=20.97	Pass
		2441	3DH5	-7.13	<=20.97	Pass
		2480	3DH5	-7.25	<=20.97	Pass

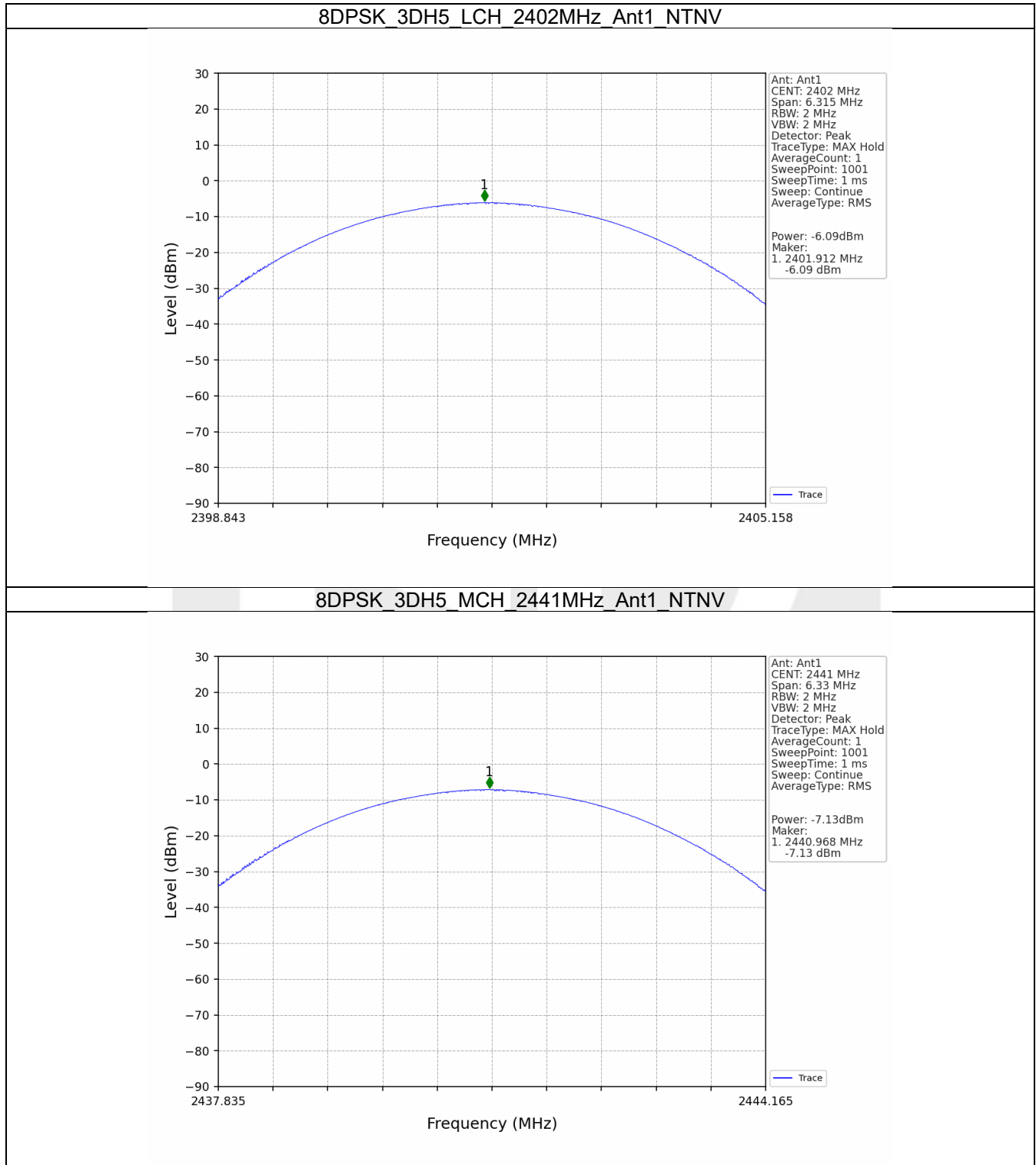
3.2 Test Graph

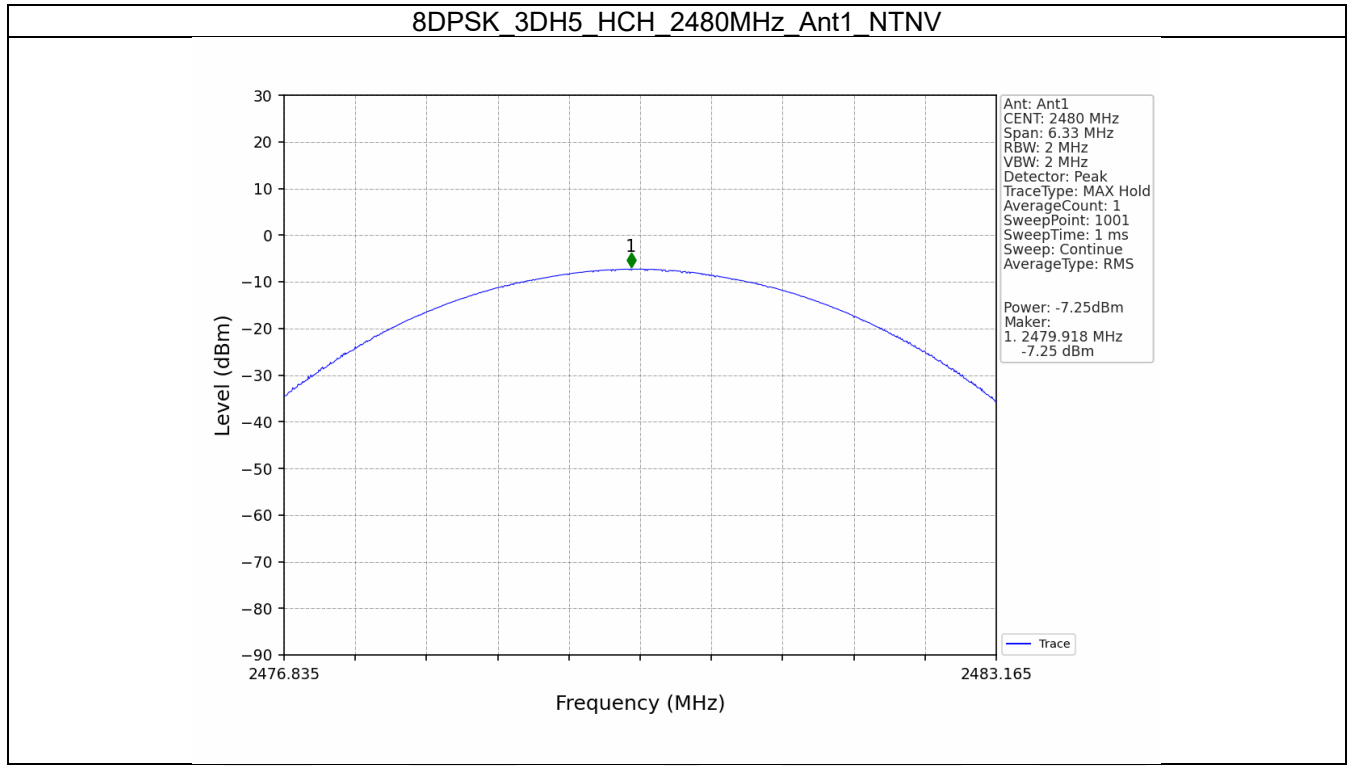
3.2.1 Power











4. Carrier Frequency Separation

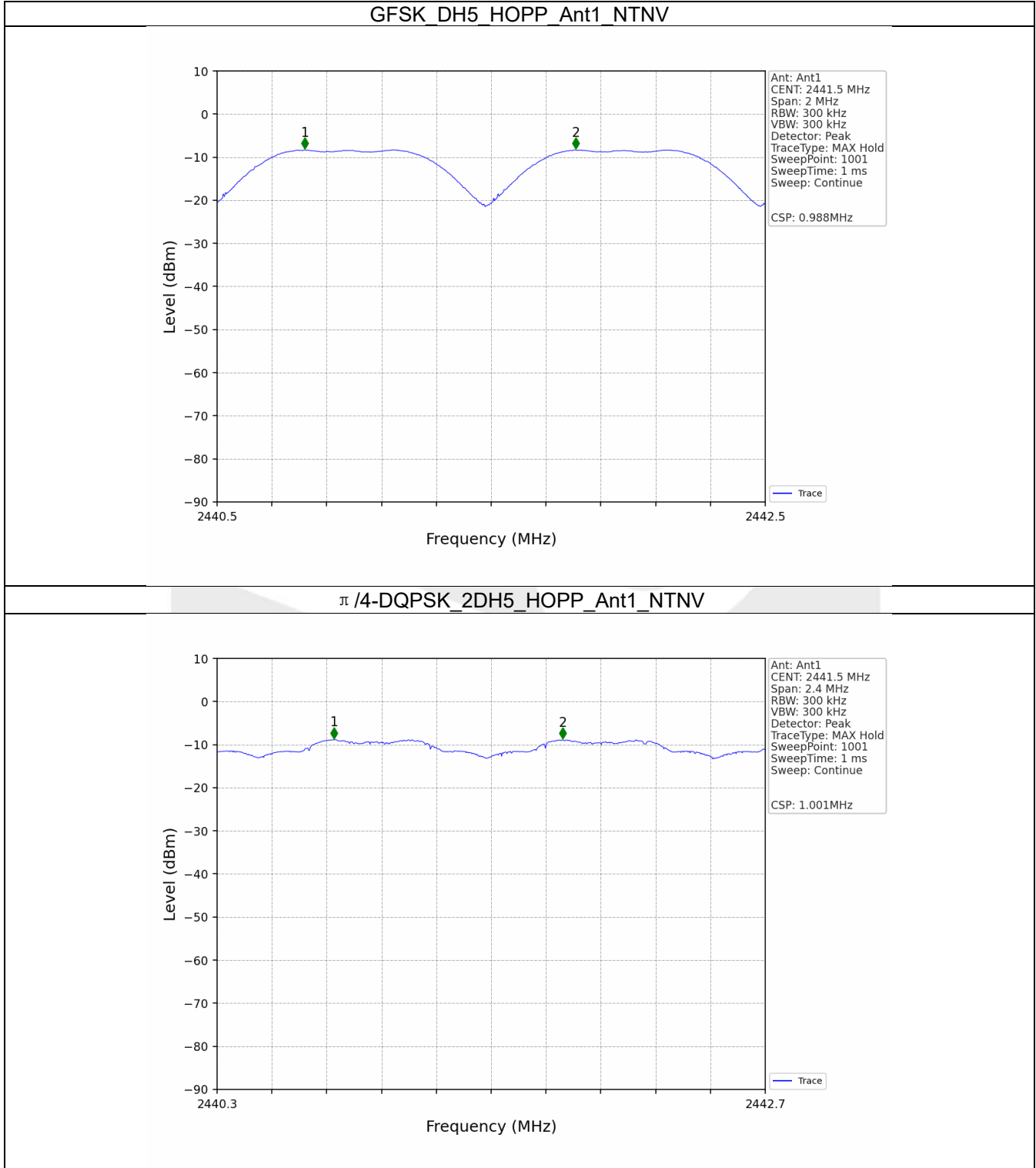
4.1 Test Result

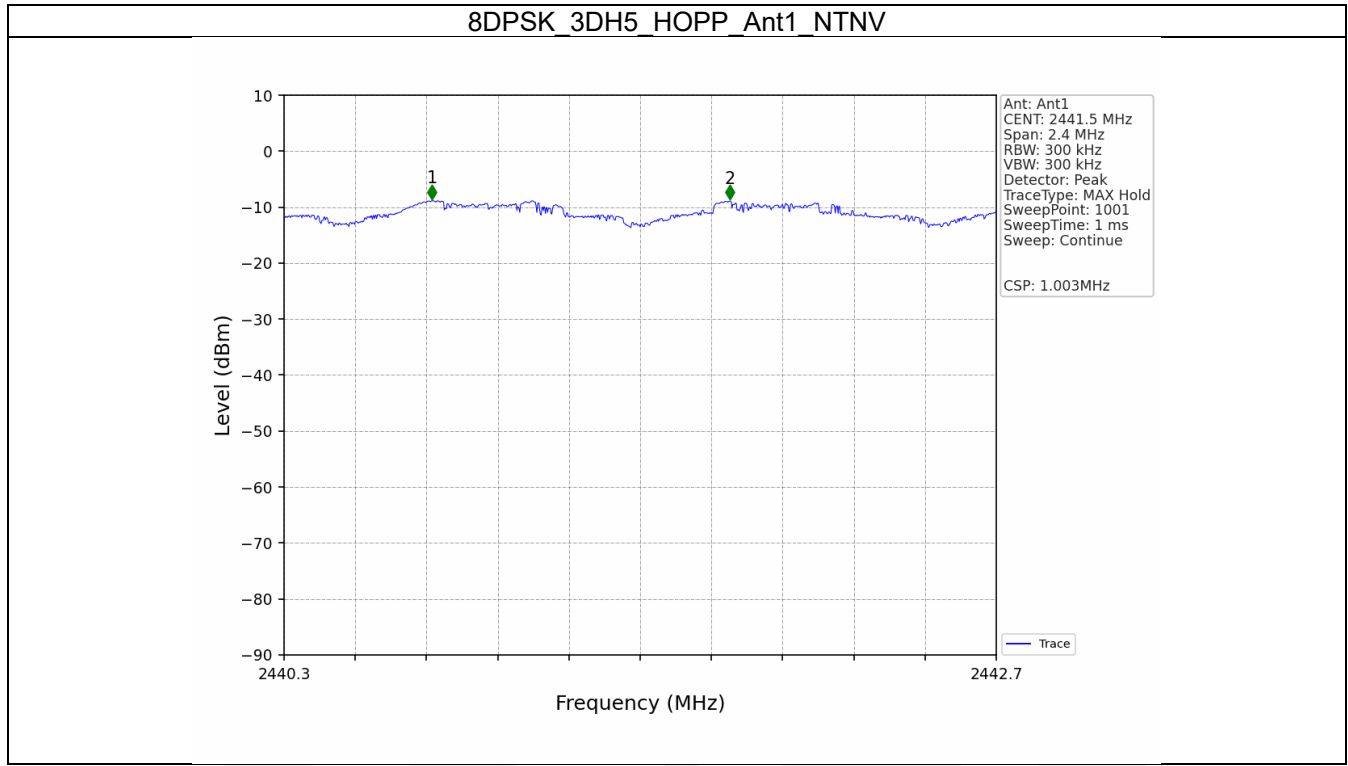
4.1.1 Ant1

Ant1							
Mode	TX Type	Frequency (MHz)	Packet Type	Channel Separation (MHz)	20dB Bandwidth (MHz)	Limit (MHz)	Verdict
GFSK	SISO	HOPP	DH5	0.988	0.951	≥ 0.951	Pass
$\pi/4$ -DQPSK	SISO	HOPP	2DH5	1.001	1.284	≥ 0.856	Pass
8DPSK	SISO	HOPP	3DH5	1.003	1.266	≥ 0.844	Pass

4.2 Test Graph

4.2.1 Ant1





5. Number of Hopping Frequencies

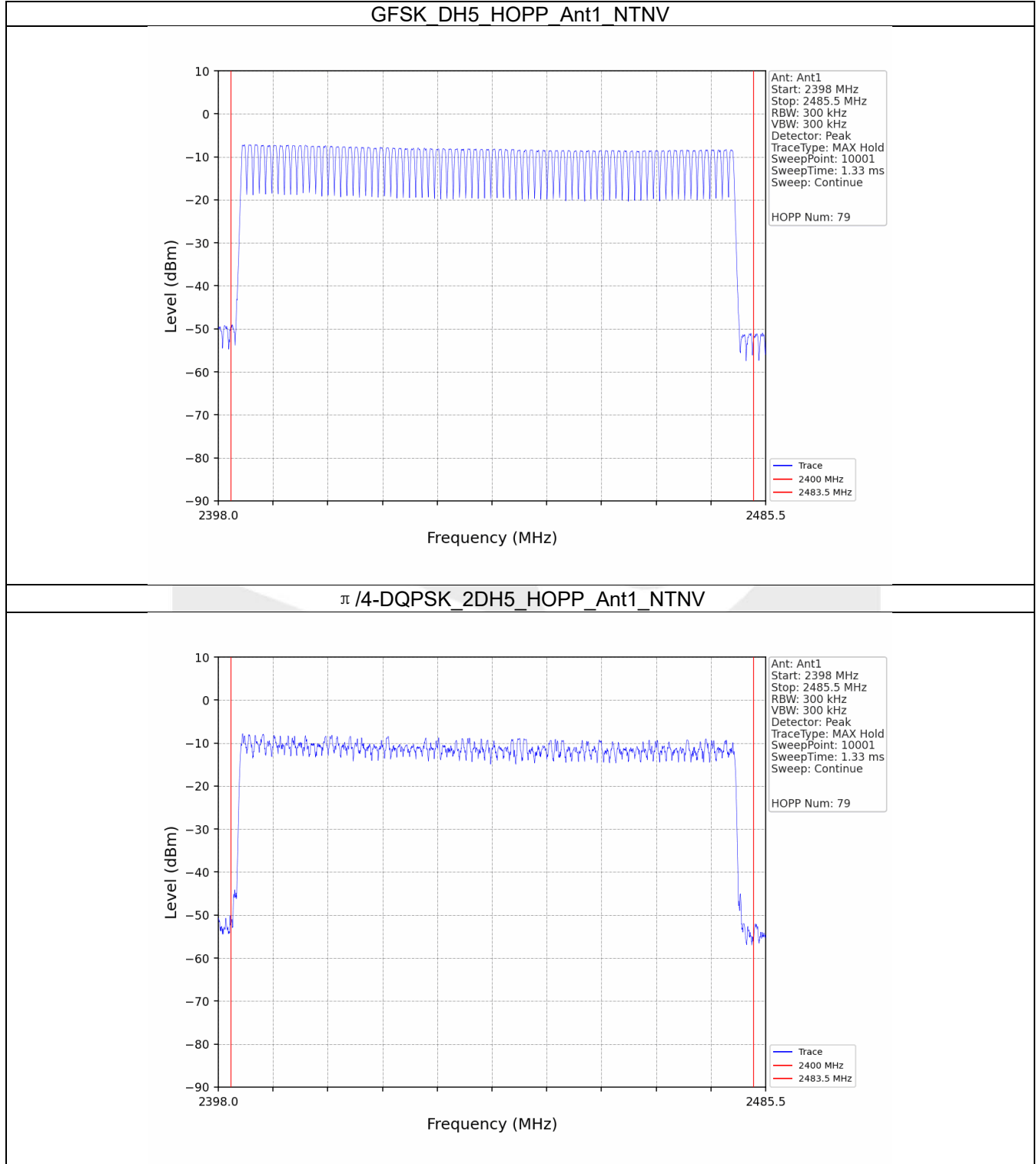
5.1 Test Result

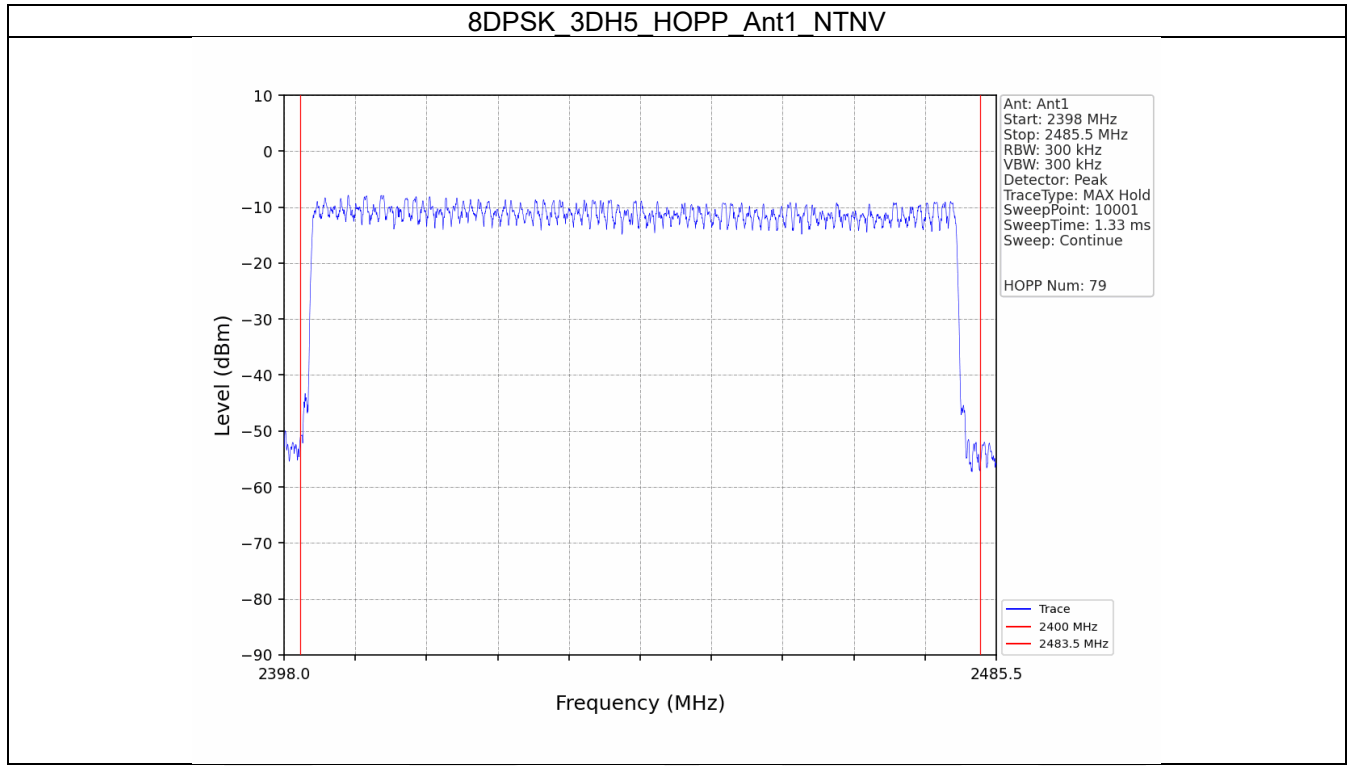
5.1.1 HoppNum

Mode	TX Type	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
				ANT1	Limit	
GFSK	SISO	HOPP	DH5	79	>=15	Pass
$\pi/4$ -DQPSK	SISO	HOPP	2DH5	79	>=15	Pass
8DPSK	SISO	HOPP	3DH5	79	>=15	Pass

5.2 Test Graph

5.2.1 HoppNum





6. Time of Occupancy (Dwell Time)

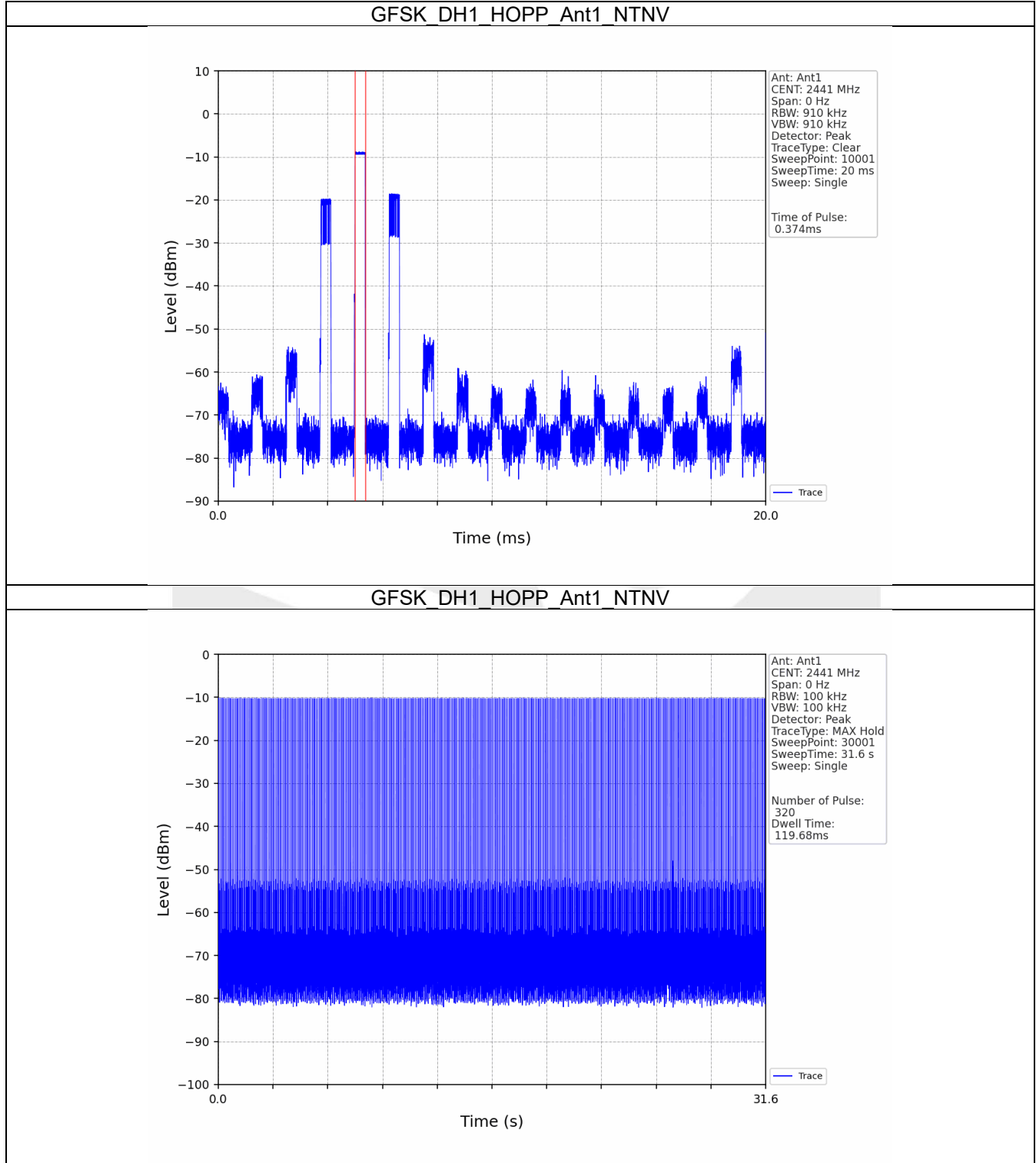
6.1 Test Result

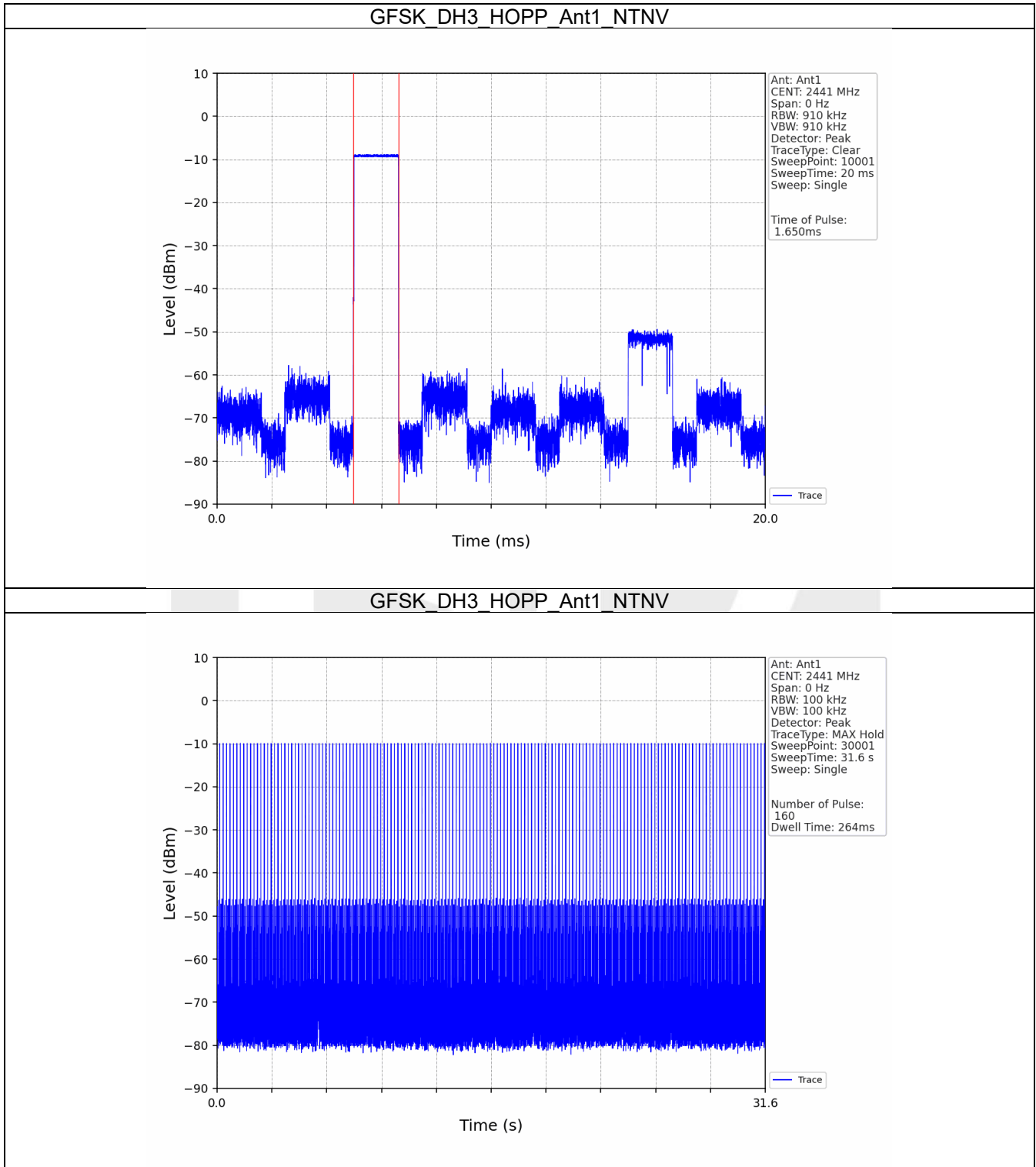
6.1.1 Ant1

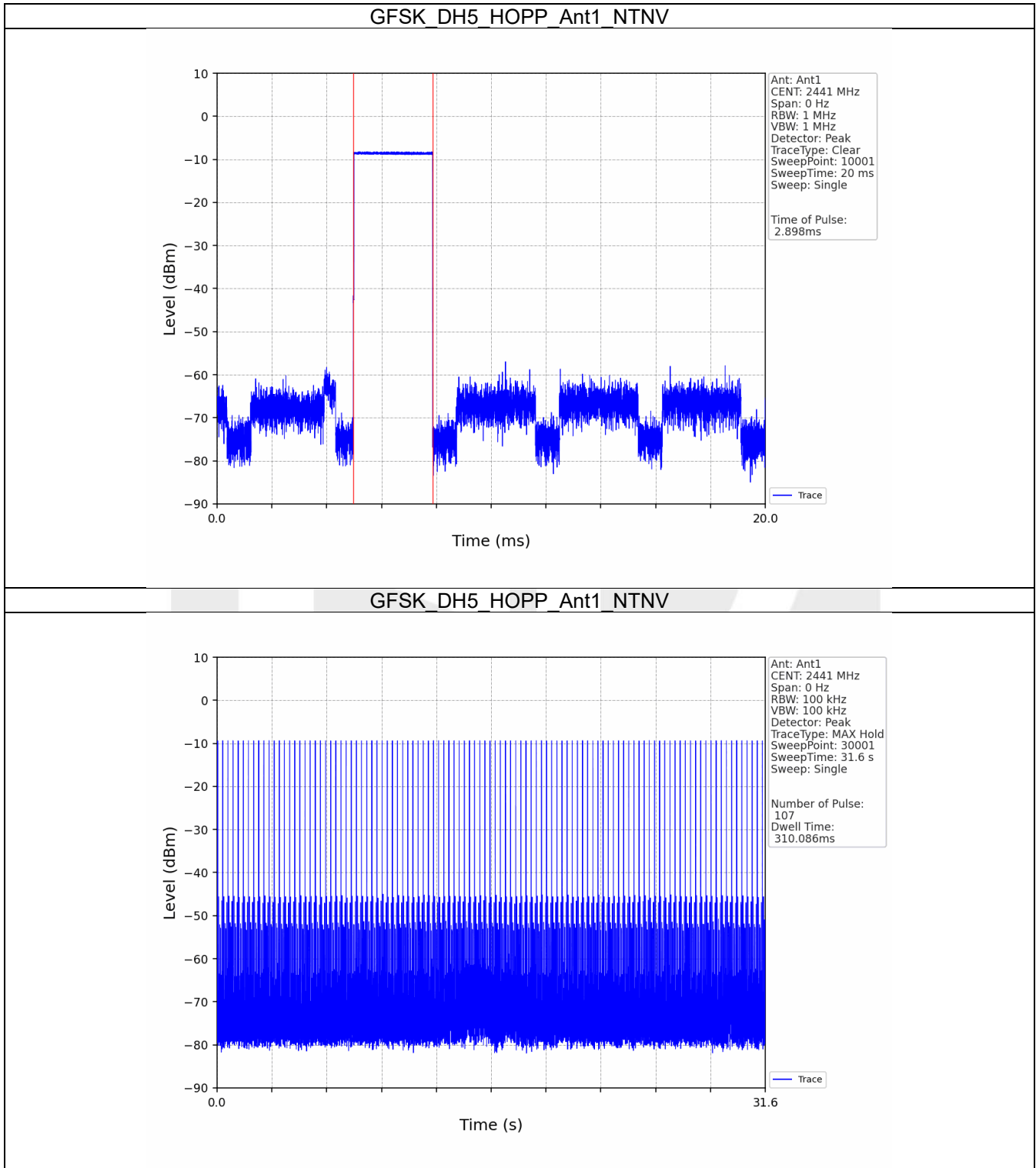
Ant1									
Mode	TX Type	Frequency (MHz)	Packet Type	Duration of Single Pulse (ms)	Observation Period (s)	Num of Pulse in Observation Period	Dwell Time (ms)	Limit (ms)	Verdict
GFSK	SISO	HOPP	DH1	0.374	31.600	320	119.680	<=400	Pass
			DH3	1.650	31.600	160	264.000	<=400	Pass
			DH5	2.898	31.600	107	310.086	<=400	Pass
$\pi/4$ -DQPSK	SISO	HOPP	2DH1	0.382	31.600	320	122.240	<=400	Pass
			2DH3	1.662	31.600	160	265.920	<=400	Pass
			2DH5	2.910	31.600	106	308.460	<=400	Pass
8DPSK	SISO	HOPP	3DH1	0.382	31.600	320	122.240	<=400	Pass
			3DH3	1.660	31.600	160	265.600	<=400	Pass
			3DH5	2.912	31.600	107	311.584	<=400	Pass

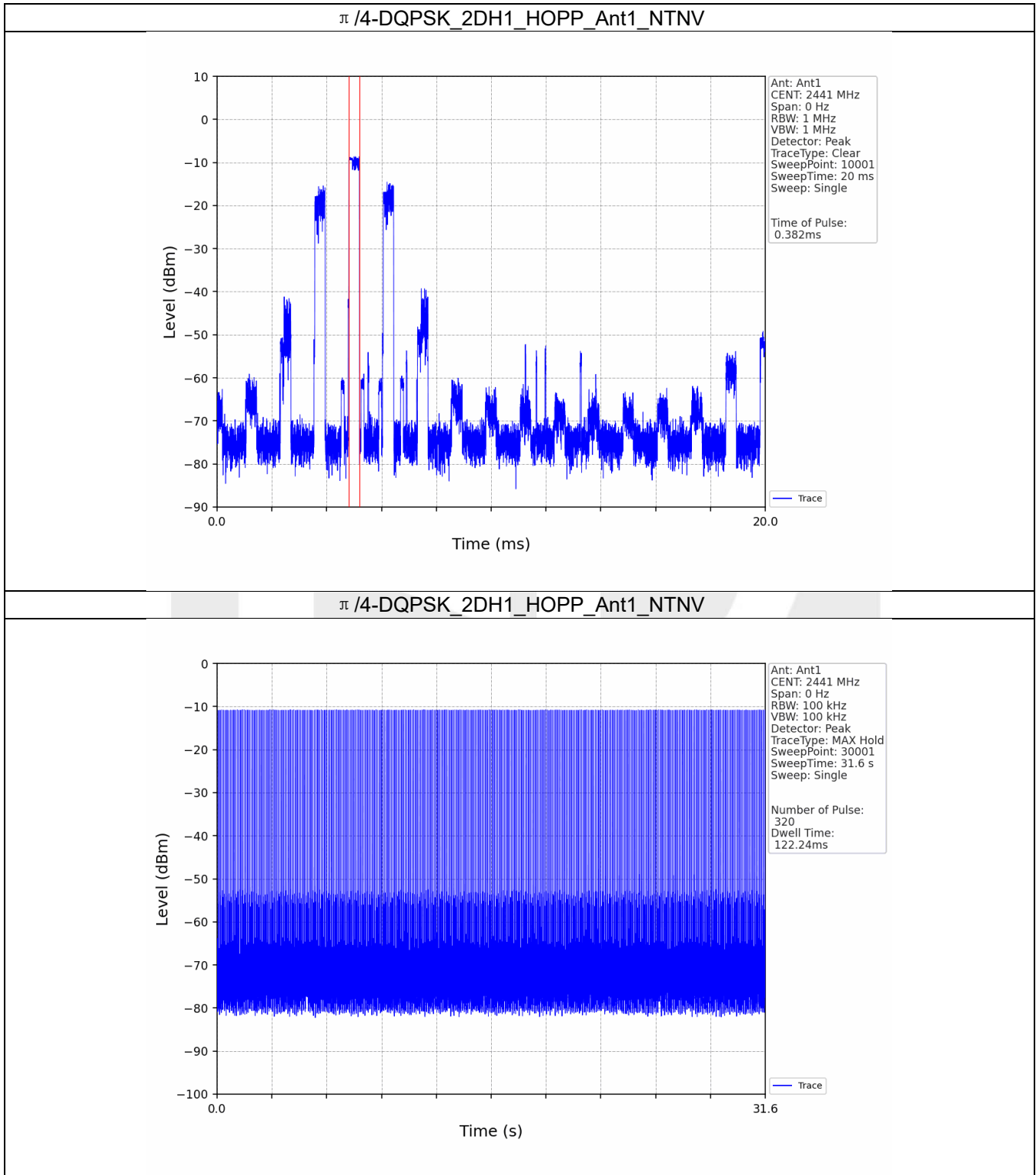
6.2 Test Graph

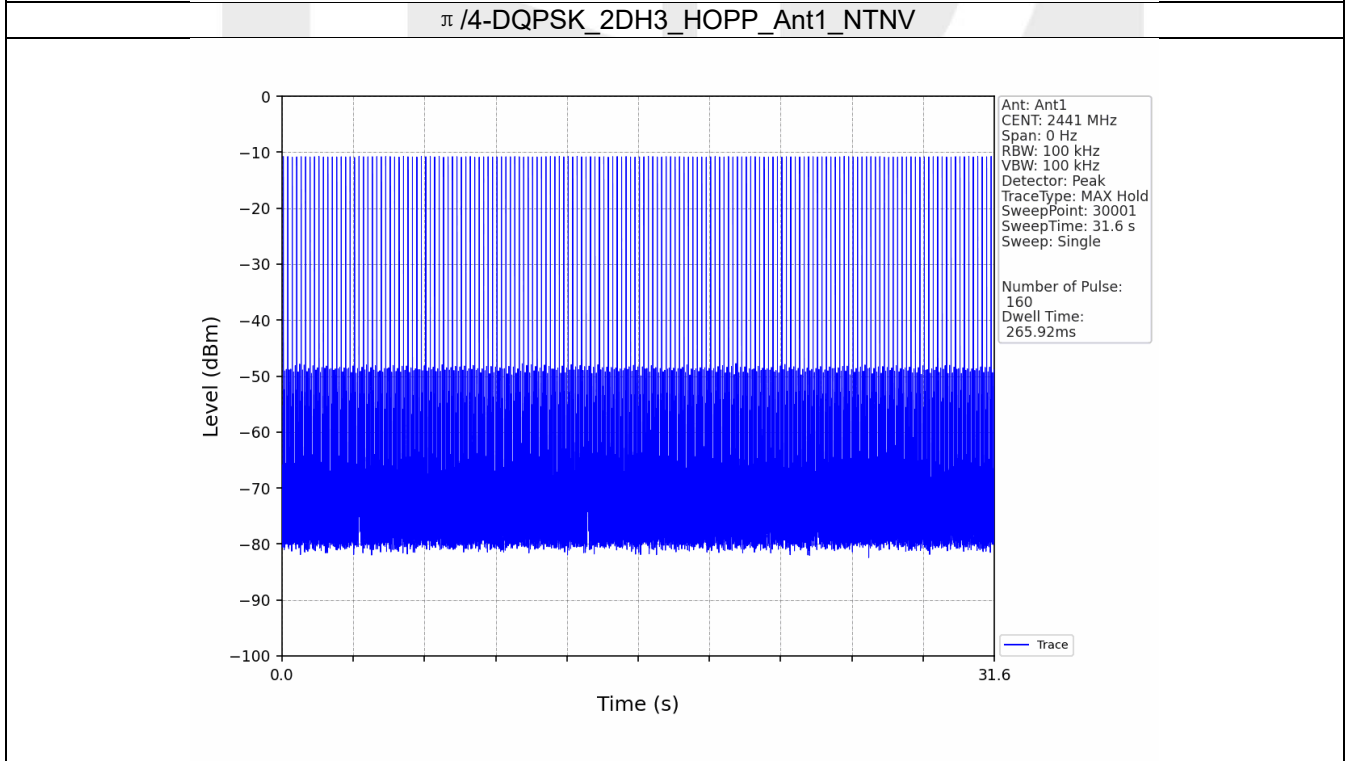
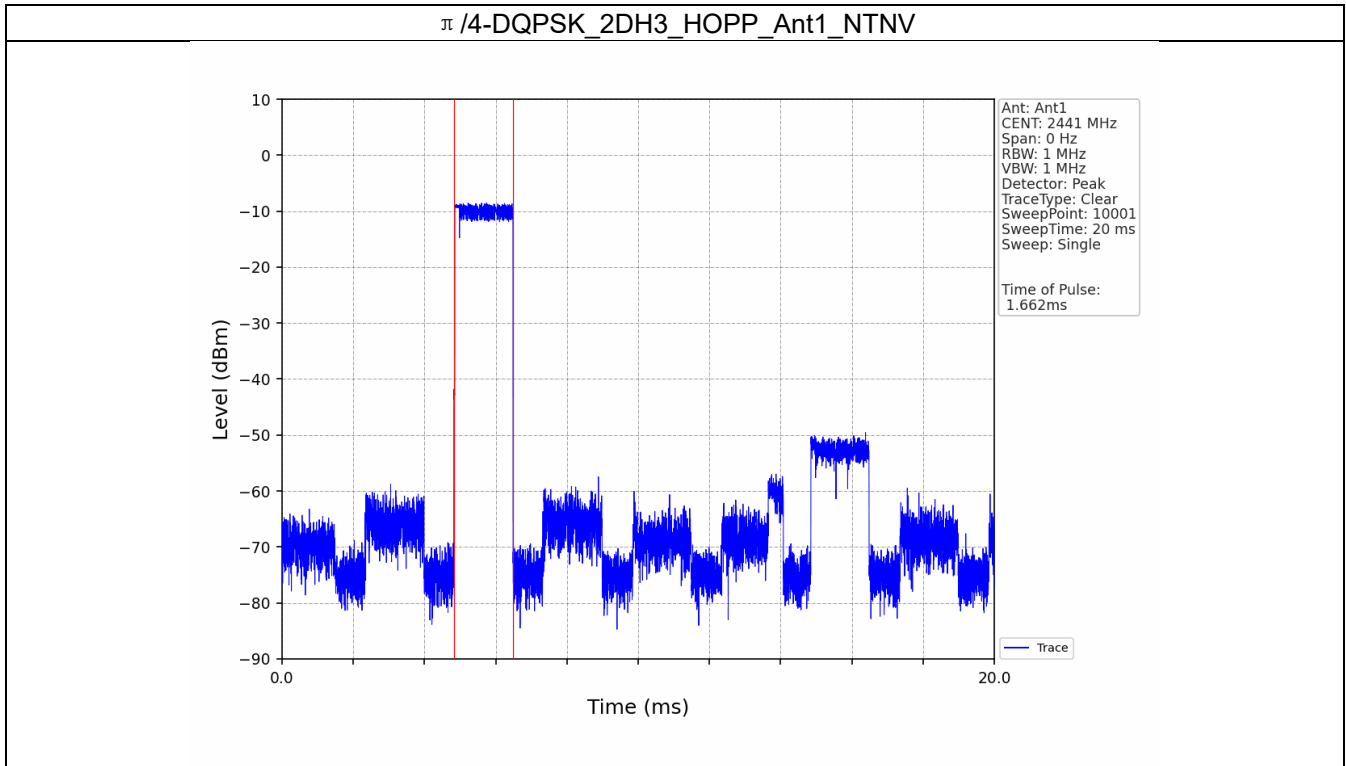
6.2.1 Ant1

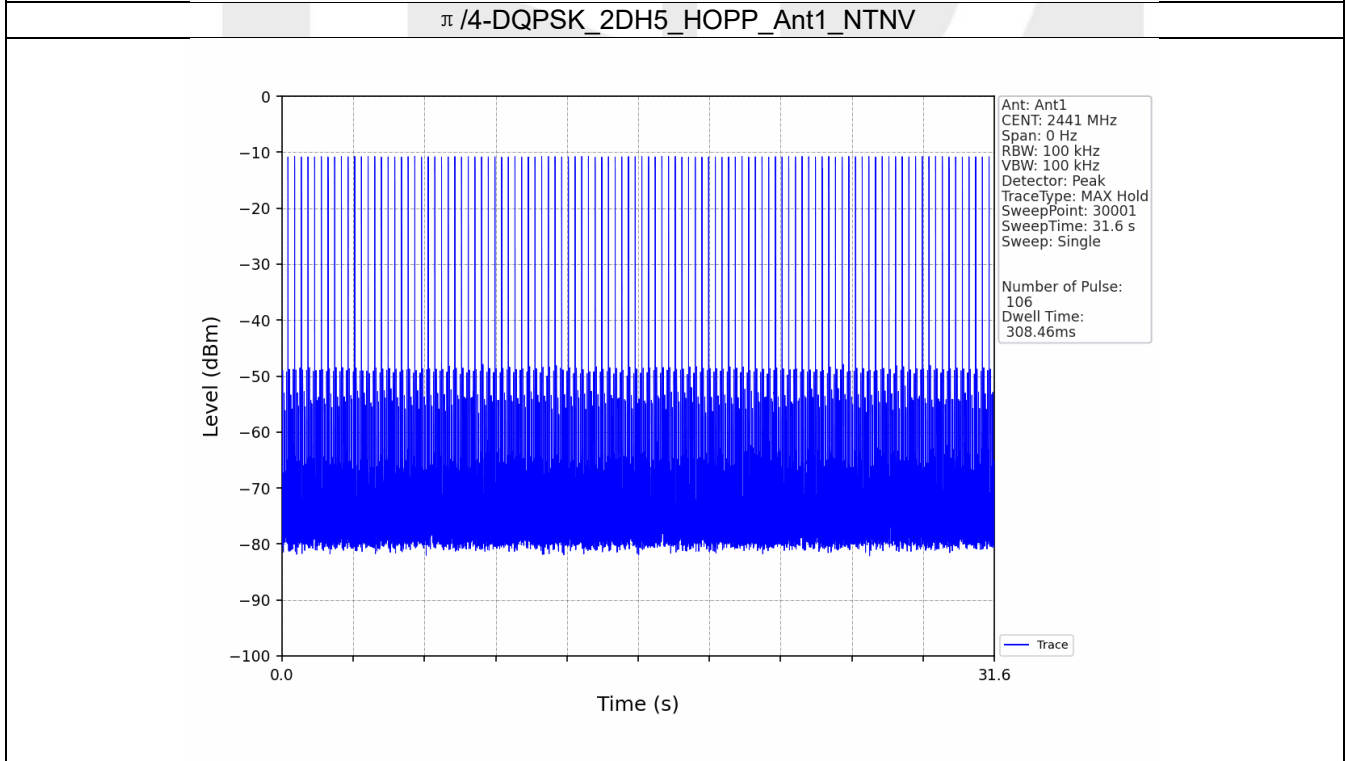
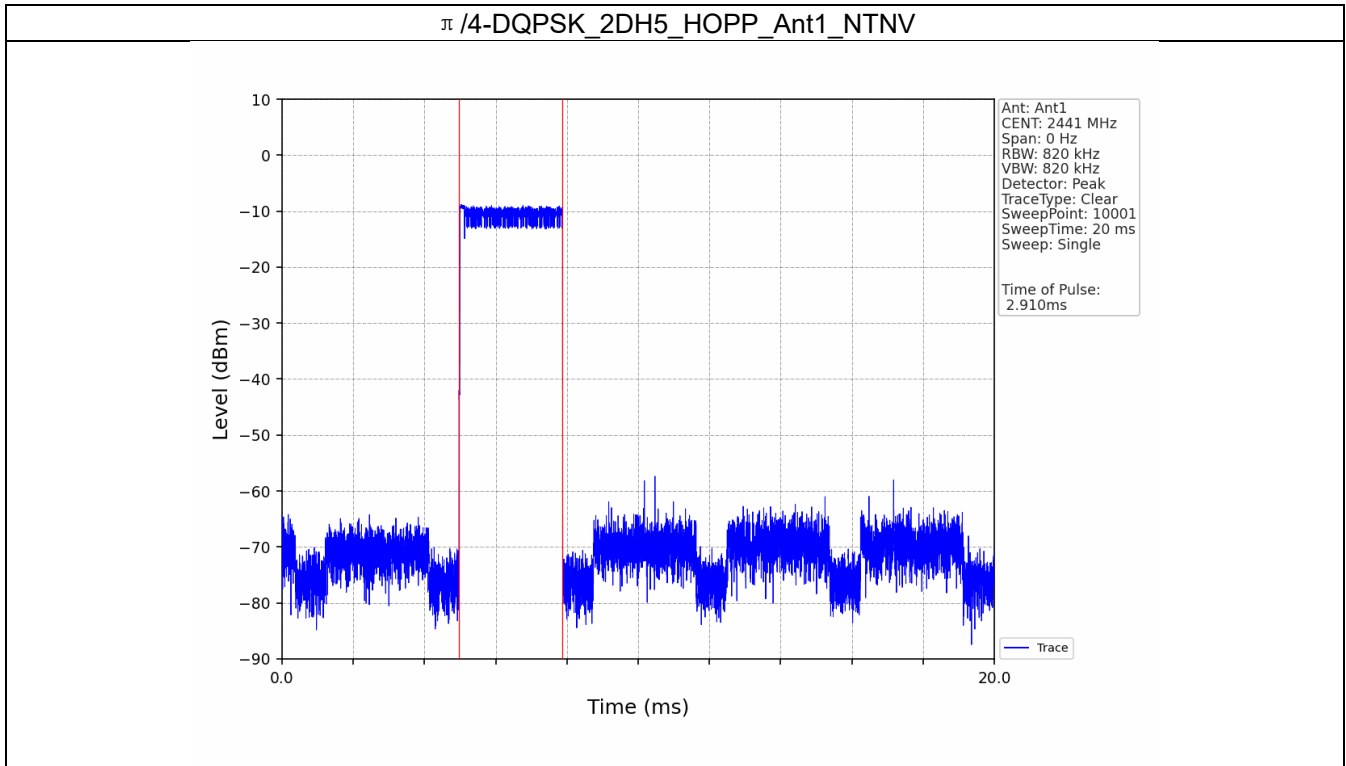


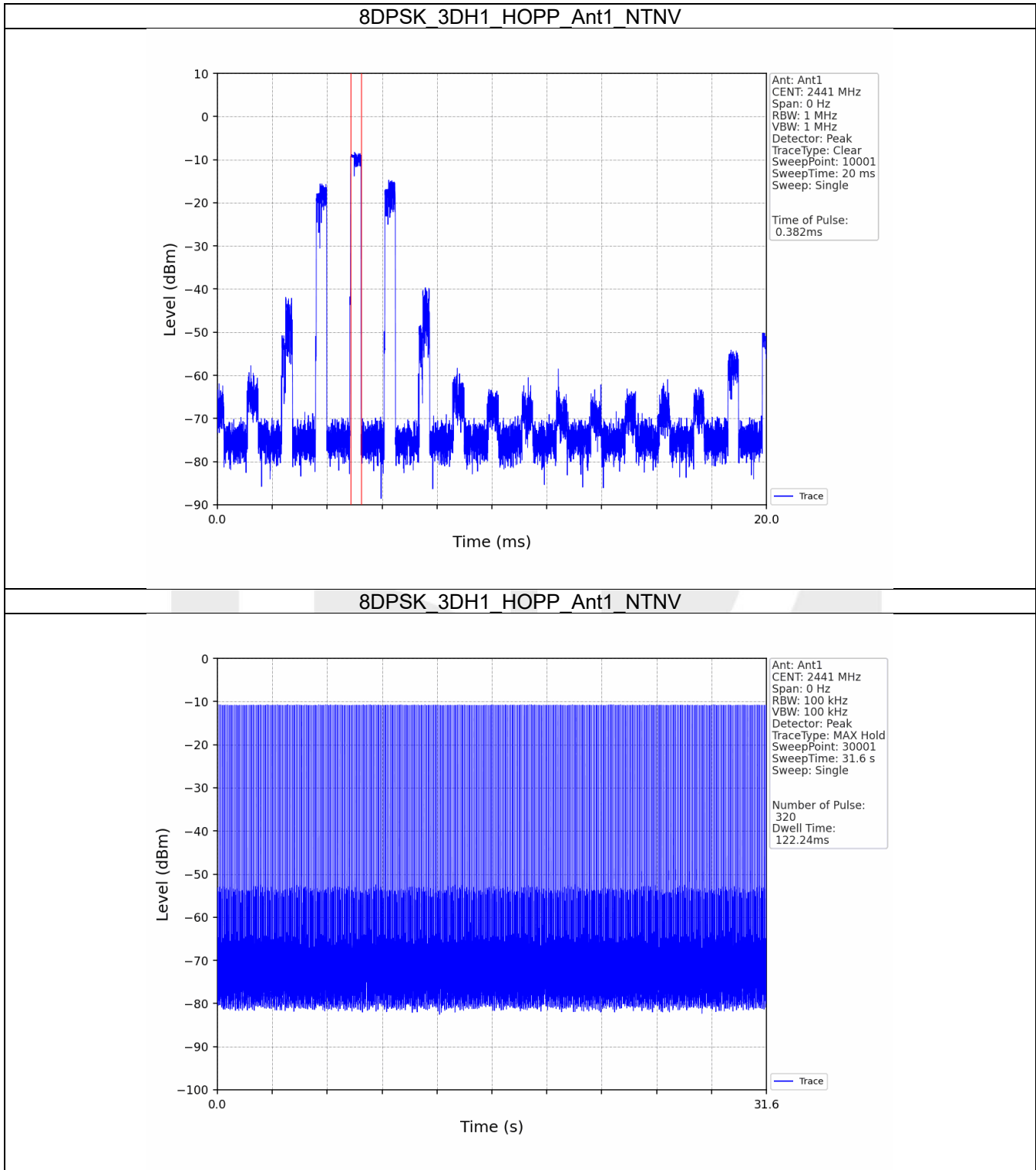


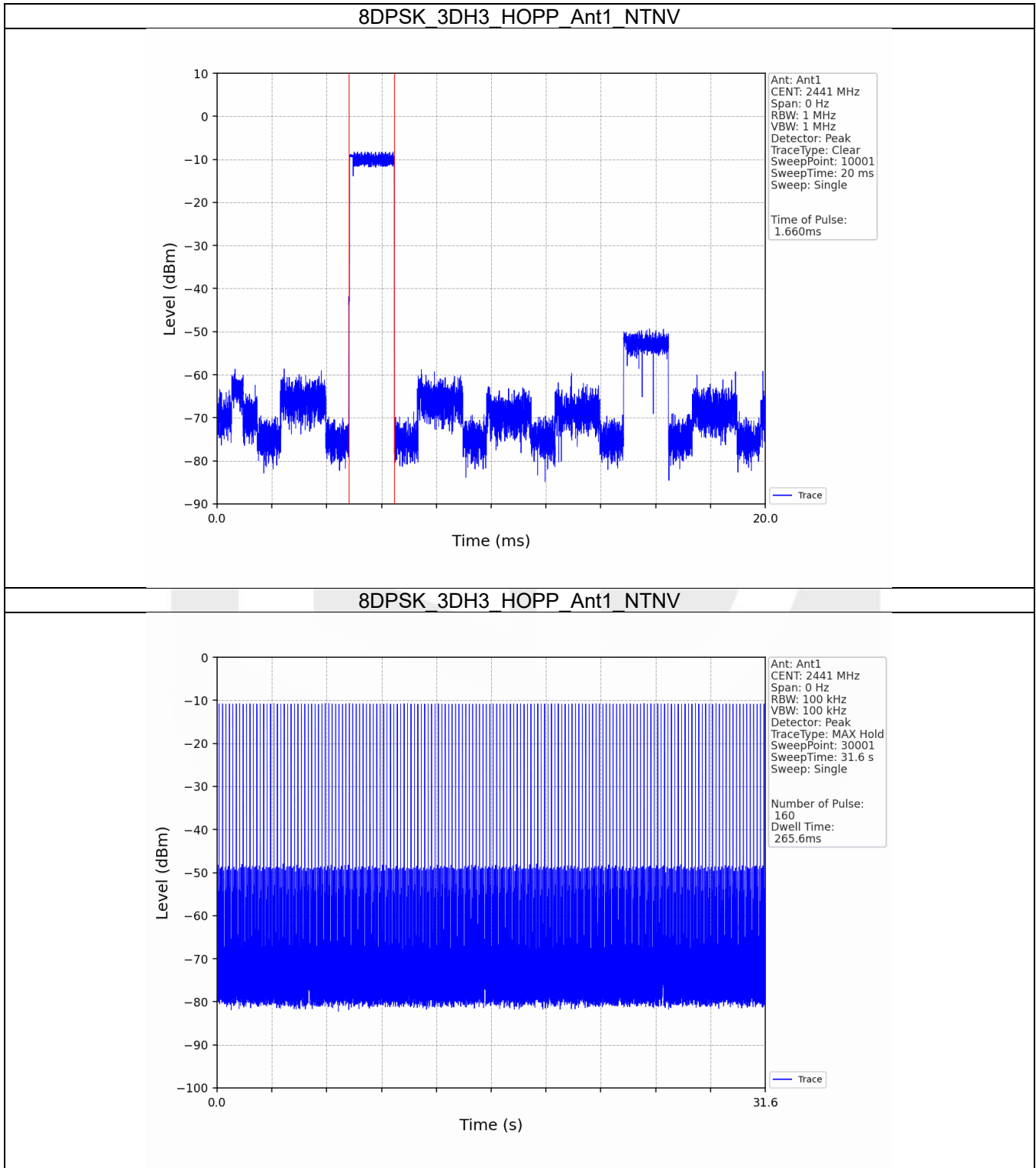


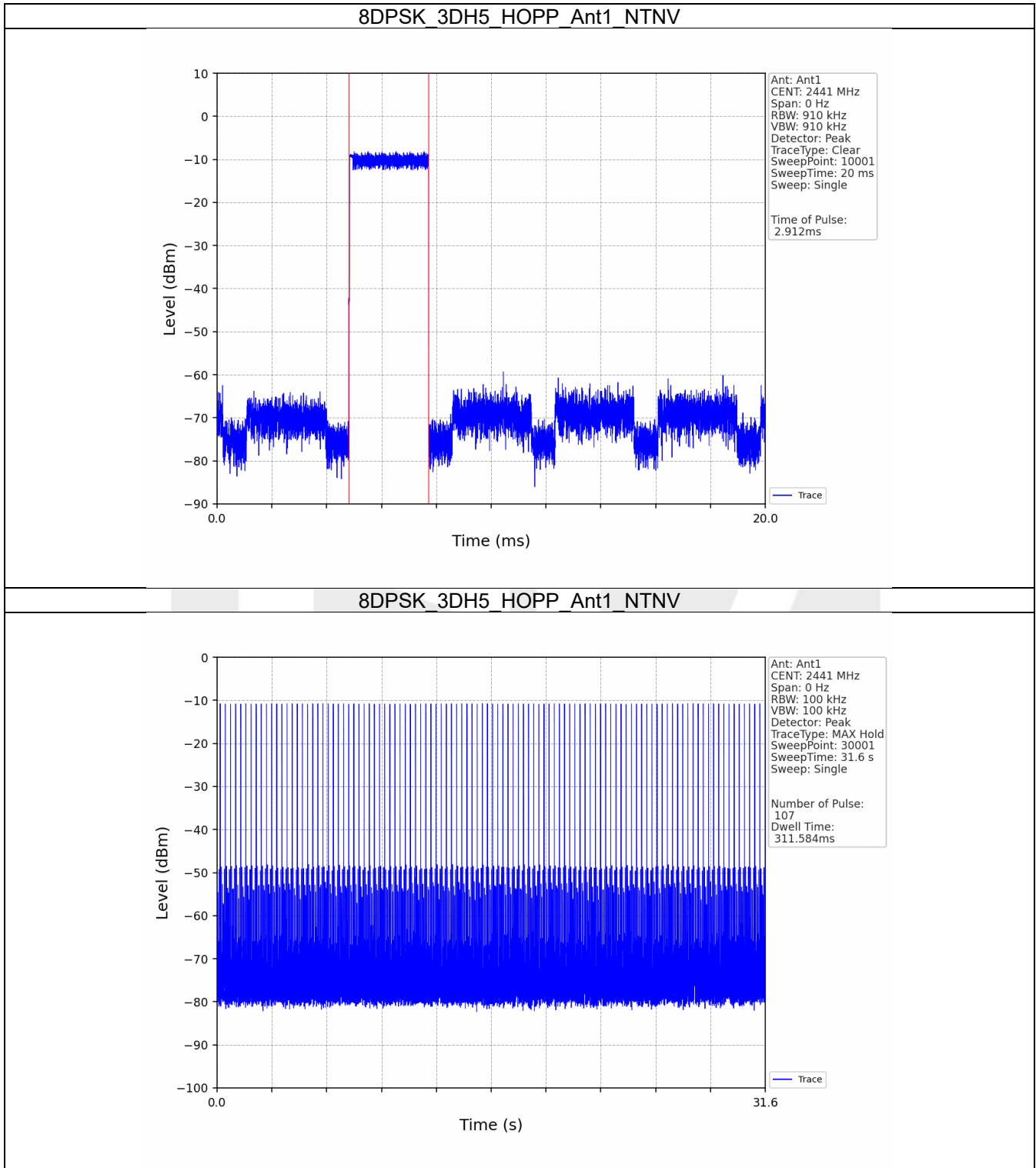












7. Unwanted Emissions In Non-restricted Frequency Bands

7.1 Test Result

7.1.1 Ref

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	-8.10
		2441	DH5	1	-8.54
		2480	DH5	1	-8.72
		HOPP	DH5	1	-7.50 -7.50
$\pi/4$ -DQPSK	SISO	2402	2DH5	1	-8.53
		2441	2DH5	1	-9.34
		2480	2DH5	1	-9.47
		HOPP	2DH5	1	-8.77 -8.77
8DPSK	SISO	2402	3DH5	1	-8.08
		2441	3DH5	1	-9.16
		2480	3DH5	1	-9.29
		HOPP	3DH5	1	-8.14 -8.14

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2020, the channel contains the maximum PSD level was used to establish the reference level.

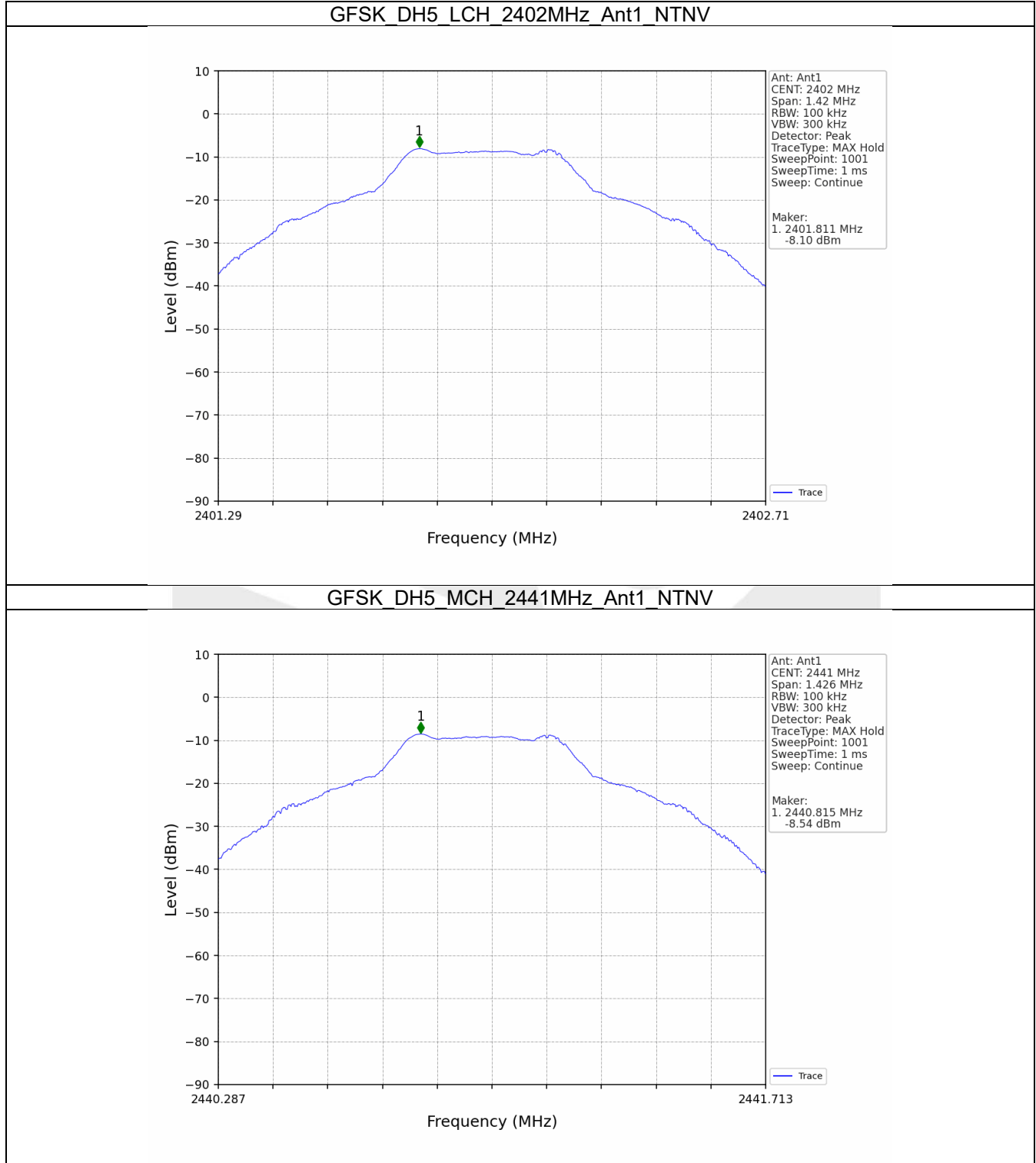
7.1.2 CSE

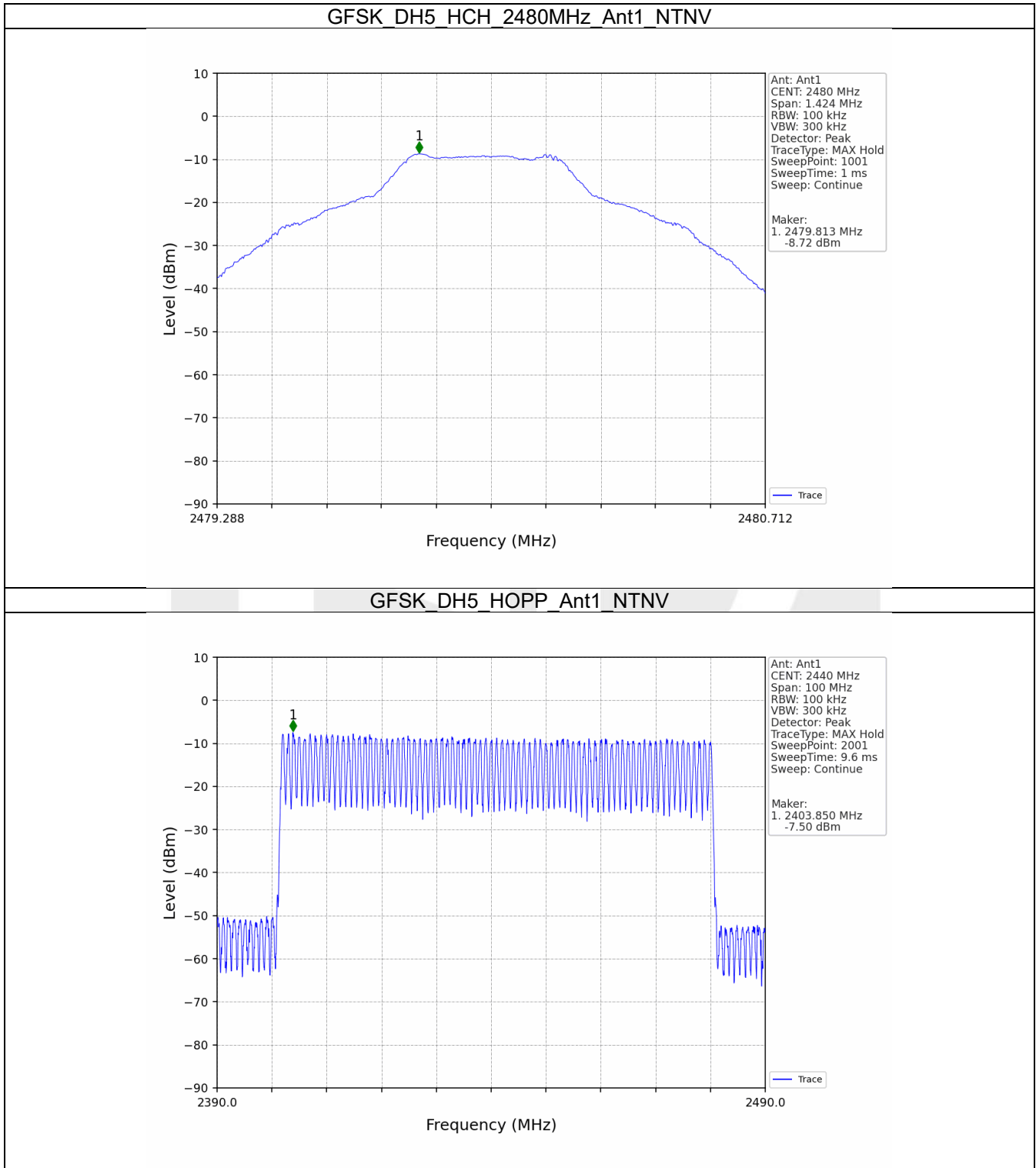
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	-8.10	-28.10	Pass
		2441	DH5	1	-8.54	-28.54	Pass
		2480	DH5	1	-8.72	-28.72	Pass
		HOPP	DH5	1	-7.50 -7.50	-27.50 -27.50	Pass Pass
$\pi/4$ -DQPSK	SISO	2402	2DH5	1	-8.53	-28.53	Pass
		2441	2DH5	1	-9.34	-29.34	Pass
		2480	2DH5	1	-9.47	-29.47	Pass
		HOPP	2DH5	1	-8.77 -8.77	-28.77 -28.77	Pass Pass
8DPSK	SISO	2402	3DH5	1	-8.08	-28.08	Pass
		2441	3DH5	1	-9.16	-29.16	Pass
		2480	3DH5	1	-9.29	-29.29	Pass
		HOPP	3DH5	1	-8.14 -8.14	-28.14 -28.14	Pass Pass

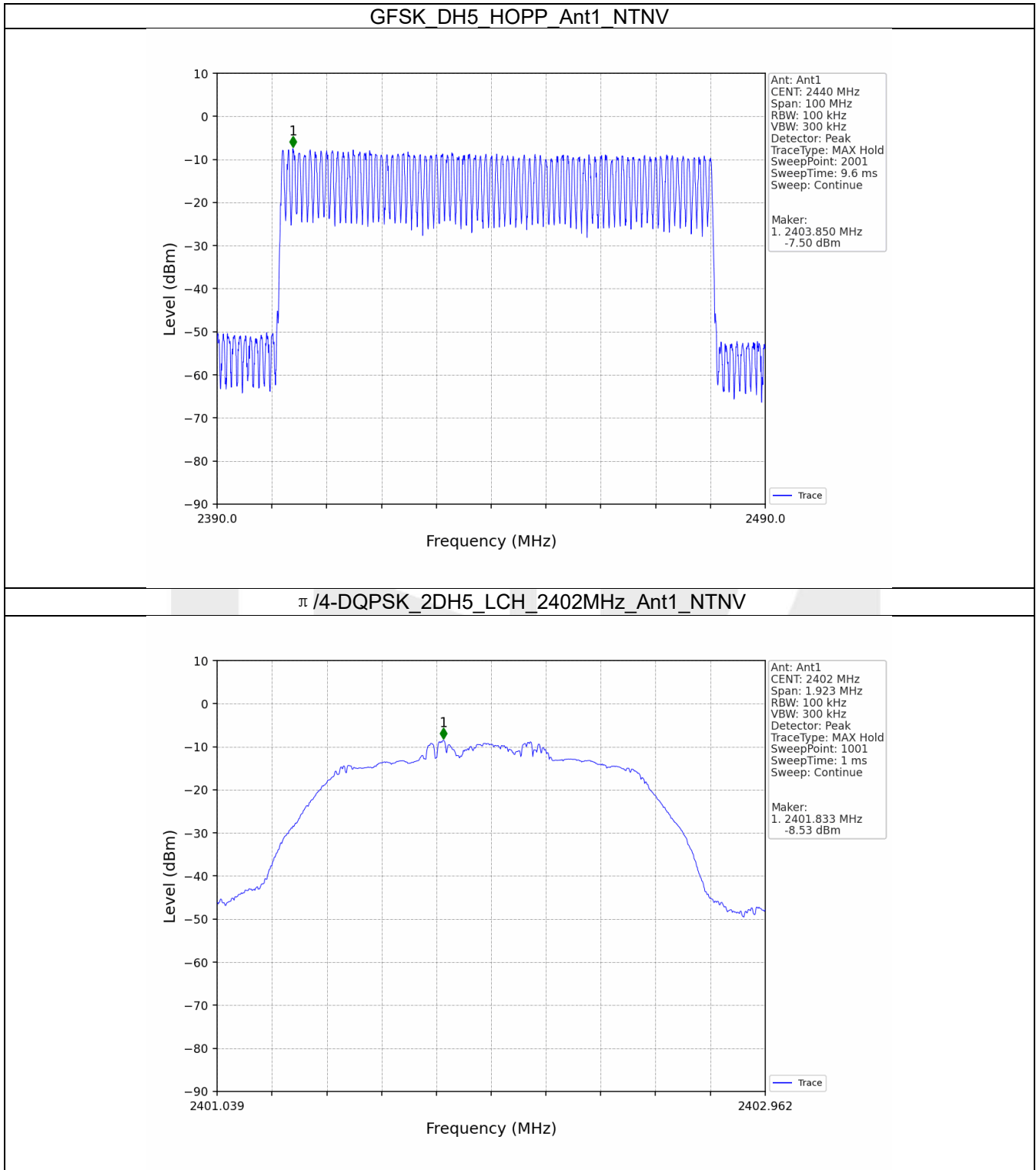
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2020, the channel contains the maximum PSD level was used to establish the reference level.

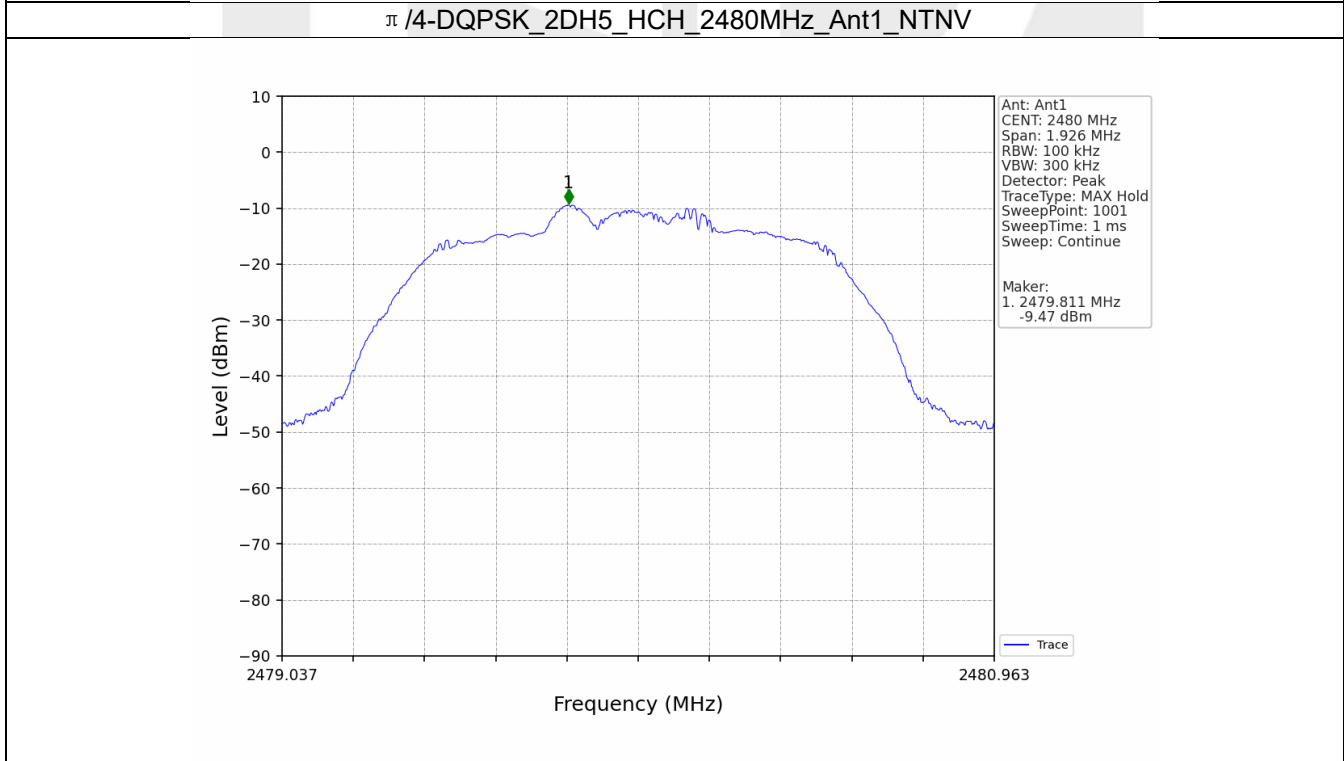
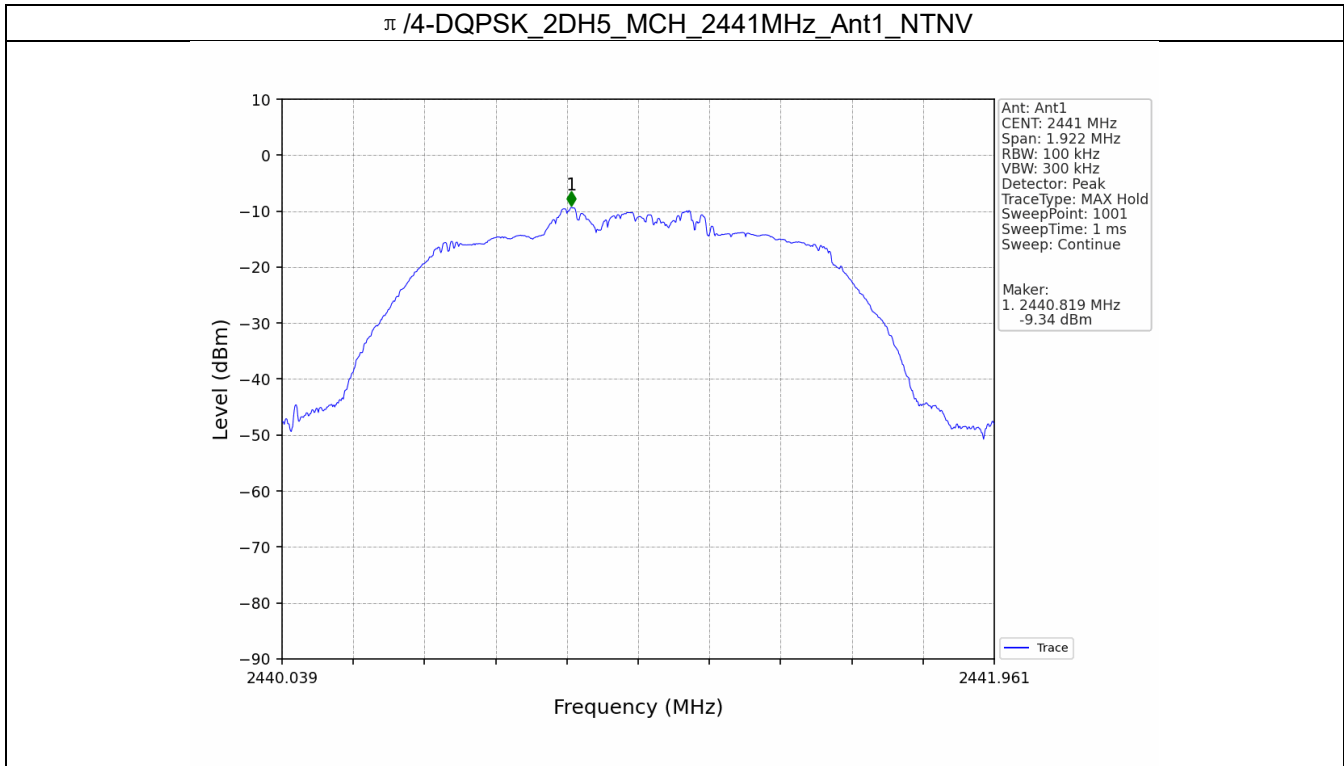
7.2 Test Graph

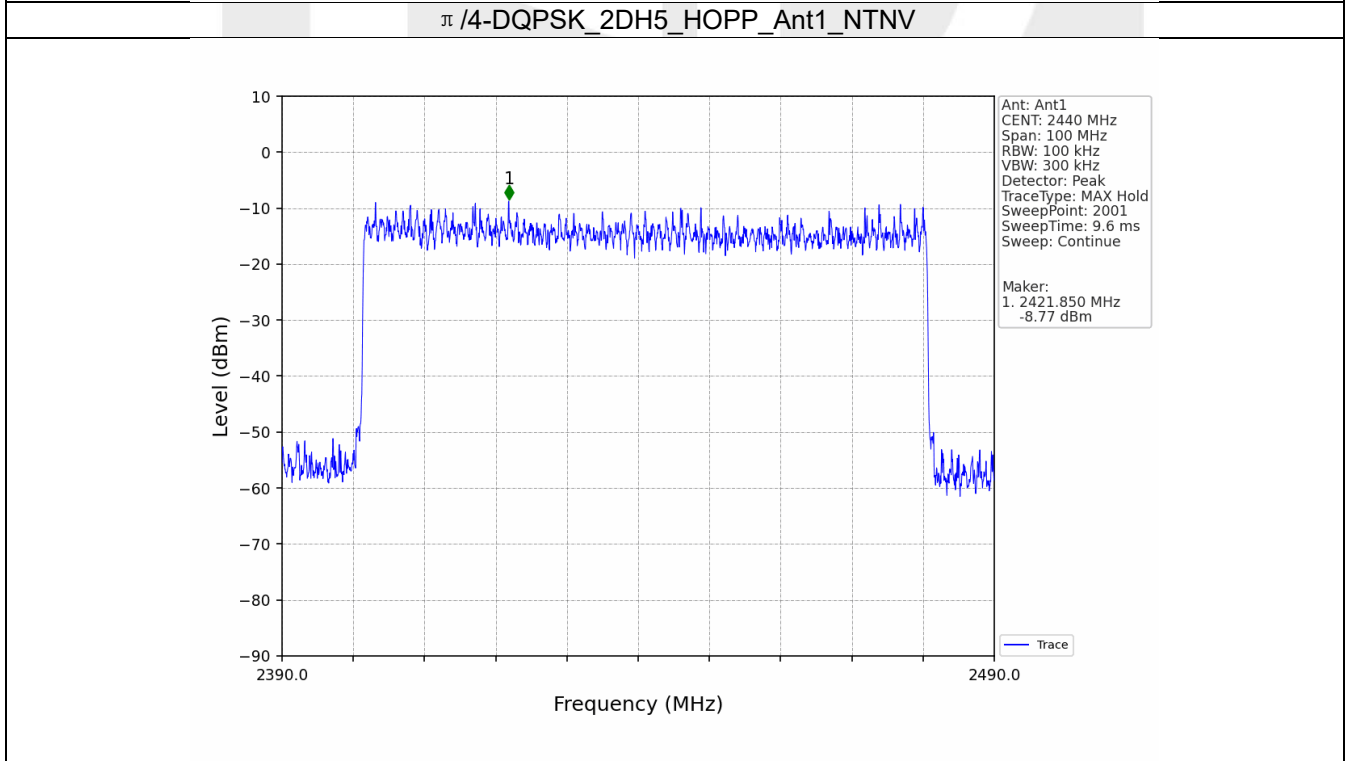
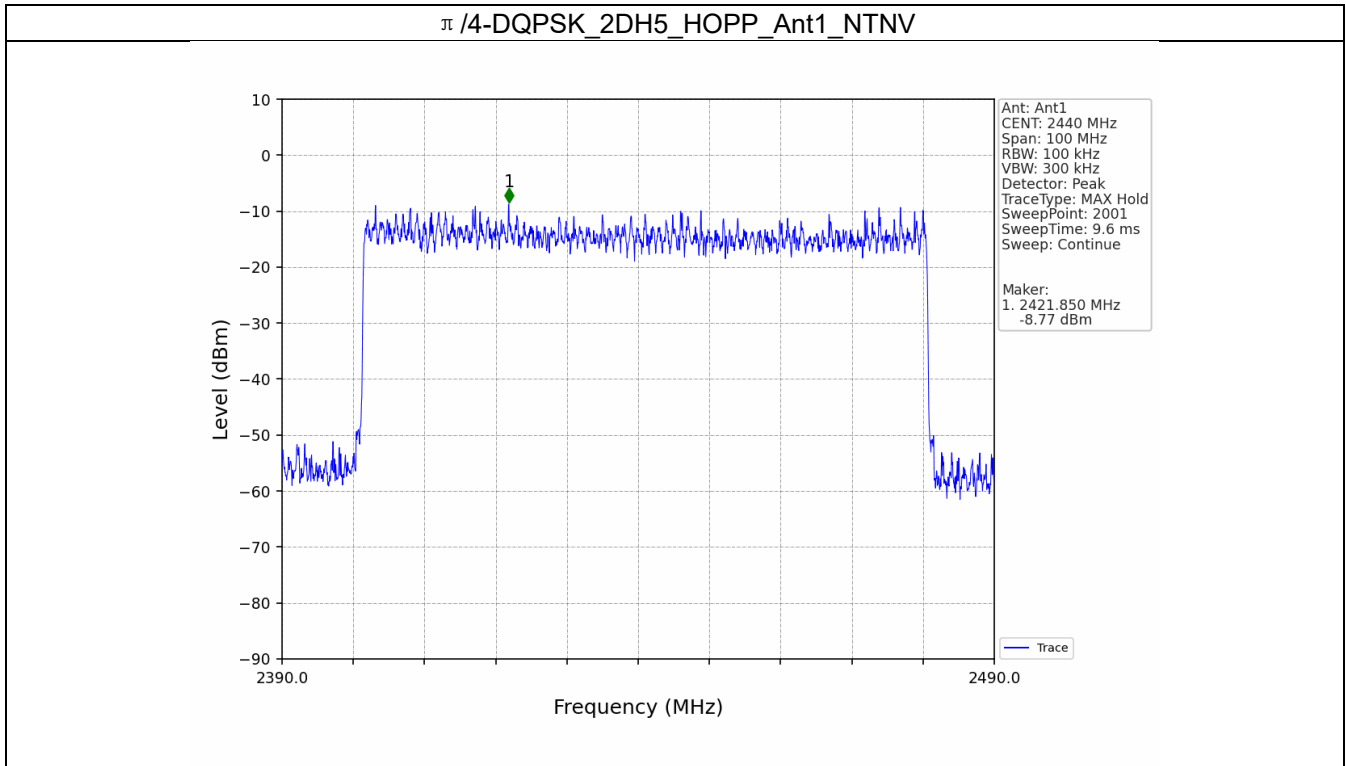
7.2.1 Ref

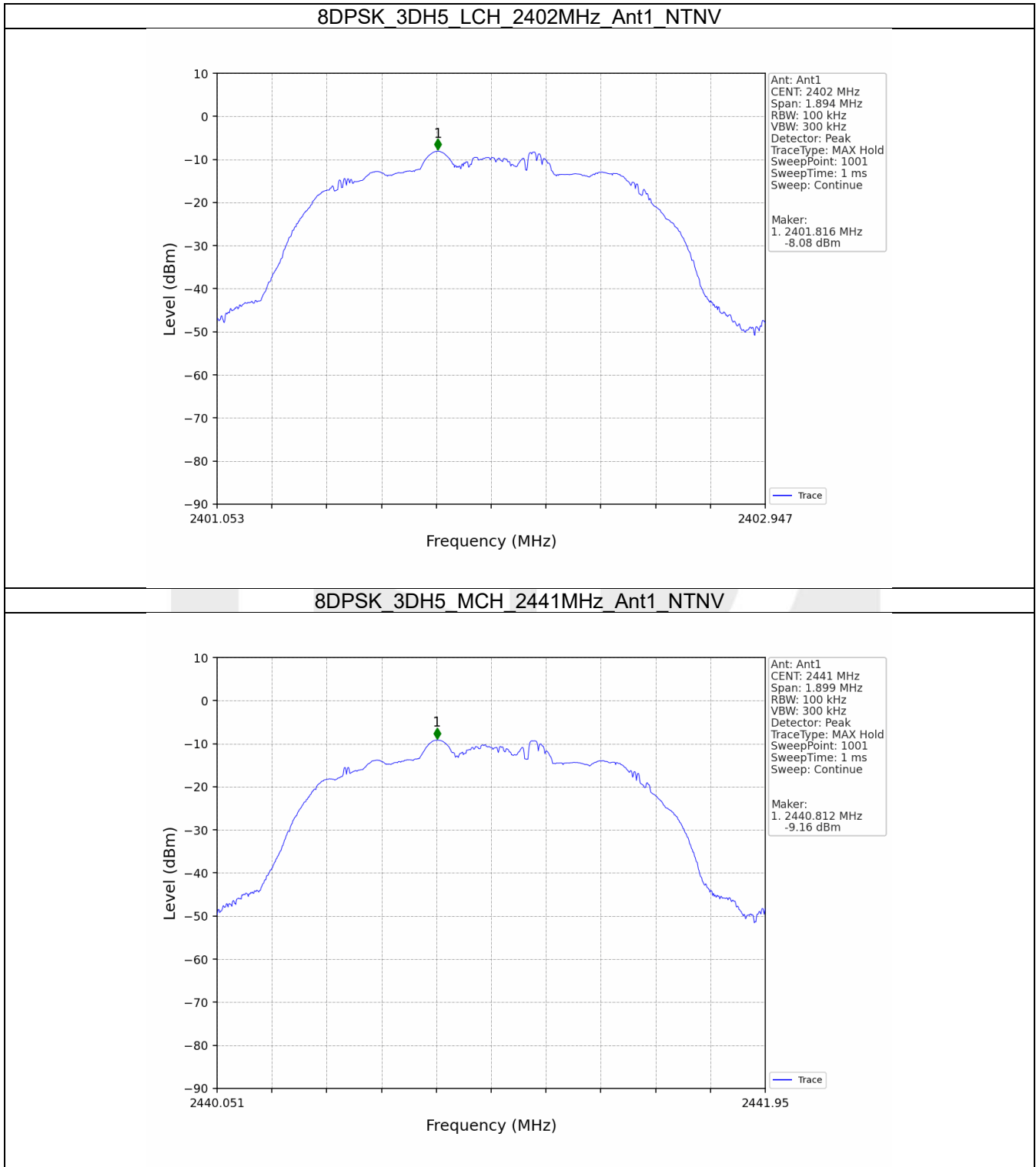


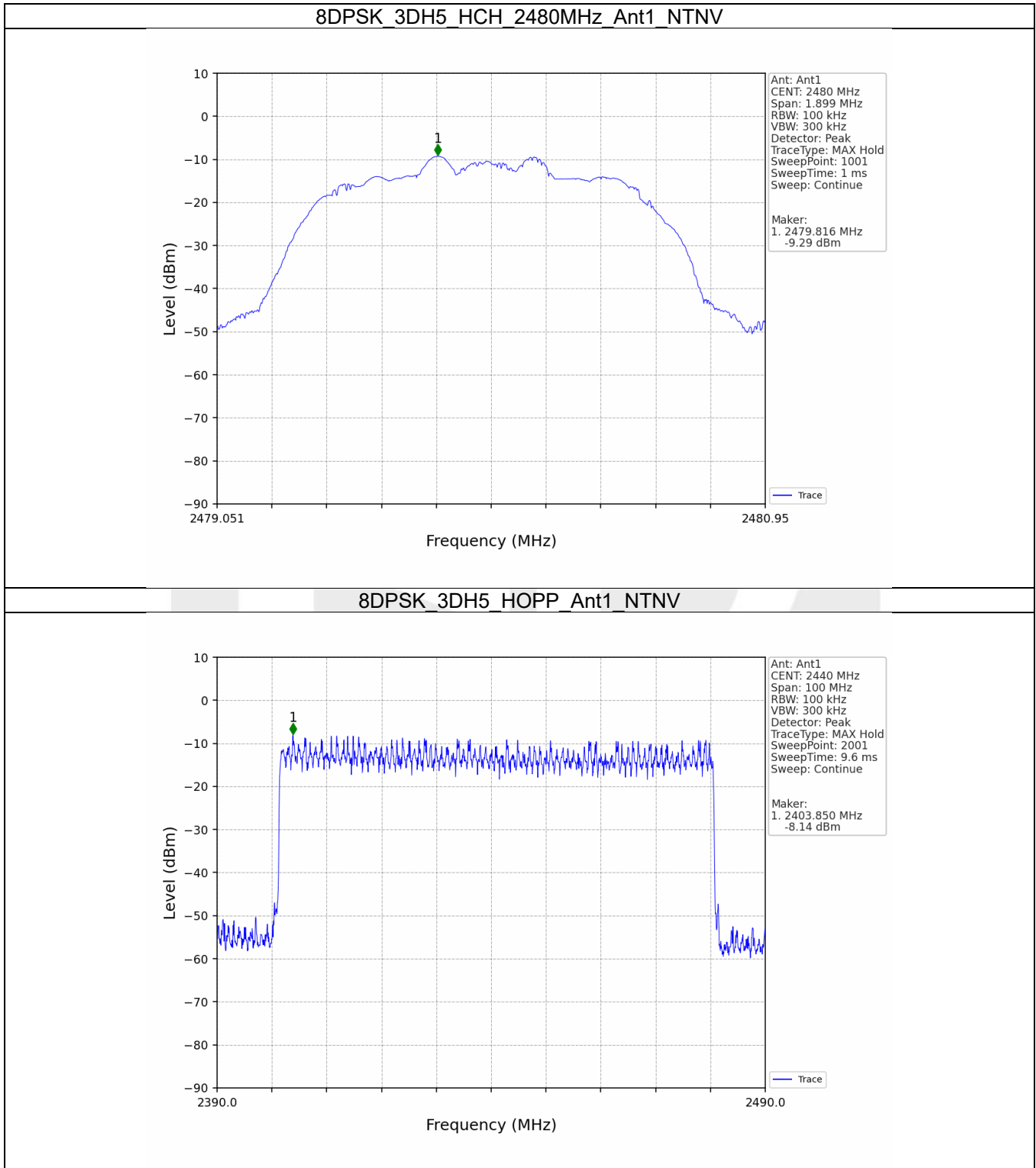


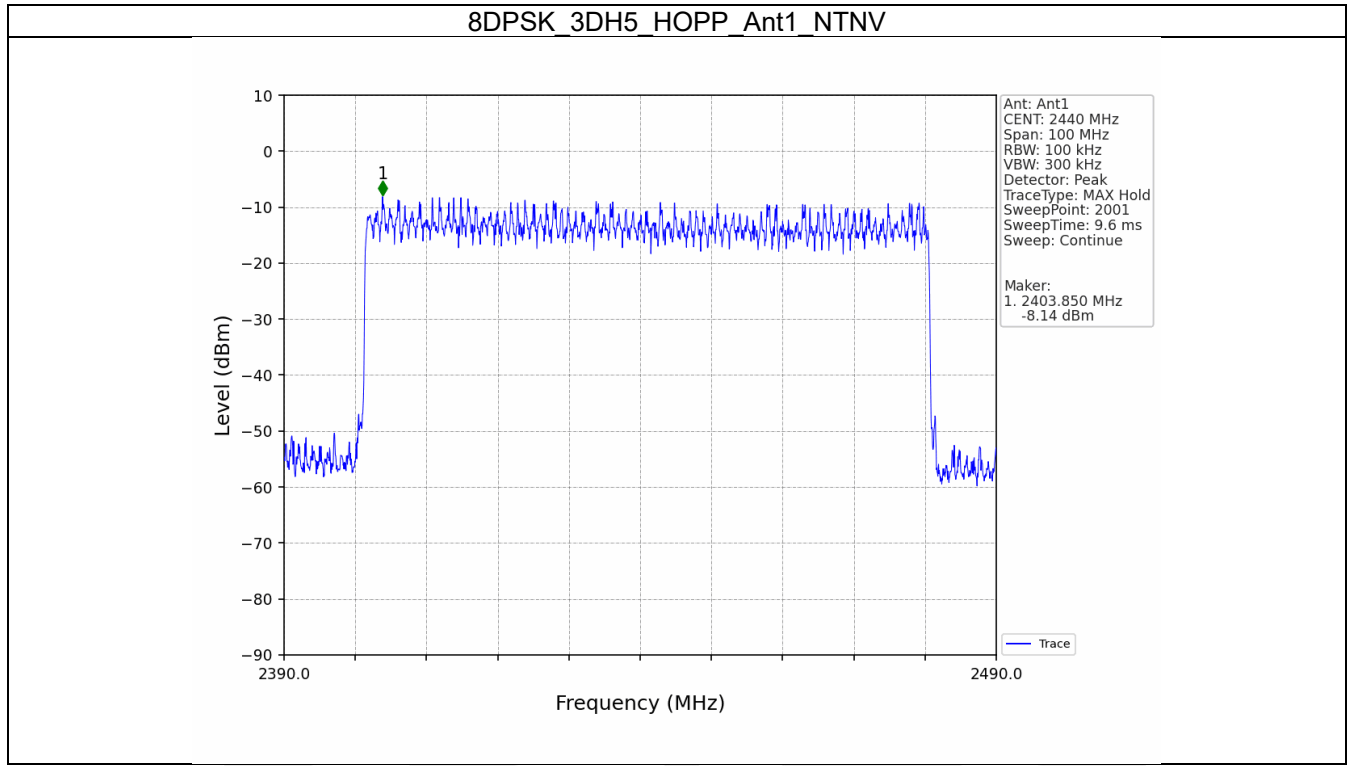




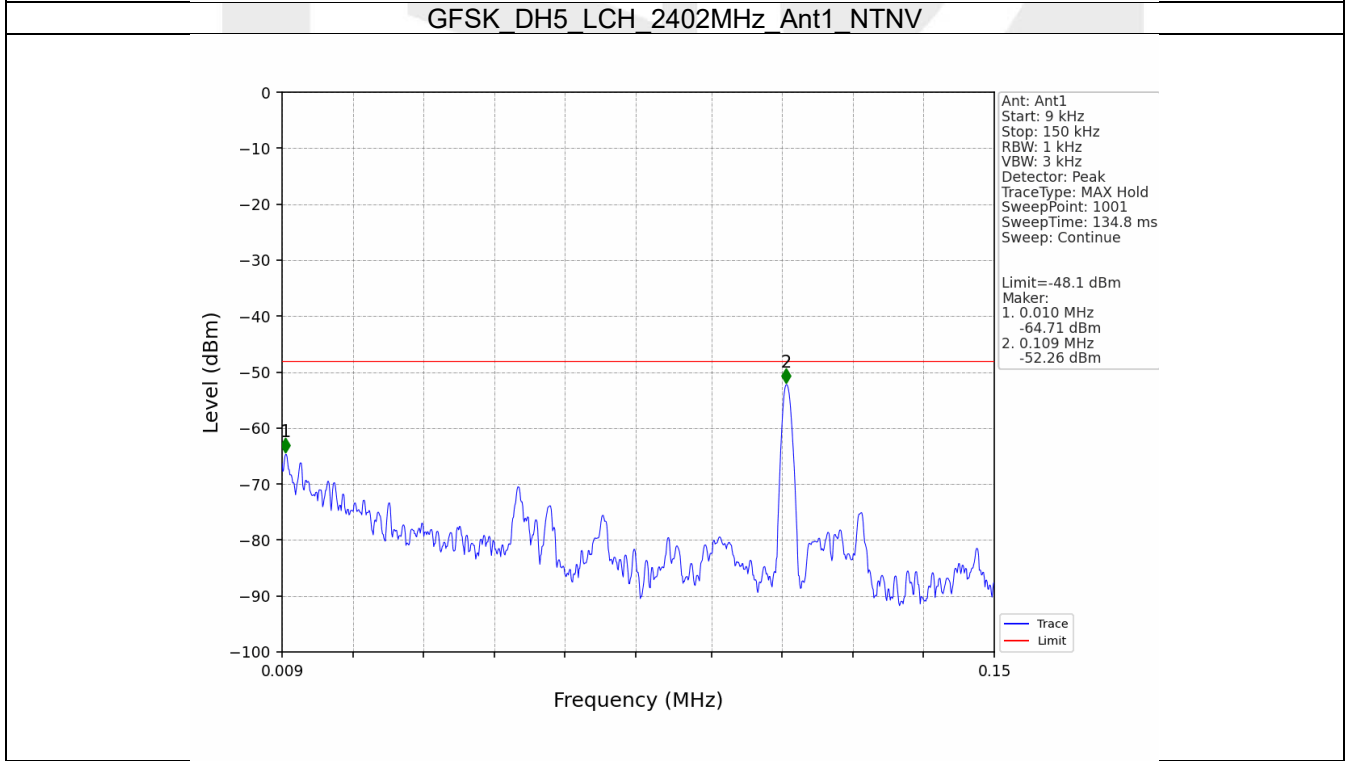
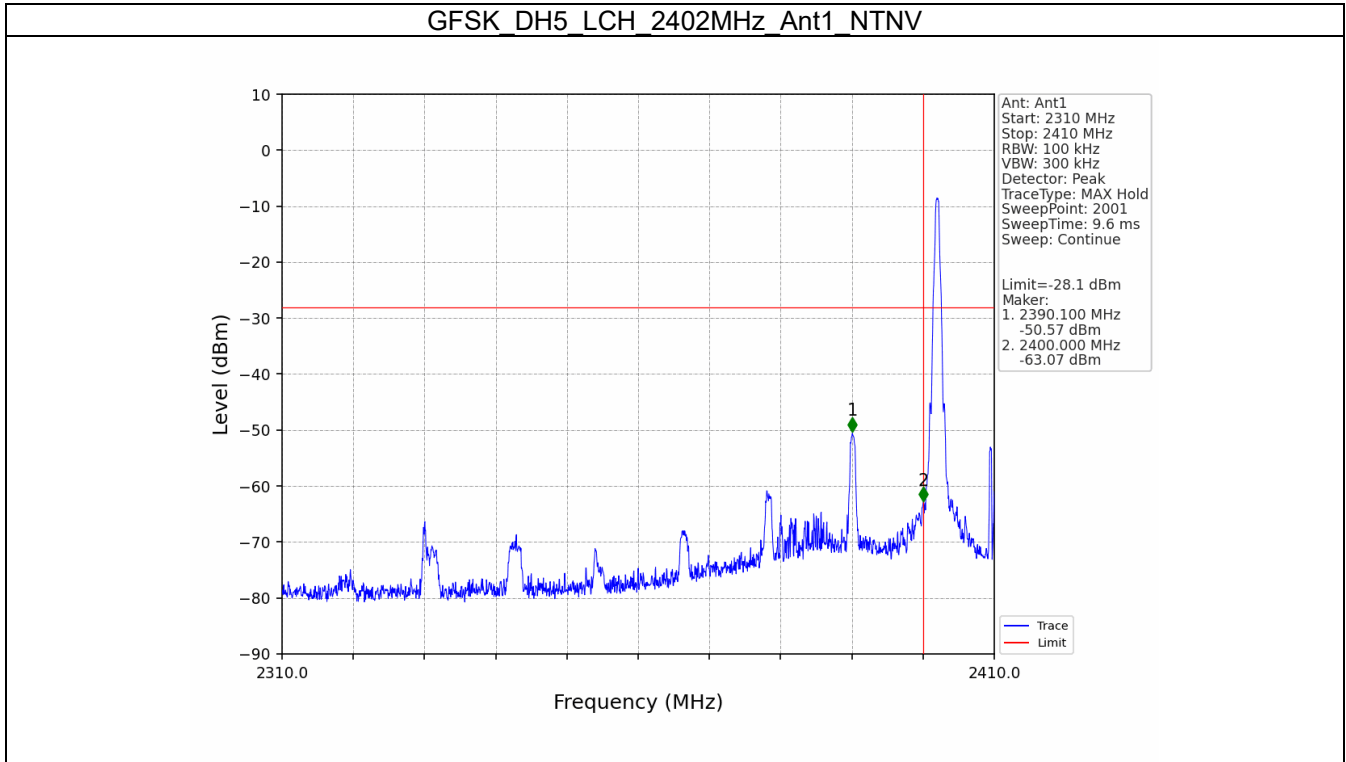


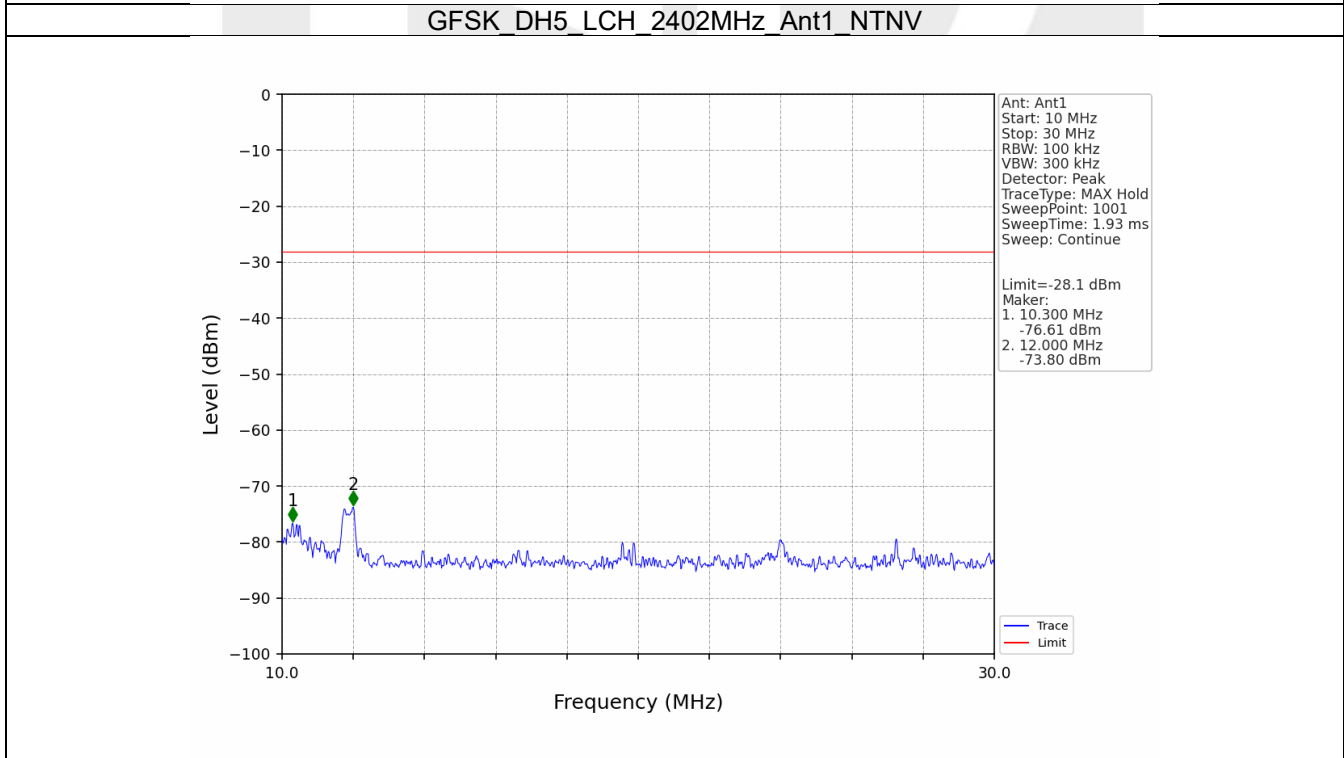
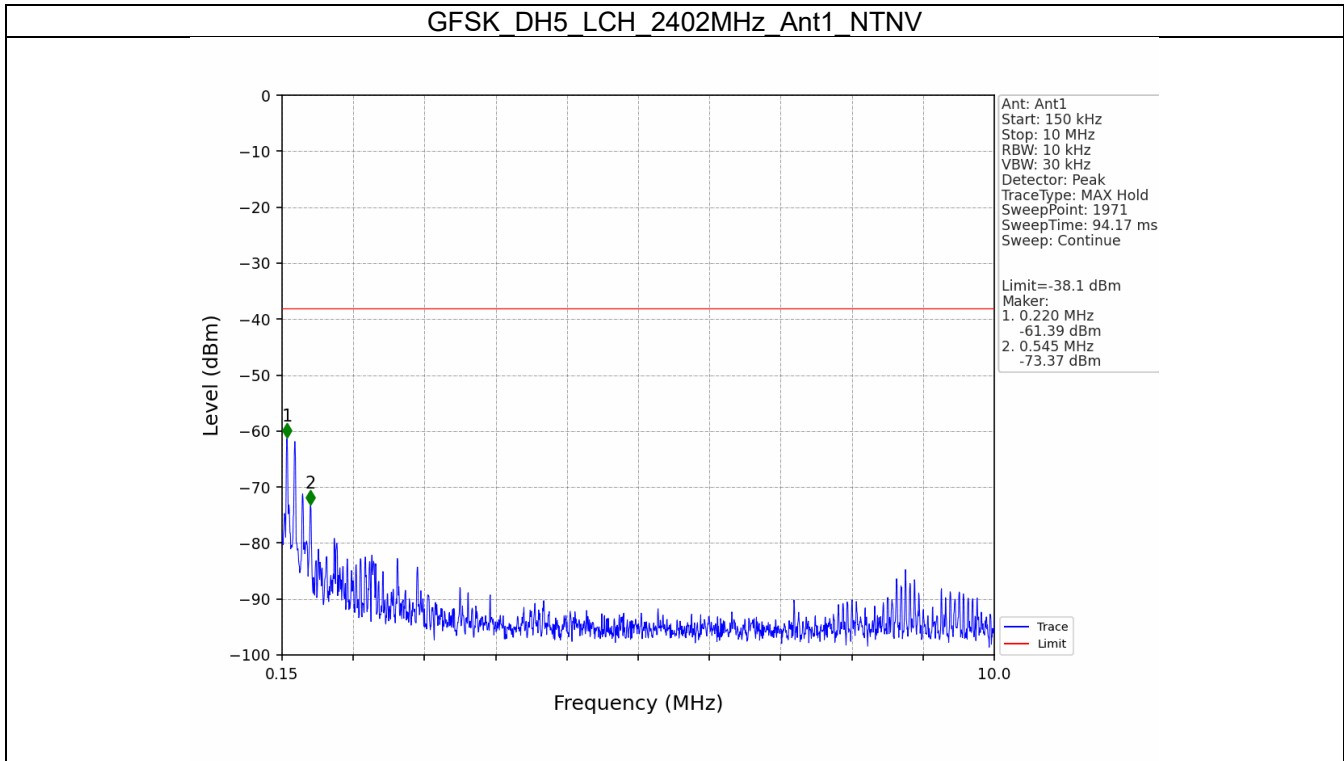


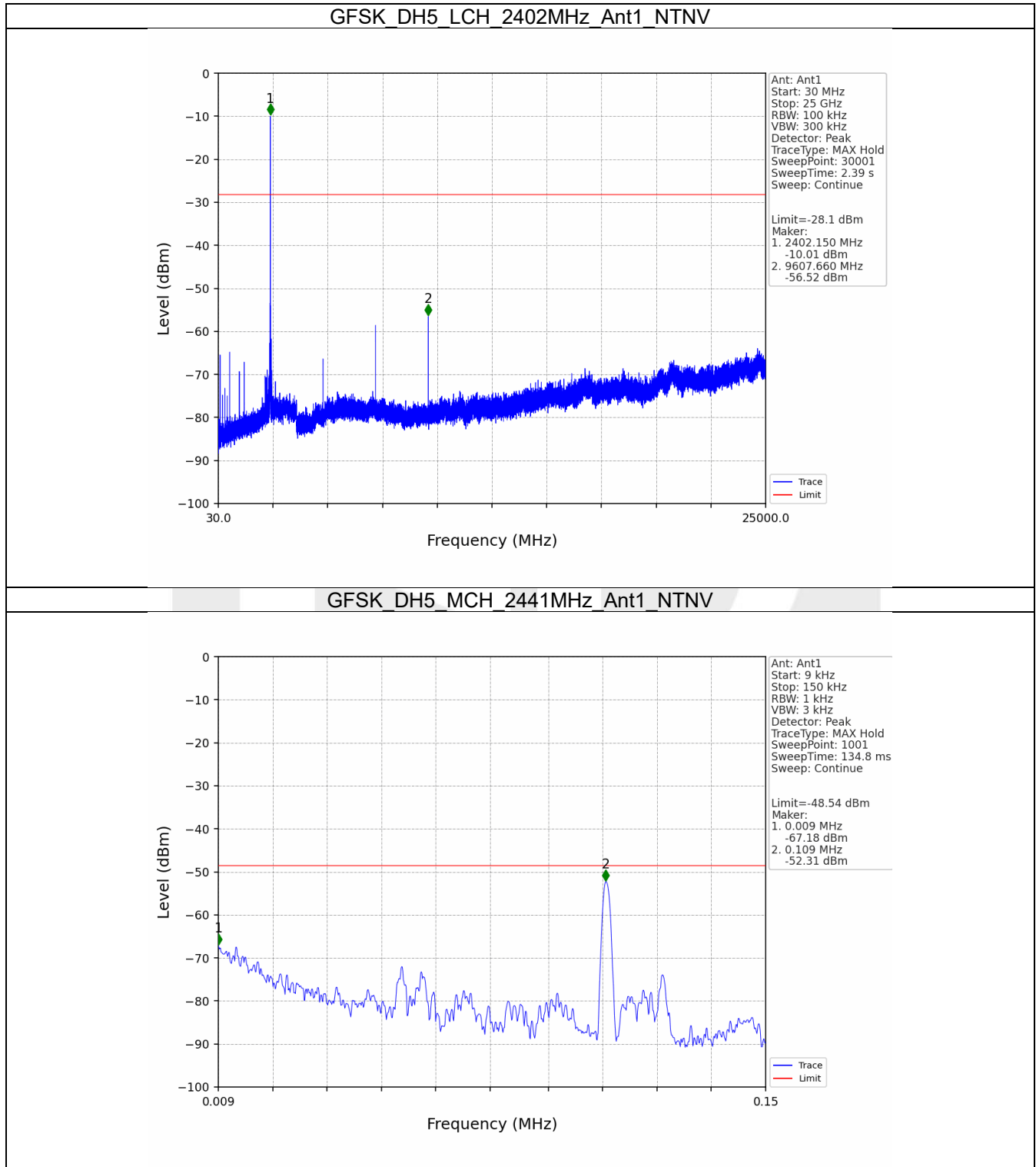


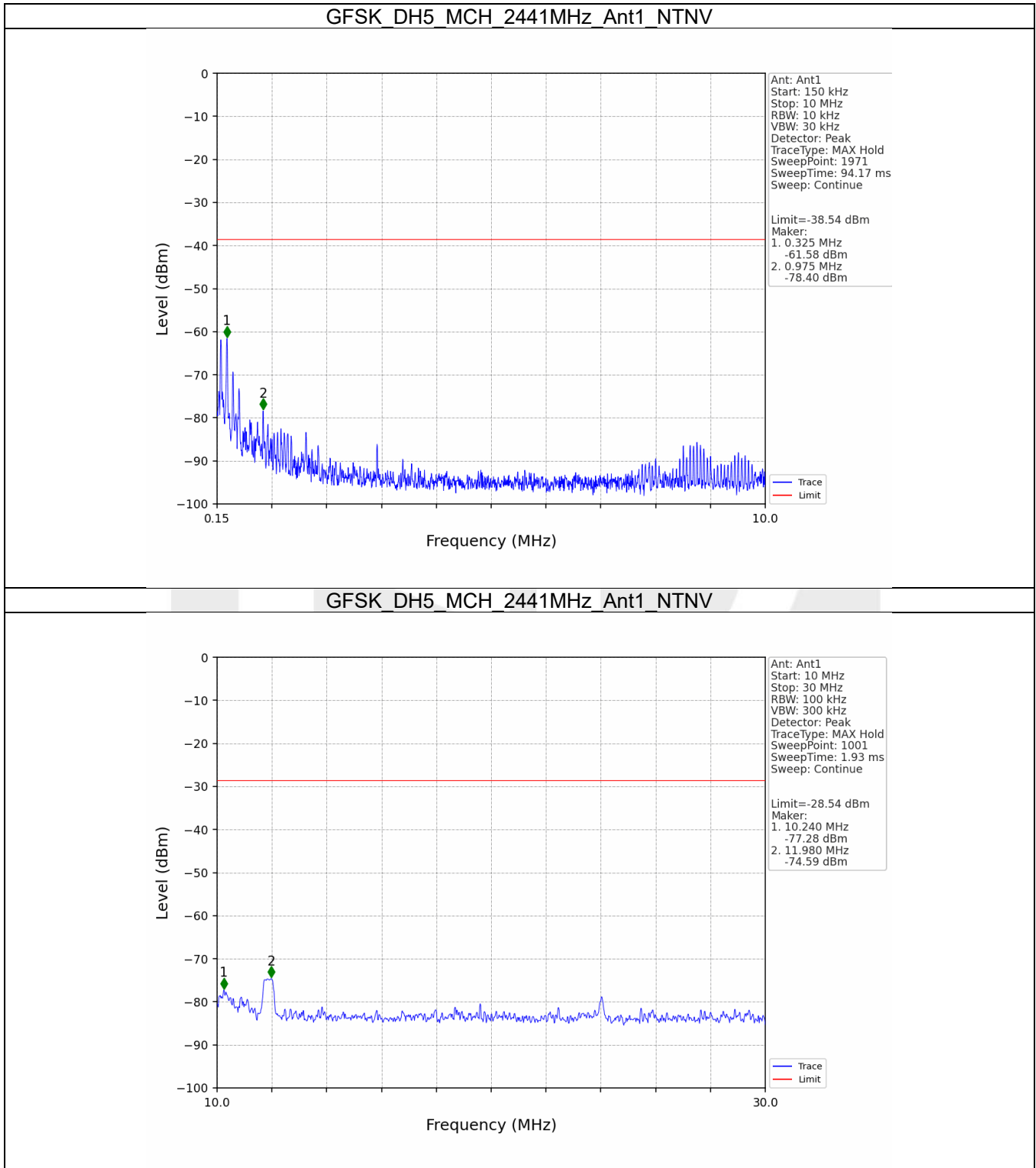


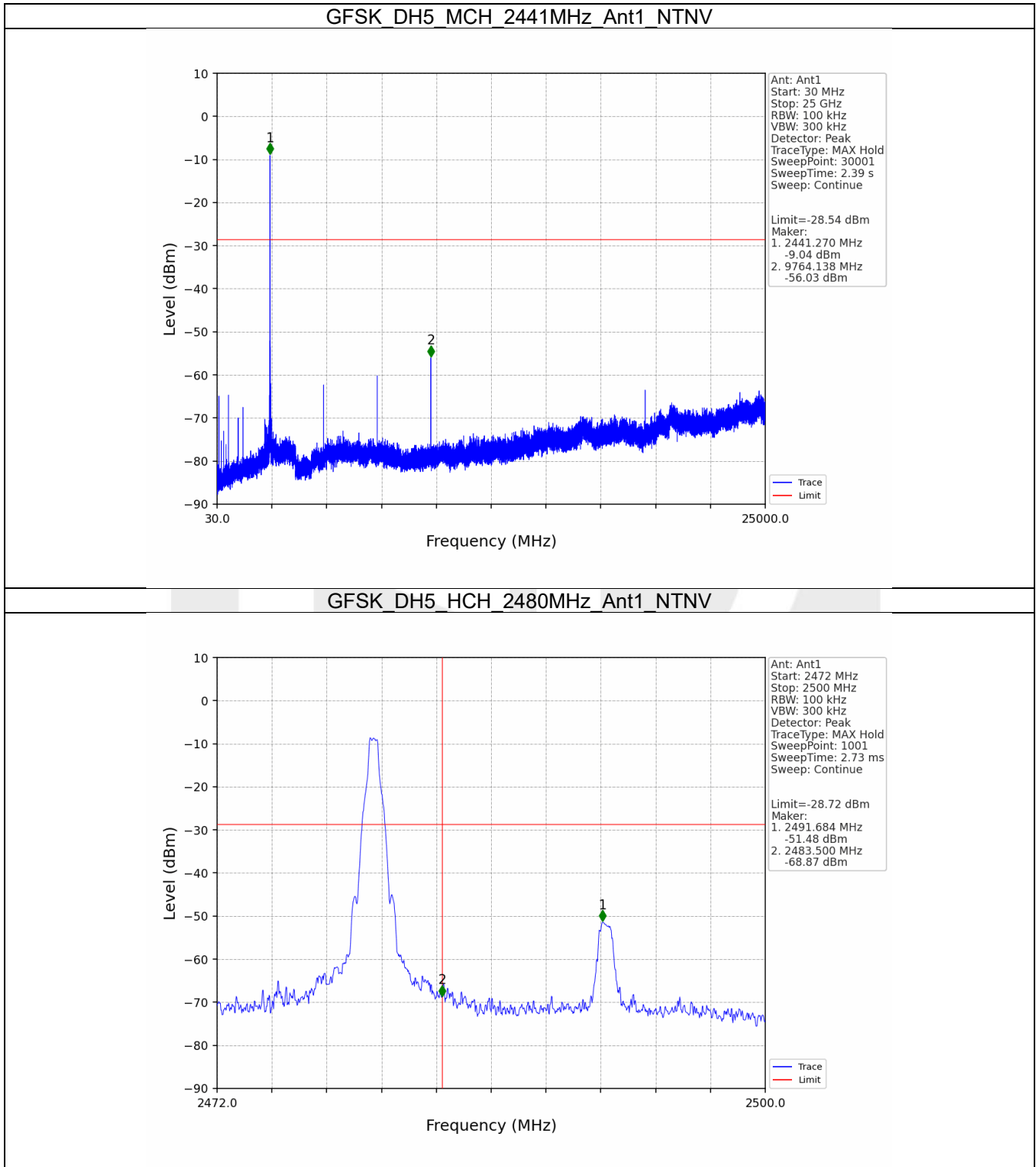
7.2.2 CSE

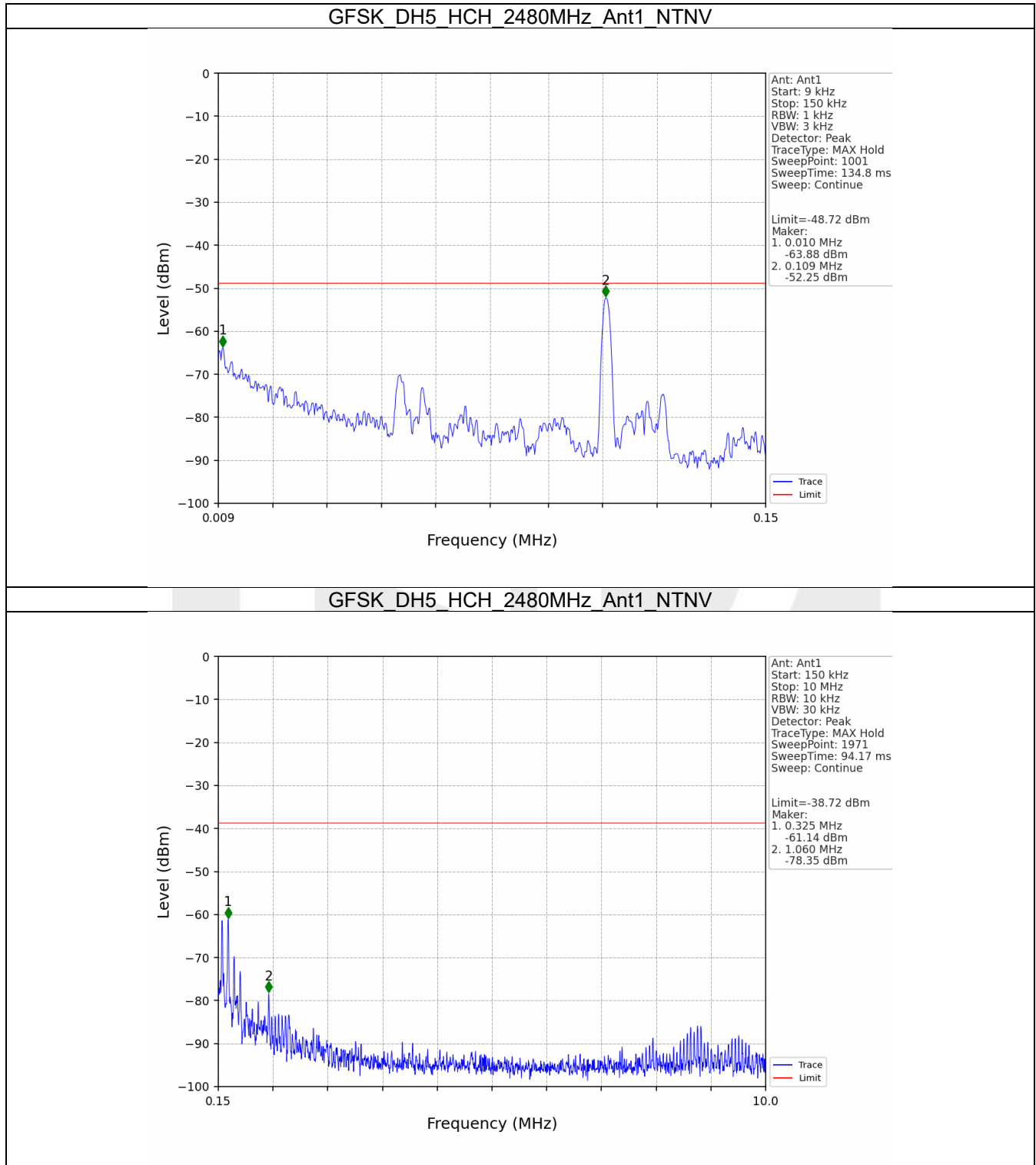


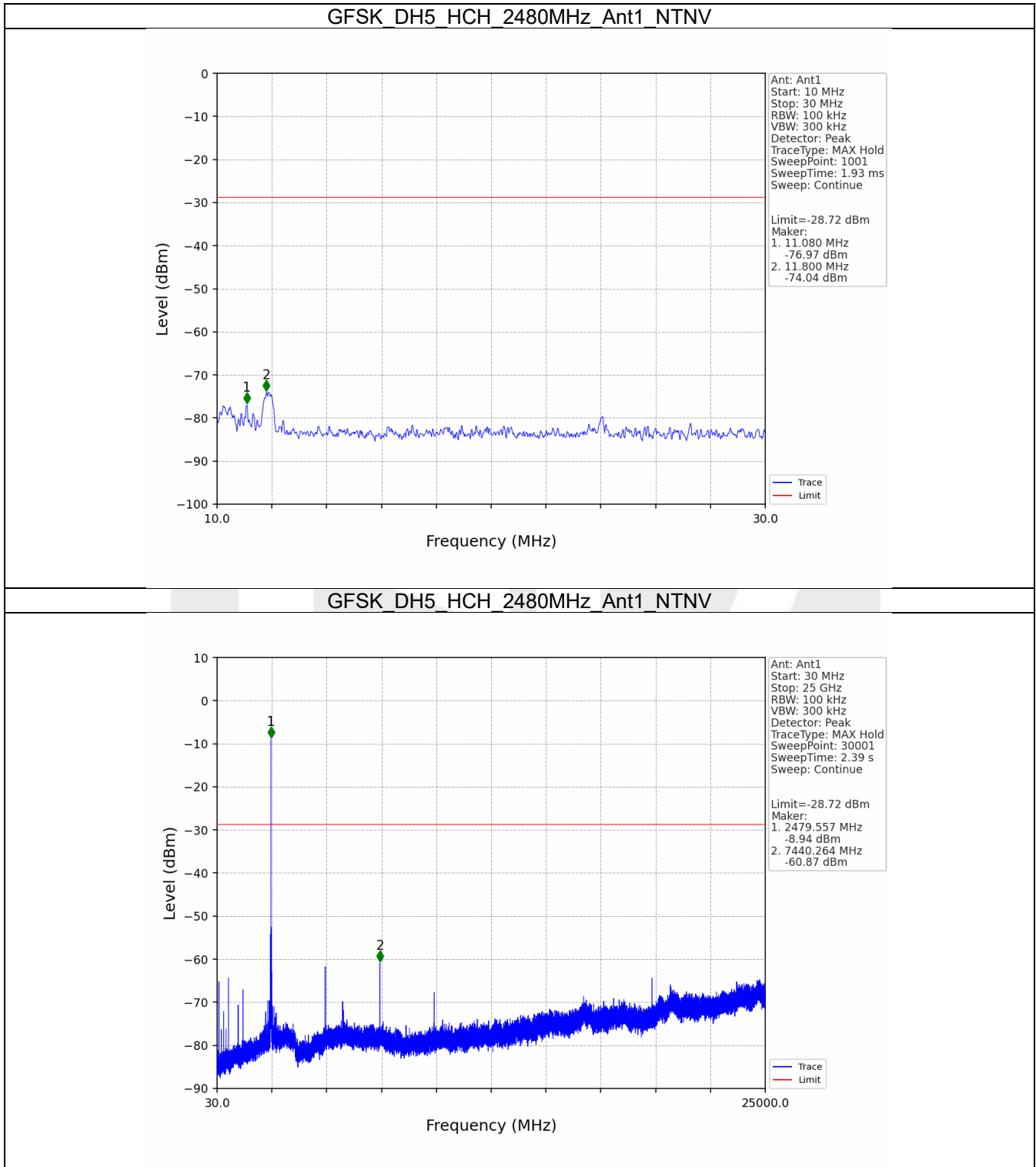


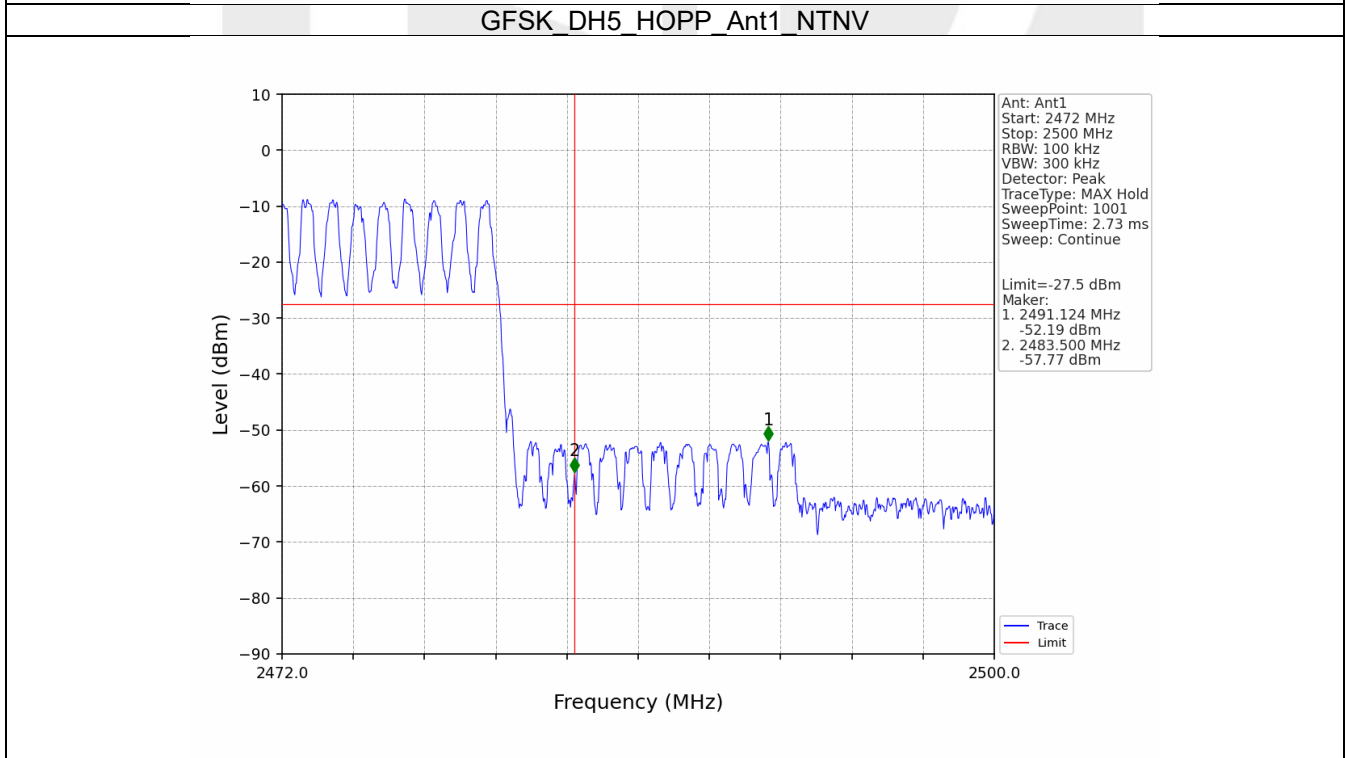
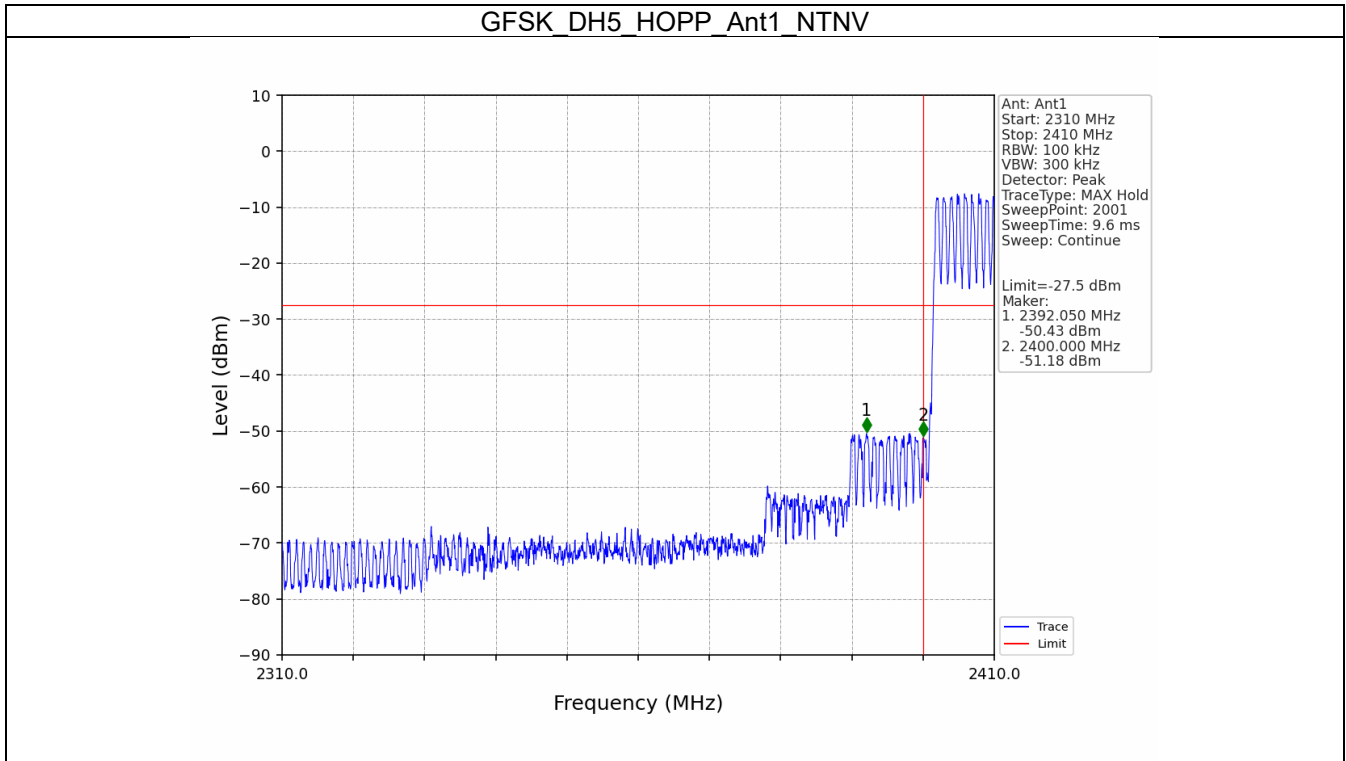


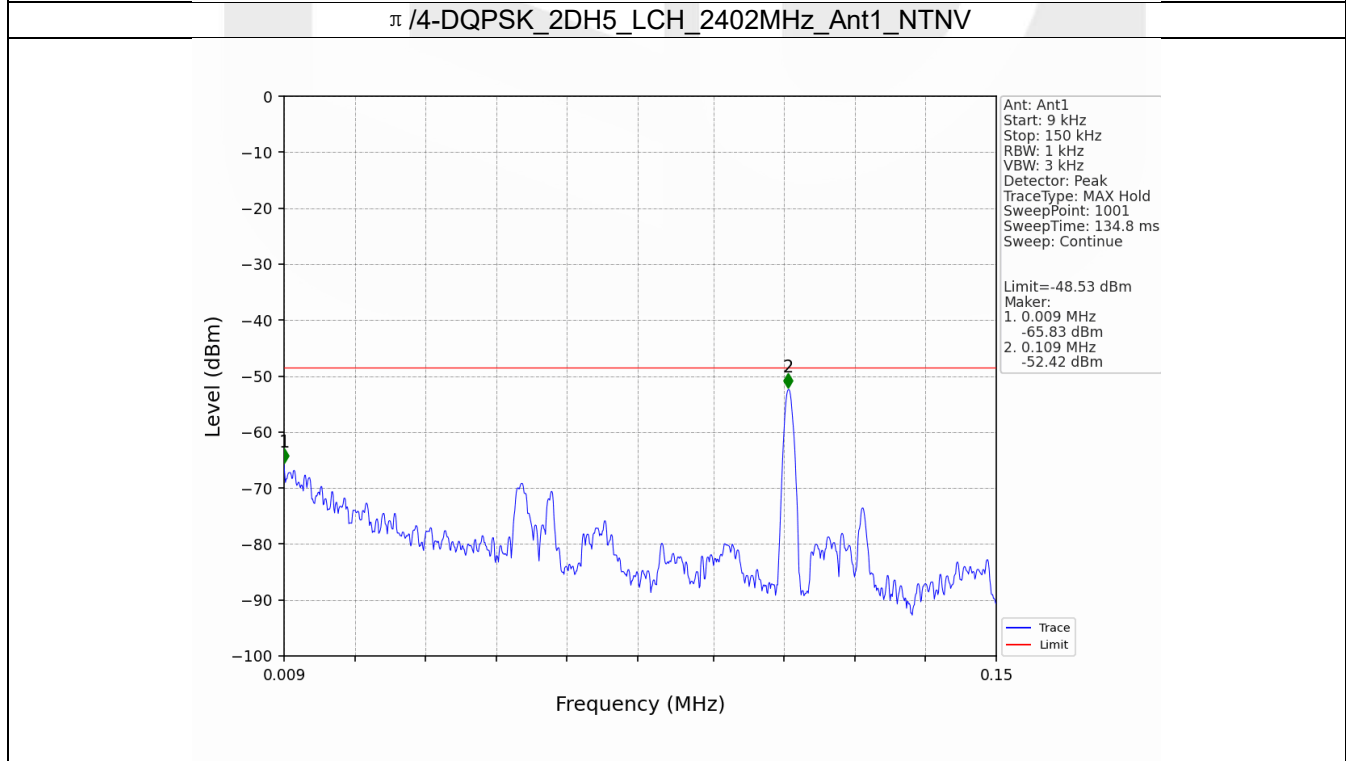
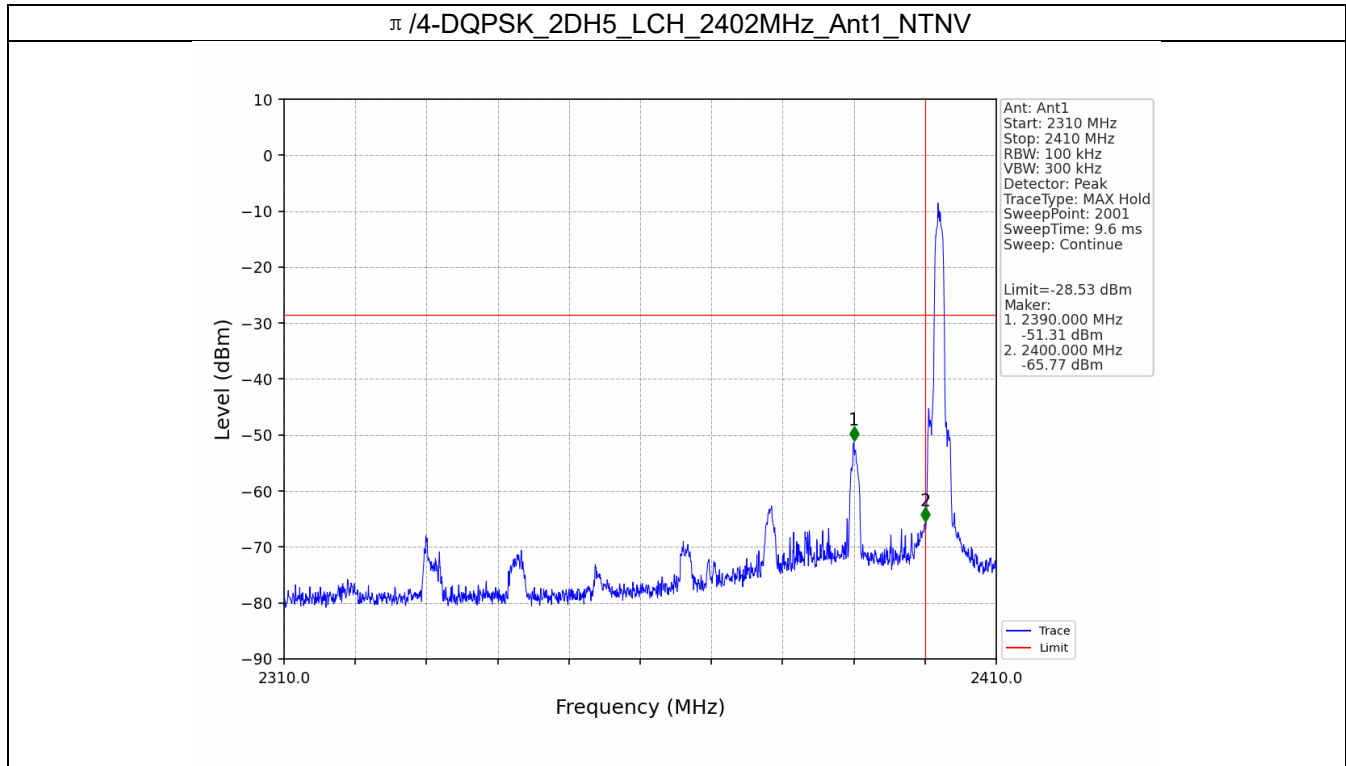


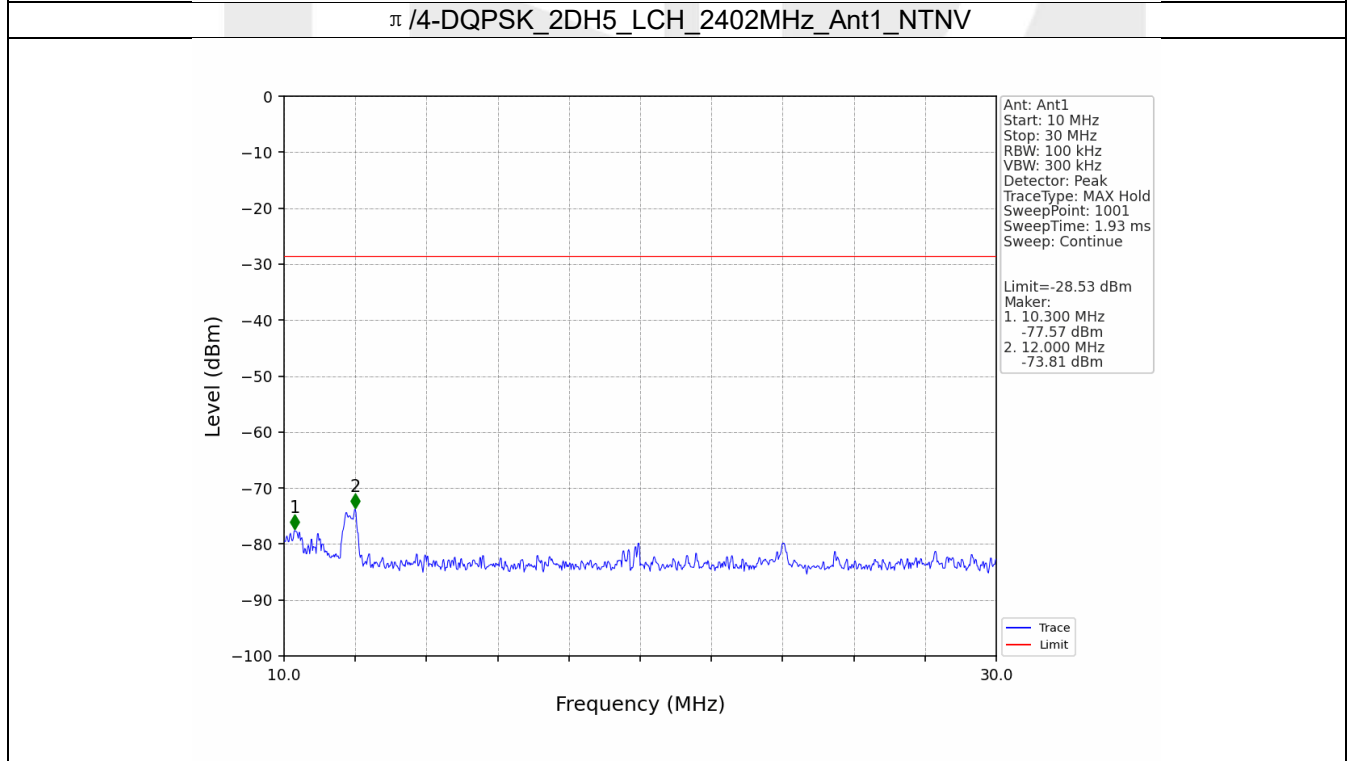
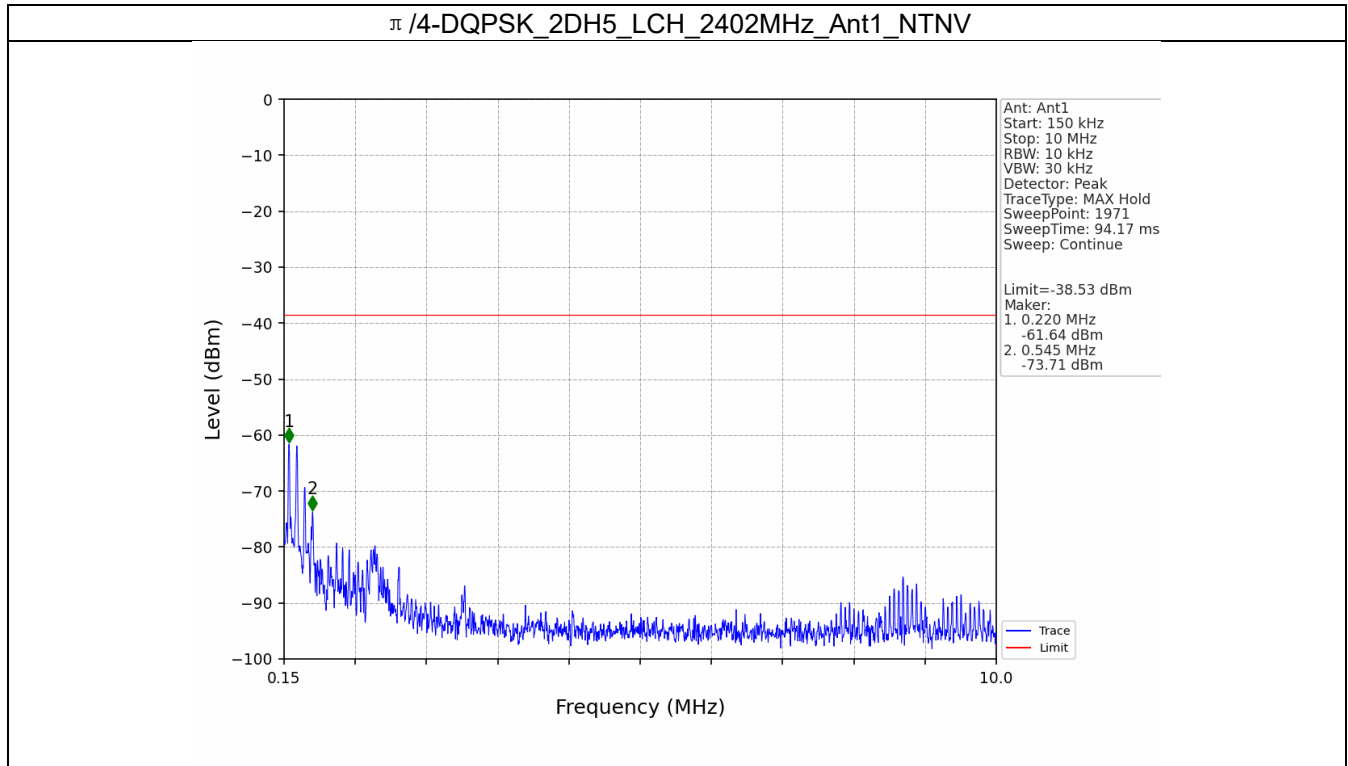


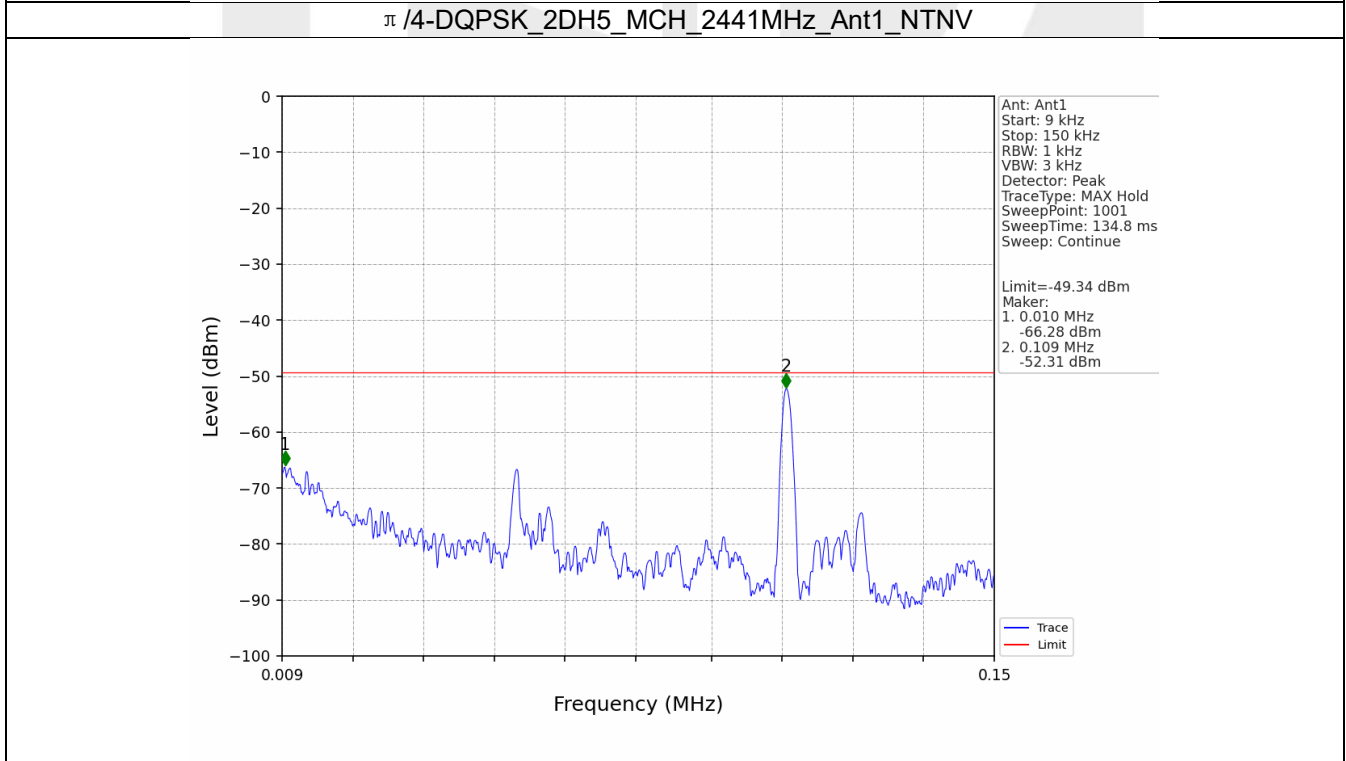
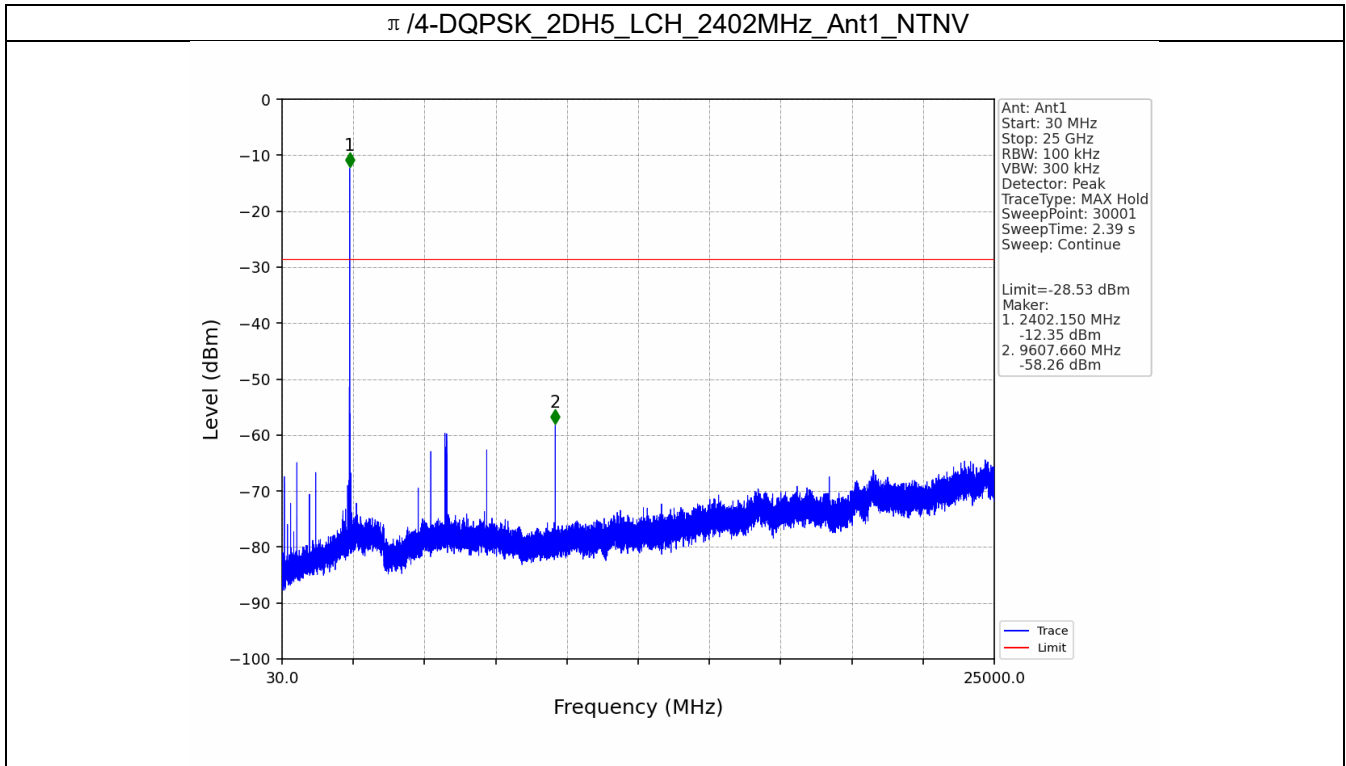


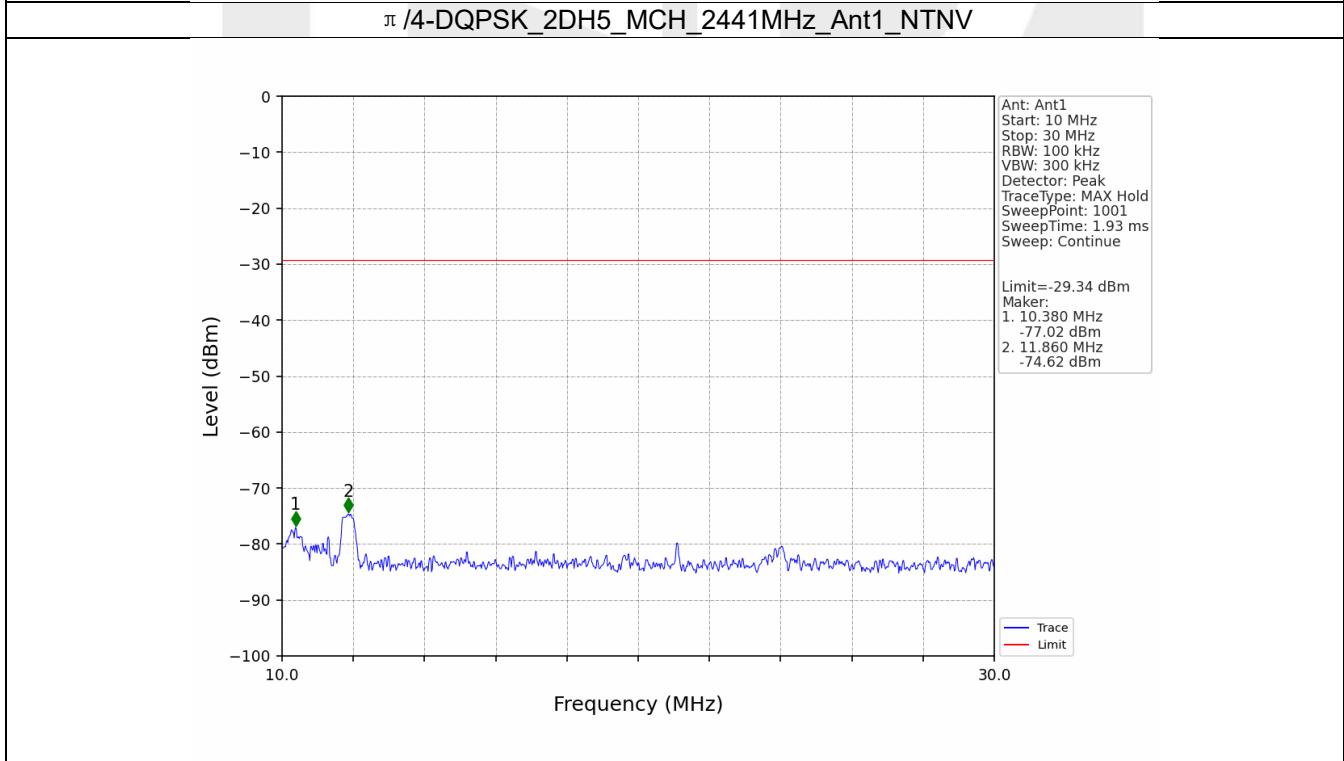
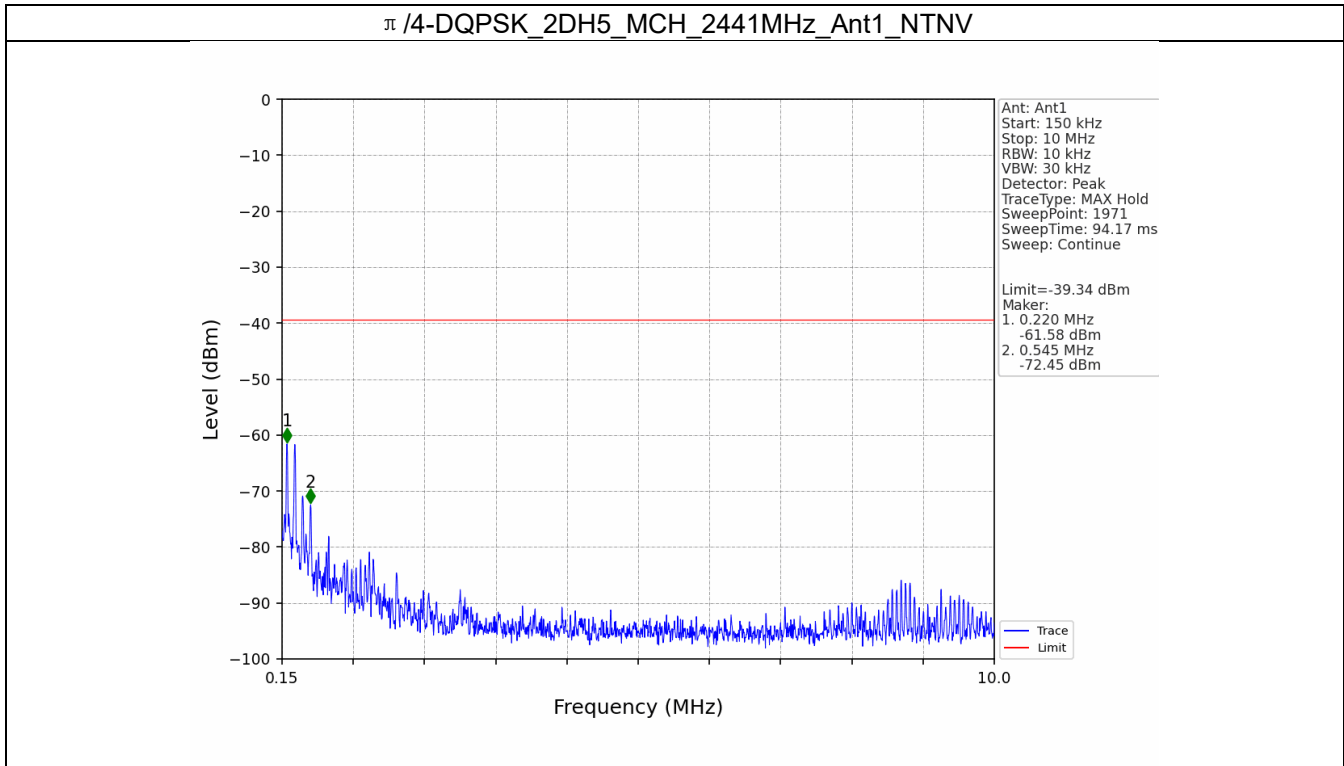


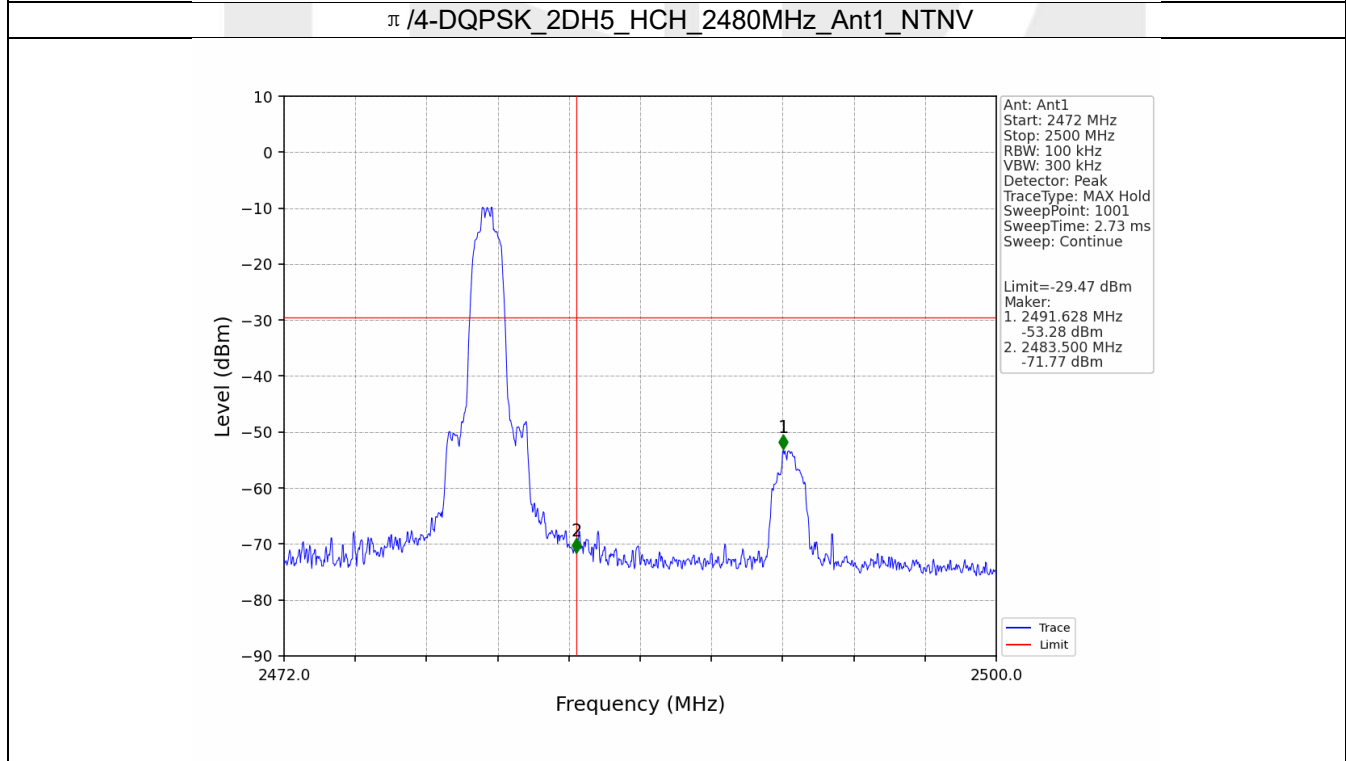
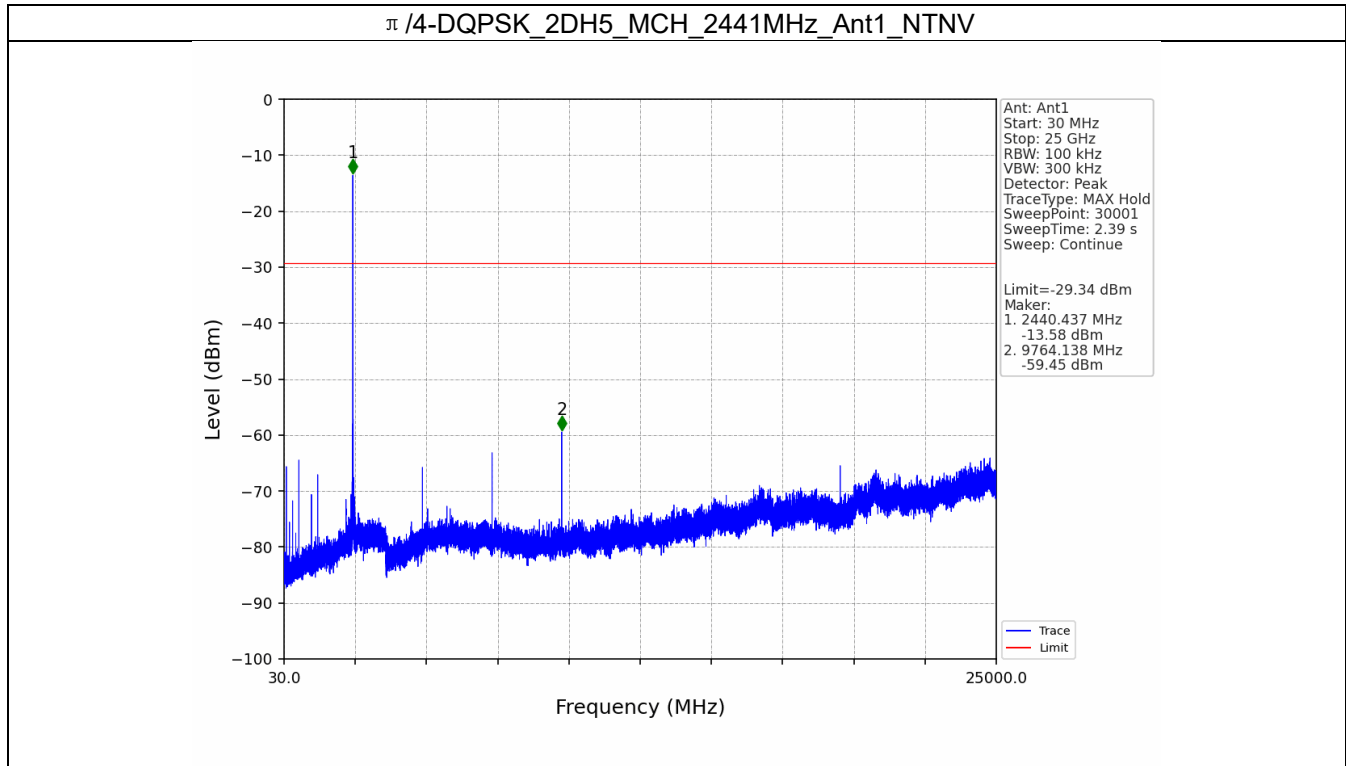


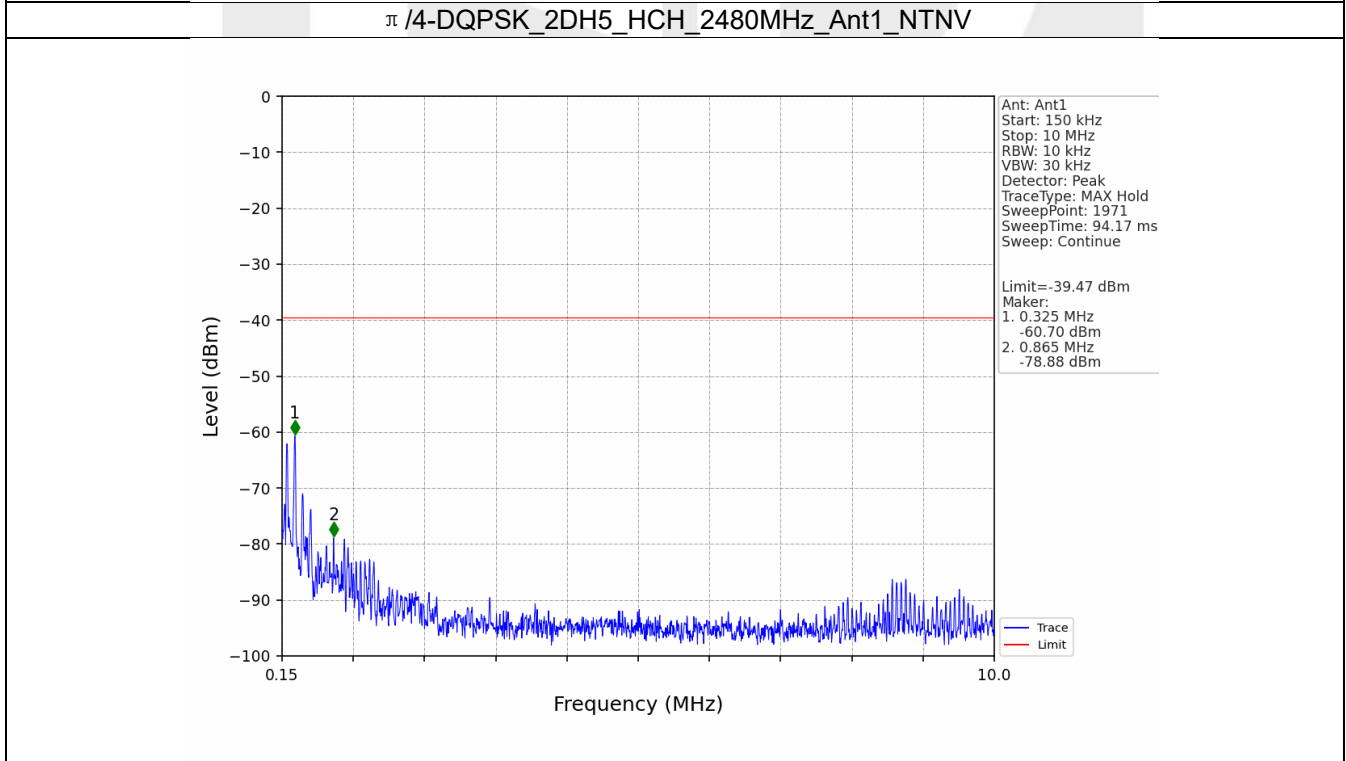
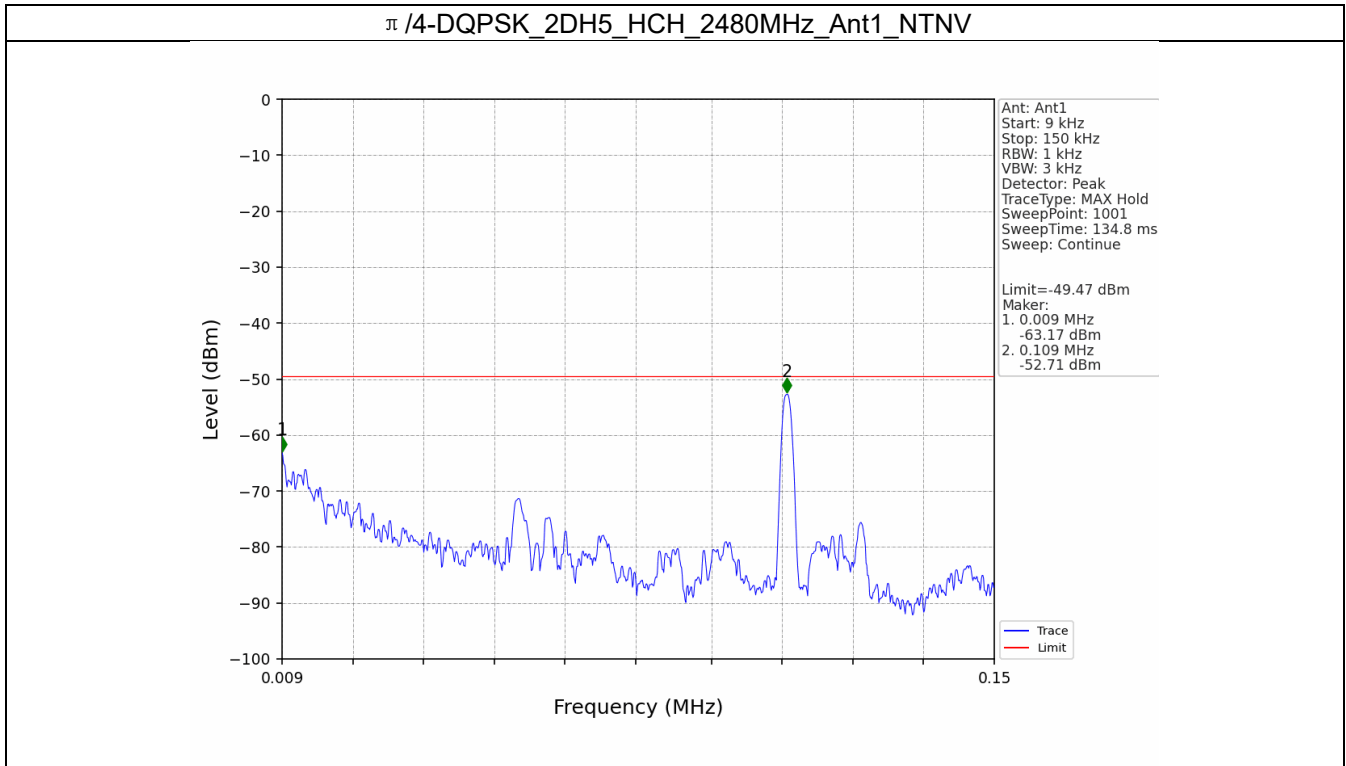


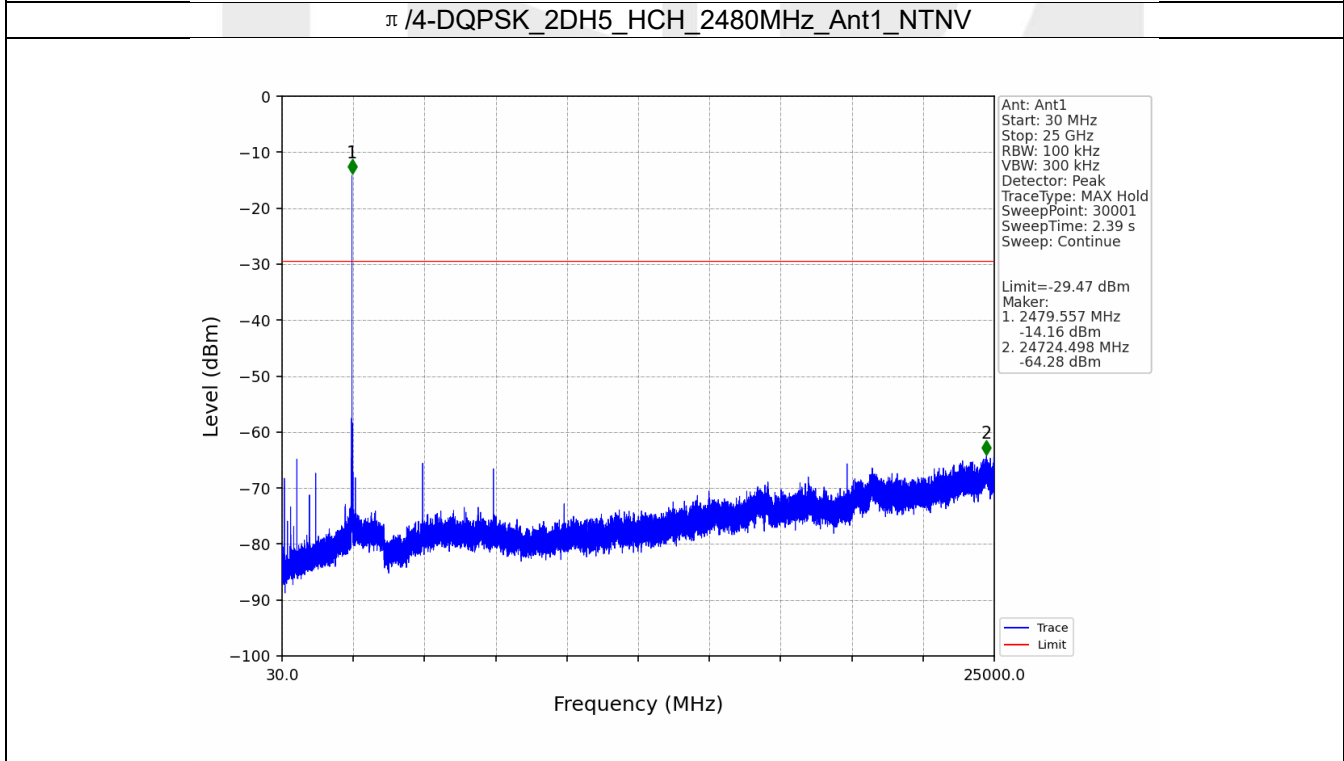
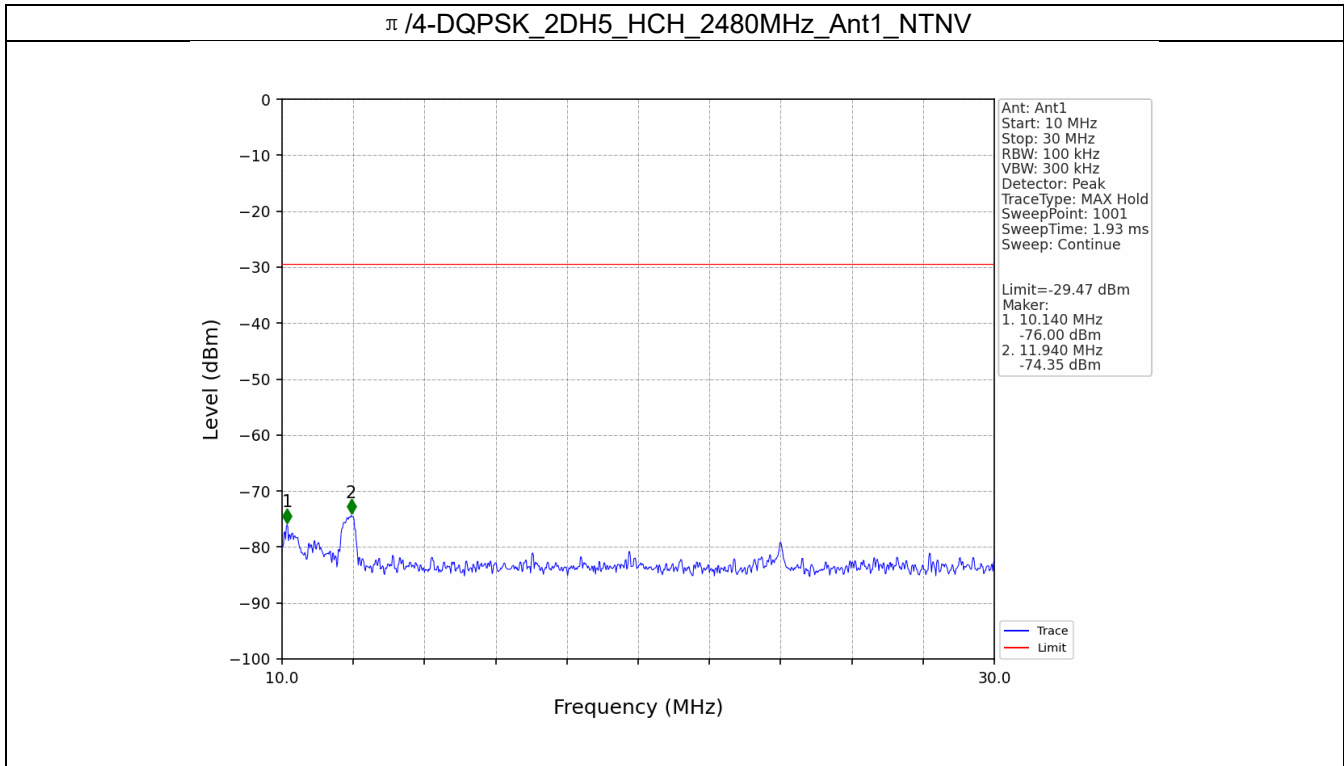


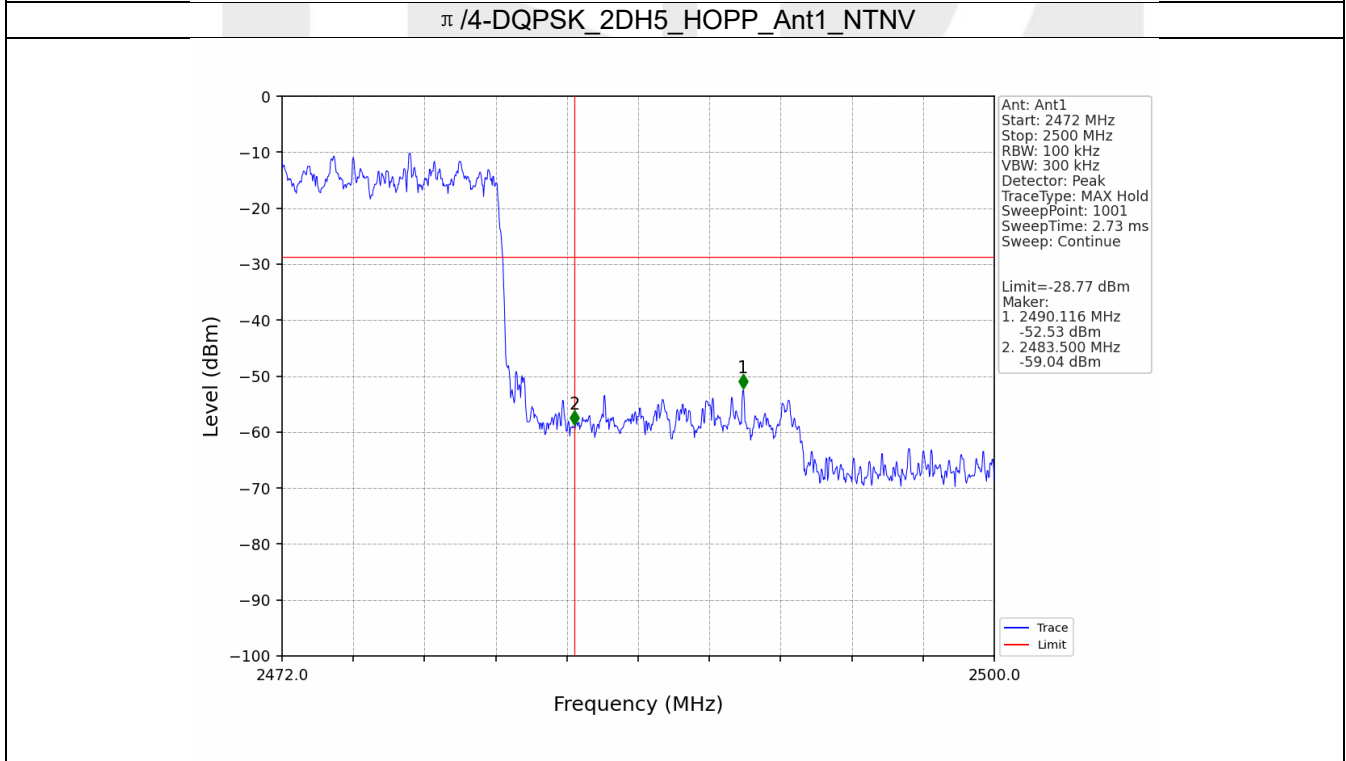
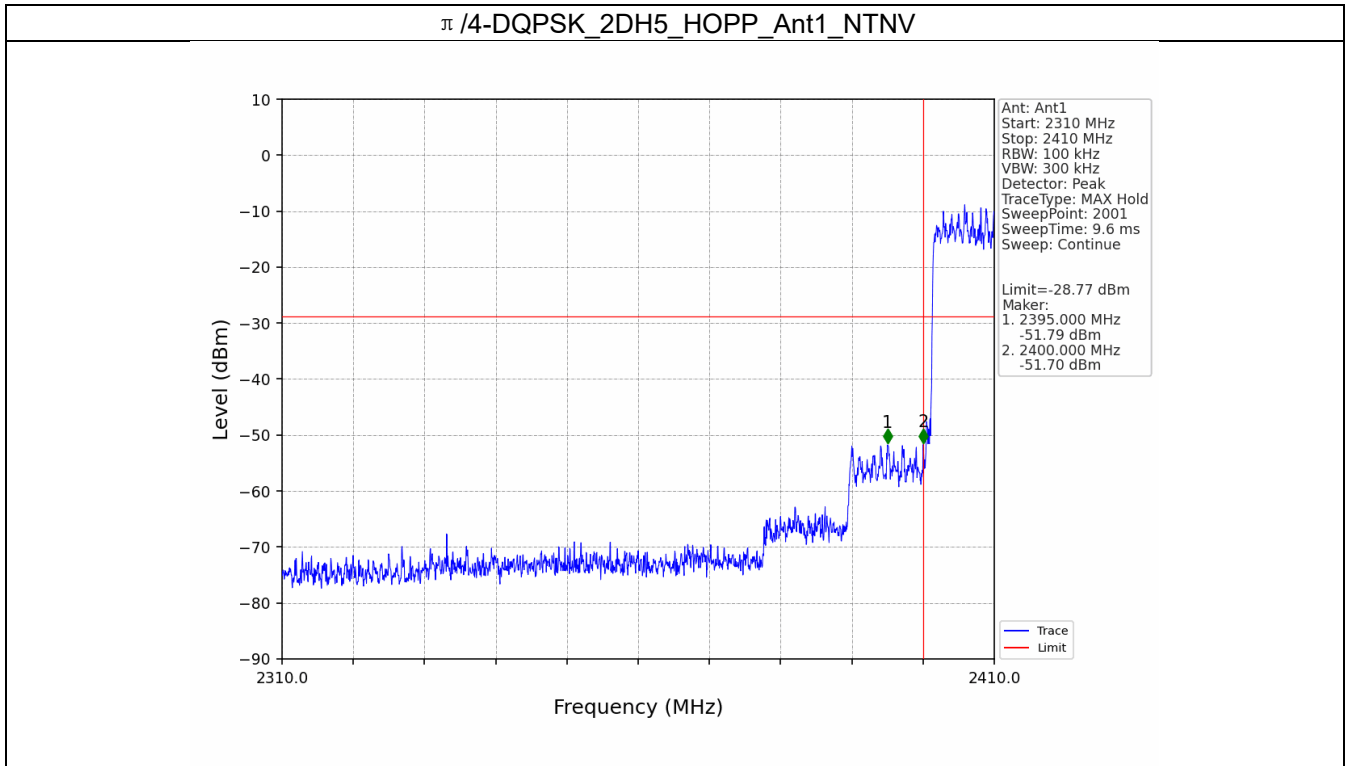


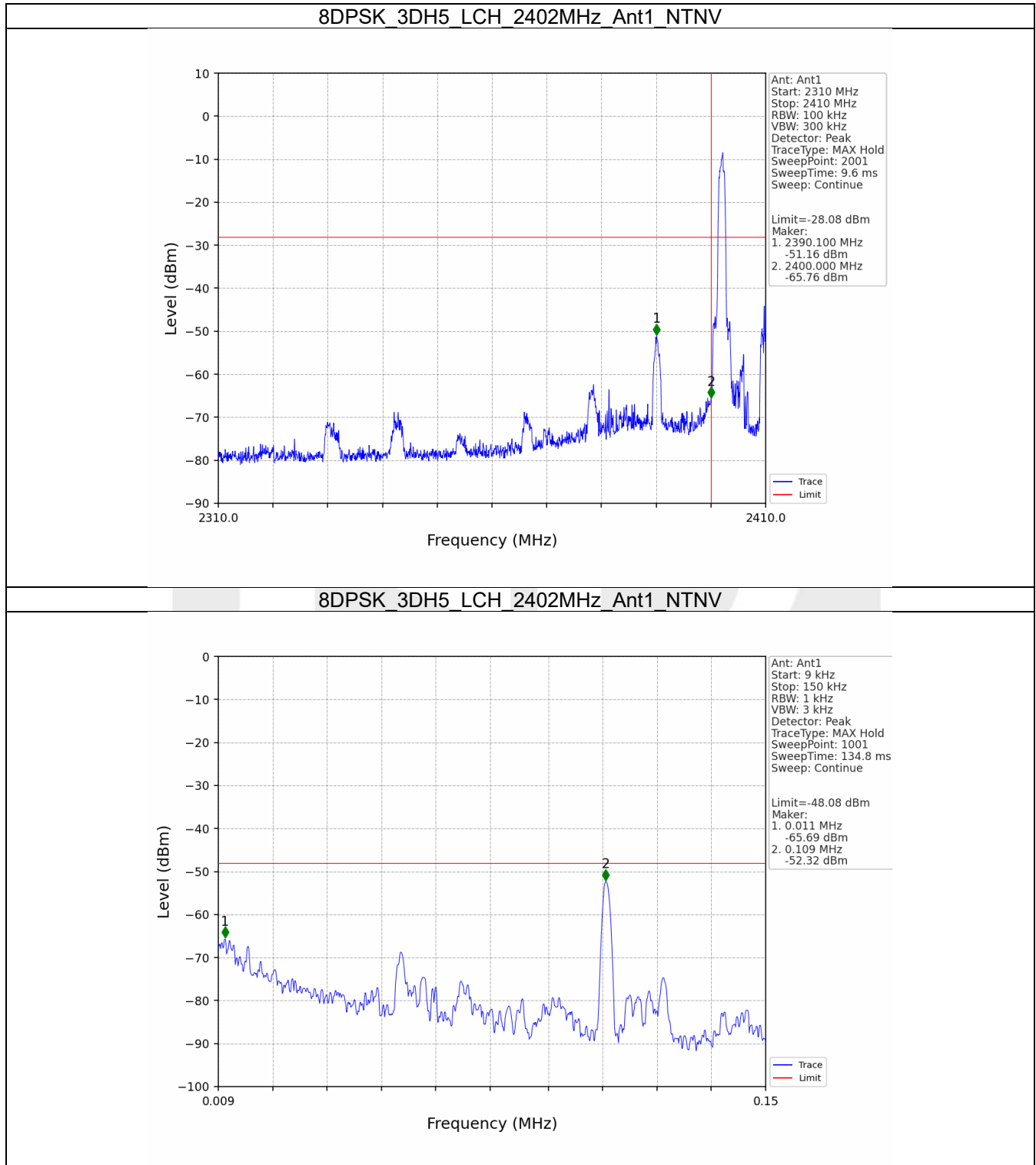


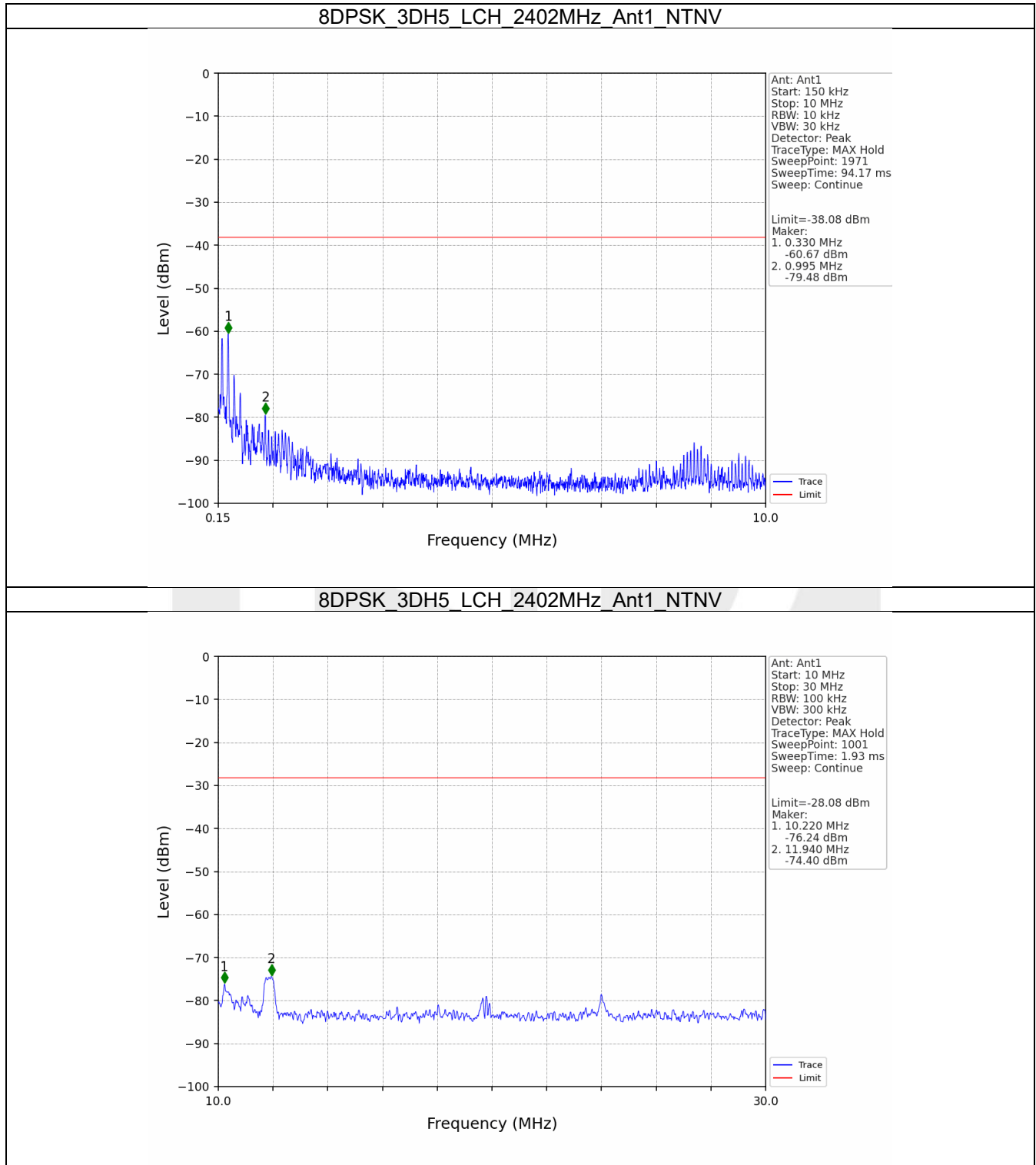


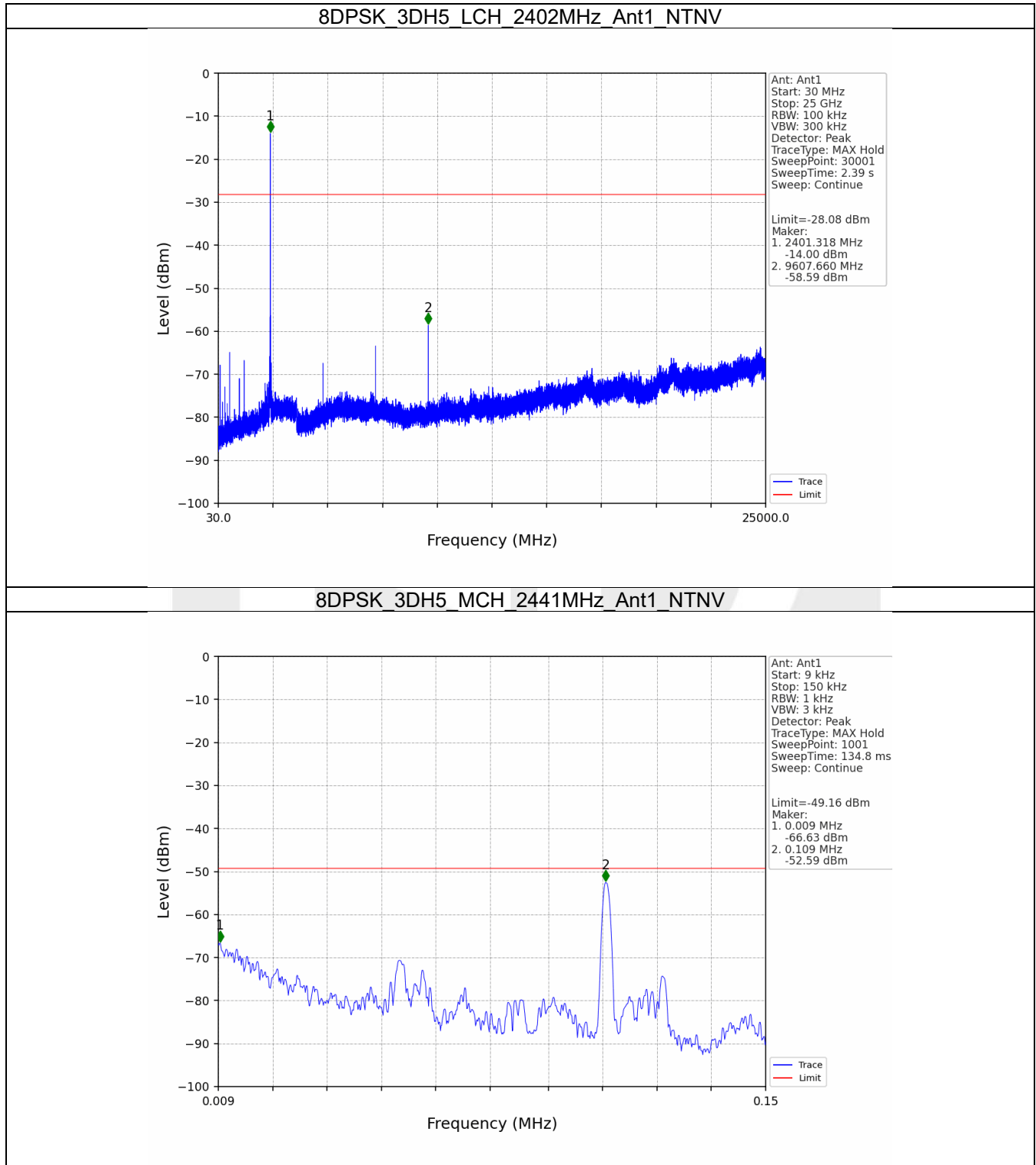


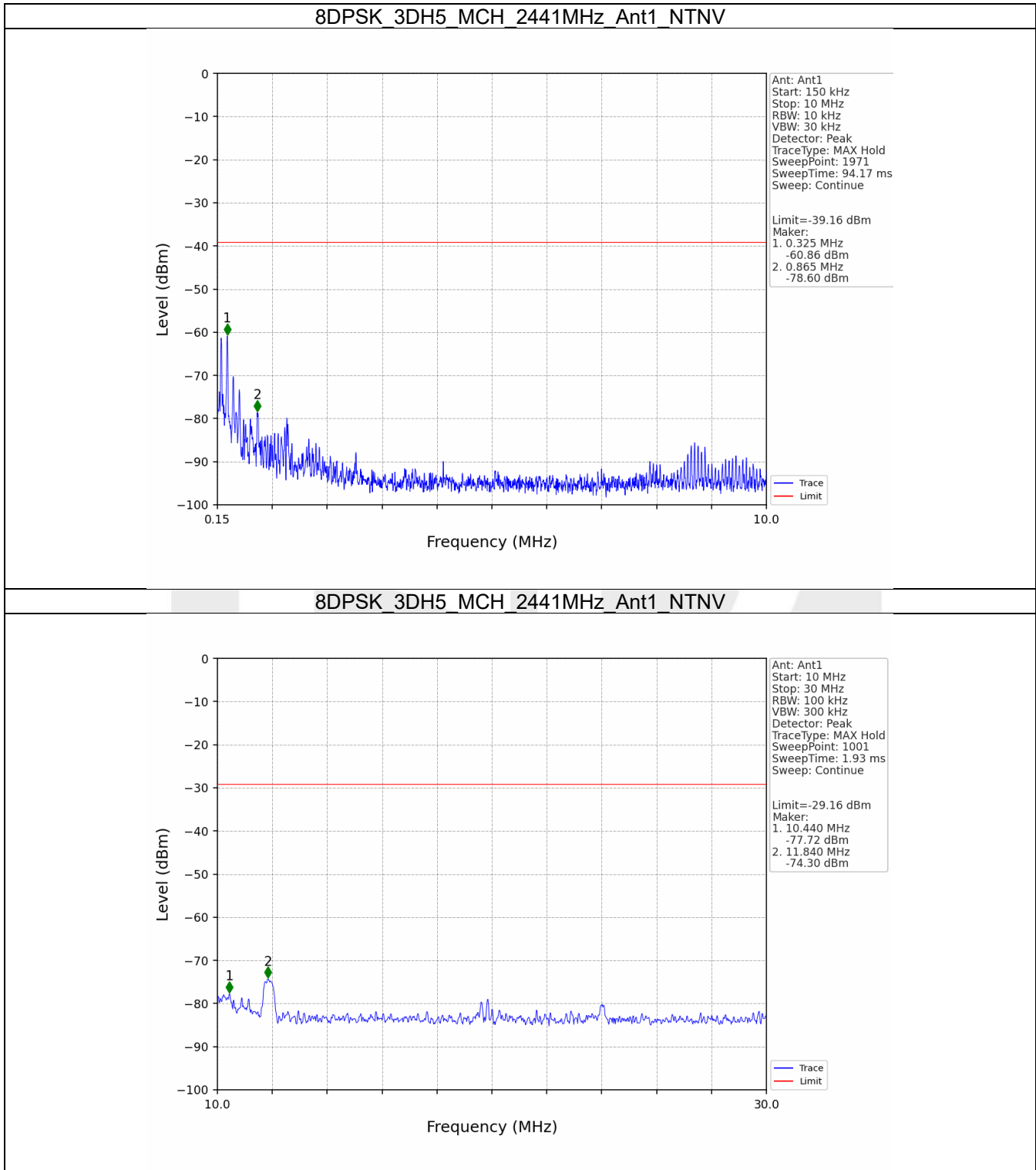


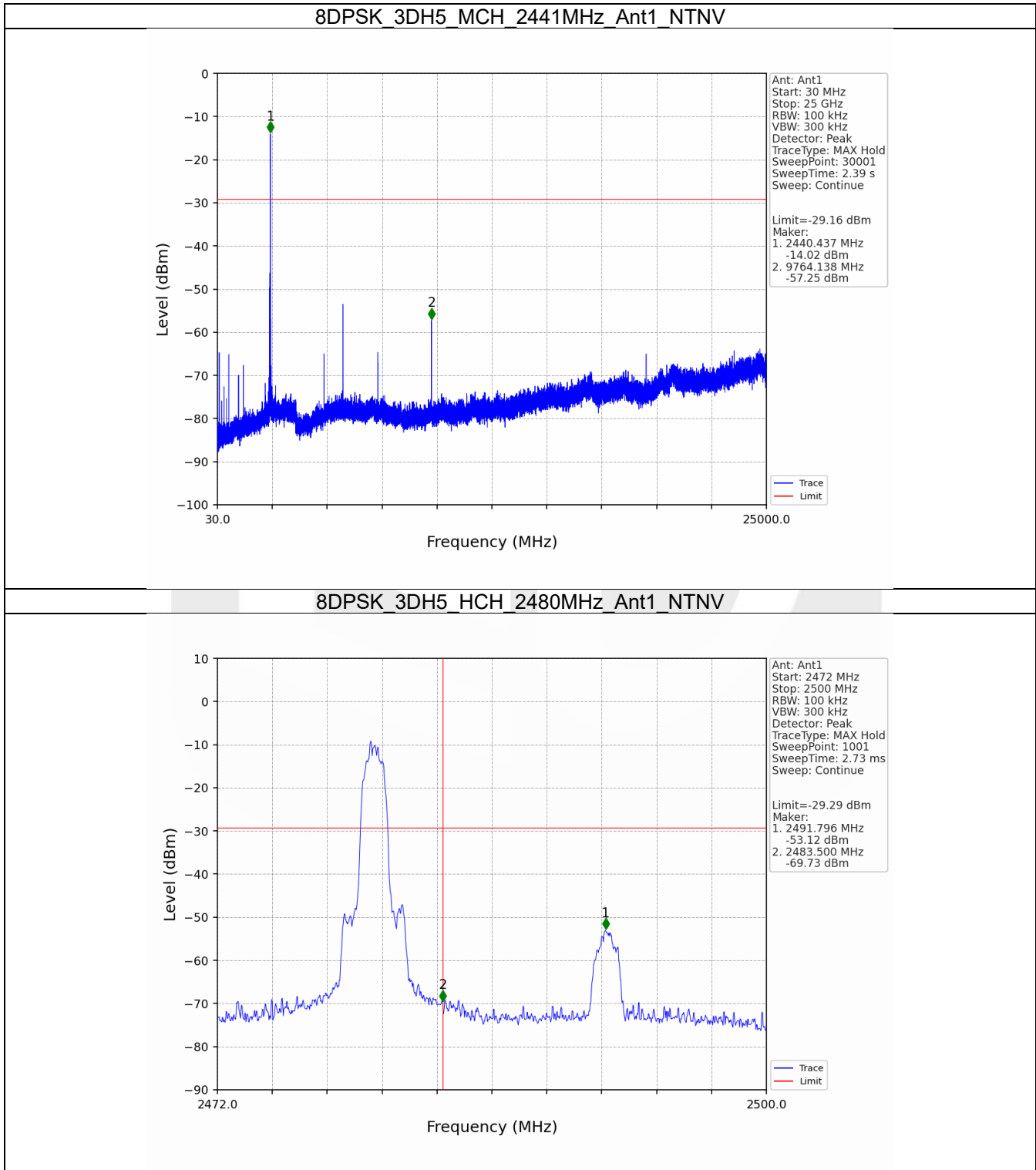


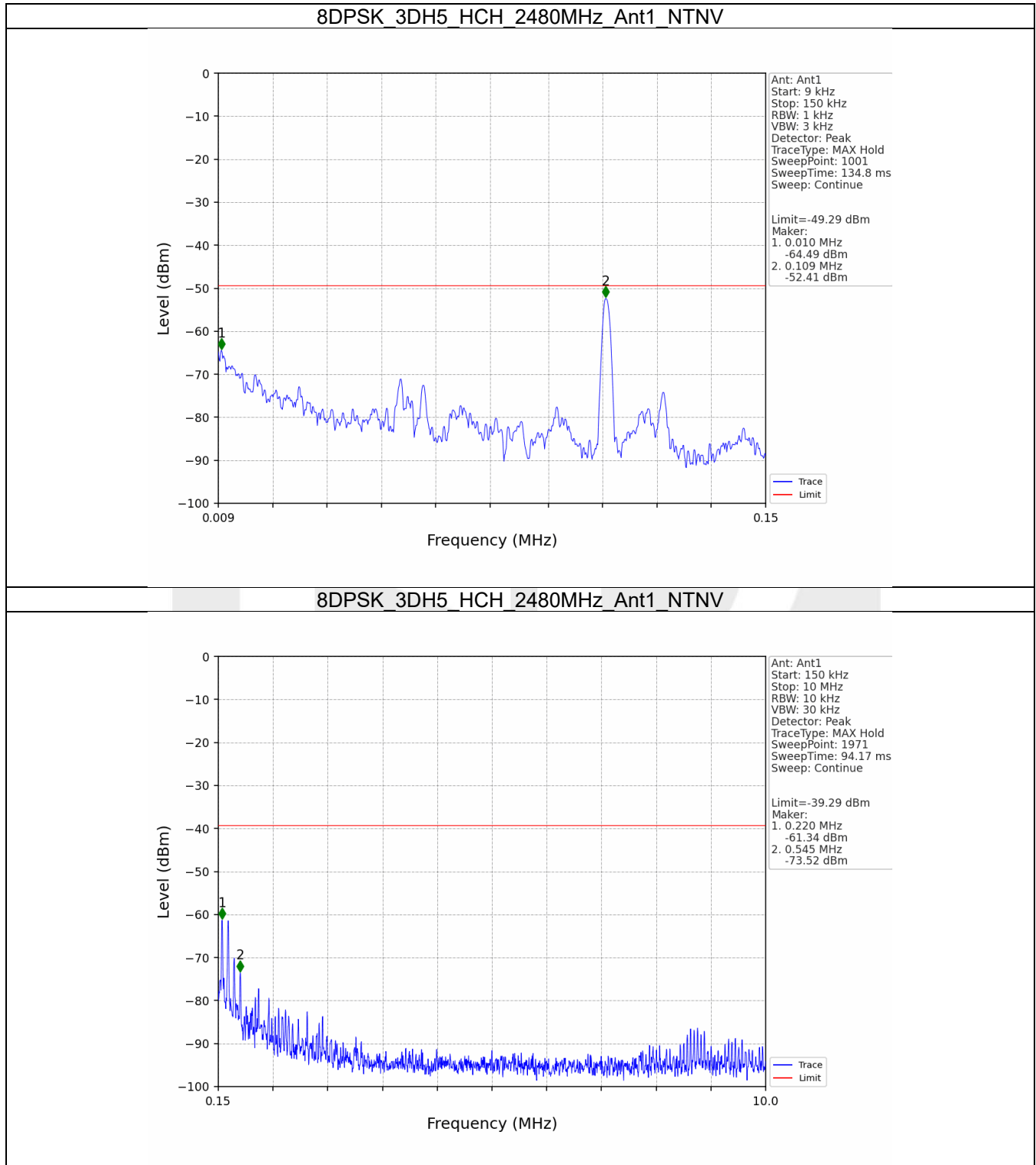


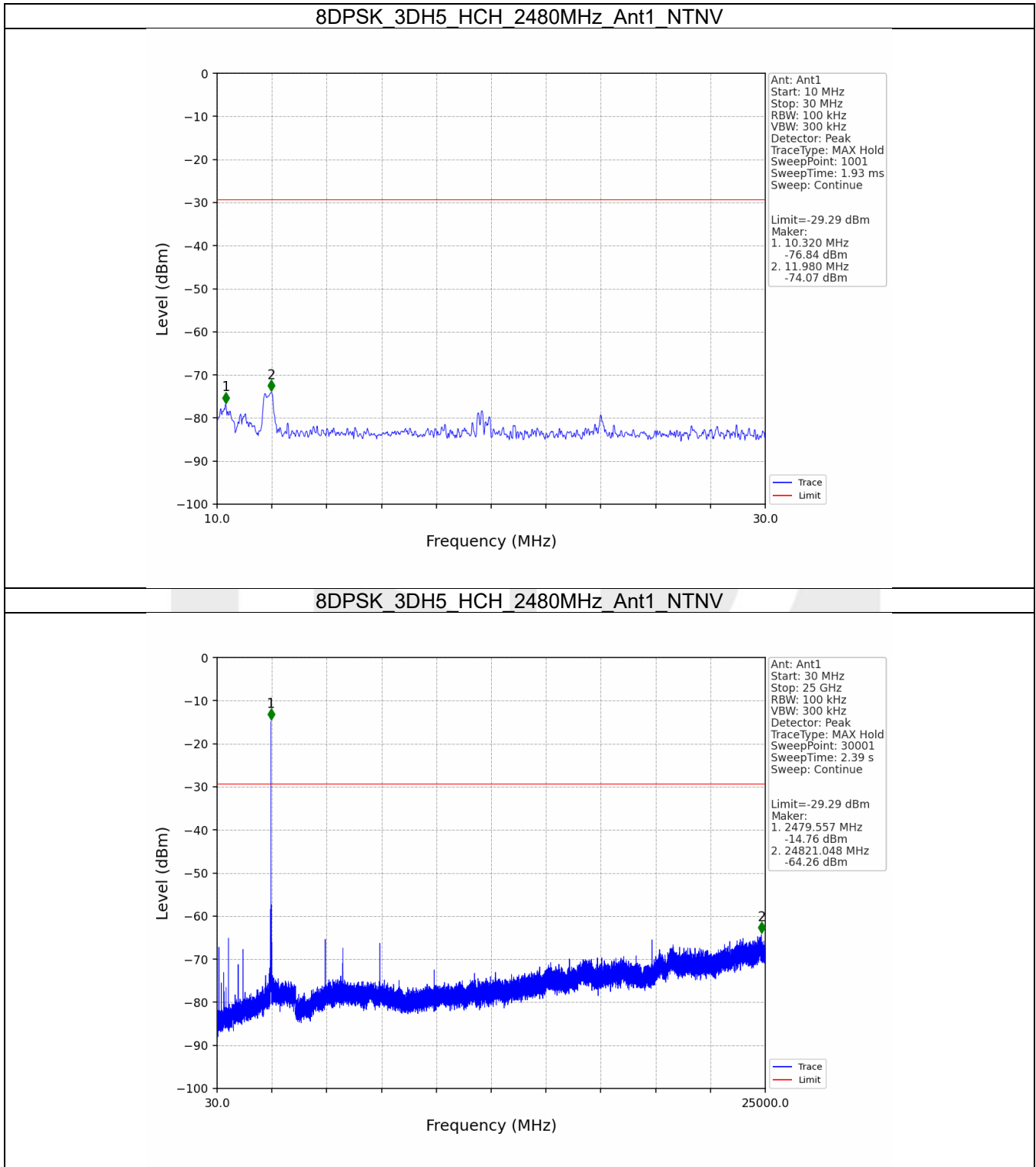


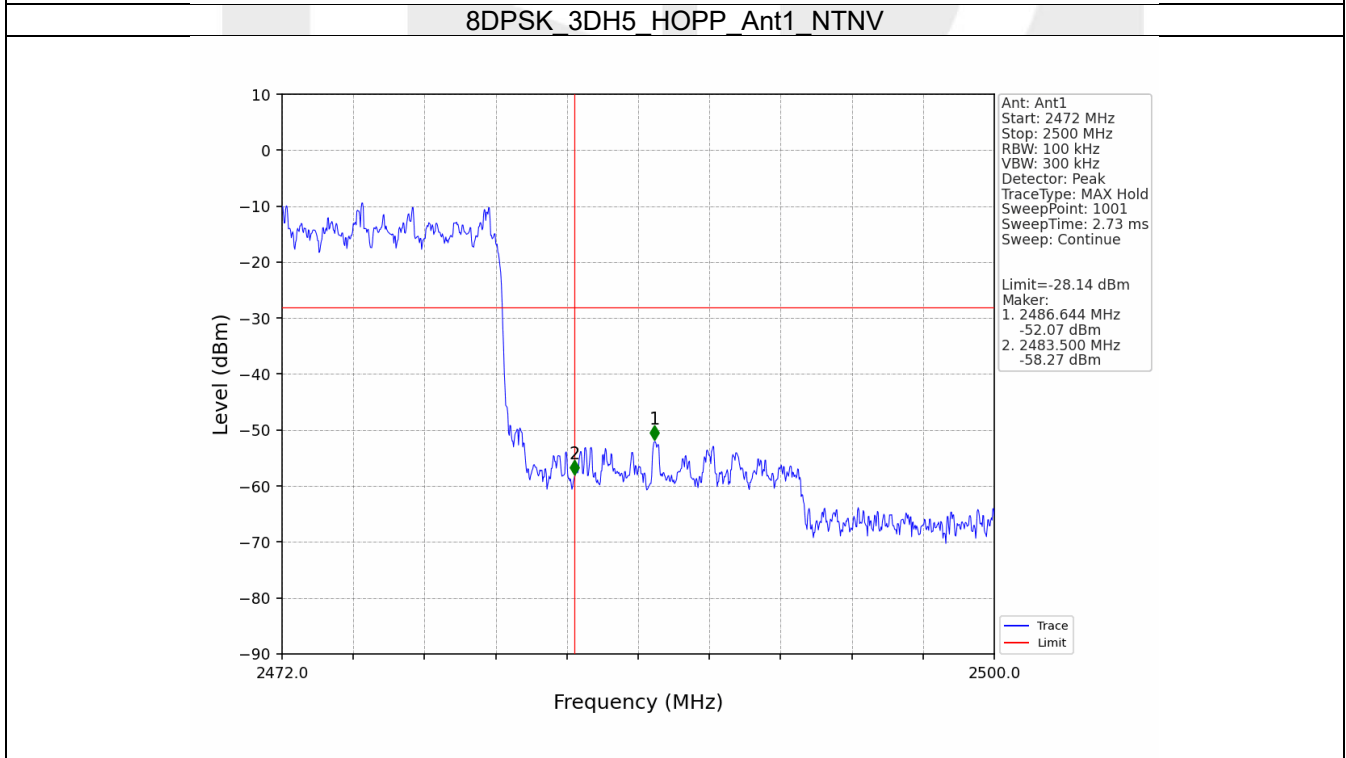
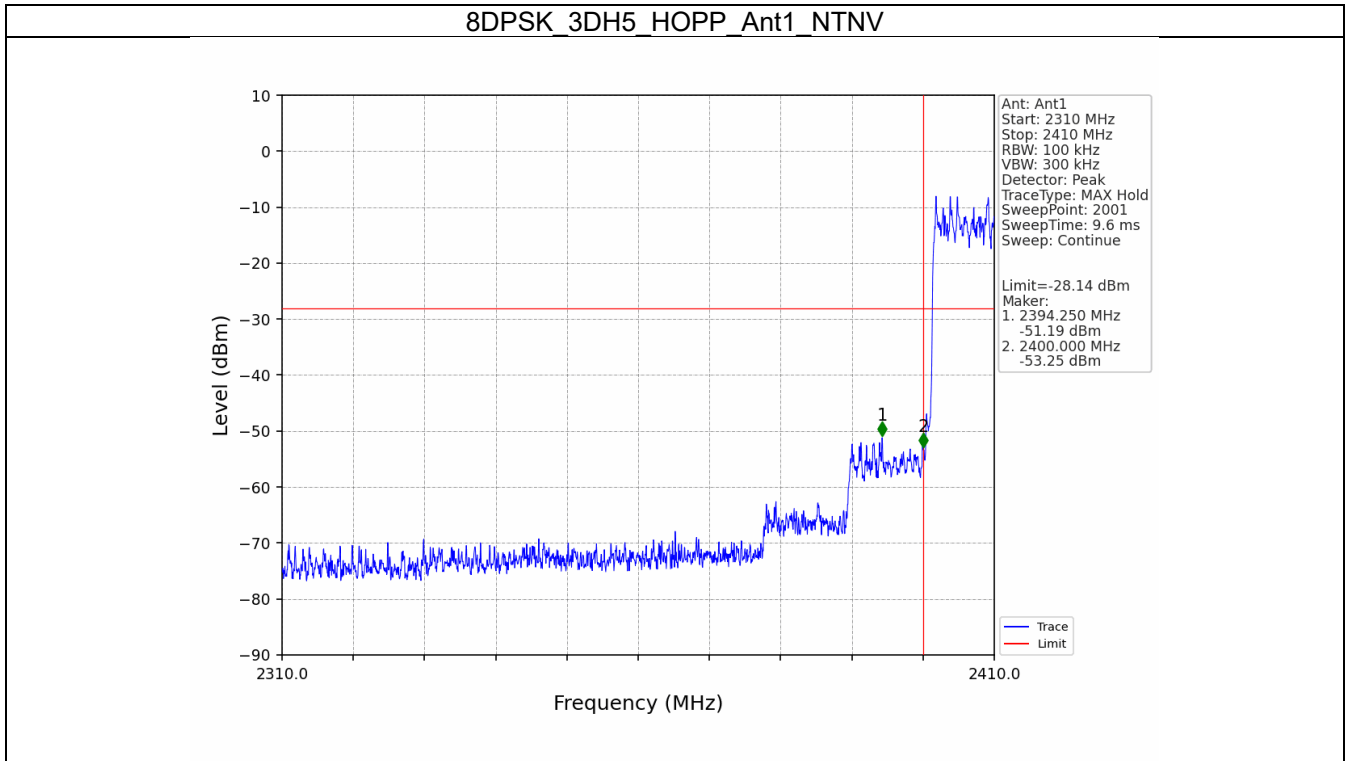












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