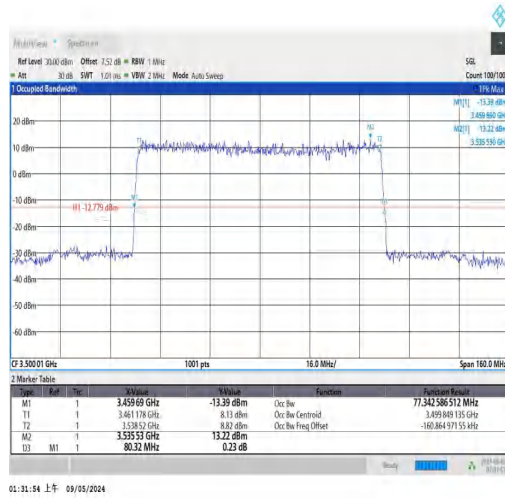
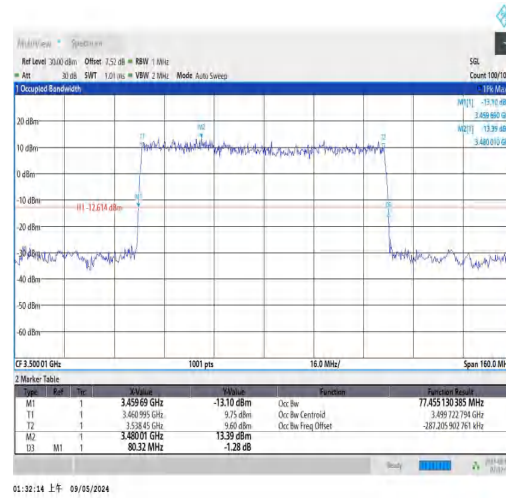




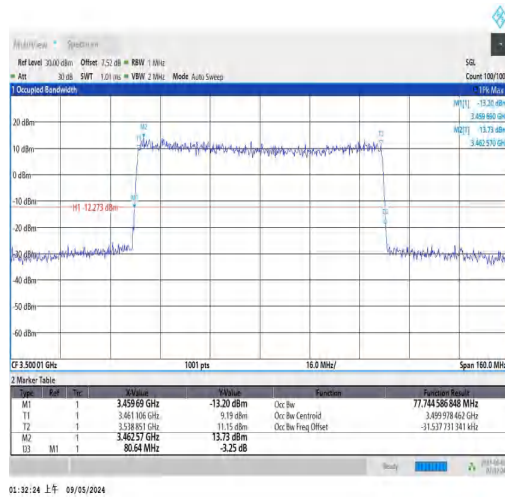
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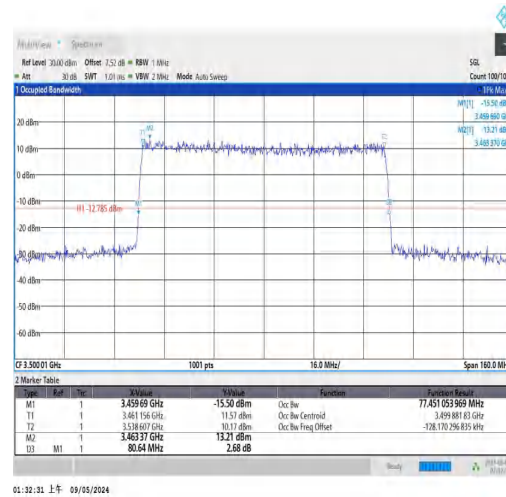
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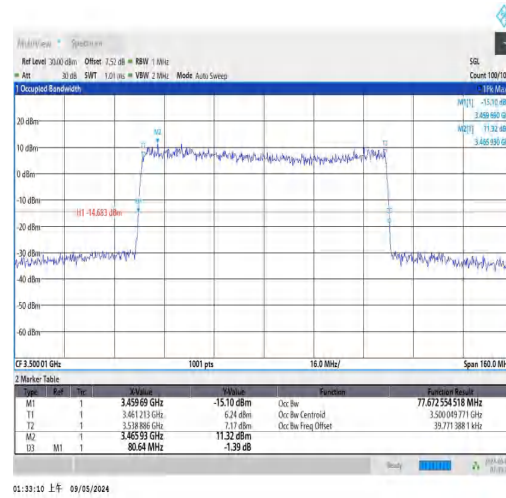
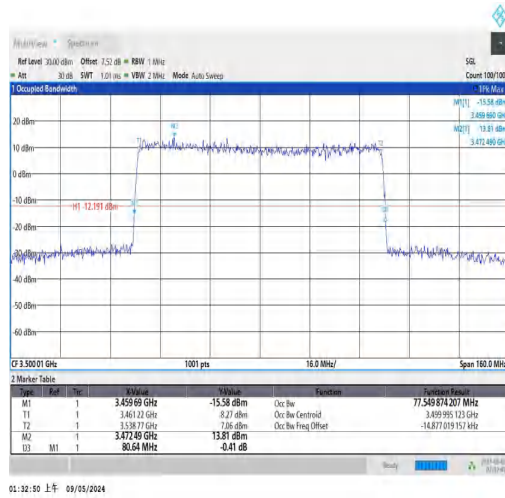
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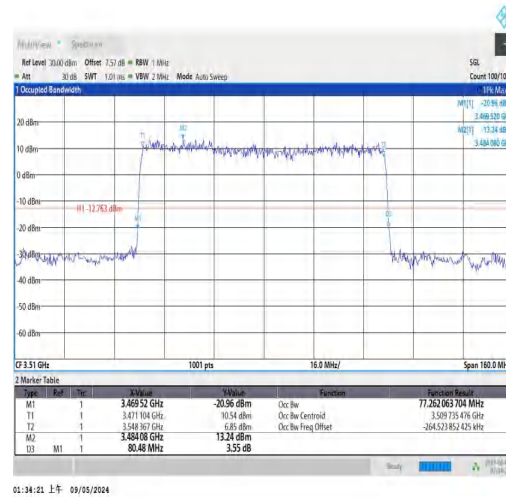
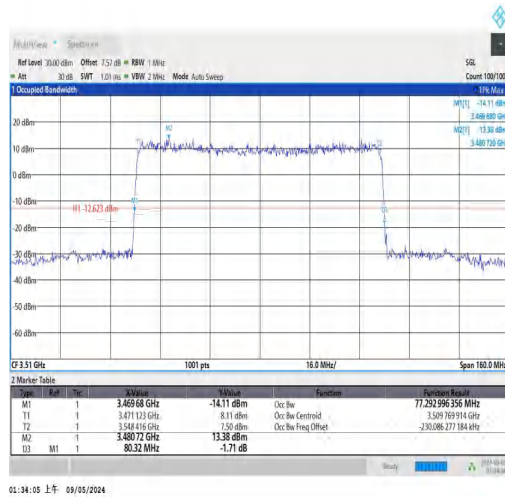


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NTNV_N78-3450-3550_PC3_30_80_M_TID6_Ant

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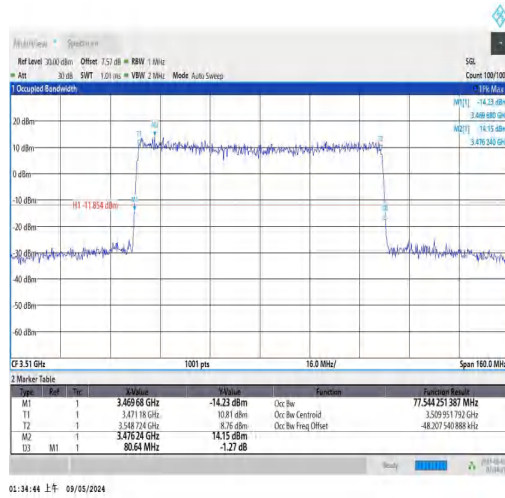


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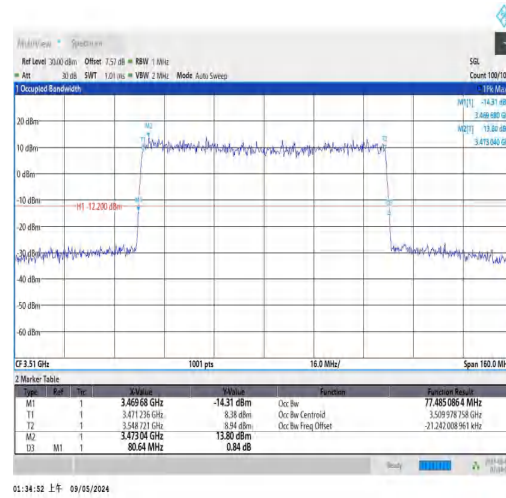
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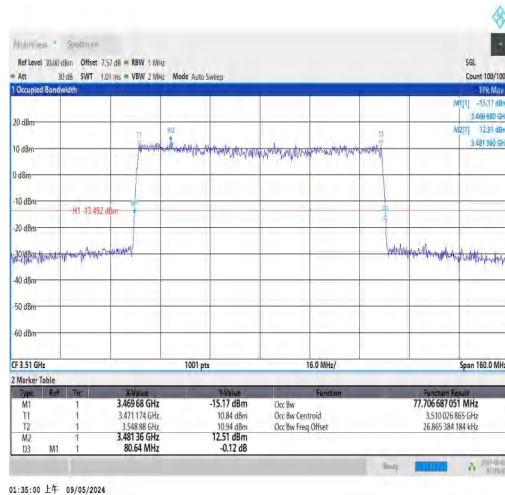
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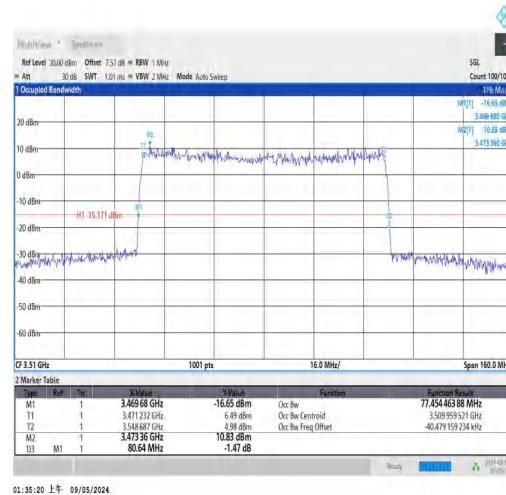
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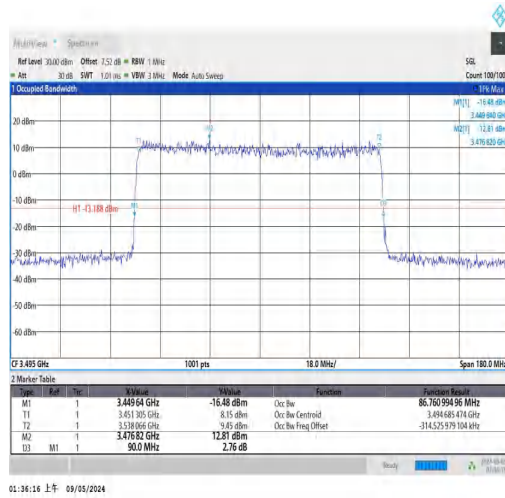
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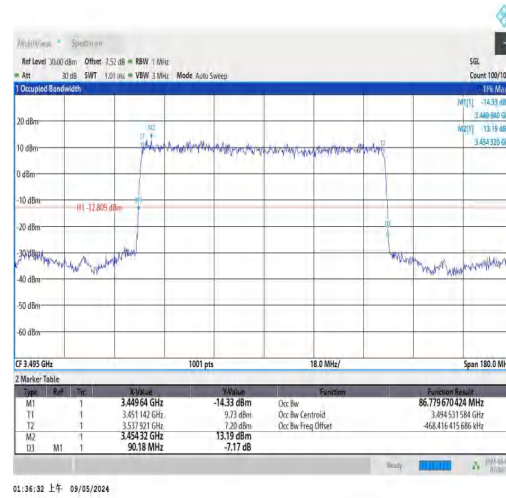
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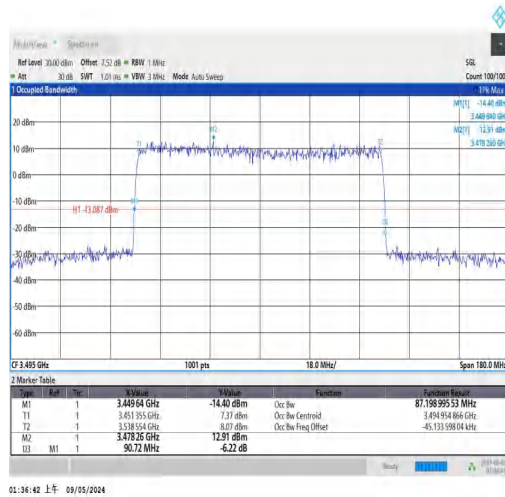
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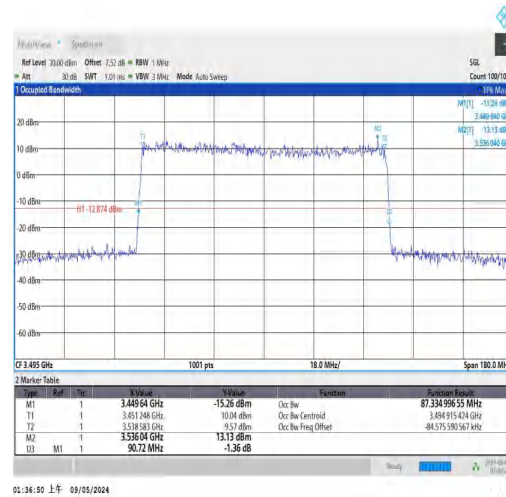
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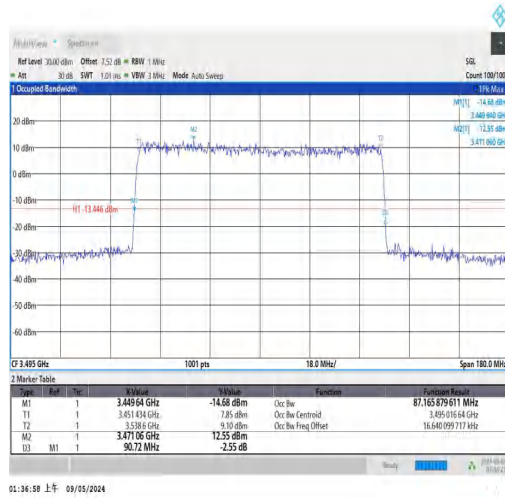
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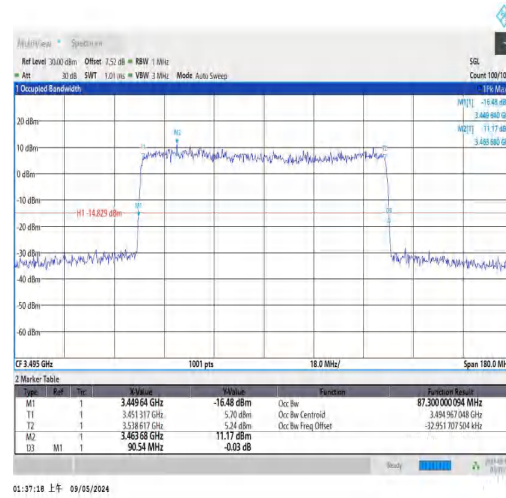
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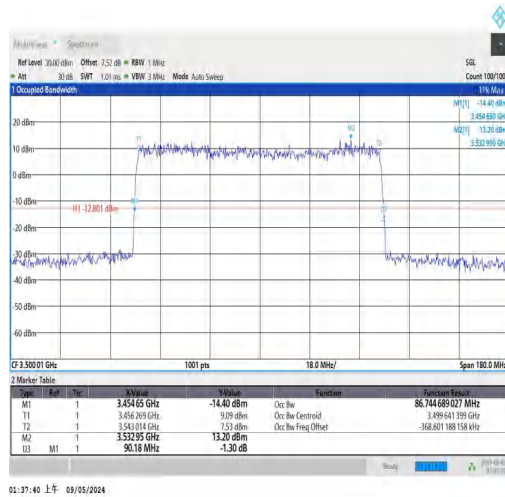
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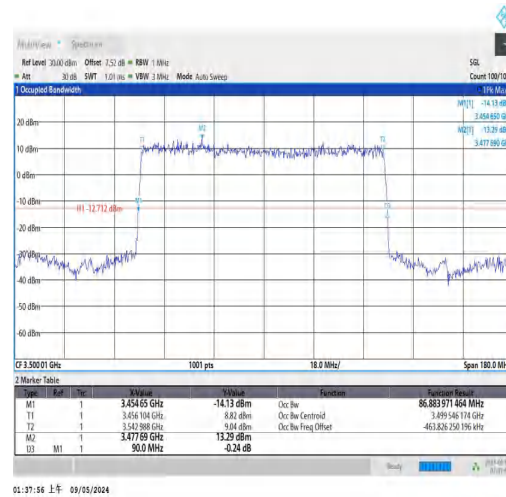
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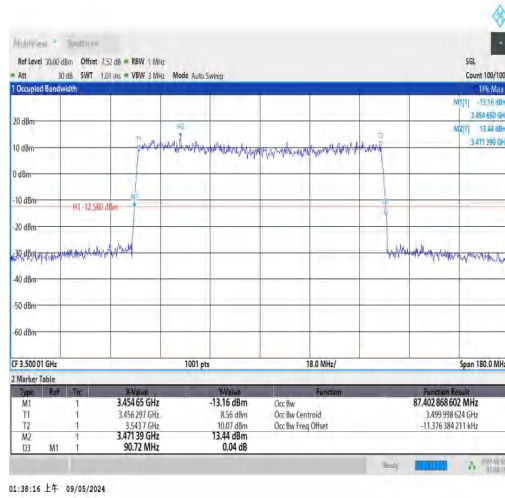
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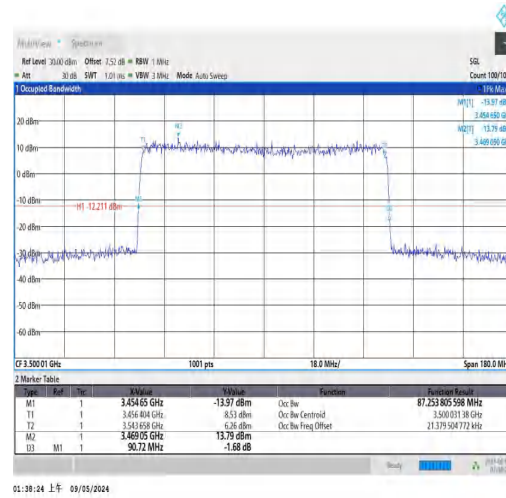
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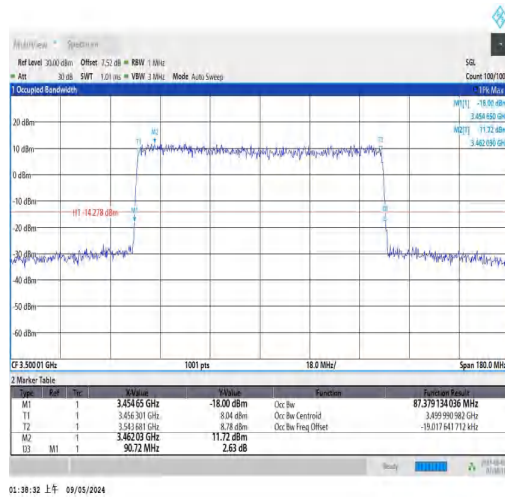
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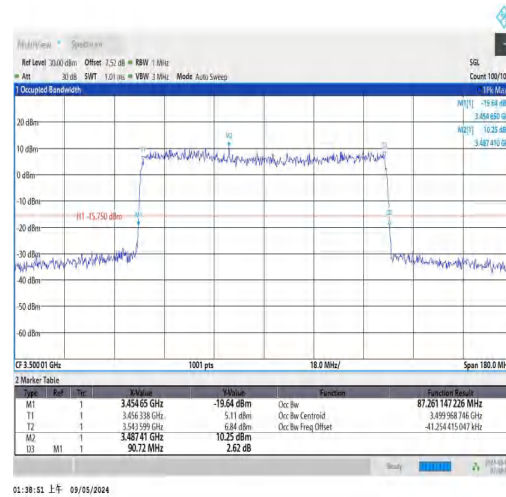
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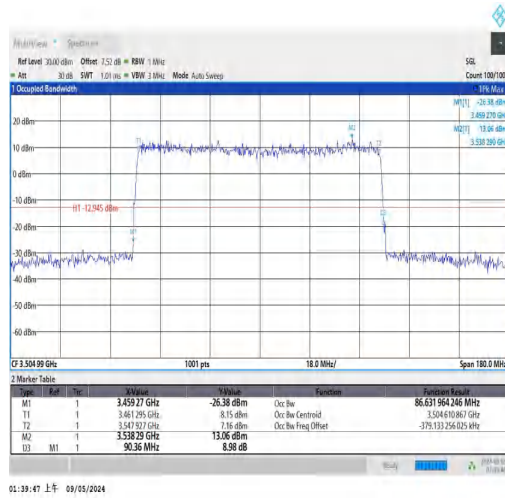
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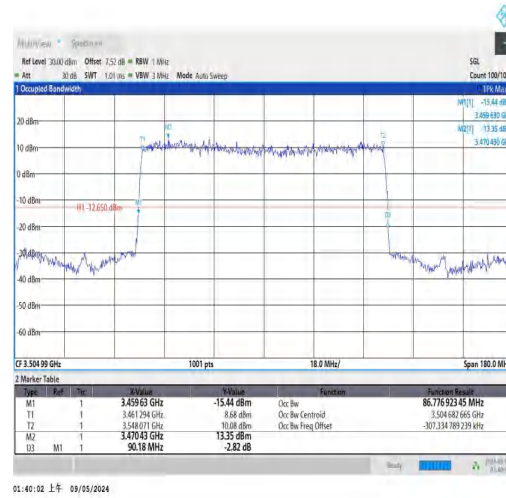


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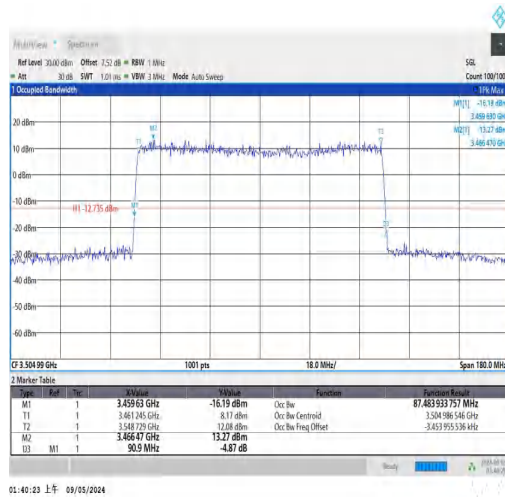
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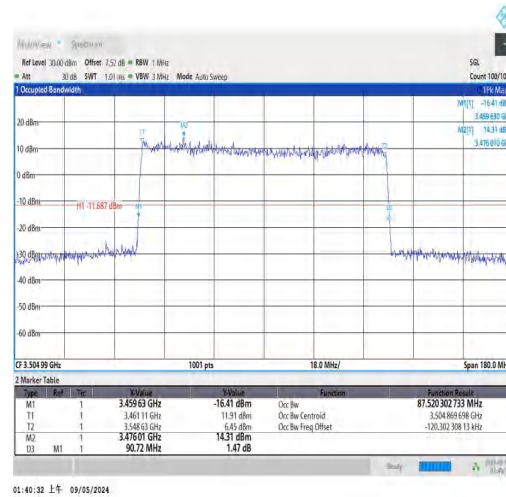
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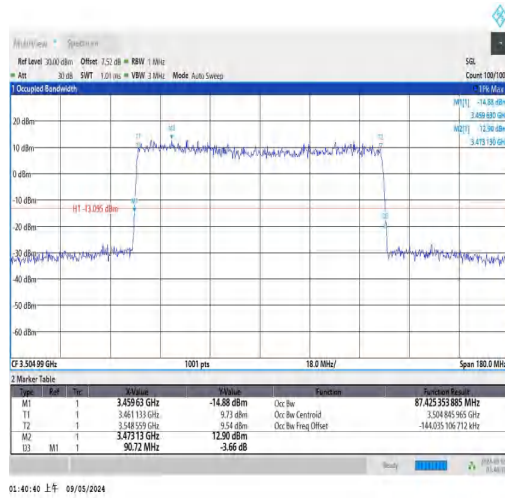
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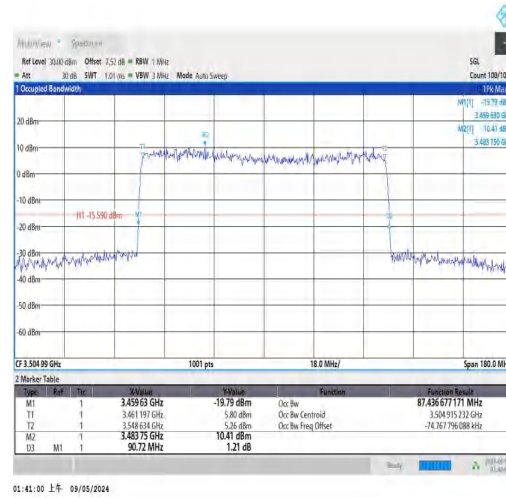
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NTNV_N78-3450-3550_PC3_30_90_H_TID4_Ant

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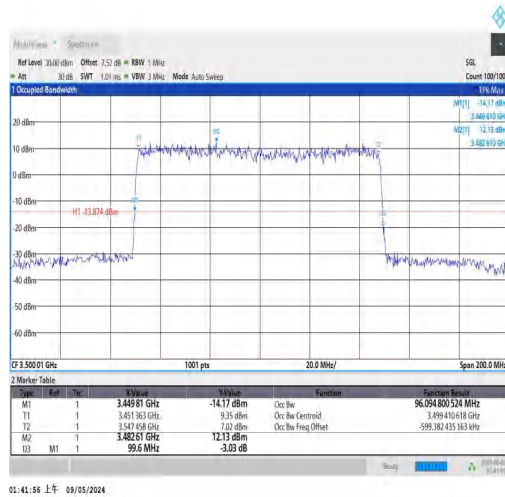
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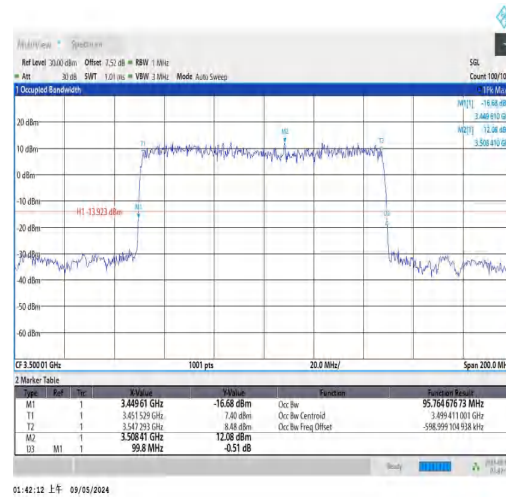
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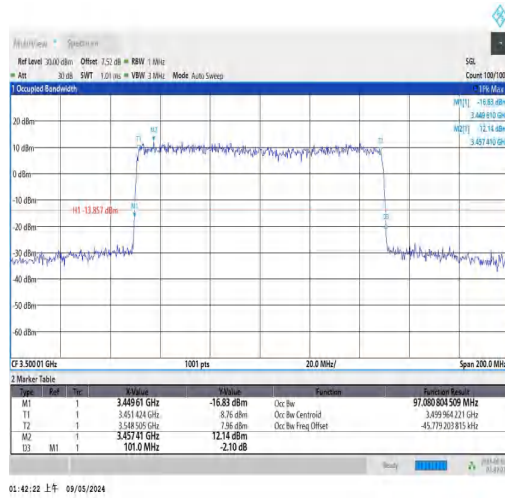
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NTNV_N78-3450-3550_PC3_30_100_L_TID1_An

NTNV_N78-3450-3550_PC3_30_100_L_TID2_An

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t1#1



01:42:22 上午 09/05/2024



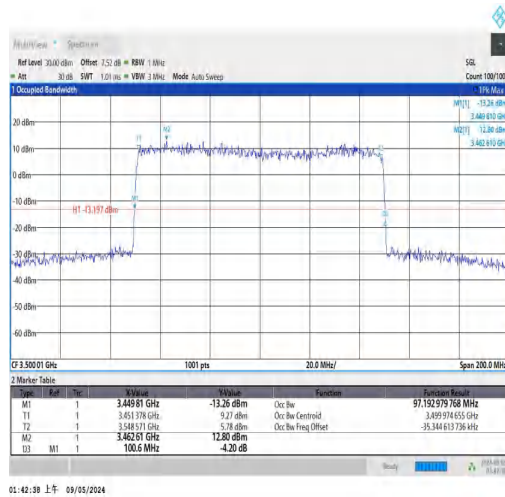
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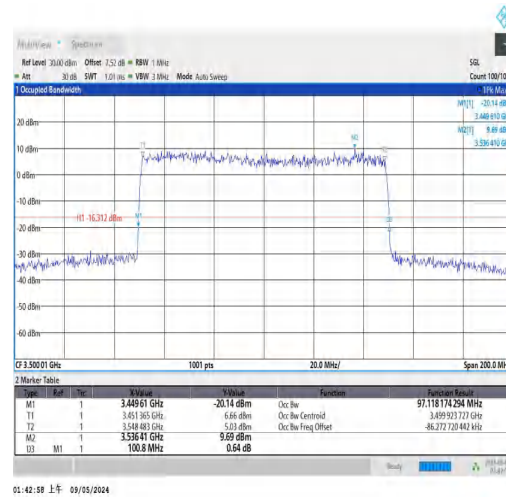
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t1#1



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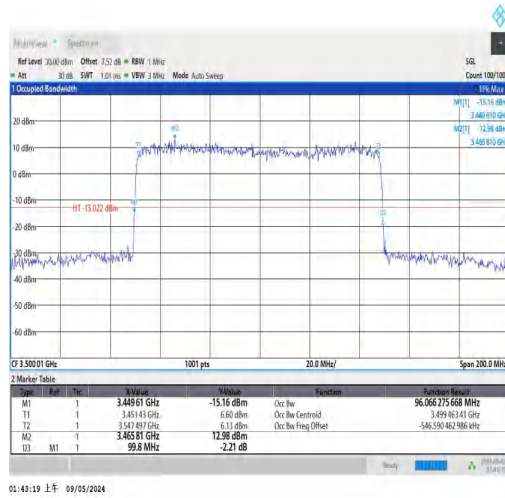
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NTNV_N78-3450-3550_PC3_30_100_L_TID5_An

NTNV_N78-3450-3550_PC3_30_100_L_TID6_An

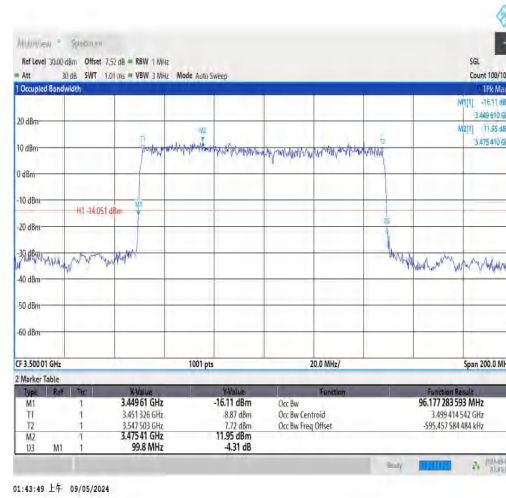
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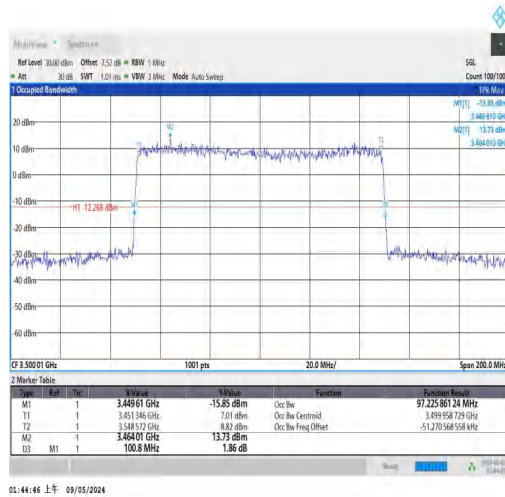
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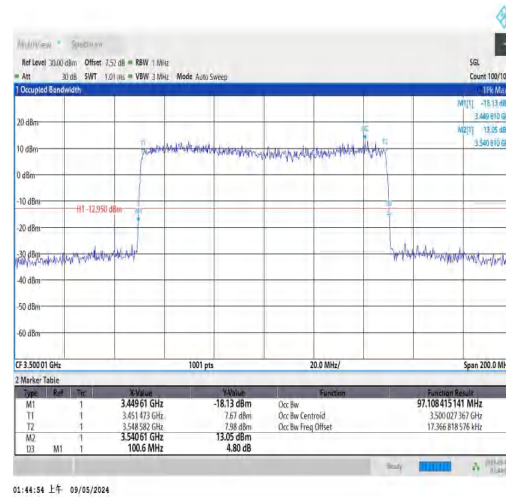
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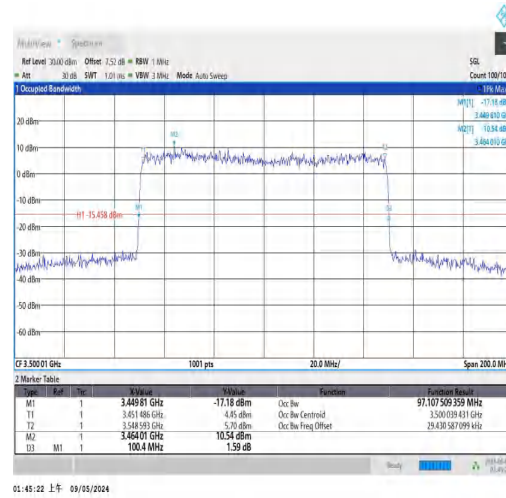
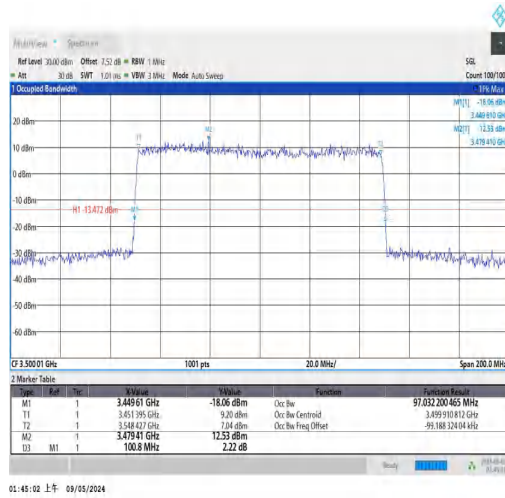
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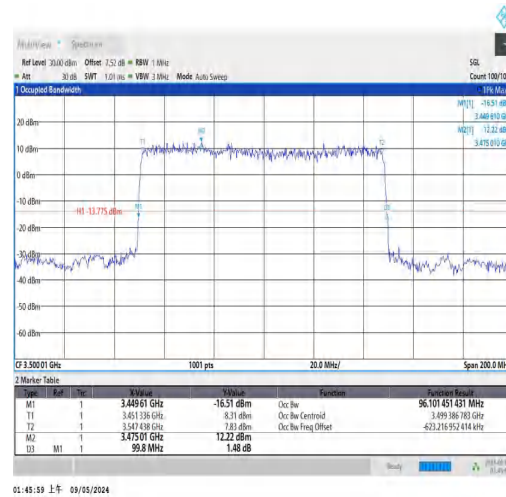
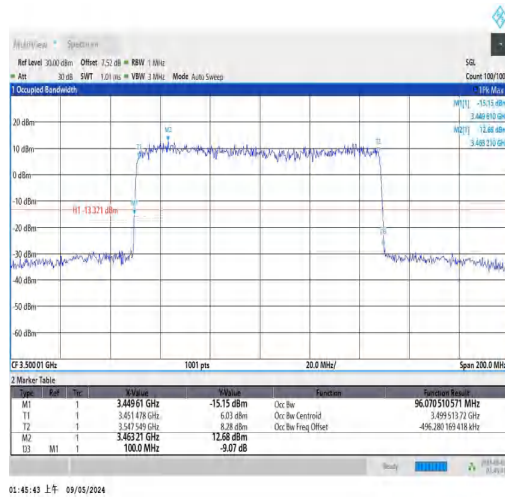


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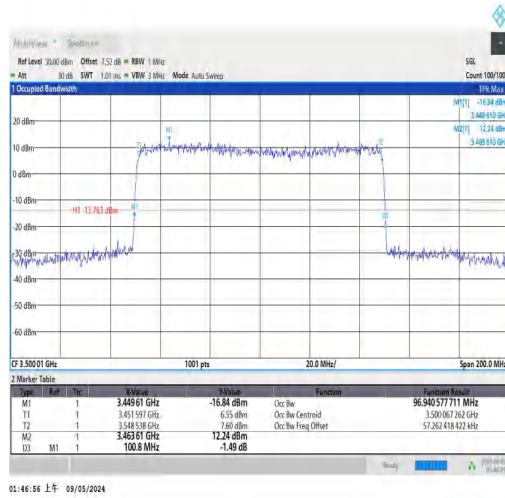


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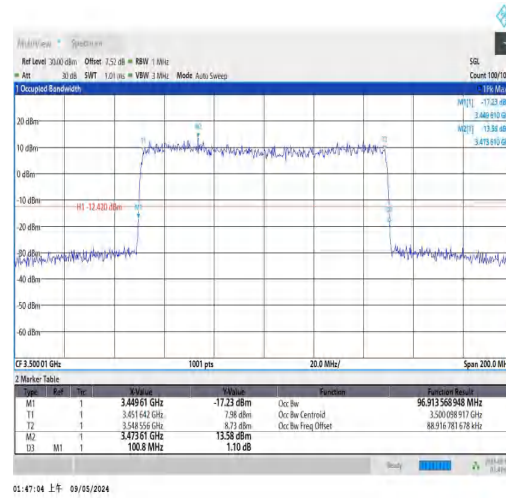
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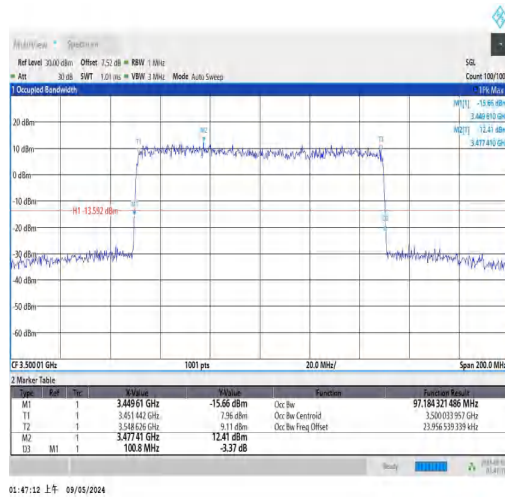
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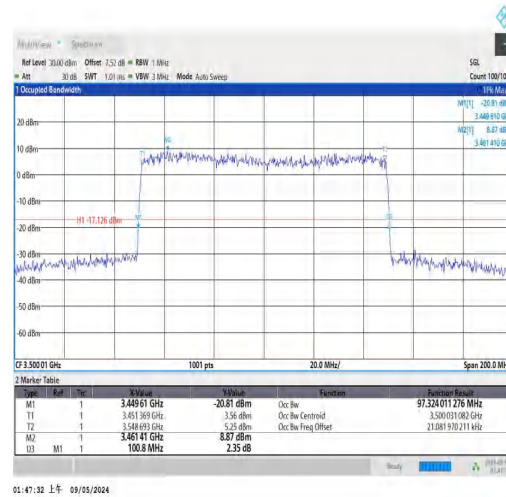
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t1#1



01:47:12 上午 09/05/2024



01:47:32 上午 09/05/2024

NTNV_N78-3450-3550_PC3_30_100_H_TID5_An

NTNV_N78-3450-3550_PC3_30_100_H_TID6_An

t1#1

t1#1



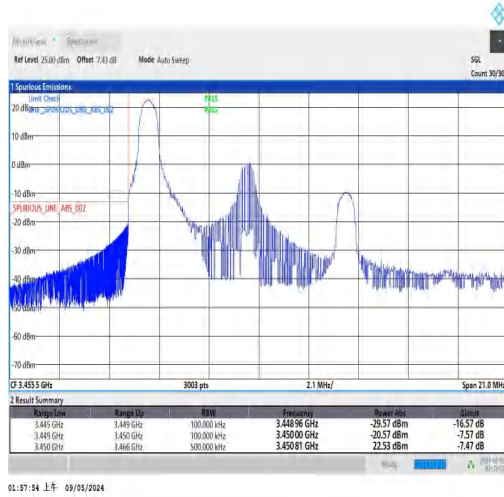
Appendix D: Band Edge

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS



Test Graphs



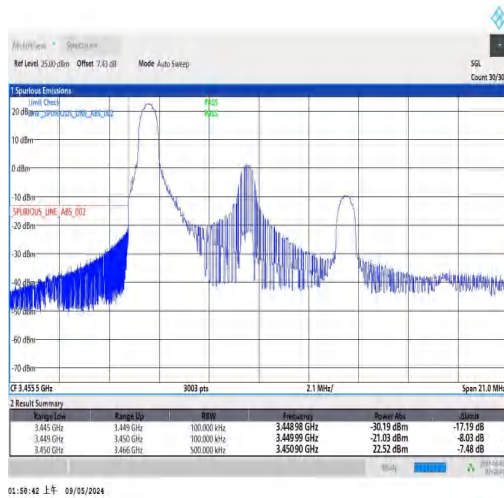
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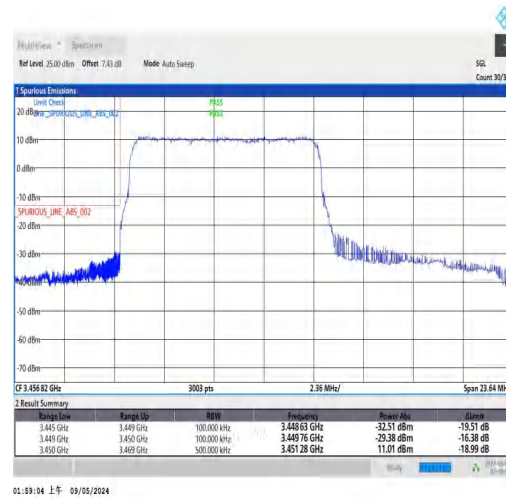
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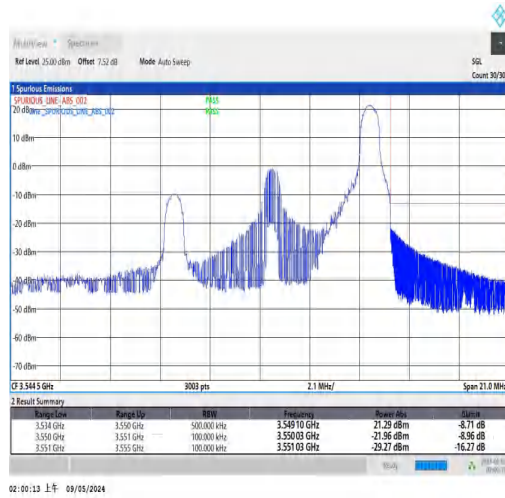
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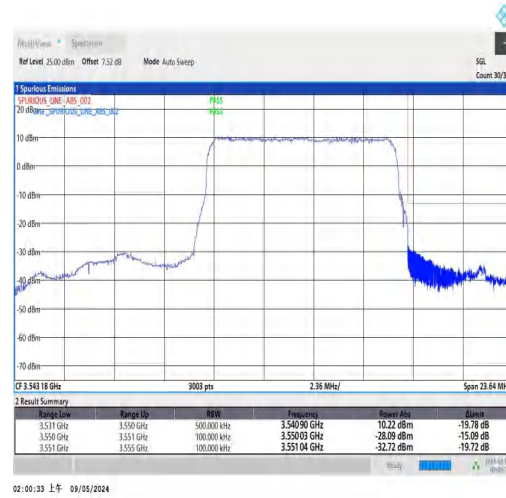
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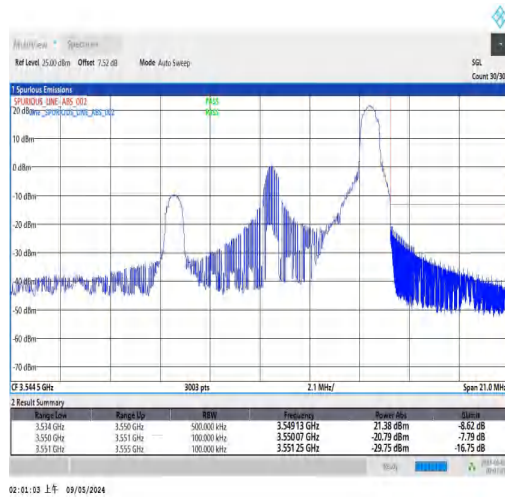
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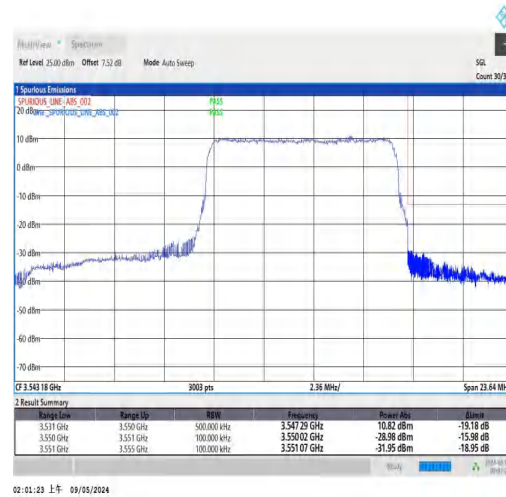
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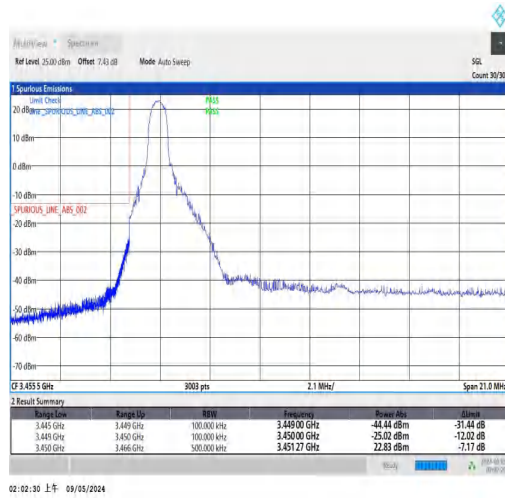
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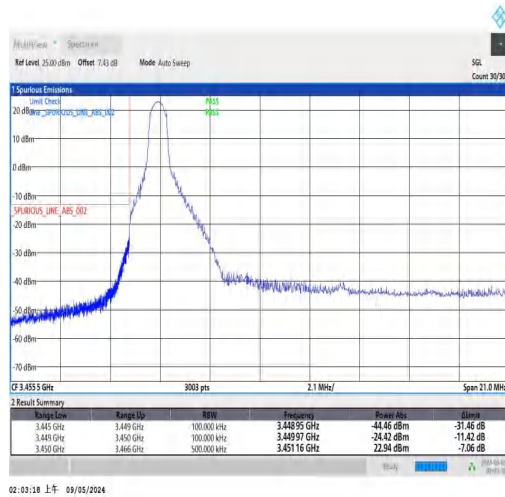
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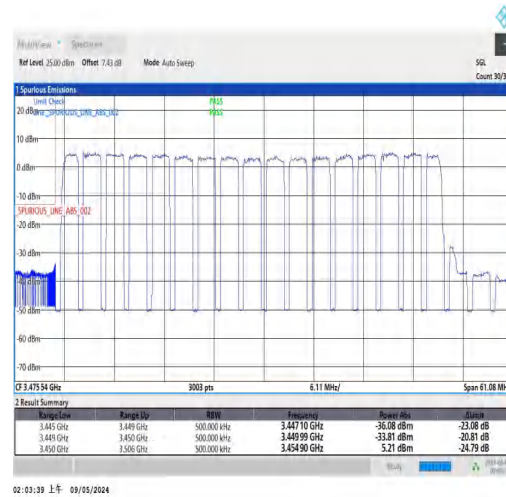
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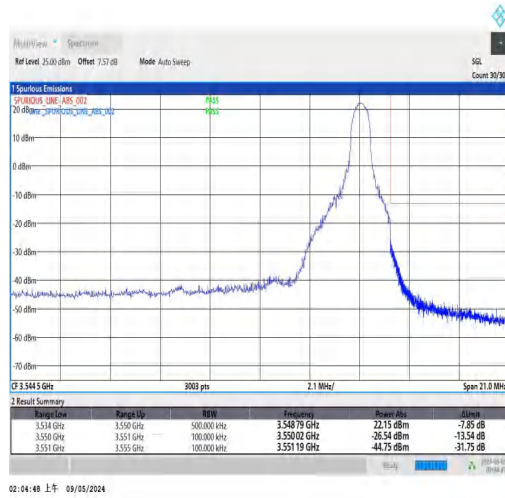
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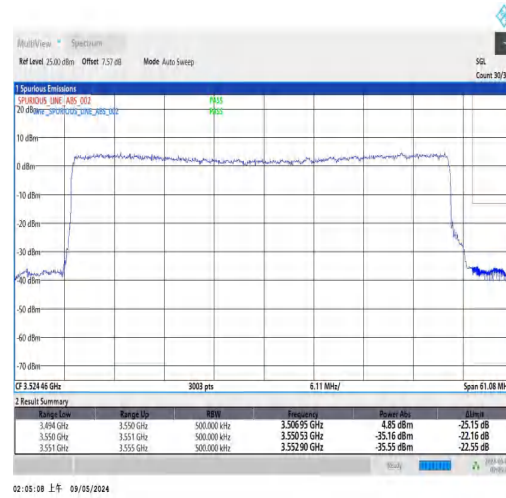
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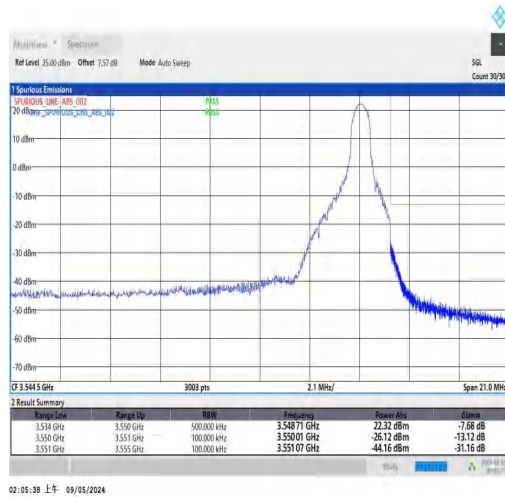
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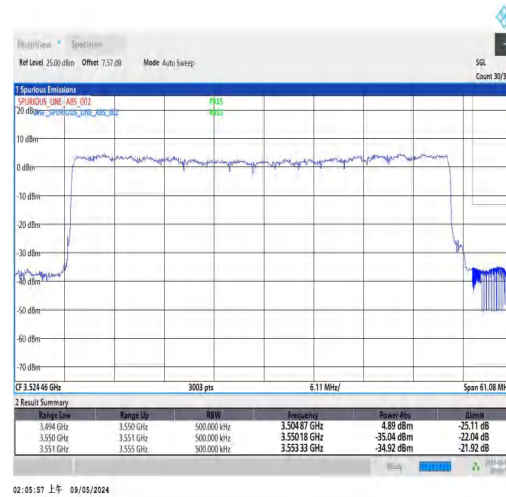
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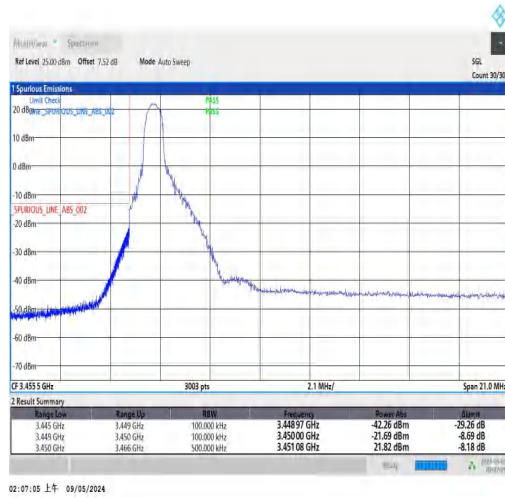
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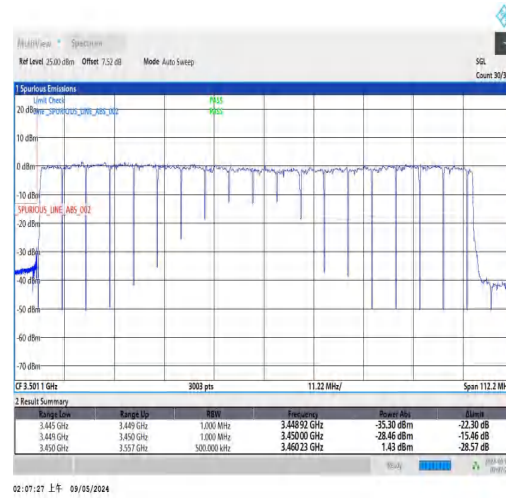


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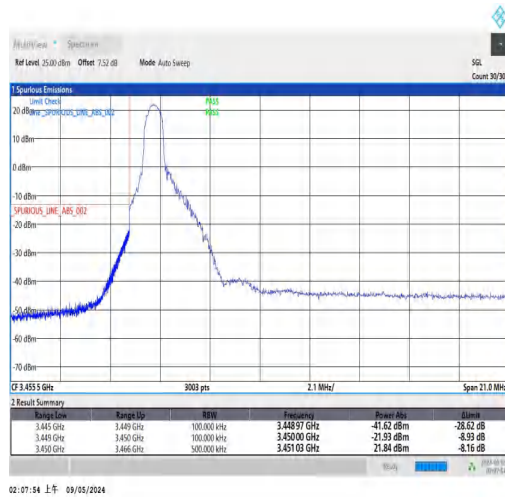
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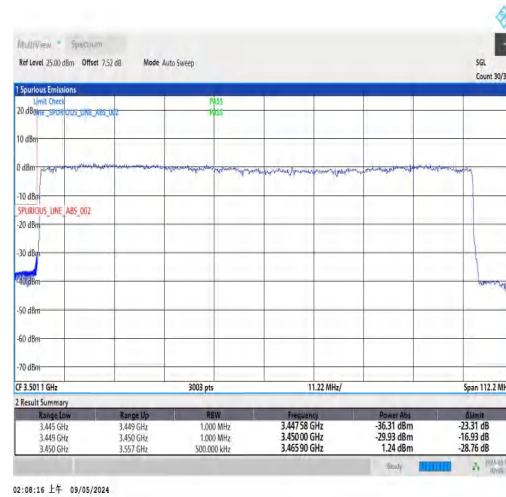
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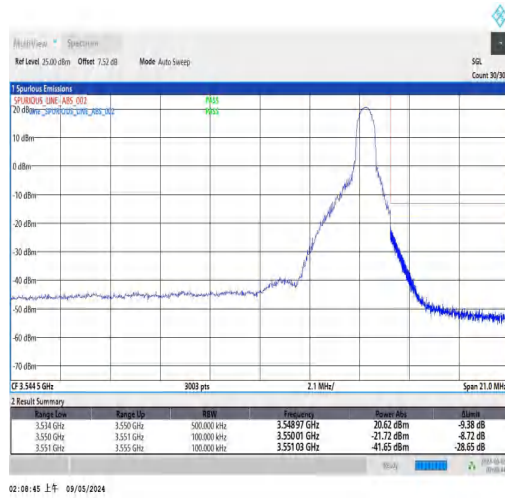
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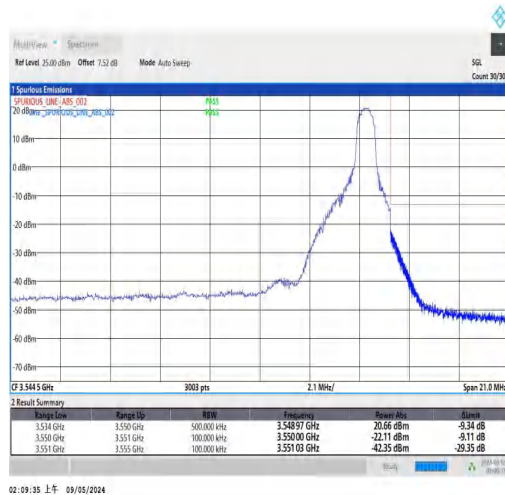
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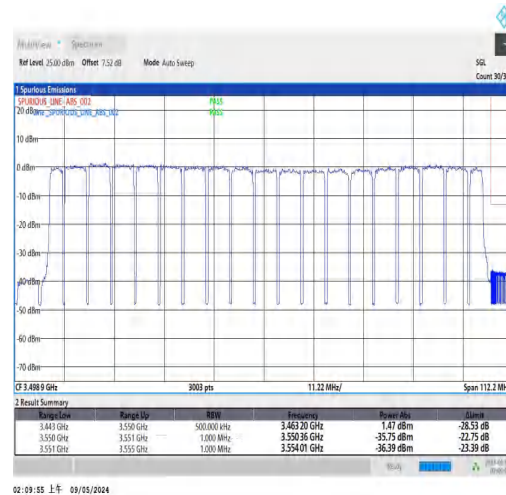
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D1#1



Appendix E: Conducted Spurious Emission

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-80.33	-43	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-84.46	-33	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-71.21	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	1000	6000	-26.42	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-27.59	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	18000	40000	-63.14	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-73.58	-43	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-84.44	-33	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-71.39	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-35.64	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-41.92	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-62.92	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-74.09	-43	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-84.51	-33	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-71.77	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-26.51	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-27.73	-13	PASS
N78-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-63.08	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-72.33	-43	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-84.60	-33	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-71.26	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-29.12	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-23.09	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-63.25	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-71.54	-43	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-86.03	-33	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-71.58	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	6000	-26.61	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-42.19	-13	PASS
N78-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	18000	40000	-63.24	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-76.76	-43	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-85.94	-33	PASS



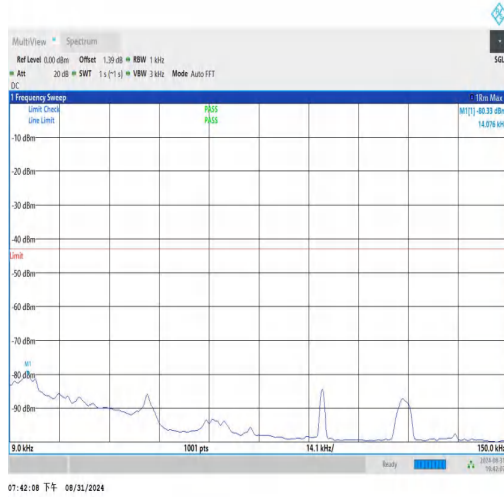
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N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-47.86	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	18000	40000	-63.24	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-74.32	-43	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-85.93	-33	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-71.03	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-33.35	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-30.86	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-63.13	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-74.57	-43	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-85.82	-33	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-71.66	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-26.74	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-32.29	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-63.01	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-81.95	-43	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-85.78	-33	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-71.36	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-36.29	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-31.19	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-63.19	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	0.009	0.15	-75.94	-43	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	0.15	30	-85.84	-33	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-71.38	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	6000	-26.69	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	6000	18000	-31.55	-13	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	18000	40000	-63.20	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-75.43	-43	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-85.84	-33	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-71.48	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	6000	-26.89	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-32.73	-13	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	18000	40000	-63.11	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-78.07	-43	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-85.33	-33	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-70.94	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	1000	6000	-39.30	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-49.30	-13	PASS
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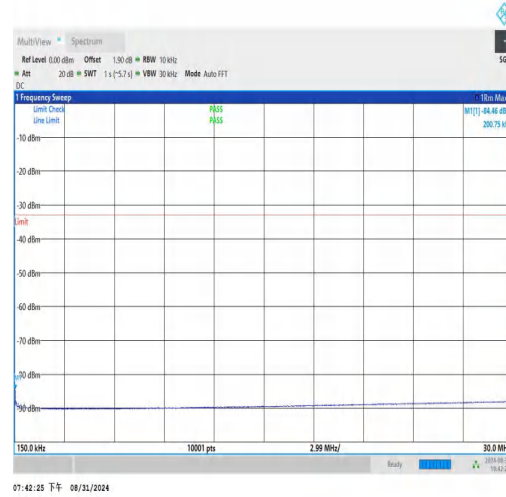
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N78-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	-71.51	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-39.60	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-56.04	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-62.54	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-80.11	-43	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-85.38	-33	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-71.56	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-37.83	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-53.66	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-62.96	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-78.70	-43	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-85.46	-33	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	-71.48	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-39.38	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-55.47	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-63.10	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	0.009	0.15	-78.95	-43	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	0.15	30	-85.33	-33	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-71.42	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	1000	6000	-41.58	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	6000	18000	-56.29	-13	PASS
N78-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	18000	40000	-62.75	-13	PASS
N78-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-83.44	-43	PASS
N78-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-85.94	-33	PASS
N78-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	-71.50	-13	PASS
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N78-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-55.98	-13	PASS
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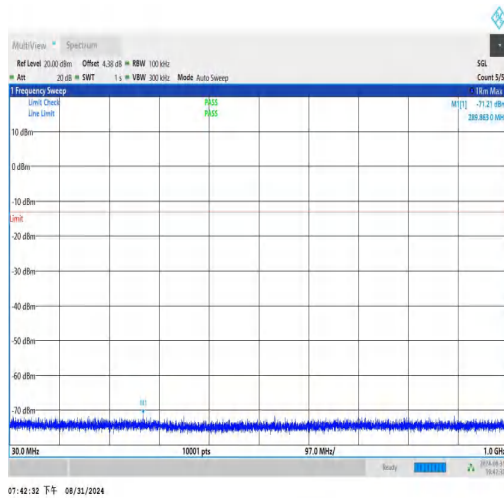
Test Graphs



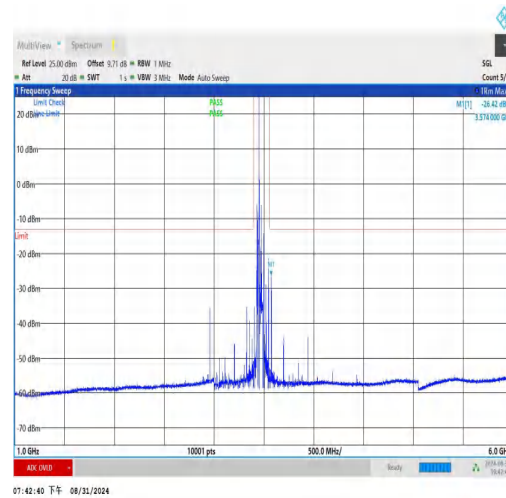
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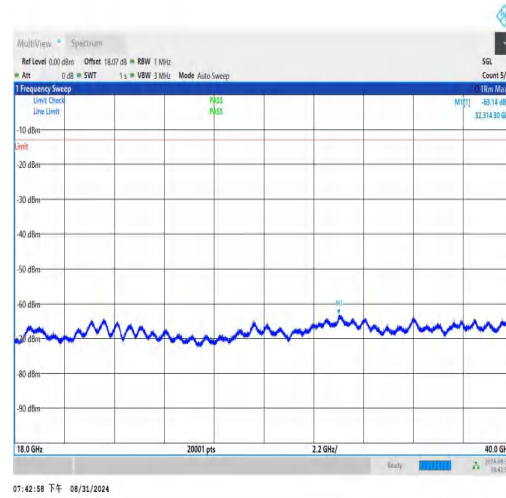
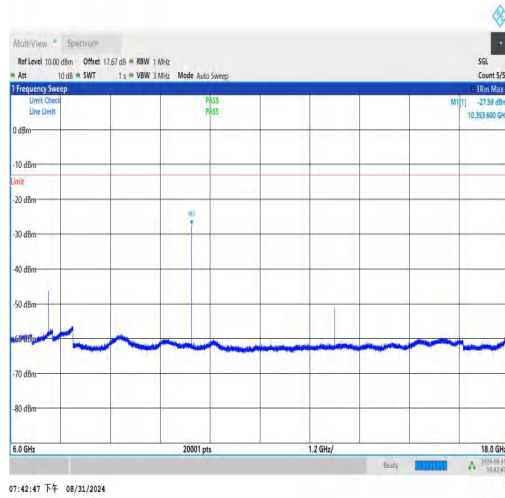
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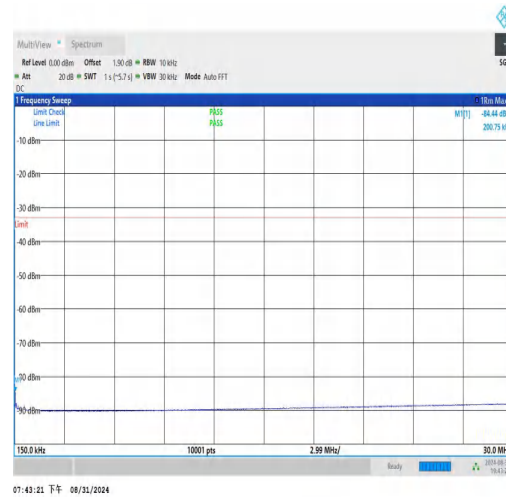
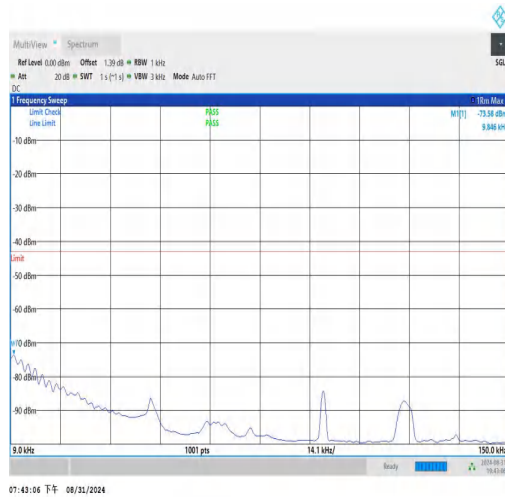


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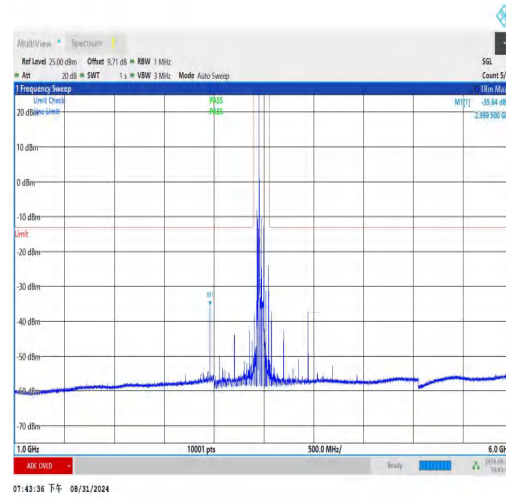
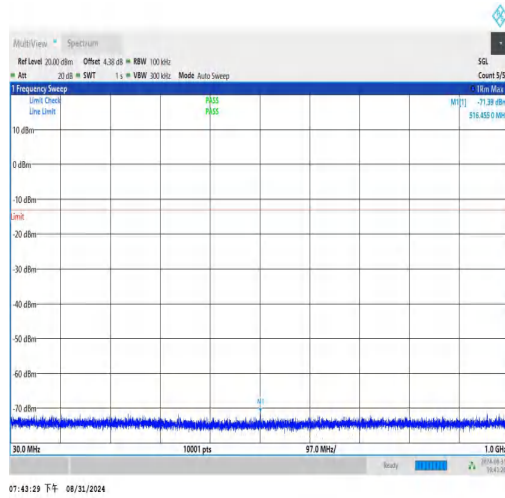
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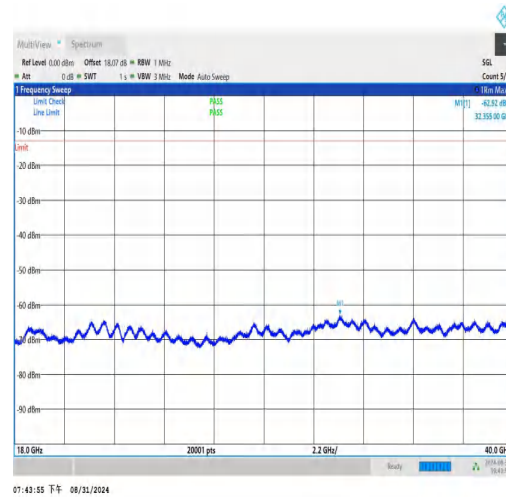
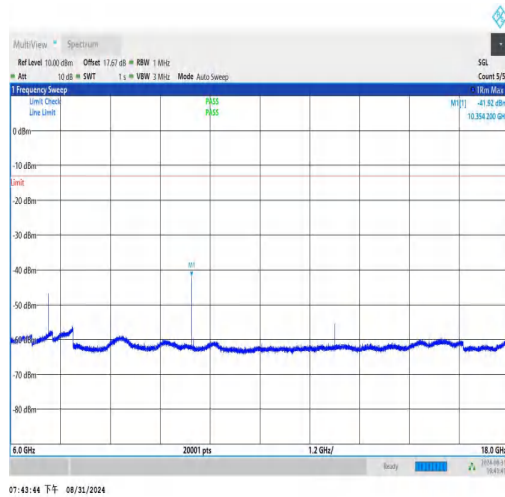
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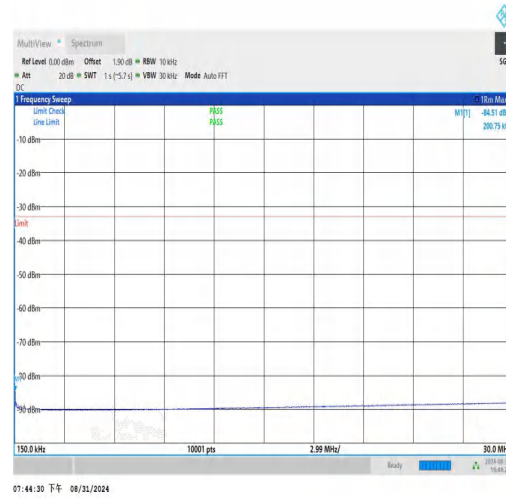
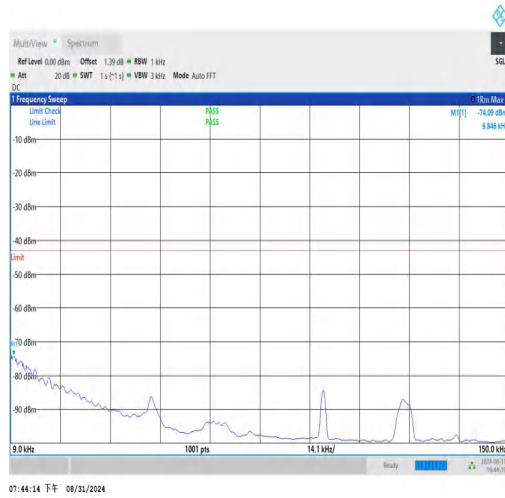
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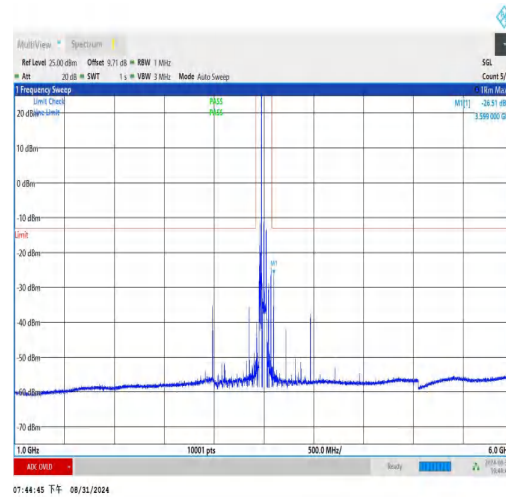
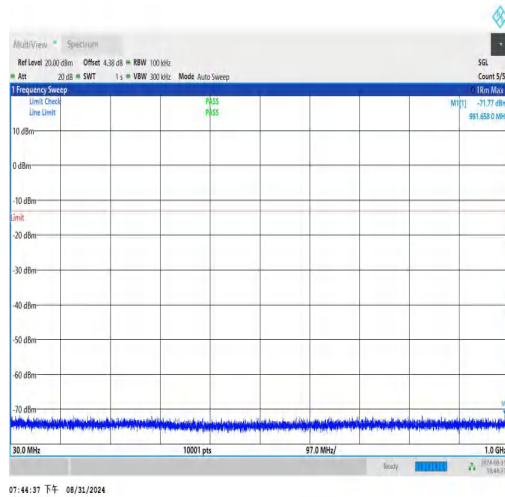
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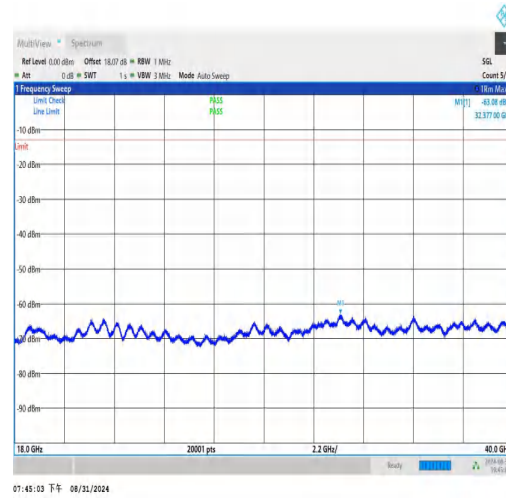
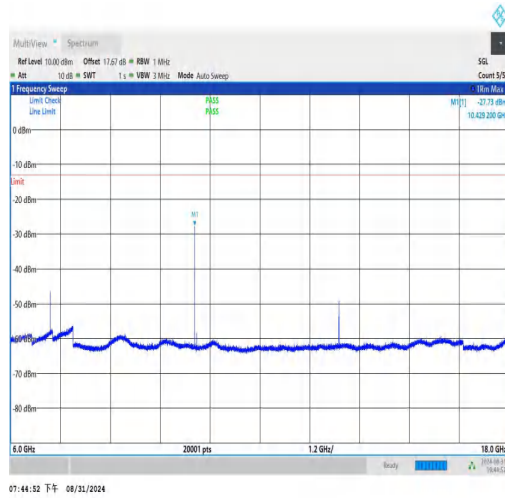
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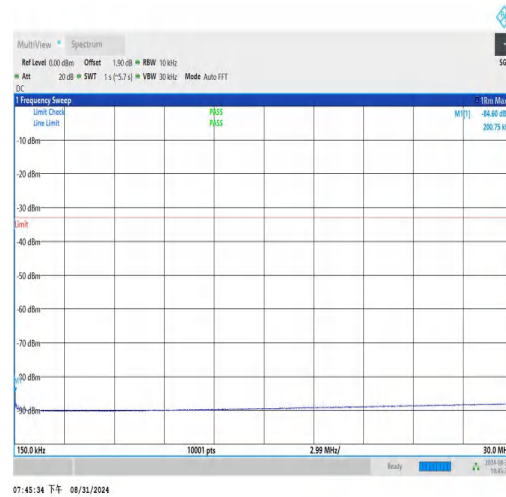
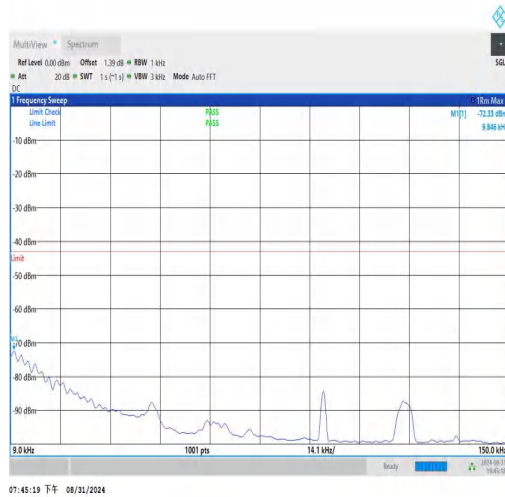
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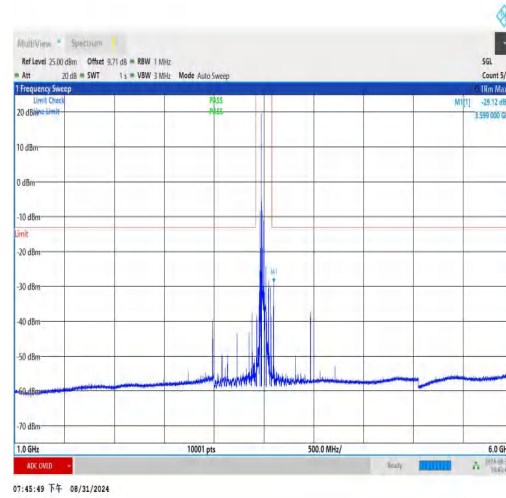
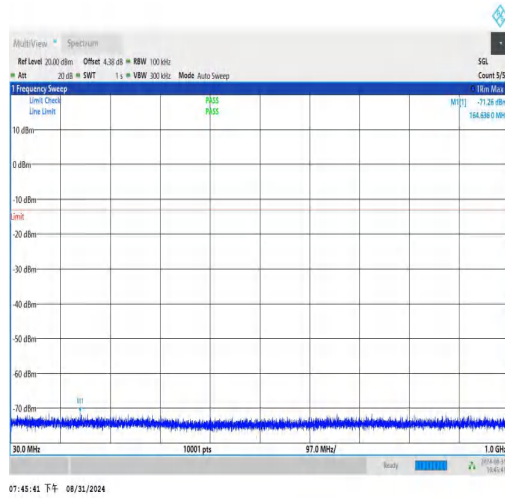
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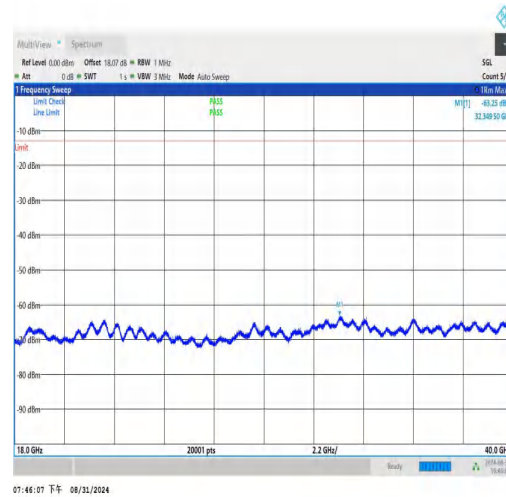
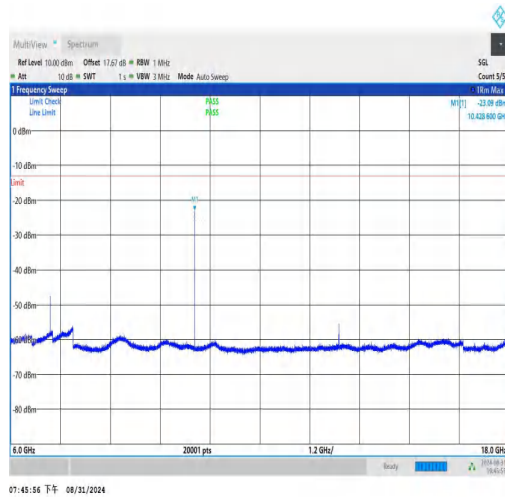
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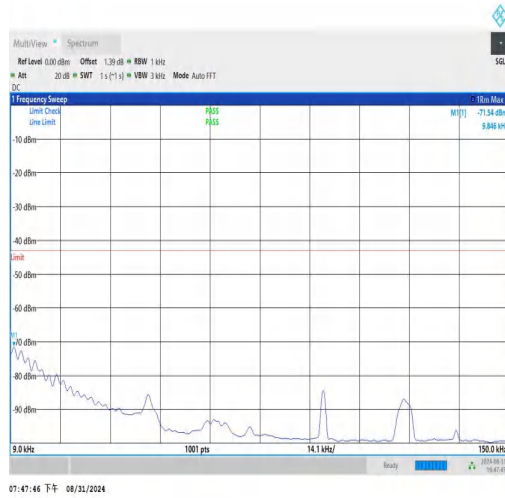
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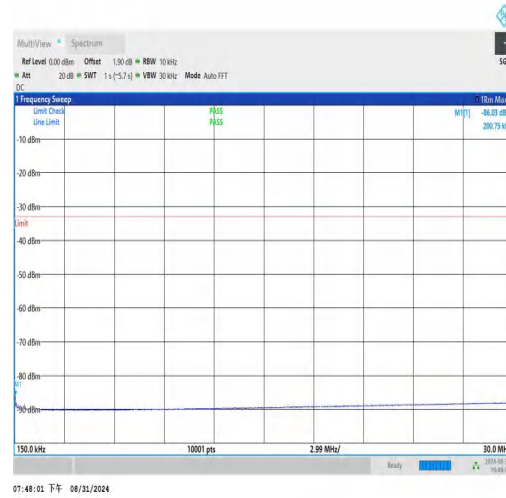


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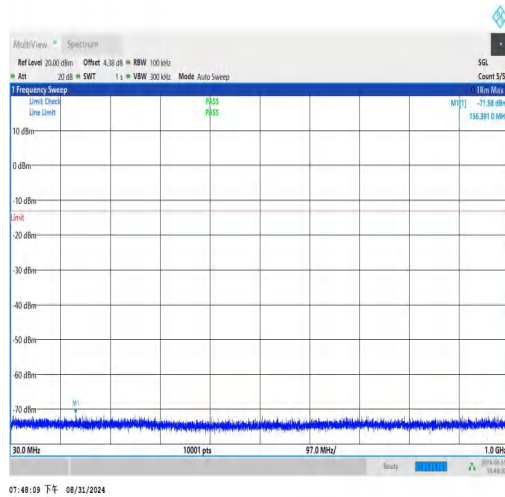
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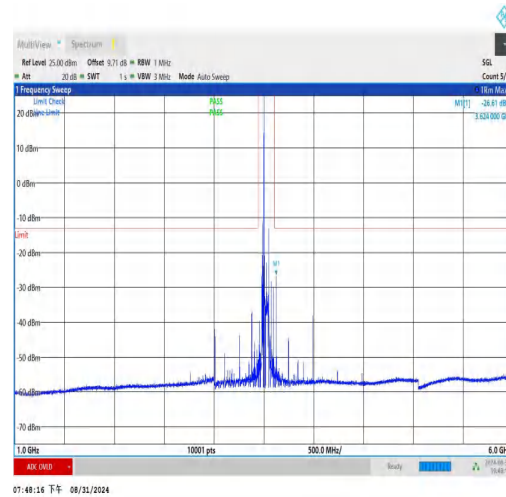
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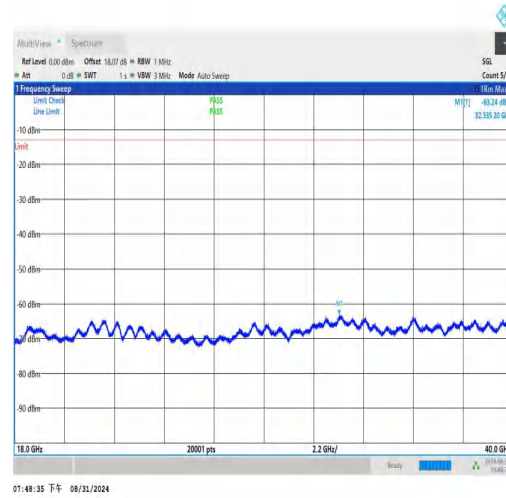
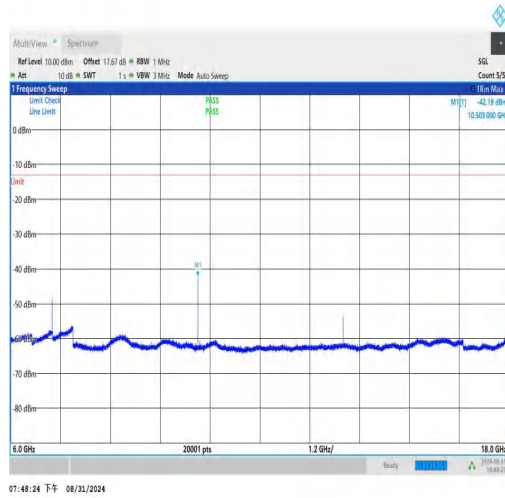
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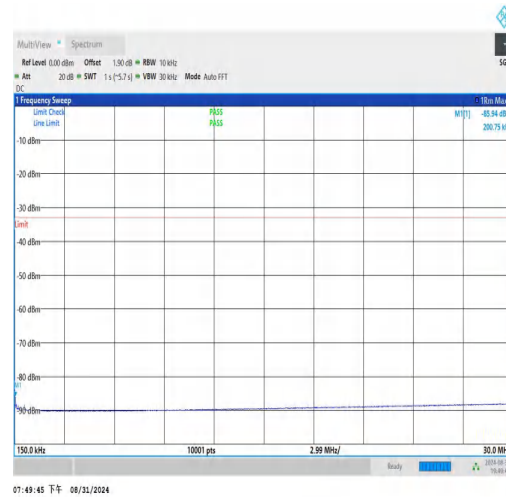
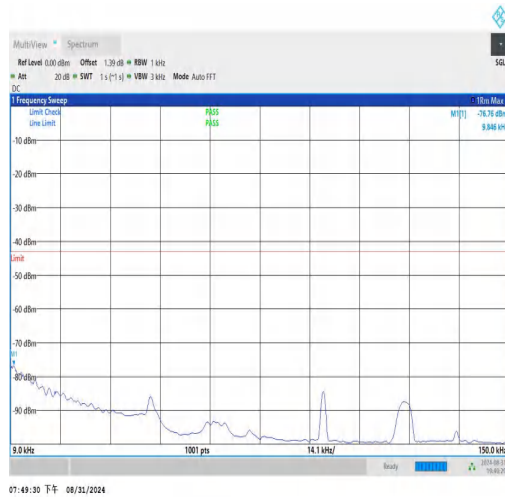


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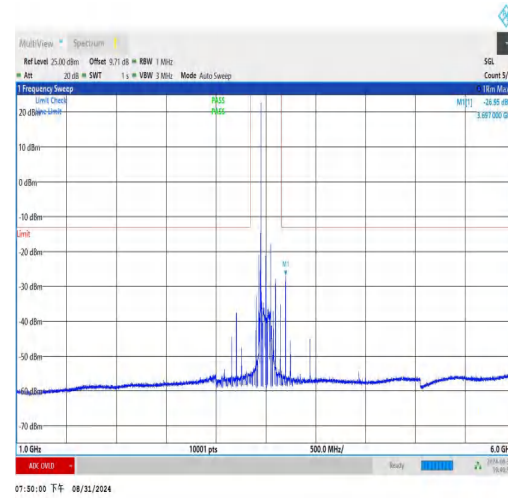
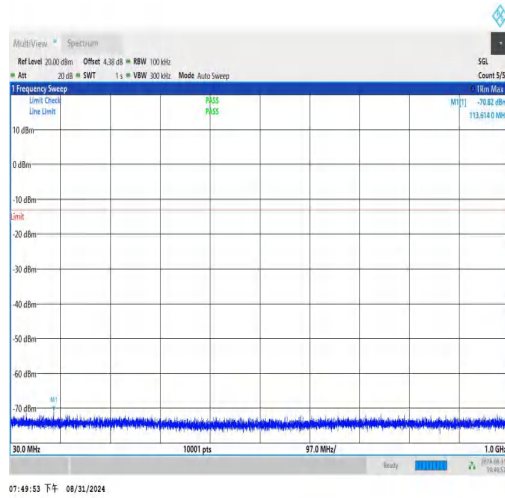
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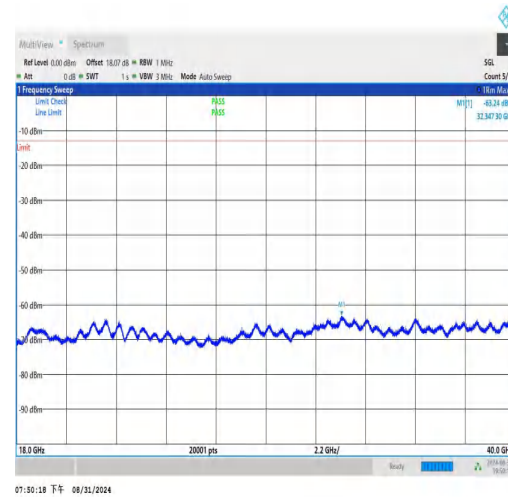
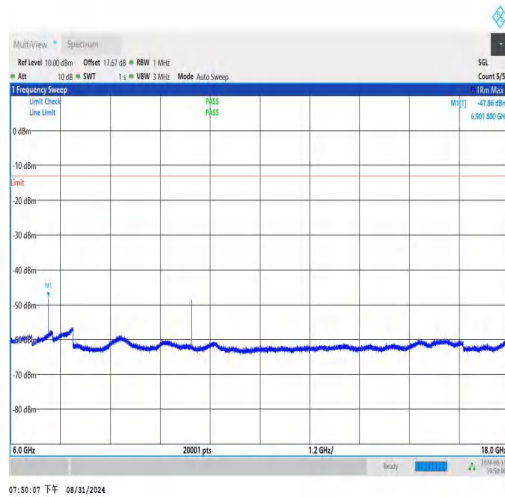
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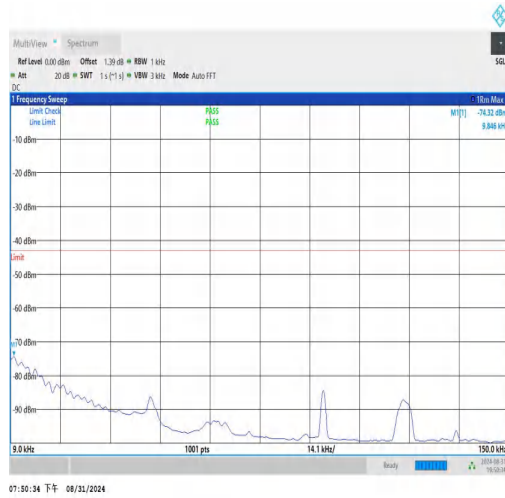
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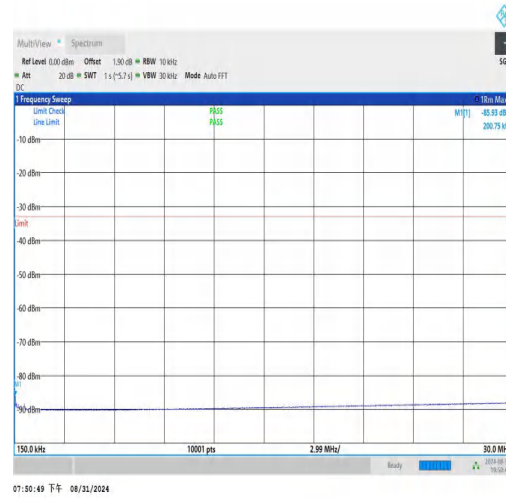


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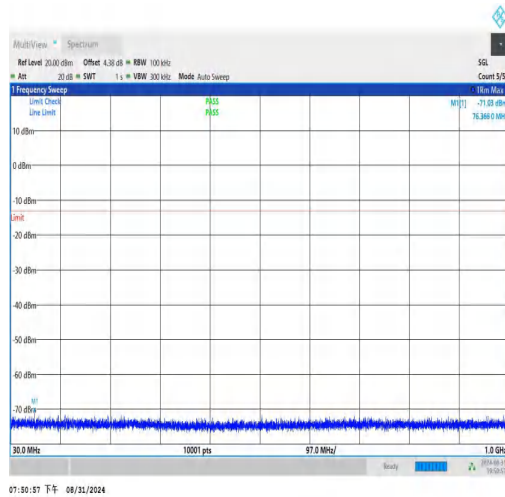
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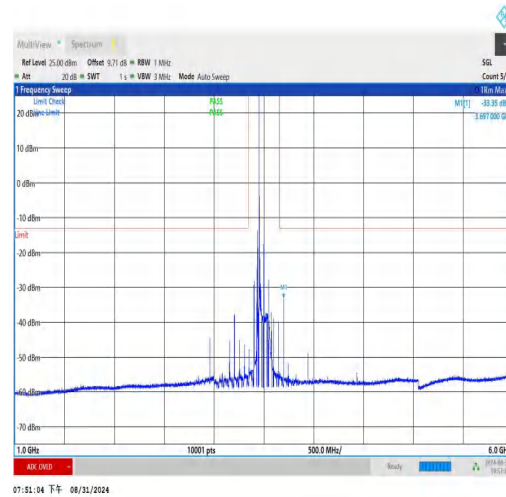
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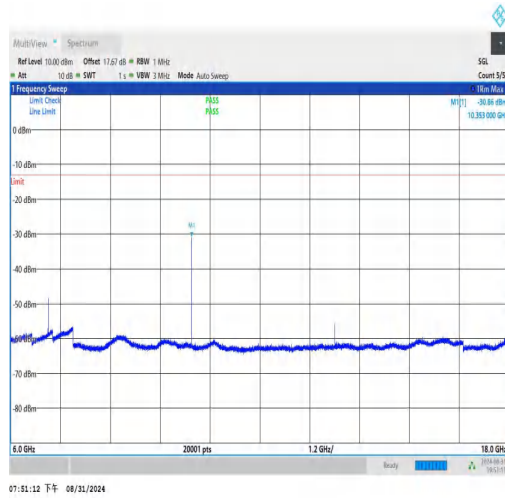
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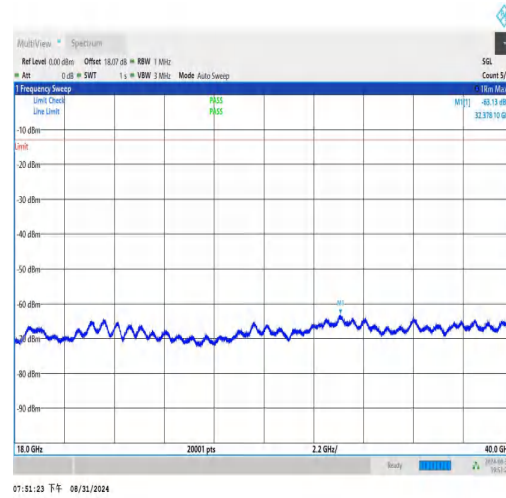
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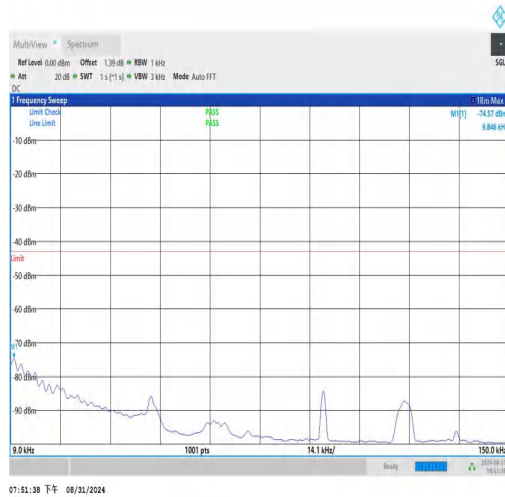
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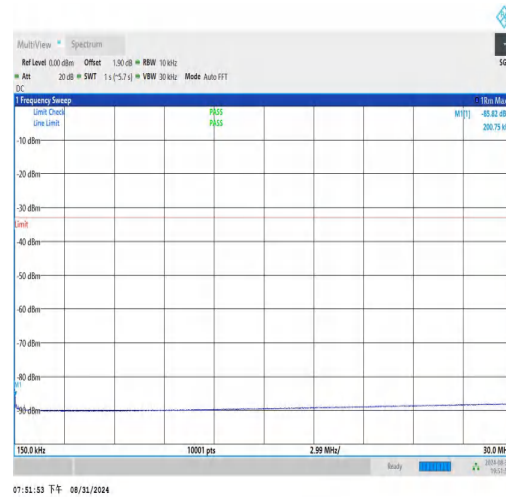
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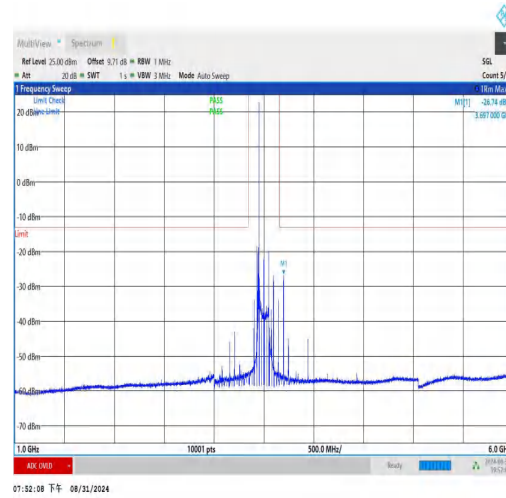
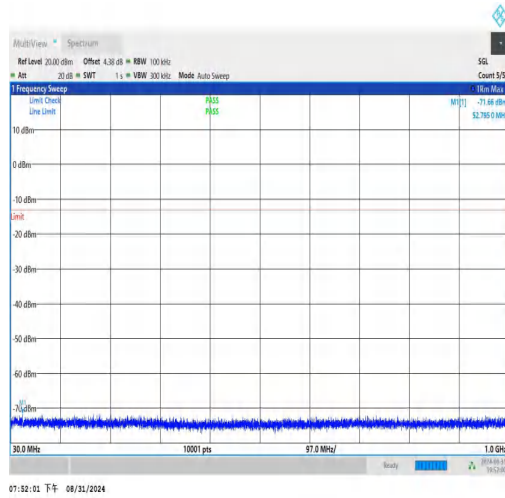
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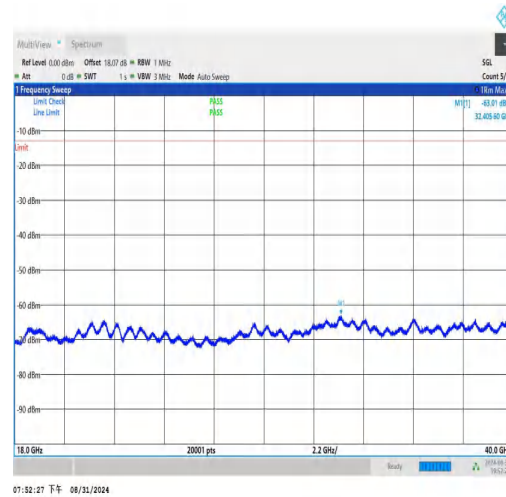
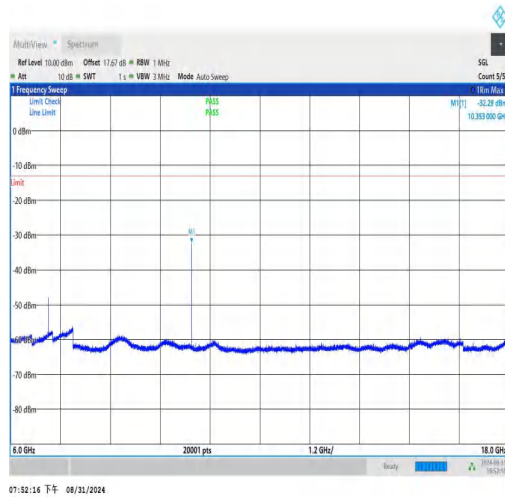


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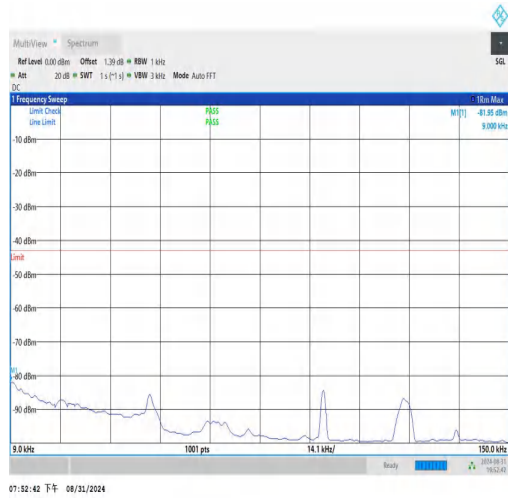
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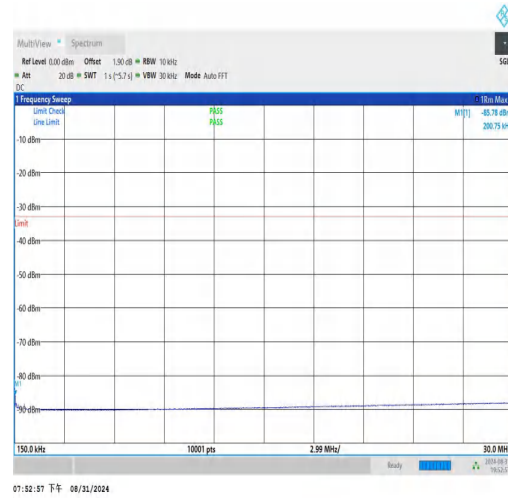


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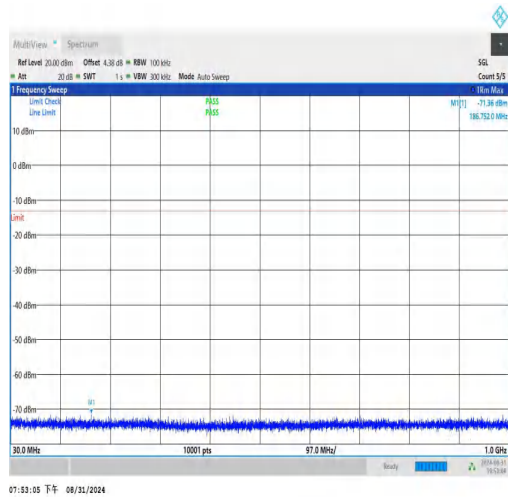
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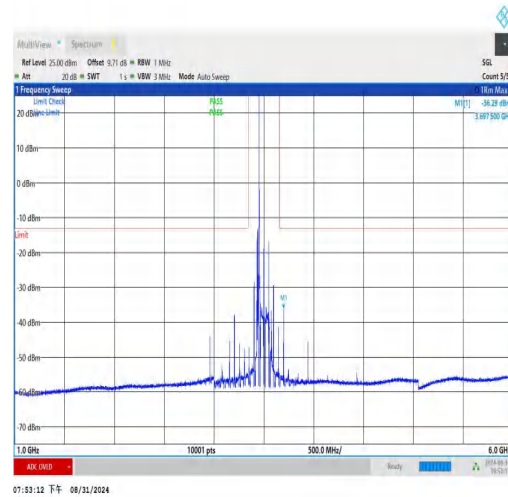
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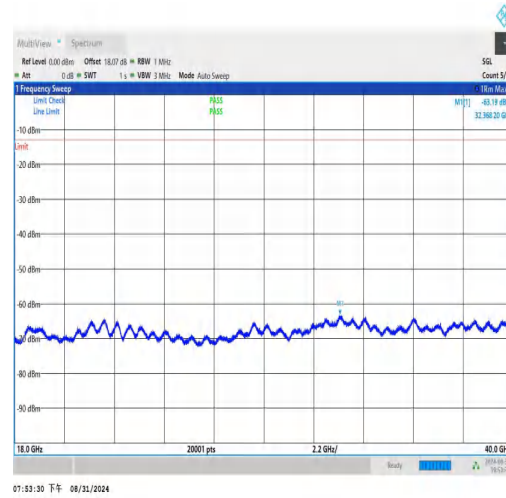
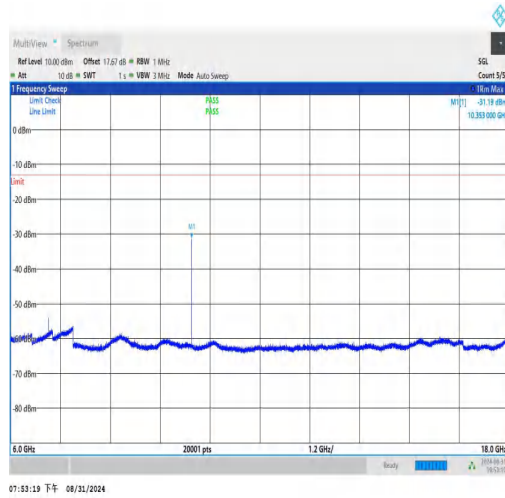
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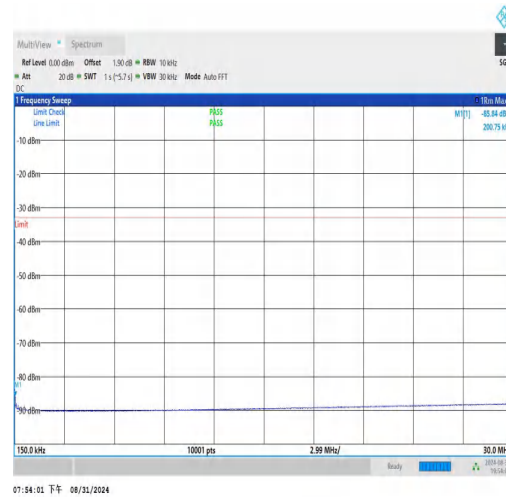
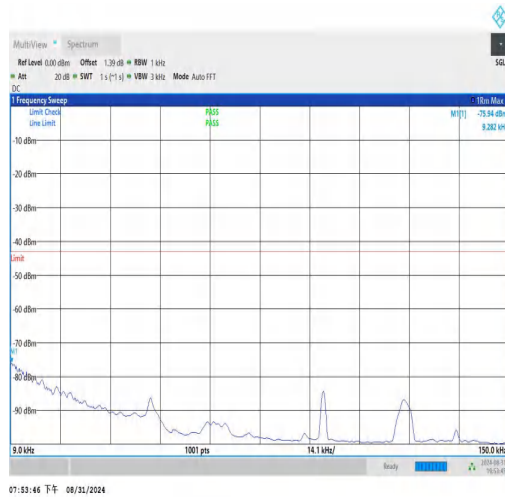


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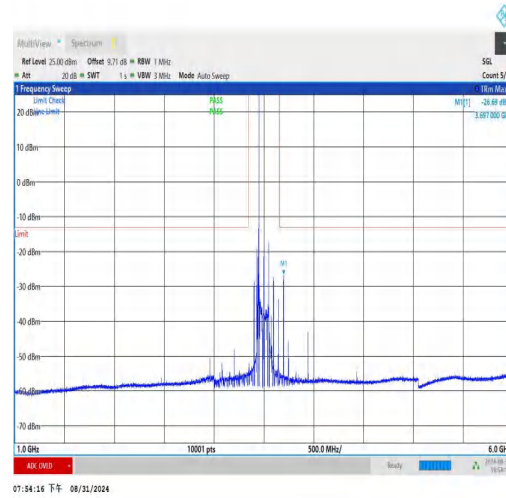
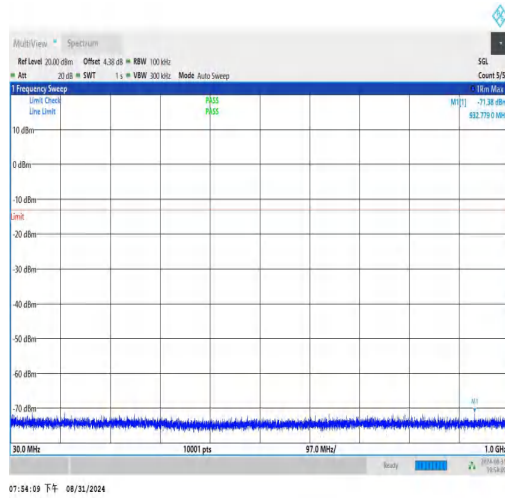
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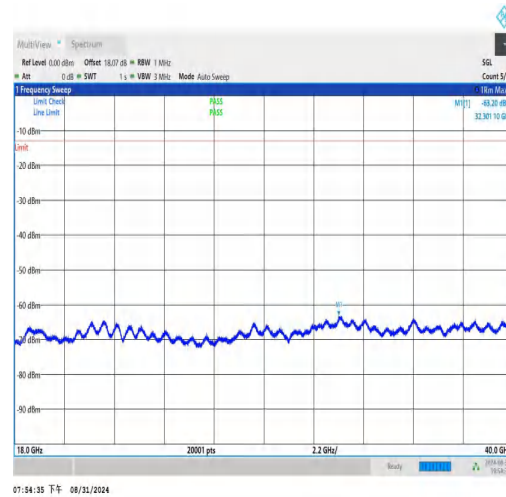
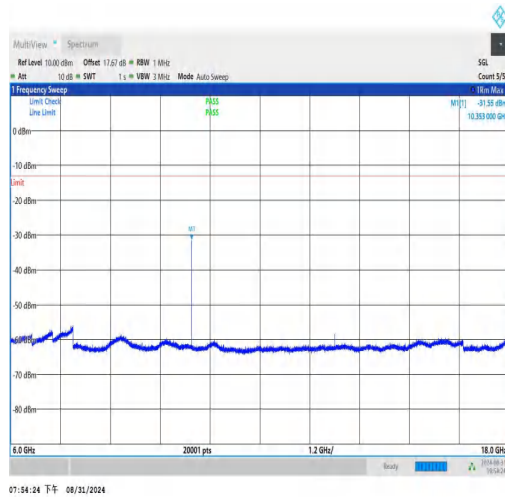
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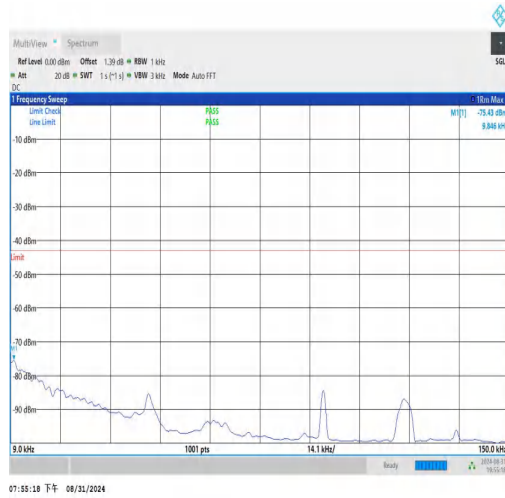
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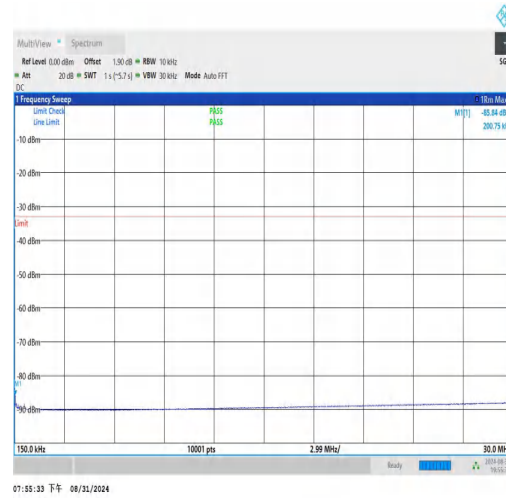


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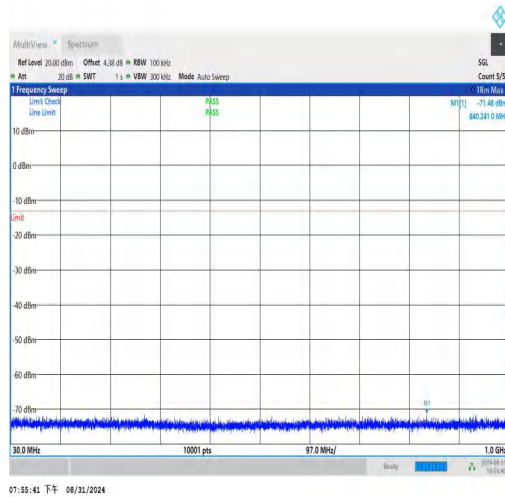
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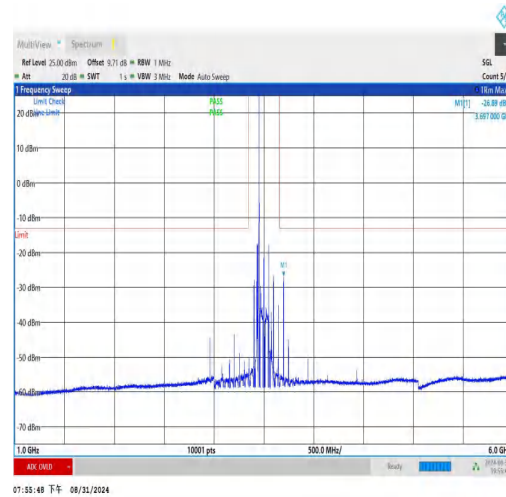
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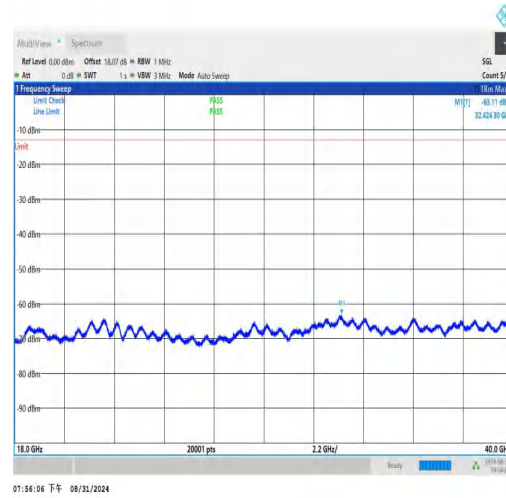
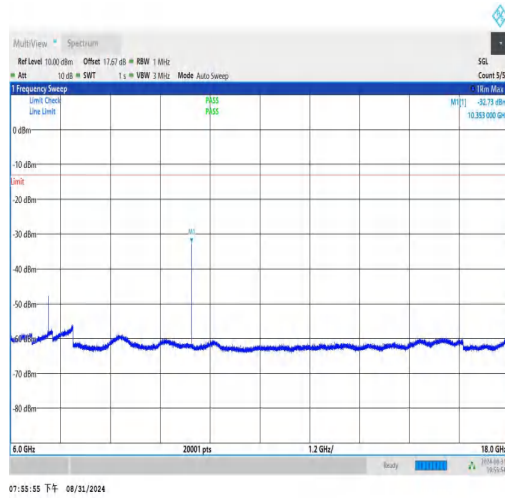
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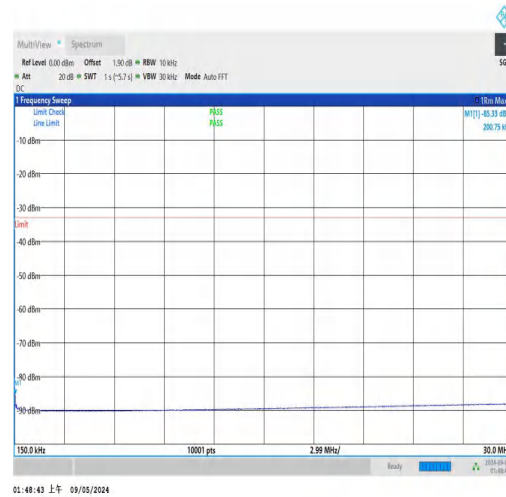
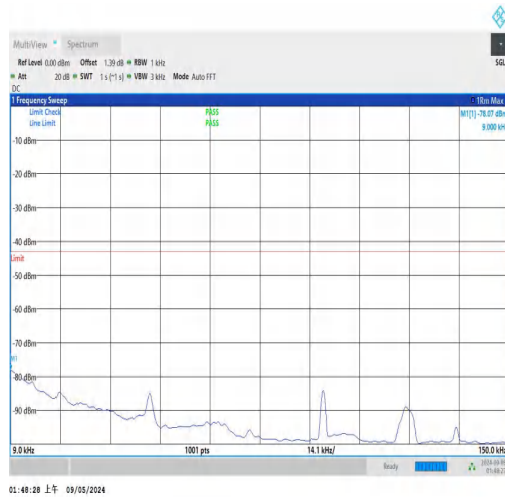


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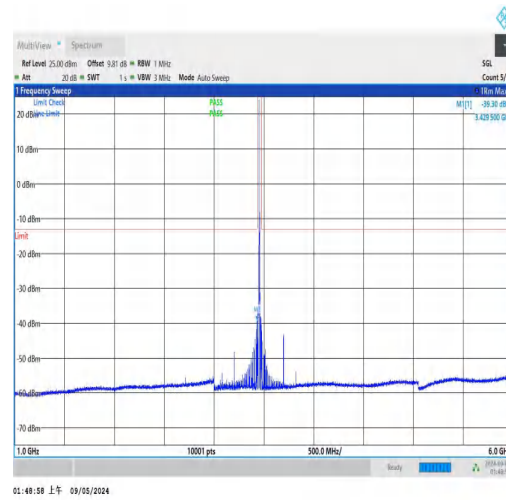
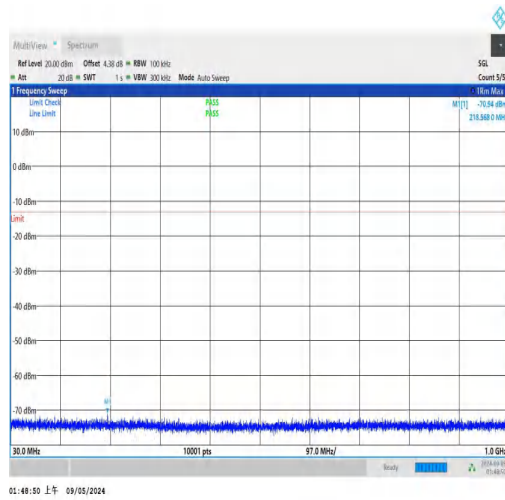
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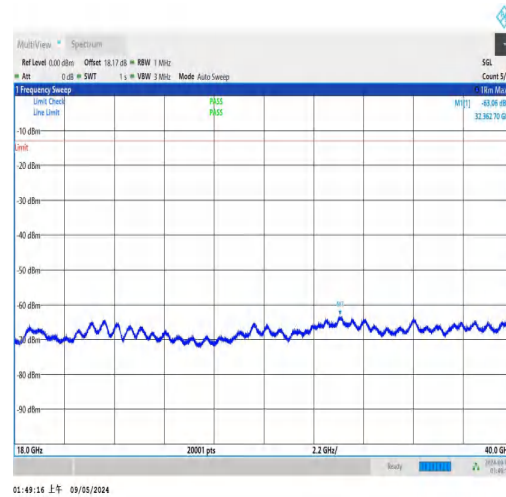
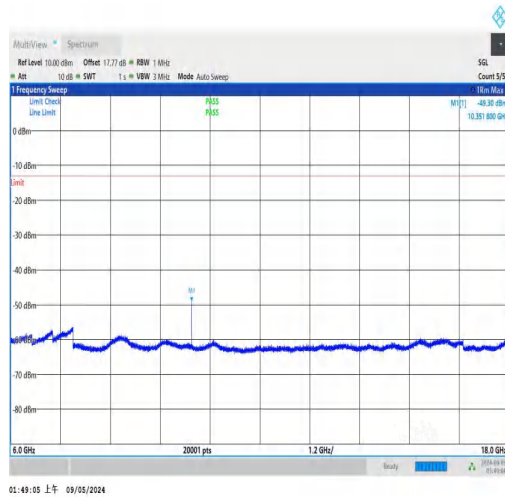
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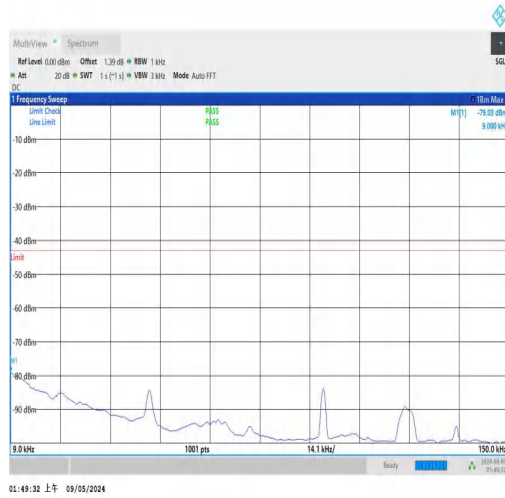
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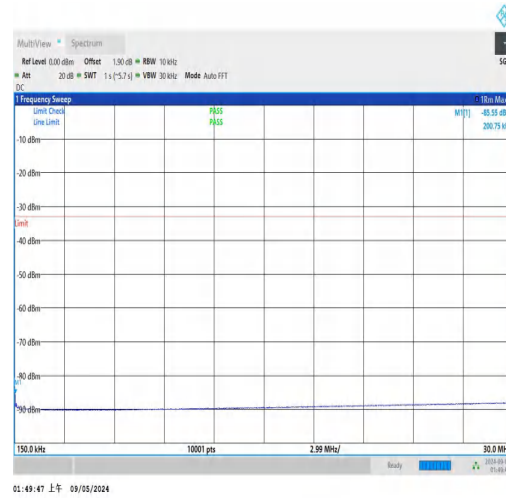


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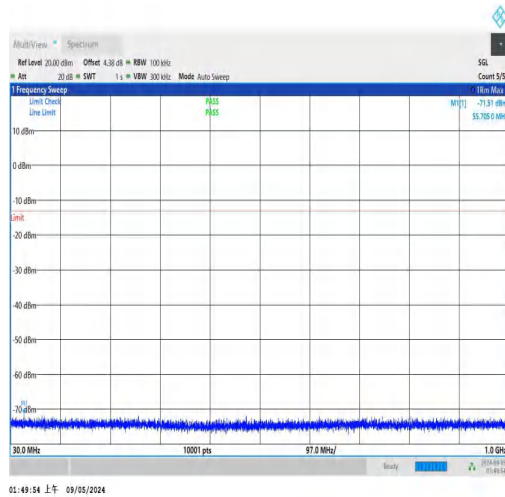
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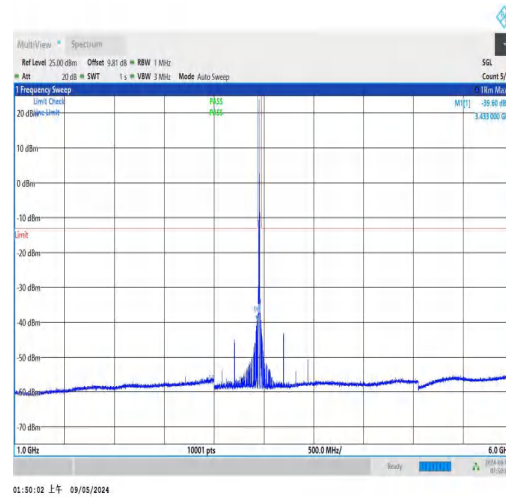
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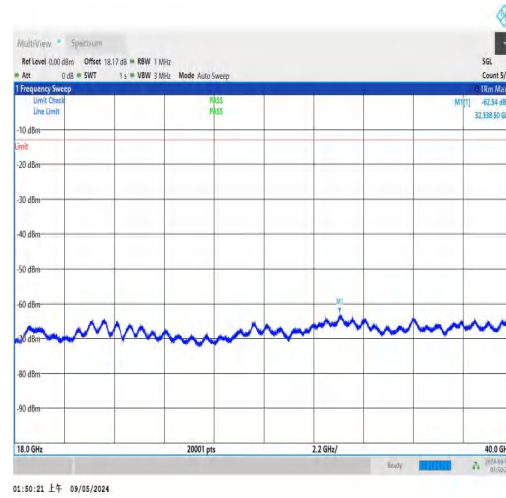
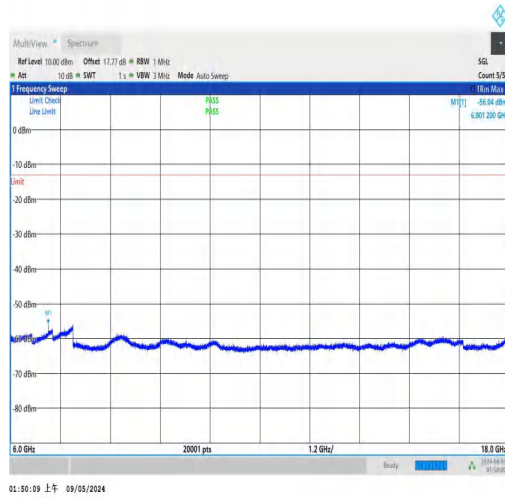
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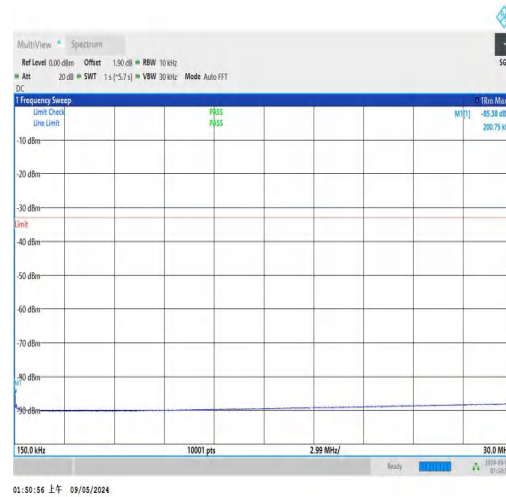
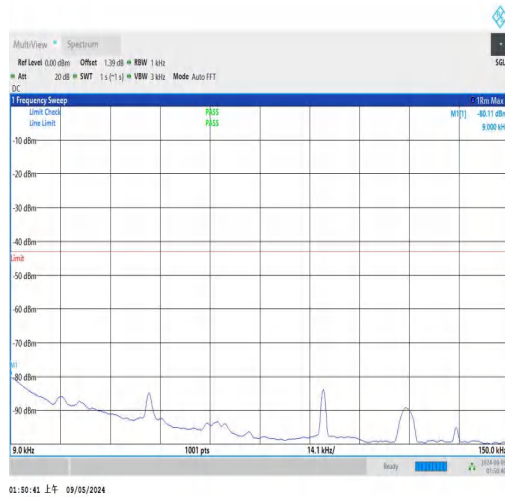


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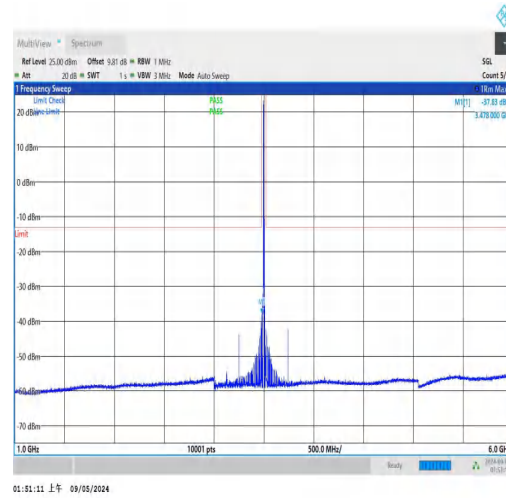
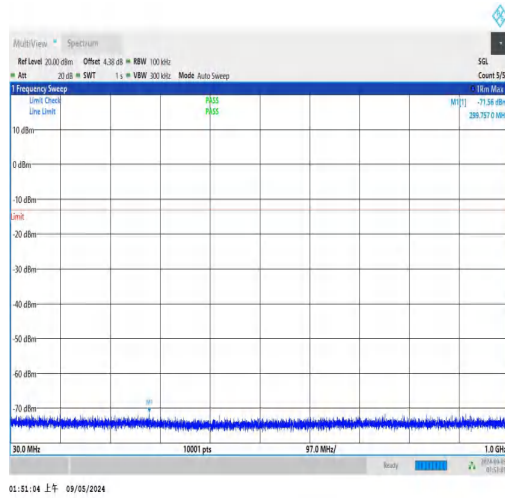
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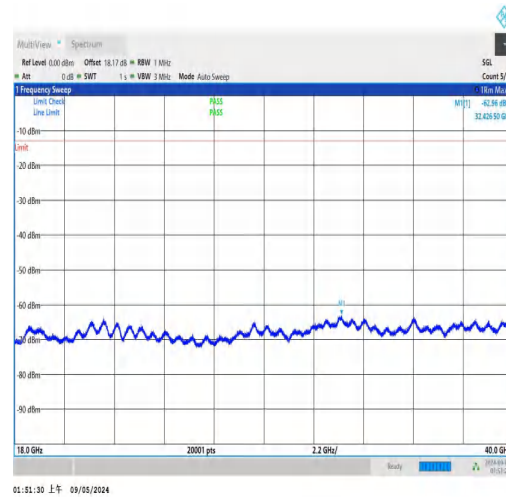
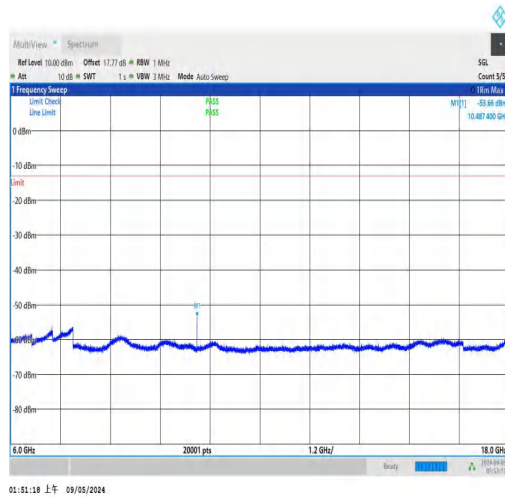
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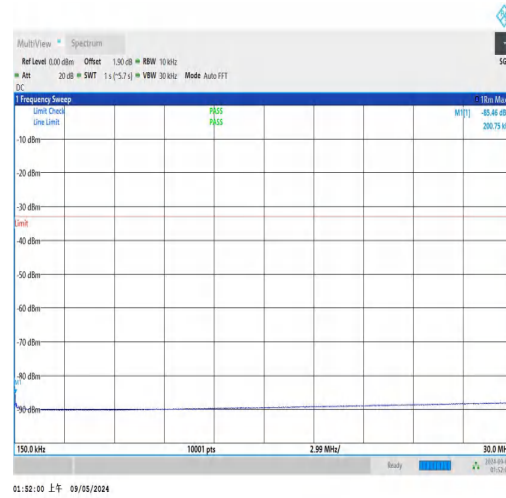
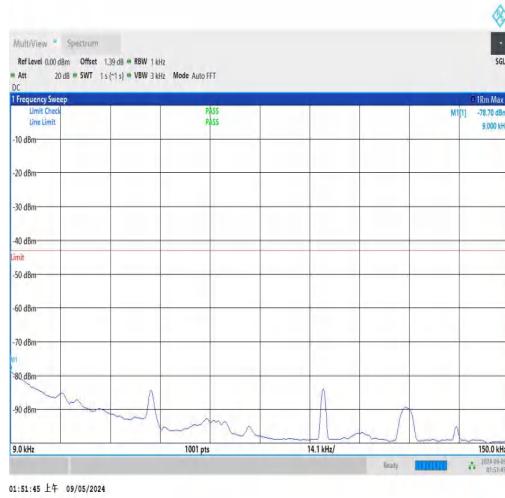
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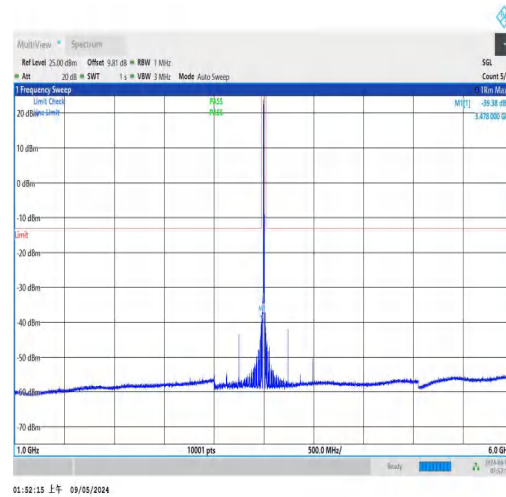
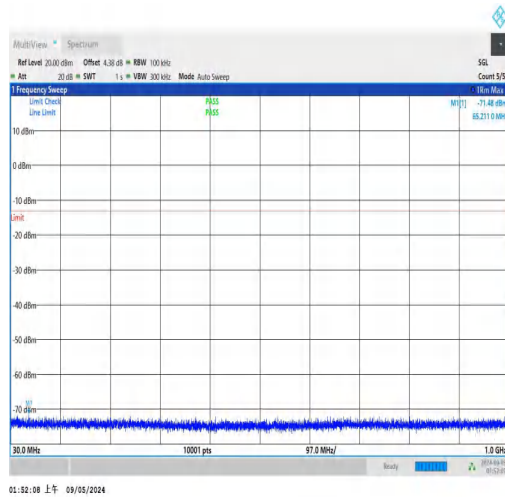
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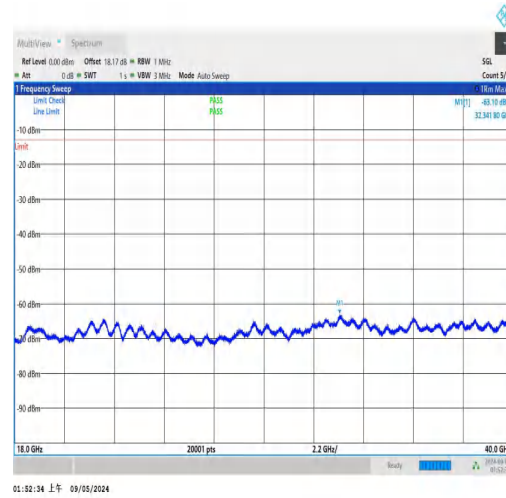
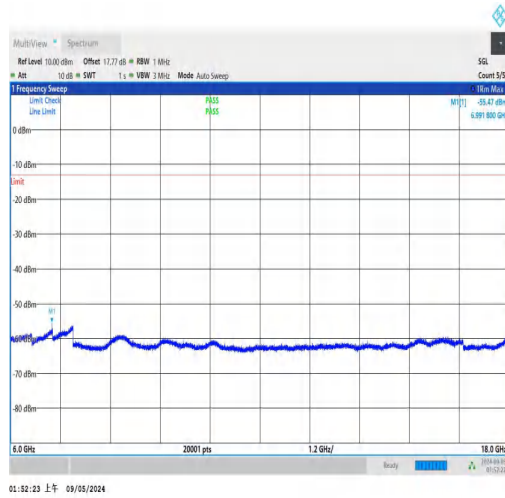
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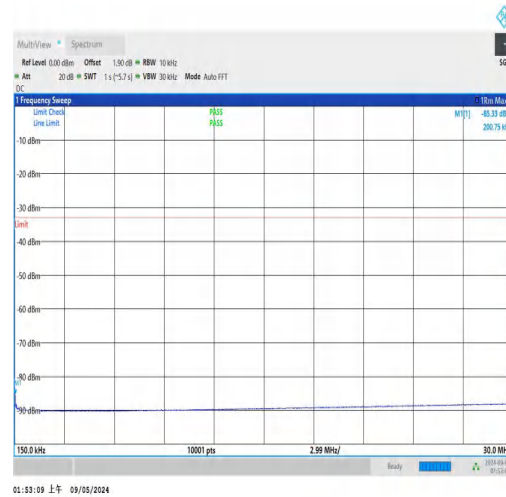
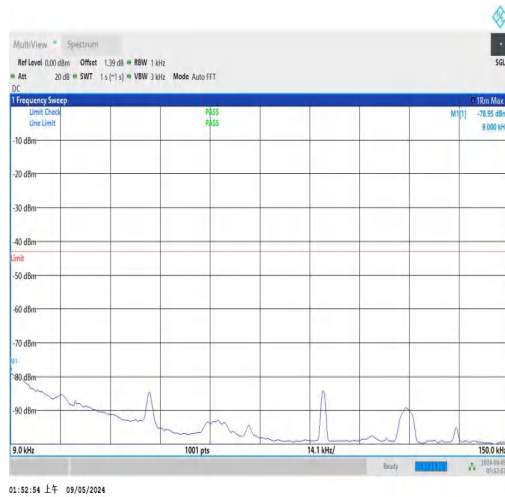
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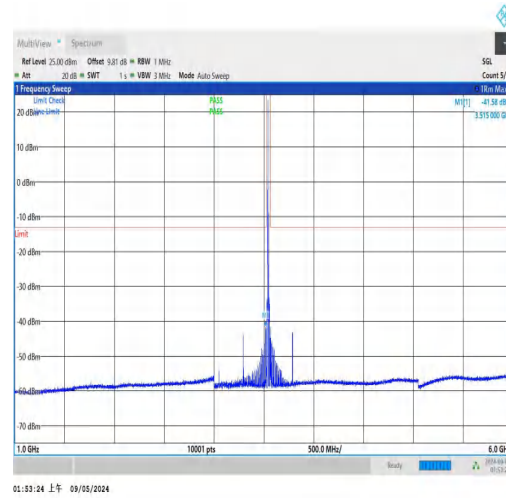
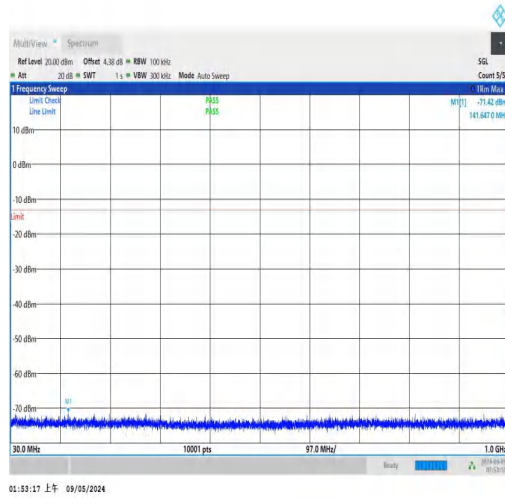
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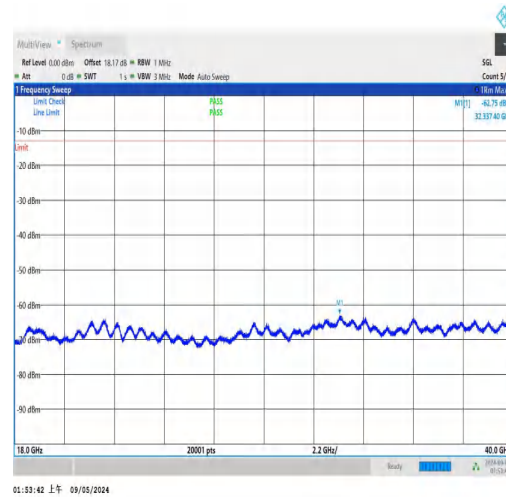
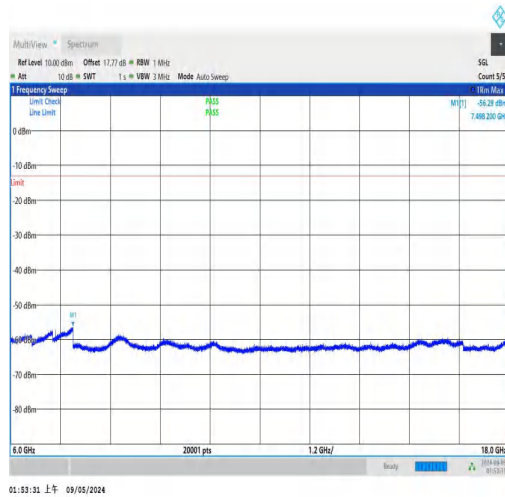
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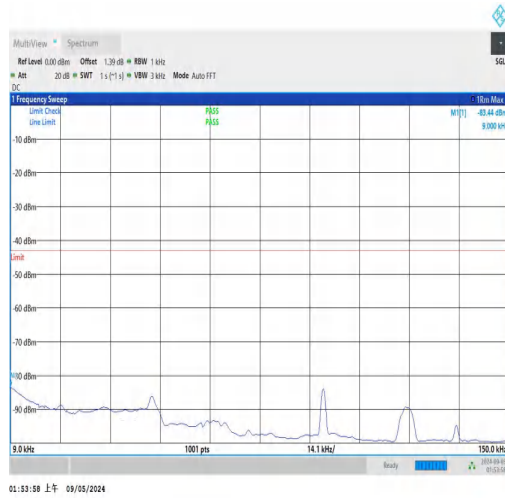
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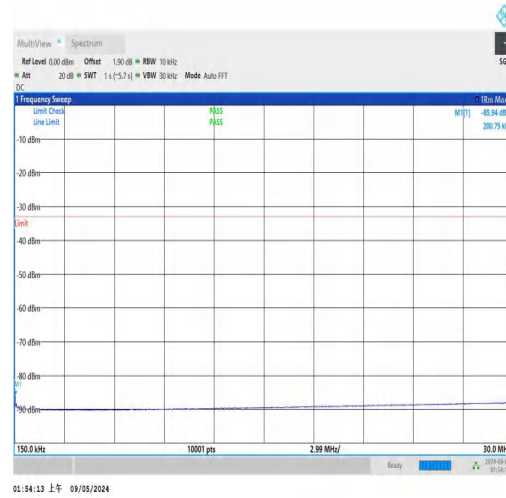


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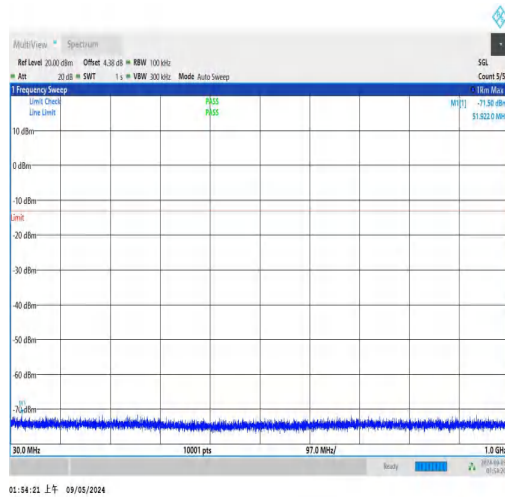
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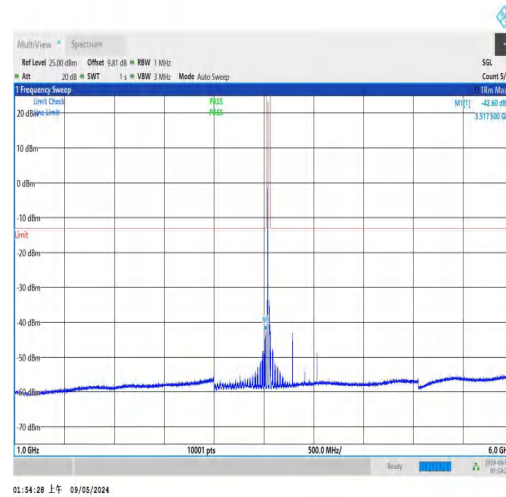
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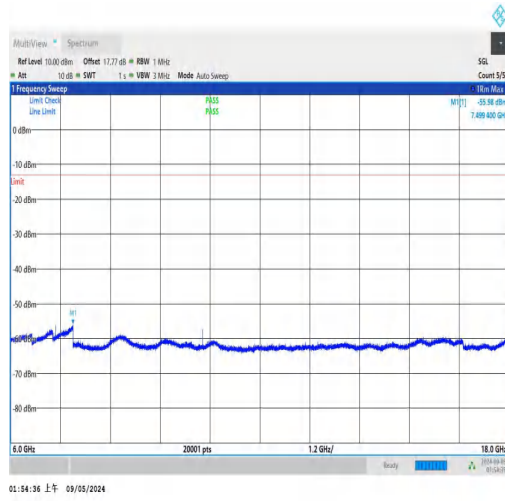
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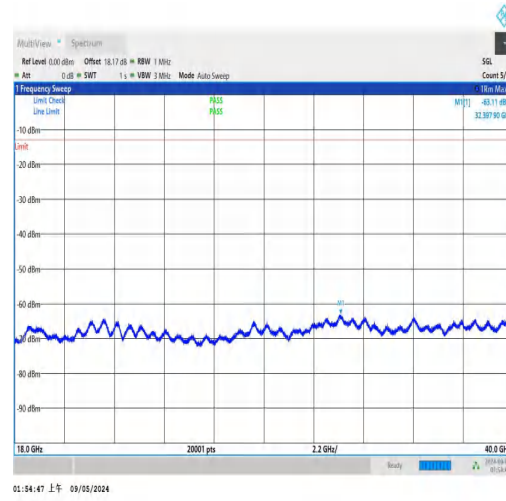
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NTNV_N78-3450-3550_PC3_30_10_H_TID2_N/A
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NTNV_N78-3450-3550_PC3_30_10_H_TID2_N/A
_18000_40000_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VN	NT	19.00	0.01	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VL	NT	3.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VH	NT	16.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VN	NT	10.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VL	NT	3.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VH	NT	6.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VN	NT	9.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VL	NT	19.00	0.01	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VH	NT	12.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VN	NT	5.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VL	NT	11.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VH	NT	4.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VN	NT	4.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VL	NT	4.00	0.00	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VH	NT	7.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VN	NT	6.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VL	NT	8.00	0.00	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VH	NT	2.00	0.00	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-30	9.000000	0.002571	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-20	19.000000	0.005429	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-10	2.000000	0.000571	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	0	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	10	5.000000	0.001429	PASS



N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	20	6.000000	0.001714	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	30	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	40	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	50	3.000000	0.000857	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-30	12.000000	0.003429	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-20	12.000000	0.003429	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-10	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	0	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	10	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	20	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	30	9.000000	0.002571	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	40	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	50	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-30	3.000000	0.000857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-20	12.000000	0.003429	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-10	9.000000	0.002571	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	0	19.000000	0.005429	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	10	9.000000	0.002571	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	20	3.000000	0.000857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	30	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	40	6.000000	0.001714	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	50	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-30	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-20	19.000000	0.005429	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-10	1.000000	0.000286	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	0	7.000000	0.002000	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	10	7.000000	0.002000	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	20	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	30	7.000000	0.002000	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	40	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	50	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-30	16.000000	0.004571	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-20	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-10	7.000000	0.002000	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	0	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	10	7.000000	0.002000	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	20	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	30	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	40	8.000000	0.002286	PASS



N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	50	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-30	15.000000	0.004286	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-20	17.000000	0.004857	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-10	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	0	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	10	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	20	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	30	8.000000	0.002286	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	40	18.000000	0.005143	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	50	8.000000	0.002286	PASS

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