

Effective (Isotropic) Radiated Power Output Data for ENDC

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Gain(dBi)	E(I)RP (dBm)	Limit (dBm)	Verdict
DC_2A-n41 A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Left	26.24	2.2	28.44	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Right	26.16	2.2	28.36	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+L	Inner_Full	26.32	2.2	28.52	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+L	Outer_Full	25.27	2.2	27.47	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Left	26.18	2.2	28.38	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Right	25.99	2.2	28.19	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+L	Inner_Full	26.27	2.2	28.47	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+L	Outer_Full	25.71	2.2	27.91	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Left	25.33	2.2	27.53	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Right	25.25	2.2	27.45	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+L	Inner_Full	25.31	2.2	27.51	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+L	Outer_Full	22.73	2.2	24.93	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Left	23.71	2.2	25.91	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Right	23.52	2.2	25.72	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+L	Inner_Full	23.76	2.2	25.96	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+L	Outer_Full	22.2	2.2	24.4	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Left	21.58	2.2	23.78	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Right	21.71	2.2	23.91	33	PASS

DC_2A-n41 A	30	5+20	DFT-256QA M	M+L	Inner_Full	21.63	2.2	23.83	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+L	Outer_Full	21.77	2.2	23.97	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Left	25.71	2.2	27.91	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Righ t	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+M	Inner_Full	25.7	2.2	27.9	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+M	Outer_Full	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Left	25.65	2.2	27.85	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Righ t	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.73	2.2	27.93	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+M	Outer_Full	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Left	24.66	2.2	26.86	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Righ t	24.55	2.2	26.75	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.76	2.2	26.96	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+M	Outer_Full	22.3	2.2	24.5	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Left	23.24	2.2	25.44	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Righ t	23.17	2.2	25.37	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+M	Inner_Full	23.23	2.2	25.43	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+M	Outer_Full	21.73	2.2	23.93	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+M	Inner_1RB_Left	21.33	2.2	23.53	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+M	Inner_1RB_Righ t	21.3	2.2	23.5	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+M	Inner_Full	21.26	2.2	23.46	33	PASS

DC_2A-n41 A	30	5+20	DFT-256QA M	M+M	Outer_Full	21.24	2.2	23.44	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Left	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Righ t	25.46	2.2	27.66	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+H	Inner_Full	25.52	2.2	27.72	33	PASS
DC_2A-n41 A	30	5+20	DFT-BPSK	M+H	Outer_Full	25.55	2.2	27.75	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Left	25.56	2.2	27.76	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Righ t	25.39	2.2	27.59	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+H	Inner_Full	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.98	2.2	27.18	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Left	24.61	2.2	26.81	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Righ t	24.43	2.2	26.63	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+H	Inner_Full	24.57	2.2	26.77	33	PASS
DC_2A-n41 A	30	5+20	DFT-16QAM	M+H	Outer_Full	21.96	2.2	24.16	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Left	22.98	2.2	25.18	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Righ t	22.96	2.2	25.16	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+H	Inner_Full	22.99	2.2	25.19	33	PASS
DC_2A-n41 A	30	5+20	DFT-64QAM	M+H	Outer_Full	21.48	2.2	23.68	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+H	Inner_1RB_Left	21.04	2.2	23.24	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+H	Inner_1RB_Righ t	20.95	2.2	23.15	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+H	Inner_Full	21.02	2.2	23.22	33	PASS
DC_2A-n41 A	30	5+20	DFT-256QA M	M+H	Outer_Full	21.13	2.2	23.33	33	PASS

DC_2A-n41 A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Left	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Right	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+L	Inner_Full	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+L	Outer_Full	25.24	2.2	27.44	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Left	25.56	2.2	27.76	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Right	25.61	2.2	27.81	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+L	Inner_Full	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+L	Outer_Full	25.16	2.2	27.36	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Left	24.59	2.2	26.79	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Right	24.58	2.2	26.78	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+L	Outer_Full	22.16	2.2	24.36	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Left	23.08	2.2	25.28	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Right	23.12	2.2	25.32	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+L	Inner_Full	23.15	2.2	25.35	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+L	Outer_Full	21.75	2.2	23.95	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Left	20.89	2.2	23.09	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Right	21.28	2.2	23.48	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QAM	M+L	Inner_Full	21.19	2.2	23.39	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QAM	M+L	Outer_Full	21.29	2.2	23.49	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Left	25.71	2.2	27.91	33	PASS

DC_2A-n41 A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Righ t	25.82	2.2	28.02	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+M	Inner_Full	25.74	2.2	27.94	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+M	Outer_Full	25.27	2.2	27.47	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Left	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Righ t	25.77	2.2	27.97	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.83	2.2	28.03	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+M	Outer_Full	25.34	2.2	27.54	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Left	24.73	2.2	26.93	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Righ t	24.92	2.2	27.12	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.74	2.2	26.94	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+M	Outer_Full	22.36	2.2	24.56	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Left	23.25	2.2	25.45	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Righ t	23.29	2.2	25.49	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+M	Inner_Full	23.25	2.2	25.45	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+M	Outer_Full	21.9	2.2	24.1	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+M	Inner_1RB_Left	21.39	2.2	23.59	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+M	Inner_1RB_Righ t	21.35	2.2	23.55	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+M	Inner_Full	21.32	2.2	23.52	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+M	Outer_Full	21.39	2.2	23.59	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Left	25.57	2.2	27.77	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Righ t	25.6	2.2	27.8	33	PASS

DC_2A-n41 A	30	5+30	DFT-BPSK	M+H	Inner_Full	25.56	2.2	27.76	33	PASS
DC_2A-n41 A	30	5+30	DFT-BPSK	M+H	Outer_Full	25.21	2.2	27.41	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Left	25.55	2.2	27.75	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Righ t	25.53	2.2	27.73	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+H	Inner_Full	25.55	2.2	27.75	33	PASS
DC_2A-n41 A	30	5+30	DFT-QPSK	M+H	Outer_Full	25.06	2.2	27.26	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Left	24.4	2.2	26.6	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Righ t	24.58	2.2	26.78	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+H	Inner_Full	24.53	2.2	26.73	33	PASS
DC_2A-n41 A	30	5+30	DFT-16QAM	M+H	Outer_Full	22.07	2.2	24.27	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Left	23.12	2.2	25.32	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Righ t	23.14	2.2	25.34	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+H	Inner_Full	23.02	2.2	25.22	33	PASS
DC_2A-n41 A	30	5+30	DFT-64QAM	M+H	Outer_Full	21.53	2.2	23.73	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+H	Inner_1RB_Left	21.12	2.2	23.32	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+H	Inner_1RB_Righ t	21.12	2.2	23.32	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+H	Inner_Full	21.14	2.2	23.34	33	PASS
DC_2A-n41 A	30	5+30	DFT-256QA M	M+H	Outer_Full	21.03	2.2	23.23	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Left	25.61	2.2	27.81	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Righ t	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+L	Inner_Full	25.49	2.2	27.69	33	PASS

DC_2A-n41 A	30	5+40	DFT-BPSK	M+L	Outer_Full	25.29	2.2	27.49	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Left	25.58	2.2	27.78	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Righ t	25.62	2.2	27.82	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+L	Inner_Full	25.44	2.2	27.64	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+L	Outer_Full	25.08	2.2	27.28	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Left	24.66	2.2	26.86	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Righ t	24.78	2.2	26.98	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+L	Outer_Full	22.18	2.2	24.38	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Left	23.05	2.2	25.25	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Righ t	23.09	2.2	25.29	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+L	Inner_Full	23.07	2.2	25.27	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+L	Outer_Full	21.78	2.2	23.98	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+L	Inner_1RB_Left	21.1	2.2	23.3	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+L	Inner_1RB_Righ t	21.24	2.2	23.44	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+L	Inner_Full	21.14	2.2	23.34	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+L	Outer_Full	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Left	25.81	2.2	28.01	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Righ t	25.68	2.2	27.88	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+M	Inner_Full	25.84	2.2	28.04	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+M	Outer_Full	25.26	2.2	27.46	33	PASS

DC_2A-n41 A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Left	25.66	2.2	27.86	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Righ t	25.7	2.2	27.9	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.8	2.2	28	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+M	Outer_Full	25.32	2.2	27.52	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Left	24.86	2.2	27.06	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Righ t	24.93	2.2	27.13	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.81	2.2	27.01	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+M	Outer_Full	22.34	2.2	24.54	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Left	23.24	2.2	25.44	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Righ t	23.28	2.2	25.48	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+M	Inner_Full	23.3	2.2	25.5	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+M	Outer_Full	21.83	2.2	24.03	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+M	Inner_1RB_Left	21.42	2.2	23.62	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+M	Inner_1RB_Righ t	21.36	2.2	23.56	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+M	Inner_Full	21.42	2.2	23.62	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+M	Outer_Full	21.41	2.2	23.61	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Left	25.6	2.2	27.8	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Righ t	25.59	2.2	27.79	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+H	Inner_Full	25.49	2.2	27.69	33	PASS
DC_2A-n41 A	30	5+40	DFT-BPSK	M+H	Outer_Full	25.2	2.2	27.4	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Left	25.52	2.2	27.72	33	PASS

DC_2A-n41 A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Righ t	25.59	2.2	27.79	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.55	2.2	27.75	33	PASS
DC_2A-n41 A	30	5+40	DFT-QPSK	M+H	Outer_Full	25.05	2.2	27.25	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Left	24.48	2.2	26.68	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Righ t	24.54	2.2	26.74	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+H	Inner_Full	24.56	2.2	26.76	33	PASS
DC_2A-n41 A	30	5+40	DFT-16QAM	M+H	Outer_Full	22.11	2.2	24.31	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Left	23.16	2.2	25.36	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Righ t	23.01	2.2	25.21	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+H	Inner_Full	23.12	2.2	25.32	33	PASS
DC_2A-n41 A	30	5+40	DFT-64QAM	M+H	Outer_Full	21.67	2.2	23.87	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+H	Inner_1RB_Left	21.16	2.2	23.36	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+H	Inner_1RB_Righ t	21.12	2.2	23.32	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+H	Inner_Full	21.22	2.2	23.42	33	PASS
DC_2A-n41 A	30	5+40	DFT-256QA M	M+H	Outer_Full	21.08	2.2	23.28	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Left	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Righ t	25.77	2.2	27.97	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+L	Inner_Full	25.83	2.2	28.03	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+L	Outer_Full	25.24	2.2	27.44	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Left	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Righ t	25.77	2.2	27.97	33	PASS

DC_2A-n41 A	30	5+50	DFT-QPSK	M+L	Inner_Full	25.77	2.2	27.97	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+L	Outer_Full	25.3	2.2	27.5	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Left	24.69	2.2	26.89	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Righ t	24.79	2.2	26.99	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+L	Inner_Full	24.74	2.2	26.94	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+L	Outer_Full	22.25	2.2	24.45	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Left	23.17	2.2	25.37	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Righ t	23.34	2.2	25.54	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+L	Inner_Full	23.29	2.2	25.49	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+L	Outer_Full	21.8	2.2	24	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+L	Inner_1RB_Left	21.23	2.2	23.43	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+L	Inner_1RB_Righ t	21.36	2.2	23.56	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+L	Inner_Full	21.27	2.2	23.47	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+L	Outer_Full	21.43	2.2	23.63	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Left	25.1	2.2	27.3	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Righ t	25.18	2.2	27.38	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+M	Inner_Full	25.59	2.2	27.79	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+M	Outer_Full	25.23	2.2	27.43	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Left	25.07	2.2	27.27	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Righ t	25.19	2.2	27.39	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+M	Inner_Full	25.61	2.2	27.81	33	PASS

DC_2A-n41 A	30	5+50	DFT-QPSK	M+M	Outer_Full	25.03	2.2	27.23	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Left	24.13	2.2	26.33	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Righ t	24.14	2.2	26.34	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+M	Inner_Full	24.59	2.2	26.79	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+M	Outer_Full	21.95	2.2	24.15	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Left	22.59	2.2	24.79	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Righ t	22.59	2.2	24.79	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+M	Inner_Full	23.25	2.2	25.45	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+M	Outer_Full	21.51	2.2	23.71	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+M	Inner_1RB_Left	20.86	2.2	23.06	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+M	Inner_1RB_Righ t	20.85	2.2	23.05	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+M	Inner_Full	21.26	2.2	23.46	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QA M	M+M	Outer_Full	21.07	2.2	23.27	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Left	25.17	2.2	27.37	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Righ t	25.08	2.2	27.28	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+H	Inner_Full	25.57	2.2	27.77	33	PASS
DC_2A-n41 A	30	5+50	DFT-BPSK	M+H	Outer_Full	25.4	2.2	27.6	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Left	25.09	2.2	27.29	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Righ t	25.06	2.2	27.26	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+H	Inner_Full	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+50	DFT-QPSK	M+H	Outer_Full	25.3	2.2	27.5	33	PASS

DC_2A-n41 A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Left	24.15	2.2	26.35	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Right	24.17	2.2	26.37	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+H	Inner_Full	24.47	2.2	26.67	33	PASS
DC_2A-n41 A	30	5+50	DFT-16QAM	M+H	Outer_Full	21.86	2.2	24.06	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Left	22.59	2.2	24.79	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Right	22.58	2.2	24.78	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+H	Inner_Full	23.02	2.2	25.22	33	PASS
DC_2A-n41 A	30	5+50	DFT-64QAM	M+H	Outer_Full	21.4	2.2	23.6	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Left	20.69	2.2	22.89	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Right	20.6	2.2	22.8	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QAM	M+H	Inner_Full	21	2.2	23.2	33	PASS
DC_2A-n41 A	30	5+50	DFT-256QAM	M+H	Outer_Full	20.88	2.2	23.08	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Left	25.17	2.2	27.37	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Right	25.06	2.2	27.26	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+L	Inner_Full	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+L	Outer_Full	25.22	2.2	27.42	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Left	25.18	2.2	27.38	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Right	25	2.2	27.2	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+L	Inner_Full	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+L	Outer_Full	25.12	2.2	27.32	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Left	24.13	2.2	26.33	33	PASS

DC_2A-n41 A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Righ t	24	2.2	26.2	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.64	2.2	26.84	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+L	Outer_Full	22.01	2.2	24.21	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Left	22.77	2.2	24.97	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Righ t	22.5	2.2	24.7	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+L	Inner_Full	23.29	2.2	25.49	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+L	Outer_Full	21.55	2.2	23.75	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+L	Inner_1RB_Left	20.79	2.2	22.99	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+L	Inner_1RB_Righ t	20.73	2.2	22.93	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+L	Inner_Full	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+L	Outer_Full	20.99	2.2	23.19	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Left	24.93	2.2	27.13	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Righ t	25.1	2.2	27.3	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+M	Inner_Full	25.55	2.2	27.75	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+M	Outer_Full	25.4	2.2	27.6	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Left	25.02	2.2	27.22	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Righ t	25.08	2.2	27.28	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+M	Inner_Full	25.53	2.2	27.73	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+M	Outer_Full	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Left	23.99	2.2	26.19	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Righ t	24.2	2.2	26.4	33	PASS

DC_2A-n41 A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+M	Outer_Full	21.91	2.2	24.11	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Left	22.41	2.2	24.61	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Righ t	22.63	2.2	24.83	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+M	Inner_Full	23.19	2.2	25.39	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+M	Outer_Full	21.43	2.2	23.63	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+M	Inner_1RB_Left	20.54	2.2	22.74	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+M	Inner_1RB_Righ t	20.61	2.2	22.81	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+M	Inner_Full	21.22	2.2	23.42	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+M	Outer_Full	20.9	2.2	23.1	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Left	24.8	2.2	27	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Righ t	24.93	2.2	27.13	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+H	Inner_Full	25.47	2.2	27.67	33	PASS
DC_2A-n41 A	30	5+60	DFT-BPSK	M+H	Outer_Full	25.31	2.2	27.51	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Left	24.88	2.2	27.08	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Righ t	24.89	2.2	27.09	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+H	Inner_Full	25.39	2.2	27.59	33	PASS
DC_2A-n41 A	30	5+60	DFT-QPSK	M+H	Outer_Full	25.28	2.2	27.48	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Left	23.86	2.2	26.06	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Righ t	23.91	2.2	26.11	33	PASS
DC_2A-n41 A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.42	2.2	26.62	33	PASS

DC_2A-n41 A	30	5+60	DFT-16QAM	M+H	Outer_Full	21.81	2.2	24.01	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Left	22.39	2.2	24.59	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Righ t	22.41	2.2	24.61	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+H	Inner_Full	22.91	2.2	25.11	33	PASS
DC_2A-n41 A	30	5+60	DFT-64QAM	M+H	Outer_Full	21.25	2.2	23.45	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+H	Inner_1RB_Left	20.45	2.2	22.65	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+H	Inner_1RB_Righ t	20.7	2.2	22.9	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+H	Inner_Full	20.93	2.2	23.13	33	PASS
DC_2A-n41 A	30	5+60	DFT-256QA M	M+H	Outer_Full	20.9	2.2	23.1	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Left	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Righ t	25.53	2.2	27.73	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+L	Inner_Full	25.71	2.2	27.91	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+L	Outer_Full	25.28	2.2	27.48	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Left	25.66	2.2	27.86	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Righ t	25.46	2.2	27.66	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+L	Inner_Full	25.68	2.2	27.88	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+L	Outer_Full	25.38	2.2	27.58	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Left	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Righ t	24.6	2.2	26.8	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+L	Inner_Full	24.65	2.2	26.85	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+L	Outer_Full	22.23	2.2	24.43	33	PASS

DC_2A-n41 A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Left	23.14	2.2	25.34	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Right	23.1	2.2	25.3	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+L	Inner_Full	23.23	2.2	25.43	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+L	Outer_Full	21.71	2.2	23.91	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Left	21.52	2.2	23.72	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Right	21.11	2.2	23.31	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QAM	M+L	Inner_Full	21.29	2.2	23.49	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QAM	M+L	Outer_Full	21.23	2.2	23.43	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Left	25.49	2.2	27.69	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Right	25.54	2.2	27.74	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+M	Inner_Full	25.59	2.2	27.79	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+M	Outer_Full	25.23	2.2	27.43	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Left	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Right	25.5	2.2	27.7	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+M	Inner_Full	25.68	2.2	27.88	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+M	Outer_Full	25.29	2.2	27.49	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Left	24.39	2.2	26.59	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Right	24.43	2.2	26.63	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+M	Inner_Full	24.67	2.2	26.87	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+M	Outer_Full	22.19	2.2	24.39	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Left	22.89	2.2	25.09	33	PASS

DC_2A-n41 A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Righ t	23.03	2.2	25.23	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+M	Inner_Full	23.09	2.2	25.29	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+M	Outer_Full	21.74	2.2	23.94	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+M	Inner_1RB_Left	21.09	2.2	23.29	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+M	Inner_1RB_Righ t	21	2.2	23.2	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+M	Inner_Full	21.28	2.2	23.48	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+M	Outer_Full	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Left	25.33	2.2	27.53	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Righ t	25.25	2.2	27.45	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+H	Inner_Full	25.49	2.2	27.69	33	PASS
DC_2A-n41 A	30	5+70	DFT-BPSK	M+H	Outer_Full	25.44	2.2	27.64	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Left	25.32	2.2	27.52	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Righ t	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+H	Inner_Full	25.38	2.2	27.58	33	PASS
DC_2A-n41 A	30	5+70	DFT-QPSK	M+H	Outer_Full	25.24	2.2	27.44	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Left	24.41	2.2	26.61	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Righ t	24.48	2.2	26.68	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+H	Inner_Full	24.48	2.2	26.68	33	PASS
DC_2A-n41 A	30	5+70	DFT-16QAM	M+H	Outer_Full	22.06	2.2	24.26	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Left	22.86	2.2	25.06	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Righ t	22.72	2.2	24.92	33	PASS

DC_2A-n41 A	30	5+70	DFT-64QAM	M+H	Inner_Full	23.01	2.2	25.21	33	PASS
DC_2A-n41 A	30	5+70	DFT-64QAM	M+H	Outer_Full	21.5	2.2	23.7	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+H	Inner_1RB_Left	20.92	2.2	23.12	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+H	Inner_1RB_Righ t	20.83	2.2	23.03	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+H	Inner_Full	20.98	2.2	23.18	33	PASS
DC_2A-n41 A	30	5+70	DFT-256QA M	M+H	Outer_Full	20.88	2.2	23.08	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Left	25.6	2.2	27.8	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Righ t	25.53	2.2	27.73	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+L	Inner_Full	25.57	2.2	27.77	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+L	Outer_Full	25.21	2.2	27.41	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Left	25.66	2.2	27.86	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Righ t	25.47	2.2	27.67	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+L	Inner_Full	25.68	2.2	27.88	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+L	Outer_Full	25.15	2.2	27.35	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Left	24.68	2.2	26.88	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Righ t	24.51	2.2	26.71	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.68	2.2	26.88	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+L	Outer_Full	22.12	2.2	24.32	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Left	23.17	2.2	25.37	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Righ t	23.12	2.2	25.32	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+L	Inner_Full	23.14	2.2	25.34	33	PASS

DC_2A-n41 A	30	5+80	DFT-64QAM	M+L	Outer_Full	21.65	2.2	23.85	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+L	Inner_1RB_Left	21.38	2.2	23.58	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+L	Inner_1RB_Righ t	21.16	2.2	23.36	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+L	Inner_Full	21.2	2.2	23.4	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+L	Outer_Full	21.23	2.2	23.43	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Left	25.34	2.2	27.54	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Righ t	25.6	2.2	27.8	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+M	Inner_Full	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+M	Outer_Full	25.28	2.2	27.48	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Left	25.32	2.2	27.52	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Righ t	25.54	2.2	27.74	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+M	Inner_Full	25.58	2.2	27.78	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+M	Outer_Full	25.01	2.2	27.21	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Left	24.4	2.2	26.6	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Righ t	24.68	2.2	26.88	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+M	Outer_Full	22.24	2.2	24.44	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Left	22.94	2.2	25.14	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Righ t	23.09	2.2	25.29	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+M	Inner_Full	23.17	2.2	25.37	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+M	Outer_Full	21.76	2.2	23.96	33	PASS

DC_2A-n41 A	30	5+80	DFT-256QA M	M+M	Inner_1RB_Left	21.01	2.2	23.21	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+M	Inner_1RB_Righ t	21.2	2.2	23.4	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+M	Inner_Full	21.16	2.2	23.36	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+M	Outer_Full	21.23	2.2	23.43	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Left	25.37	2.2	27.57	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Righ t	25.38	2.2	27.58	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+H	Inner_Full	25.54	2.2	27.74	33	PASS
DC_2A-n41 A	30	5+80	DFT-BPSK	M+H	Outer_Full	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Left	25.36	2.2	27.56	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Righ t	25.38	2.2	27.58	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+H	Inner_Full	25.54	2.2	27.74	33	PASS
DC_2A-n41 A	30	5+80	DFT-QPSK	M+H	Outer_Full	25.07	2.2	27.27	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Left	24.35	2.2	26.55	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Righ t	24.33	2.2	26.53	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.45	2.2	26.65	33	PASS
DC_2A-n41 A	30	5+80	DFT-16QAM	M+H	Outer_Full	21.99	2.2	24.19	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Left	22.93	2.2	25.13	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Righ t	22.84	2.2	25.04	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+H	Inner_Full	23.07	2.2	25.27	33	PASS
DC_2A-n41 A	30	5+80	DFT-64QAM	M+H	Outer_Full	21.55	2.2	23.75	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+H	Inner_1RB_Left	20.95	2.2	23.15	33	PASS

DC_2A-n41 A	30	5+80	DFT-256QA M	M+H	Inner_1RB_Righ t	21.02	2.2	23.22	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+H	Inner_Full	21.08	2.2	23.28	33	PASS
DC_2A-n41 A	30	5+80	DFT-256QA M	M+H	Outer_Full	21.11	2.2	23.31	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Left	25.62	2.2	27.82	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Righ t	25.7	2.2	27.9	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+L	Inner_Full	25.74	2.2	27.94	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+L	Outer_Full	25.2	2.2	27.4	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Left	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Righ t	25.78	2.2	27.98	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+L	Inner_Full	25.83	2.2	28.03	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+L	Outer_Full	25.27	2.2	27.47	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Left	24.65	2.2	26.85	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Righ t	25.02	2.2	27.22	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.75	2.2	26.95	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+L	Outer_Full	22.31	2.2	24.51	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Left	23.23	2.2	25.43	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Righ t	23.28	2.2	25.48	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+L	Inner_Full	23.24	2.2	25.44	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+L	Outer_Full	21.81	2.2	24.01	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+L	Inner_1RB_Left	21.26	2.2	23.46	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+L	Inner_1RB_Righ t	21.24	2.2	23.44	33	PASS

DC_2A-n41 A	30	5+90	DFT-256QA M	M+L	Inner_Full	21.22	2.2	23.42	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+L	Outer_Full	21.22	2.2	23.42	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Left	25.47	2.2	27.67	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Righ t	25.74	2.2	27.94	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+M	Inner_Full	25.7	2.2	27.9	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+M	Outer_Full	25.24	2.2	27.44	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Left	25.46	2.2	27.66	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Righ t	25.69	2.2	27.89	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+M	Inner_Full	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+M	Outer_Full	25.12	2.2	27.32	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Left	24.42	2.2	26.62	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Righ t	24.63	2.2	26.83	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.67	2.2	26.87	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+M	Outer_Full	22.2	2.2	24.4	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Left	22.97	2.2	25.17	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Righ t	23.23	2.2	25.43	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+M	Inner_Full	23.25	2.2	25.45	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+M	Outer_Full	21.75	2.2	23.95	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+M	Inner_1RB_Left	21.09	2.2	23.29	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+M	Inner_1RB_Righ t	21.4	2.2	23.6	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+M	Inner_Full	21.24	2.2	23.44	33	PASS

DC_2A-n41 A	30	5+90	DFT-256QA M	M+M	Outer_Full	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Left	25.49	2.2	27.69	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Righ t	25.6	2.2	27.8	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+H	Inner_Full	25.58	2.2	27.78	33	PASS
DC_2A-n41 A	30	5+90	DFT-BPSK	M+H	Outer_Full	25.26	2.2	27.46	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Left	25.34	2.2	27.54	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Righ t	25.53	2.2	27.73	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.57	2.2	27.77	33	PASS
DC_2A-n41 A	30	5+90	DFT-QPSK	M+H	Outer_Full	25.06	2.2	27.26	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Left	24.42	2.2	26.62	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Righ t	24.64	2.2	26.84	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.58	2.2	26.78	33	PASS
DC_2A-n41 A	30	5+90	DFT-16QAM	M+H	Outer_Full	22.16	2.2	24.36	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Left	23.02	2.2	25.22	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Righ t	23.09	2.2	25.29	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+H	Inner_Full	23.1	2.2	25.3	33	PASS
DC_2A-n41 A	30	5+90	DFT-64QAM	M+H	Outer_Full	21.69	2.2	23.89	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+H	Inner_1RB_Left	21.02	2.2	23.22	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+H	Inner_1RB_Righ t	21.16	2.2	23.36	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+H	Inner_Full	21.2	2.2	23.4	33	PASS
DC_2A-n41 A	30	5+90	DFT-256QA M	M+H	Outer_Full	21.17	2.2	23.37	33	PASS

DC_2A-n41 A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Left	25.78	2.2	27.98	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Right	25.84	2.2	28.04	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+L	Inner_Full	25.69	2.2	27.89	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+L	Outer_Full	25.34	2.2	27.54	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Left	25.66	2.2	27.86	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Right	25.84	2.2	28.04	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+L	Inner_Full	25.66	2.2	27.86	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+L	Outer_Full	25.37	2.2	27.57	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Left	24.8	2.2	27	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Right	24.82	2.2	27.02	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+L	Inner_Full	24.59	2.2	26.79	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+L	Outer_Full	22.28	2.2	24.48	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Left	23.24	2.2	25.44	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Right	23.35	2.2	25.55	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+L	Inner_Full	23.25	2.2	25.45	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+L	Outer_Full	21.69	2.2	23.89	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Left	21.41	2.2	23.61	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Right	21.37	2.2	23.57	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QAM	M+L	Inner_Full	21.33	2.2	23.53	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QAM	M+L	Outer_Full	21.27	2.2	23.47	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+M	Inner_1RB_Left	25.55	2.2	27.75	33	PASS

DC_2A-n41 A	30	5+100	DFT-BPSK	M+M	Inner_1RB_Righ t	25.67	2.2	27.87	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+M	Inner_Full	25.68	2.2	27.88	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+M	Outer_Full	25.31	2.2	27.51	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+M	Inner_1RB_Left	25.51	2.2	27.71	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+M	Inner_1RB_Righ t	25.78	2.2	27.98	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+M	Inner_Full	25.64	2.2	27.84	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+M	Outer_Full	25.13	2.2	27.33	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+M	Inner_1RB_Left	24.7	2.2	26.9	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+M	Inner_1RB_Righ t	24.78	2.2	26.98	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+M	Inner_Full	24.65	2.2	26.85	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+M	Outer_Full	22.21	2.2	24.41	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+M	Inner_1RB_Left	23.03	2.2	25.23	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+M	Inner_1RB_Righ t	23.31	2.2	25.51	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+M	Inner_Full	23.22	2.2	25.42	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+M	Outer_Full	21.69	2.2	23.89	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+M	Inner_1RB_Left	21.25	2.2	23.45	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+M	Inner_1RB_Righ t	21.42	2.2	23.62	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+M	Inner_Full	21.25	2.2	23.45	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+M	Outer_Full	21.12	2.2	23.32	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+H	Inner_1RB_Left	25.6	2.2	27.8	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+H	Inner_1RB_Righ t	25.58	2.2	27.78	33	PASS

DC_2A-n41 A	30	5+100	DFT-BPSK	M+H	Inner_Full	25.61	2.2	27.81	33	PASS
DC_2A-n41 A	30	5+100	DFT-BPSK	M+H	Outer_Full	25.38	2.2	27.58	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+H	Inner_1RB_Left	25.49	2.2	27.69	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+H	Inner_1RB_Righ t	25.58	2.2	27.78	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+H	Inner_Full	25.63	2.2	27.83	33	PASS
DC_2A-n41 A	30	5+100	DFT-QPSK	M+H	Outer_Full	25.12	2.2	27.32	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+H	Inner_1RB_Left	24.62	2.2	26.82	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+H	Inner_1RB_Righ t	24.46	2.2	26.66	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+H	Inner_Full	24.54	2.2	26.74	33	PASS
DC_2A-n41 A	30	5+100	DFT-16QAM	M+H	Outer_Full	22.1	2.2	24.3	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+H	Inner_1RB_Left	23.21	2.2	25.41	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+H	Inner_1RB_Righ t	23.05	2.2	25.25	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+H	Inner_Full	23.1	2.2	25.3	33	PASS
DC_2A-n41 A	30	5+100	DFT-64QAM	M+H	Outer_Full	21.82	2.2	24.02	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+H	Inner_1RB_Left	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+H	Inner_1RB_Righ t	21.12	2.2	23.32	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+H	Inner_Full	21.15	2.2	23.35	33	PASS
DC_2A-n41 A	30	5+100	DFT-256QA M	M+H	Outer_Full	21.12	2.2	23.32	33	PASS

Peak-to-Average Ratio for ENDC

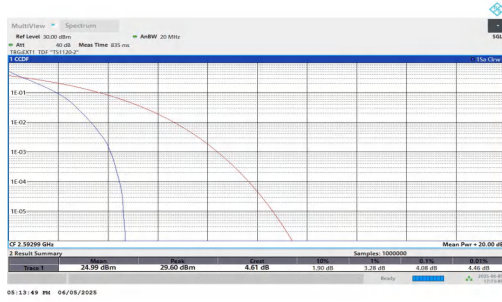
Peak-to-Average Ratio(CCDF)

Test Result

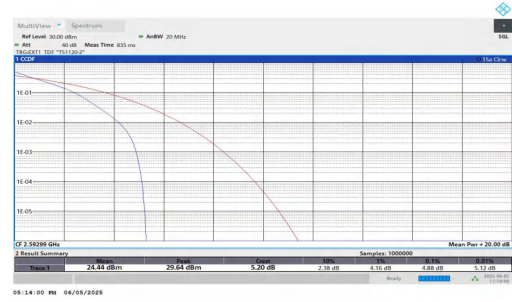
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
DC_2A-n41A	30	5+20	DFT-BPSK	M+M	Outer_Full	4.08	≤13	PASS
DC_2A-n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	4.88	≤13	PASS
DC_2A-n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	5.66	≤13	PASS
DC_2A-n41A	30	5+20	DFT-64QAM	M+M	Outer_Full	6.16	≤13	PASS
DC_2A-n41A	30	5+20	DFT-256QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_2A-n41A	30	5+30	DFT-BPSK	M+M	Outer_Full	3.78	≤13	PASS
DC_2A-n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	4.8	≤13	PASS
DC_2A-n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	5.46	≤13	PASS
DC_2A-n41A	30	5+30	DFT-64QAM	M+M	Outer_Full	5.96	≤13	PASS
DC_2A-n41A	30	5+30	DFT-256QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_2A-n41A	30	5+40	DFT-BPSK	M+M	Outer_Full	4.06	≤13	PASS
DC_2A-n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	4.66	≤13	PASS
DC_2A-n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	5.54	≤13	PASS
DC_2A-n41A	30	5+40	DFT-64QAM	M+M	Outer_Full	6.1	≤13	PASS
DC_2A-n41A	30	5+40	DFT-256QAM	M+M	Outer_Full	6.4	≤13	PASS
DC_2A-n41A	30	5+50	DFT-BPSK	M+M	Outer_Full	4.26	≤13	PASS
DC_2A-n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	4.86	≤13	PASS
DC_2A-n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	5.52	≤13	PASS
DC_2A-n41A	30	5+50	DFT-64QAM	M+M	Outer_Full	5.92	≤13	PASS
DC_2A-n41A	30	5+50	DFT-256QAM	M+M	Outer_Full	6.48	≤13	PASS
DC_2A-n41A	30	5+60	DFT-BPSK	M+M	Outer_Full	4.12	≤13	PASS
DC_2A-n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	4.86	≤13	PASS
DC_2A-n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	5.82	≤13	PASS
DC_2A-n41A	30	5+60	DFT-64QAM	M+M	Outer_Full	6.18	≤13	PASS
DC_2A-n41A	30	5+60	DFT-256QAM	M+M	Outer_Full	6.38	≤13	PASS
DC_2A-n41A	30	5+70	DFT-BPSK	M+M	Outer_Full	4.2	≤13	PASS
DC_2A-n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	4.84	≤13	PASS
DC_2A-n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	5.7	≤13	PASS
DC_2A-n41A	30	5+70	DFT-64QAM	M+M	Outer_Full	6.12	≤13	PASS
DC_2A-n41A	30	5+70	DFT-256QAM	M+M	Outer_Full	6.32	≤13	PASS
DC_2A-n41A	30	5+80	DFT-BPSK	M+M	Outer_Full	4.5	≤13	PASS
DC_2A-n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	5.12	≤13	PASS
DC_2A-n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_2A-n41A	30	5+80	DFT-64QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_2A-n41A	30	5+80	DFT-256QAM	M+M	Outer_Full	6.52	≤13	PASS
DC_2A-n41A	30	5+90	DFT-BPSK	M+M	Outer_Full	4.32	≤13	PASS

DC_2A-n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	5.06	≤13	PASS
DC_2A-n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	5.88	≤13	PASS
DC_2A-n41A	30	5+90	DFT-64QAM	M+M	Outer_Full	6.26	≤13	PASS
DC_2A-n41A	30	5+90	DFT-256QAM	M+M	Outer_Full	6.5	≤13	PASS
DC_2A-n41A	30	5+100	DFT-BPSK	M+M	Outer_Full	4.46	≤13	PASS
DC_2A-n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.02	≤13	PASS
DC_2A-n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	5.8	≤13	PASS
DC_2A-n41A	30	5+100	DFT-64QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_2A-n41A	30	5+100	DFT-256QAM	M+M	Outer_Full	6.56	≤13	PASS

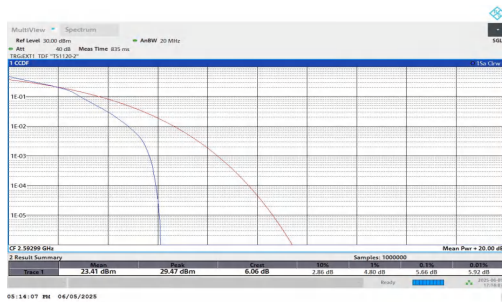
Test Graphs



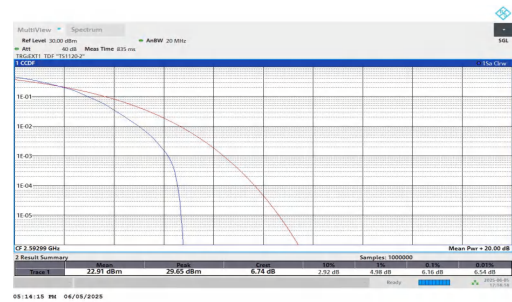
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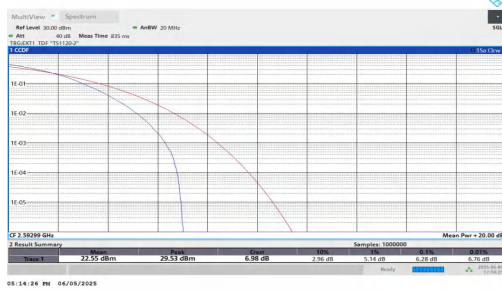
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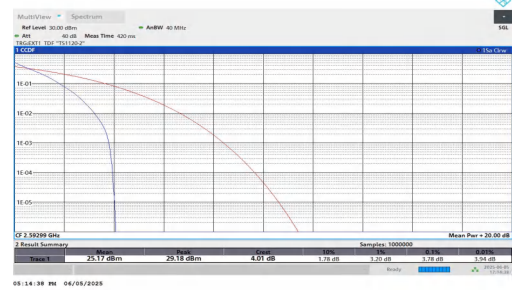
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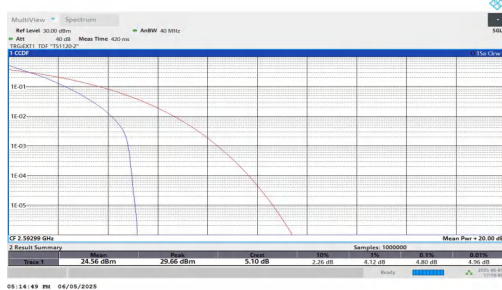
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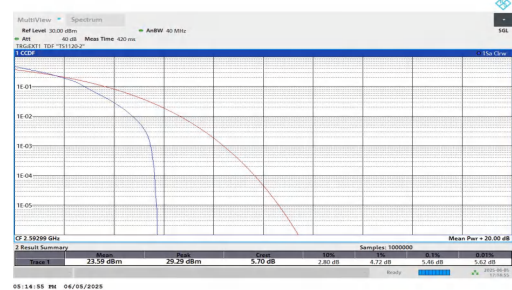
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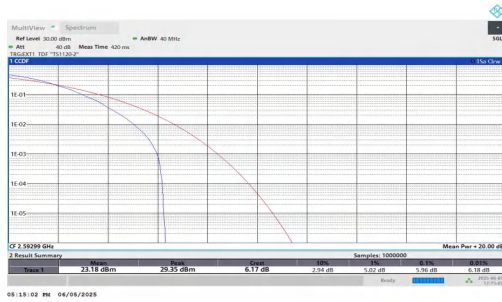
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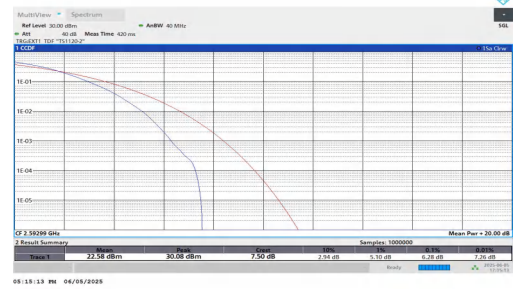
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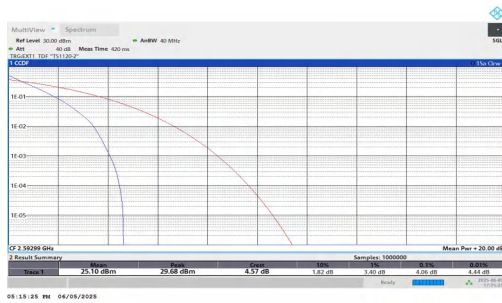
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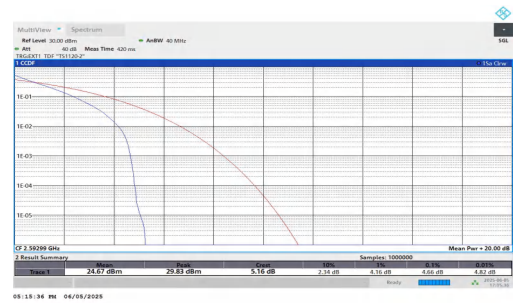
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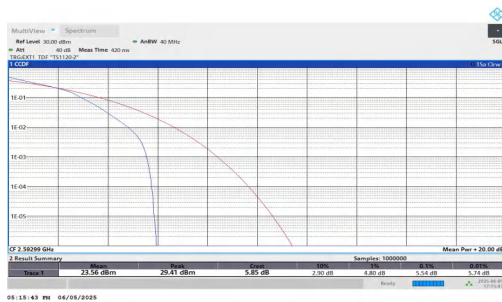
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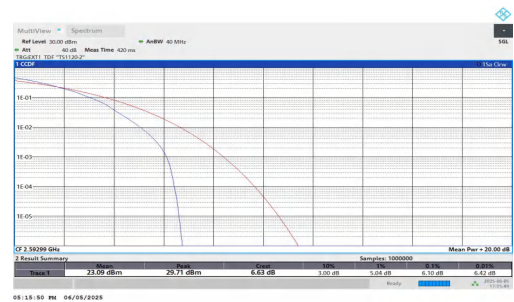
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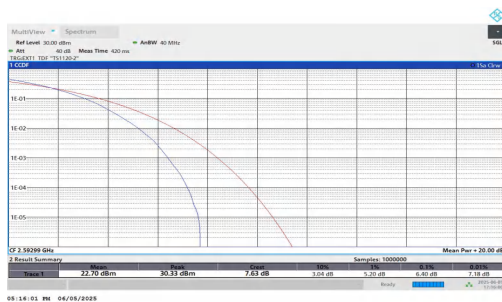
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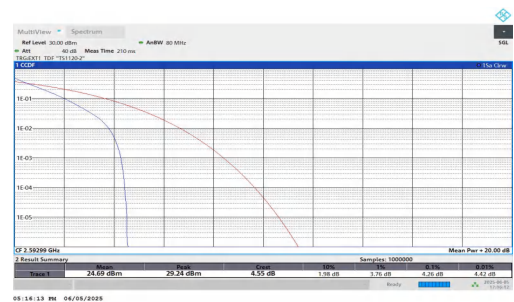
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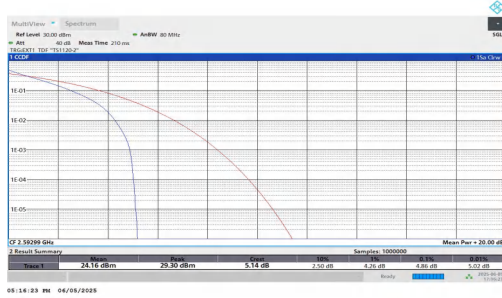
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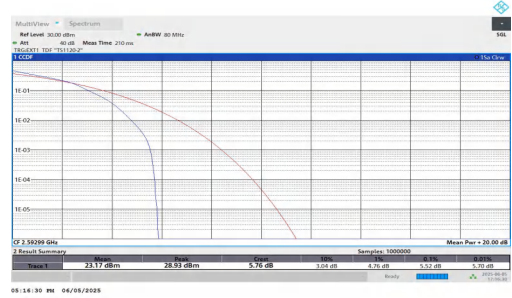
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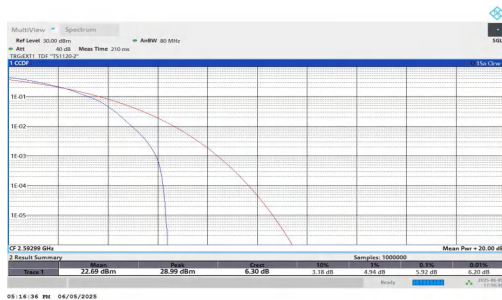
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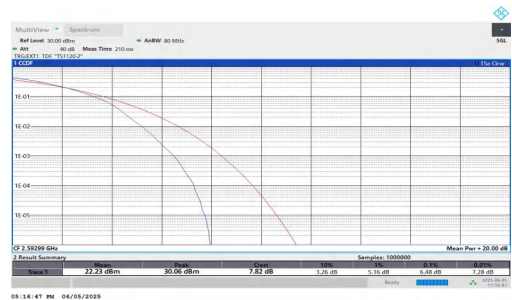
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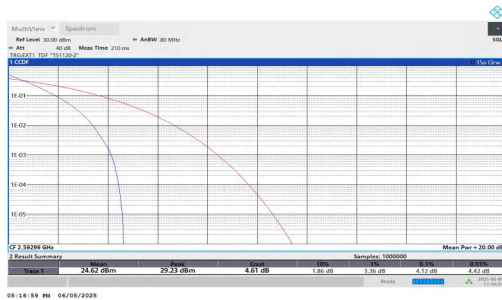
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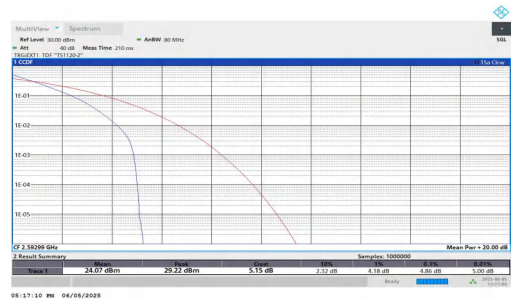
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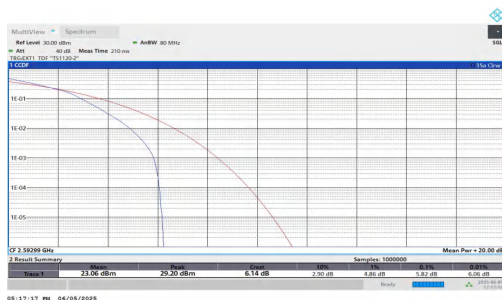
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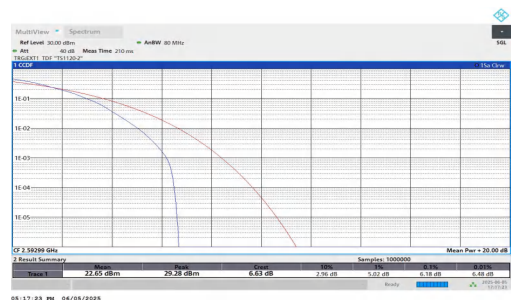
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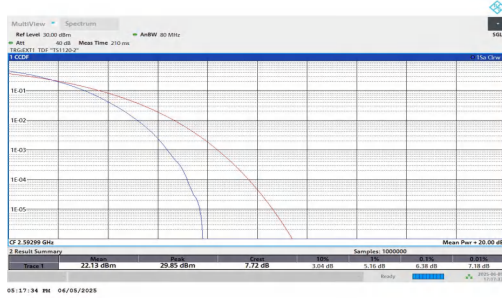
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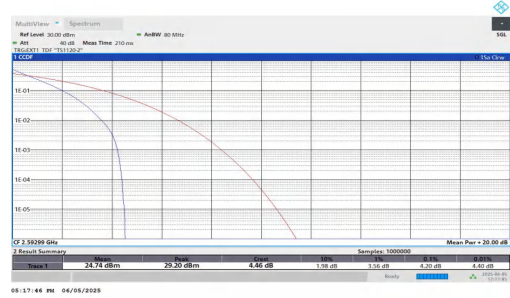
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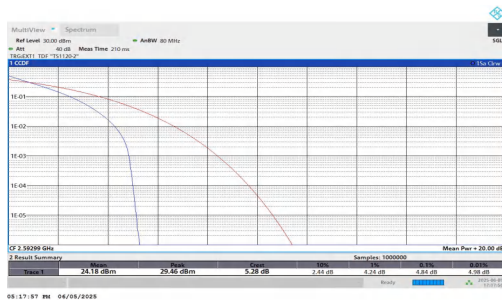
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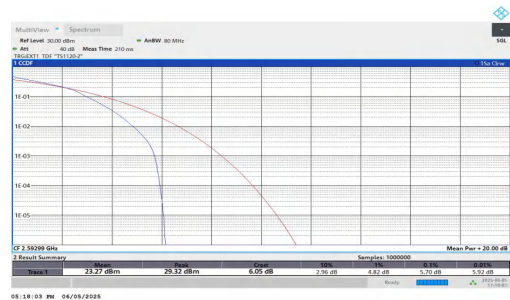
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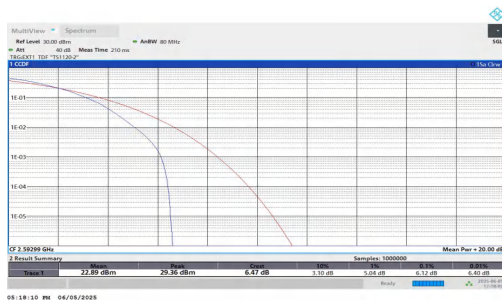
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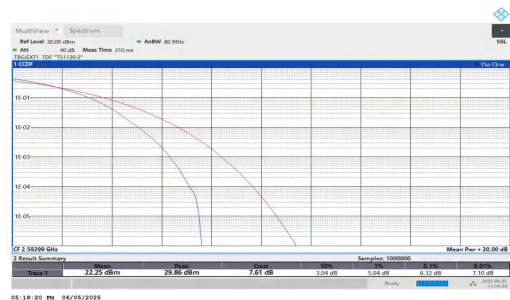
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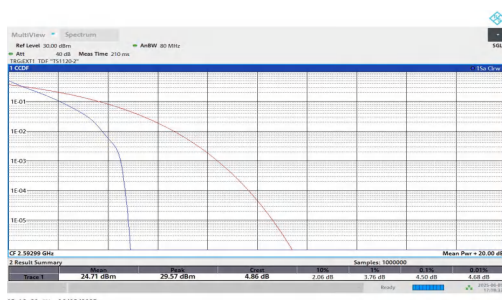
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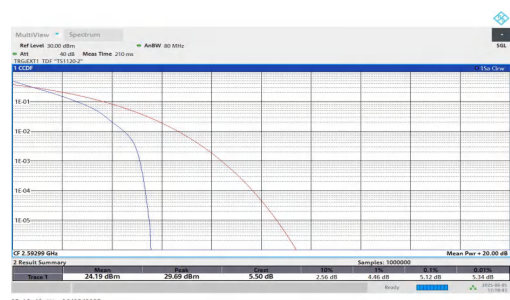
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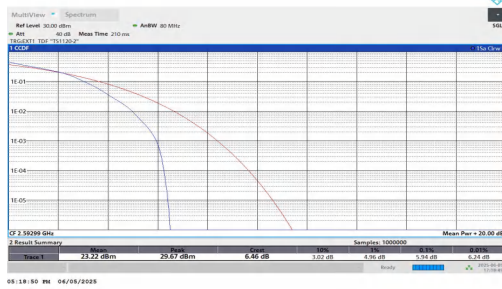
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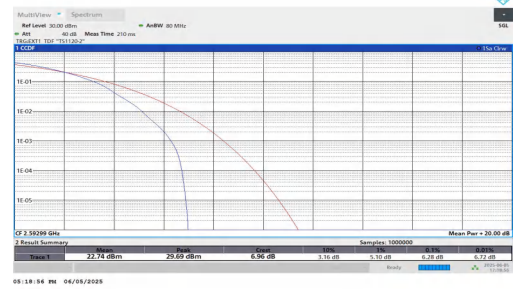
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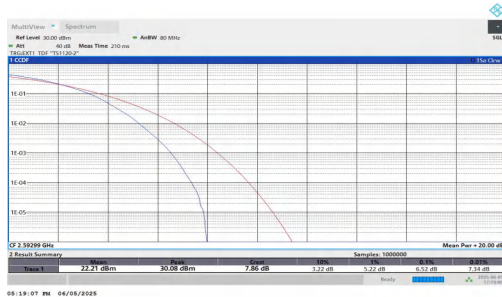
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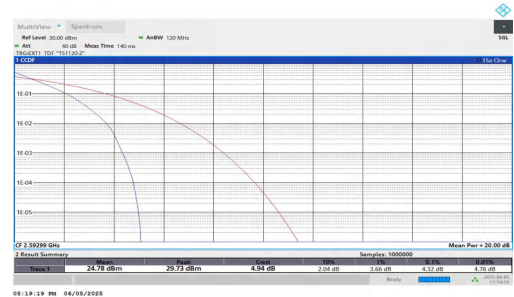
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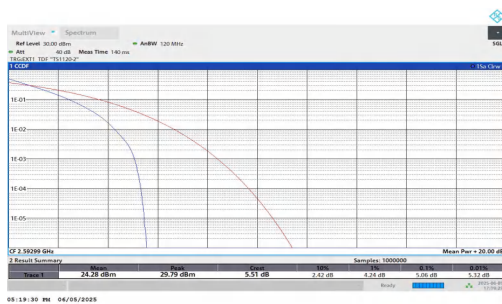
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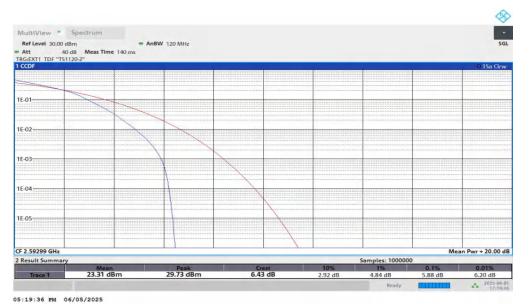
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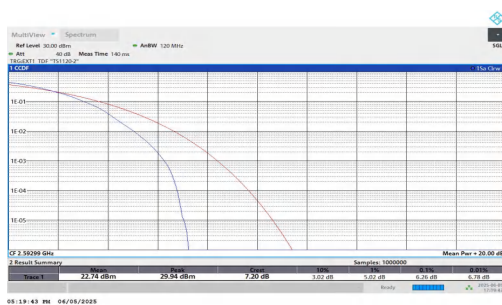
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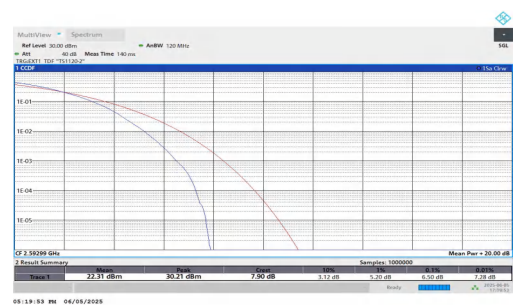
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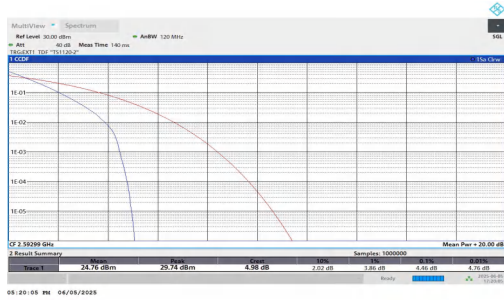
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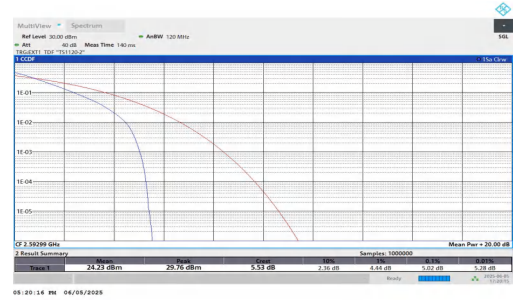
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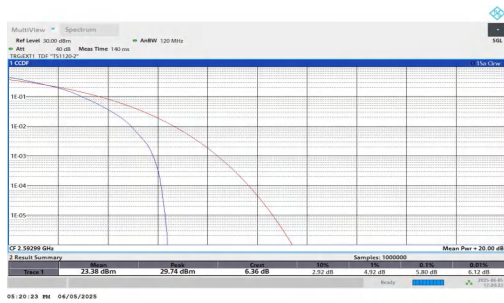
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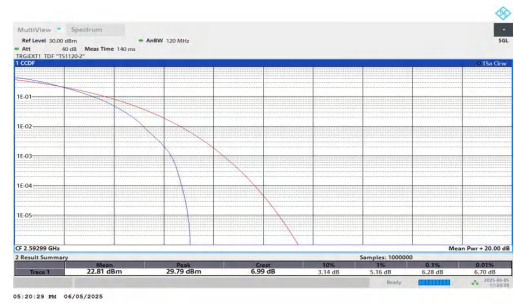
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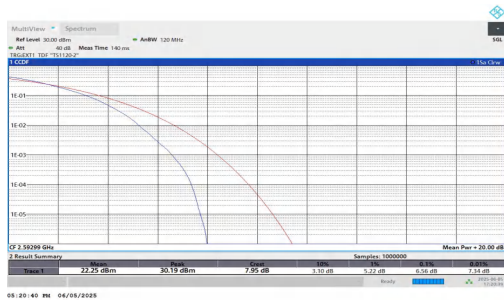
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NTNV-DC_2A-n41A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-64QA M-Outer_Full-PASS



NTNV-DC_2A-n41A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-256QA M-Outer_Full-PASS

26dB Bandwidth and Occupied Bandwidth for ENDC

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A-n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18.227	19.77	PASS
DC_2A-n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18.279	19.78	PASS
DC_2A-n41A	30	5+20	CP-64QAM	M+M	Outer_Full	18.259	19.80	PASS
DC_2A-n41A	30	5+20	CP-256QAM	M+M	Outer_Full	18.288	19.78	PASS
DC_2A-n41A	30	5+30	CP-QPSK	M+M	Outer_Full	27.867	29.89	PASS
DC_2A-n41A	30	5+30	CP-16QAM	M+M	Outer_Full	27.908	29.73	PASS
DC_2A-n41A	30	5+30	CP-64QAM	M+M	Outer_Full	27.959	29.77	PASS
DC_2A-n41A	30	5+30	CP-256QAM	M+M	Outer_Full	27.869	29.61	PASS
DC_2A-n41A	30	5+40	CP-QPSK	M+M	Outer_Full	37.872	40.00	PASS
DC_2A-n41A	30	5+40	CP-16QAM	M+M	Outer_Full	37.825	39.94	PASS
DC_2A-n41A	30	5+40	CP-64QAM	M+M	Outer_Full	37.968	40.03	PASS
DC_2A-n41A	30	5+40	CP-256QAM	M+M	Outer_Full	37.847	39.92	PASS
DC_2A-n41A	30	5+50	CP-QPSK	M+M	Outer_Full	47.531	49.94	PASS
DC_2A-n41A	30	5+50	CP-16QAM	M+M	Outer_Full	47.594	49.77	PASS
DC_2A-n41A	30	5+50	CP-64QAM	M+M	Outer_Full	47.513	49.87	PASS
DC_2A-n41A	30	5+50	CP-256QAM	M+M	Outer_Full	47.465	49.91	PASS
DC_2A-n41A	30	5+60	CP-QPSK	M+M	Outer_Full	57.921	60.60	PASS
DC_2A-n41A	30	5+60	CP-16QAM	M+M	Outer_Full	57.899	60.52	PASS
DC_2A-n41A	30	5+60	CP-64QAM	M+M	Outer_Full	57.943	60.62	PASS
DC_2A-n41A	30	5+60	CP-256QAM	M+M	Outer_Full	57.906	60.69	PASS
DC_2A-n41A	30	5+70	CP-QPSK	M+M	Outer_Full	67.565	74.51	PASS
DC_2A-n41A	30	5+70	CP-16QAM	M+M	Outer_Full	67.566	75.47	PASS
DC_2A-n41A	30	5+70	CP-64QAM	M+M	Outer_Full	67.635	80.70	PASS
DC_2A-n41A	30	5+70	CP-256QAM	M+M	Outer_Full	67.566	70.55	PASS
DC_2A-n41A	30	5+80	CP-QPSK	M+M	Outer_Full	77.545	80.92	PASS
DC_2A-n41A	30	5+80	CP-16QAM	M+M	Outer_Full	77.477	80.94	PASS
DC_2A-n41A	30	5+80	CP-64QAM	M+M	Outer_Full	77.575	80.91	PASS
DC_2A-n41A	30	5+80	CP-256QAM	M+M	Outer_Full	77.525	80.90	PASS
DC_2A-n41A	30	5+90	CP-QPSK	M+M	Outer_Full	87.542	91.26	PASS
DC_2A-n41A	30	5+90	CP-16QAM	M+M	Outer_Full	87.425	91.20	PASS
DC_2A-n41A	30	5+90	CP-64QAM	M+M	Outer_Full	87.670	91.28	PASS
DC_2A-n41A	30	5+90	CP-256QAM	M+M	Outer_Full	87.547	91.22	PASS
DC_2A-n41A	30	5+100	CP-QPSK	M+M	Outer_Full	97.688	101.8	PASS
DC_2A-n41A	30	5+100	CP-16QAM	M+M	Outer_Full	97.677	101.8	PASS
DC_2A-n41A	30	5+100	CP-64QAM	M+M	Outer_Full	97.662	101.8	PASS
DC_2A-n41A	30	5+100	CP-256QAM	M+M	Outer_Full	97.550	101.6	PASS

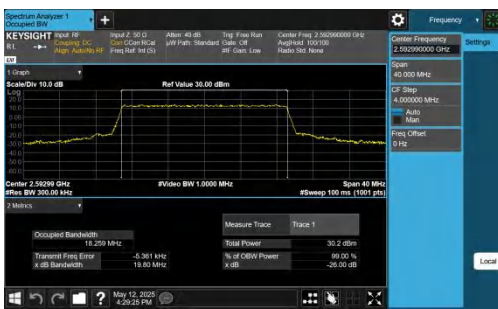
Test Graphs



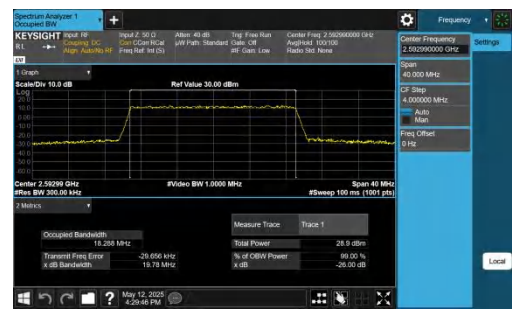
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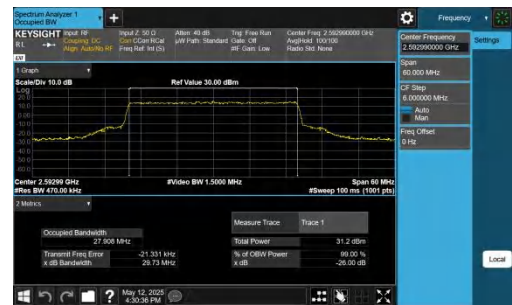
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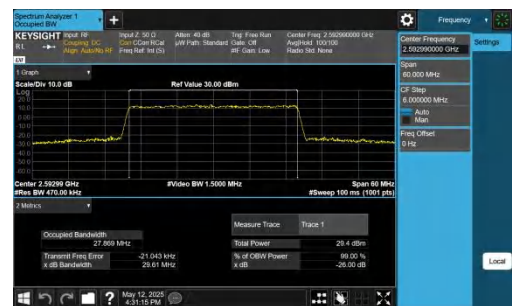
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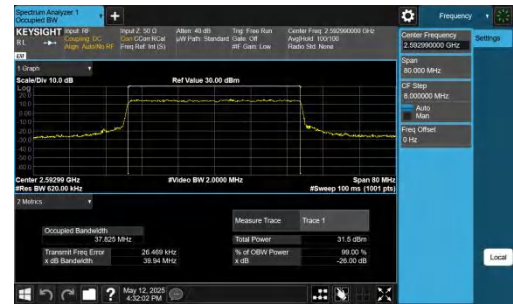
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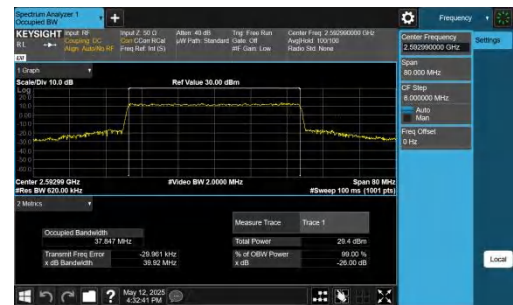


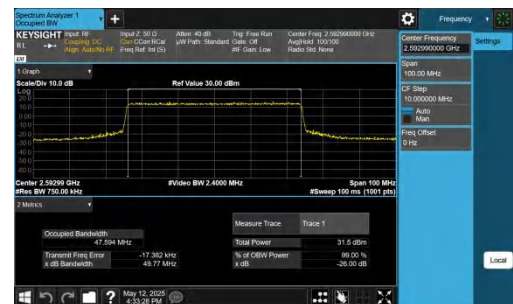
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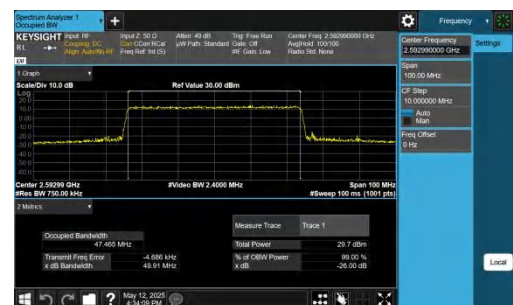


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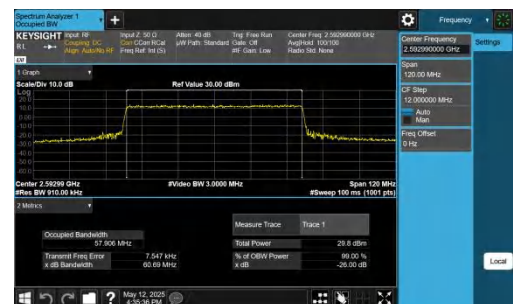
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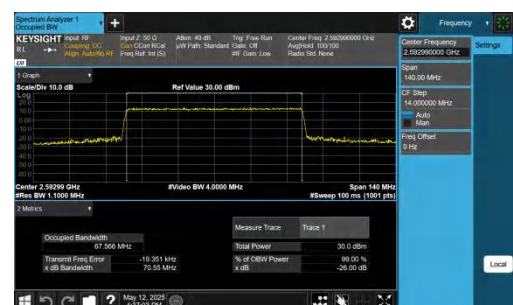

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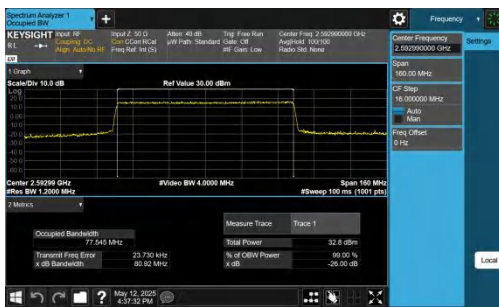
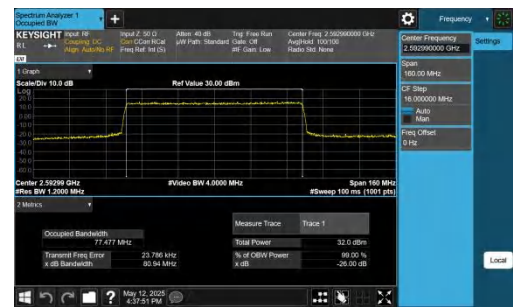
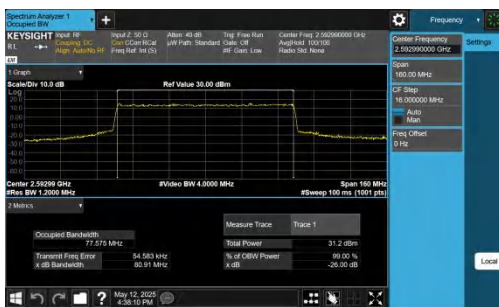
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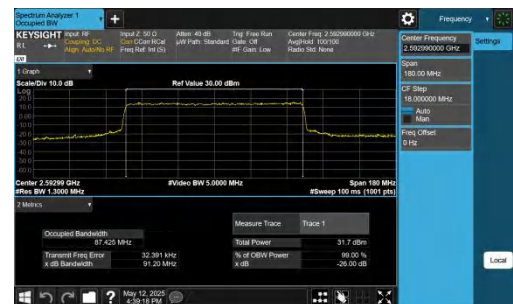
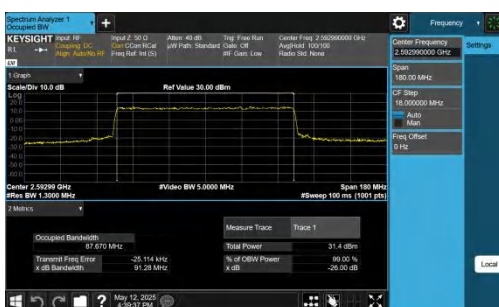
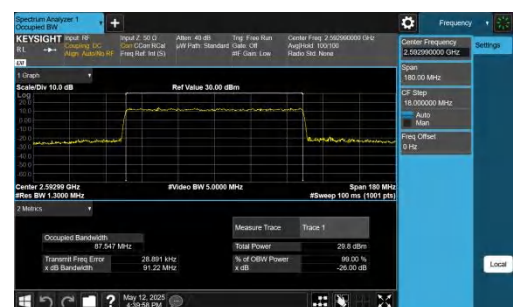
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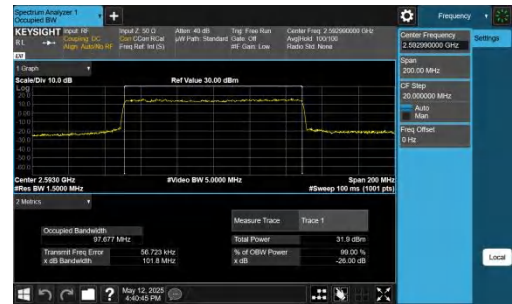

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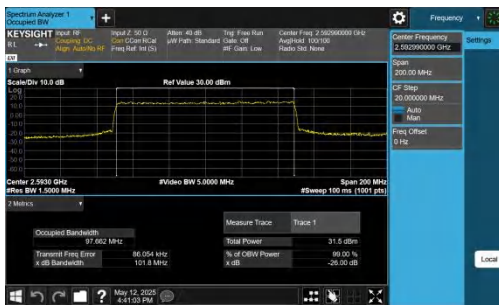
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uter_Full-Ant1-PASS

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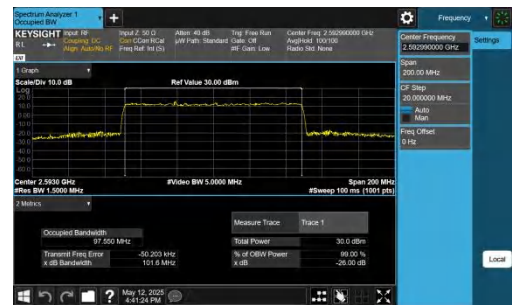
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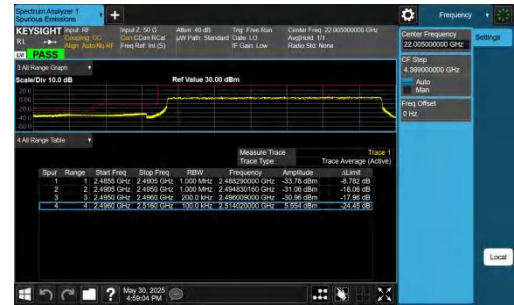
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Band Edge for ENDC

Test Graphs



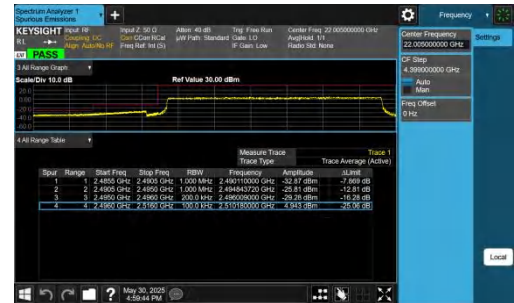
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NTNV-DC_2A-n41A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-Edge_1RB_Left-PASS



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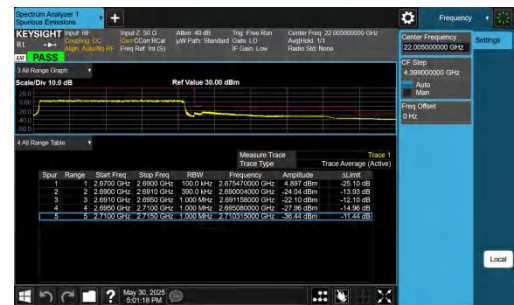
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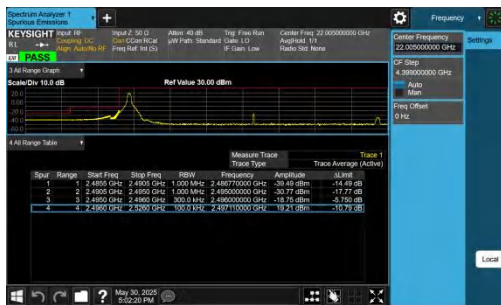
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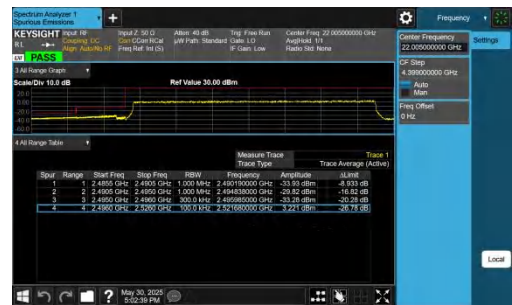
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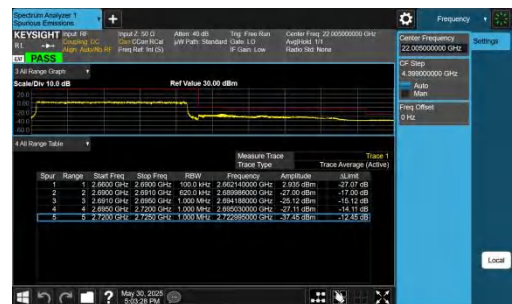
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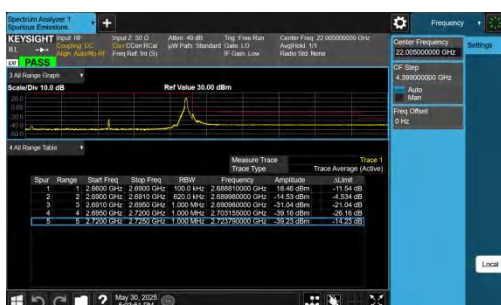
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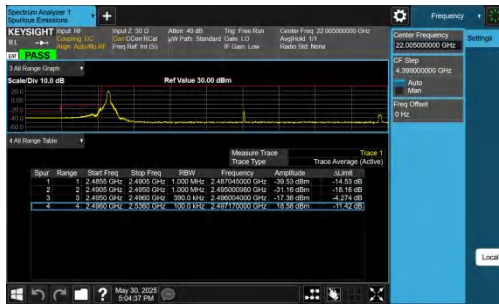
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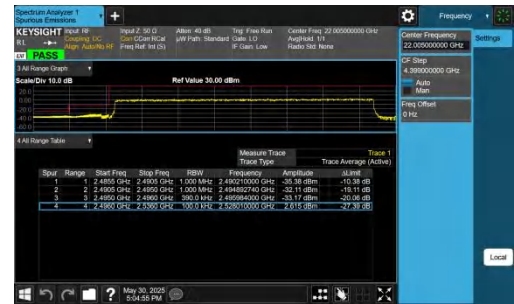
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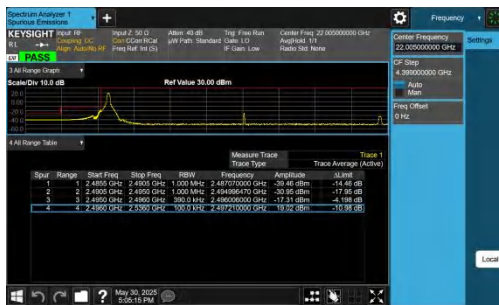
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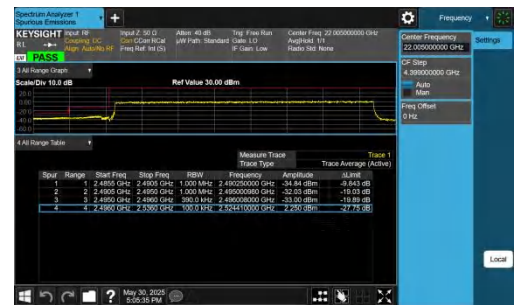
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NTN-DC_2A-n41A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



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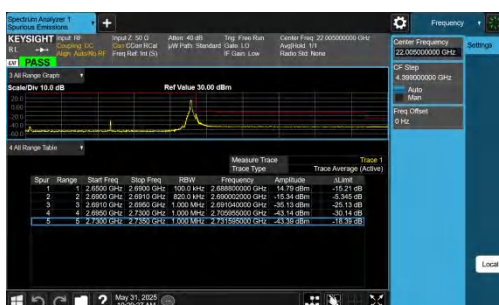
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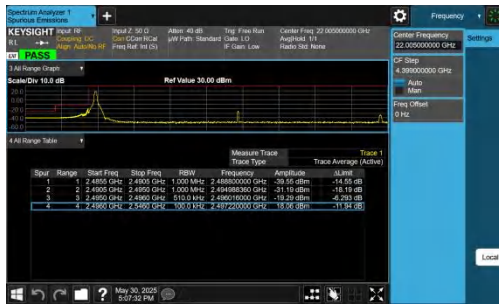
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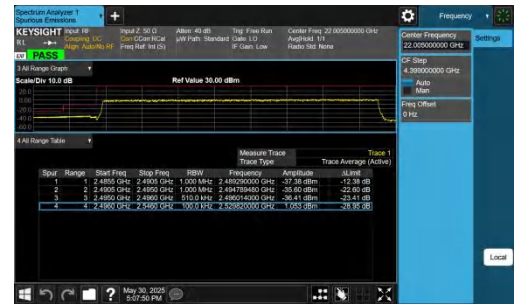
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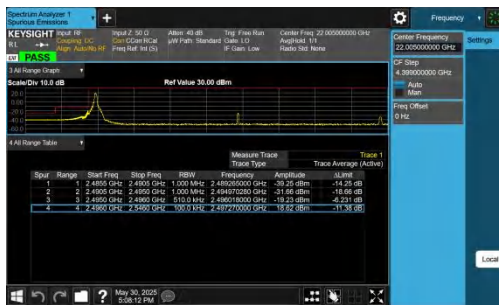
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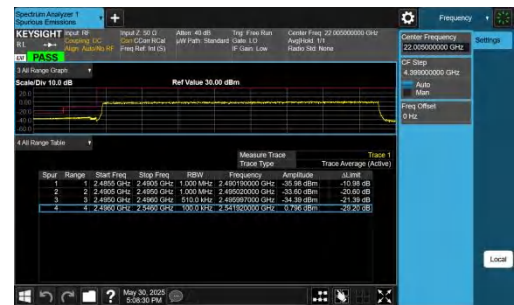
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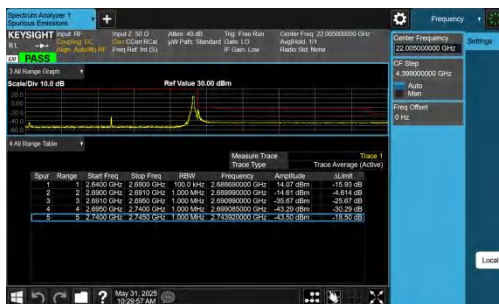
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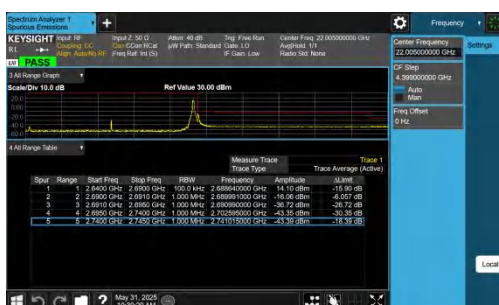
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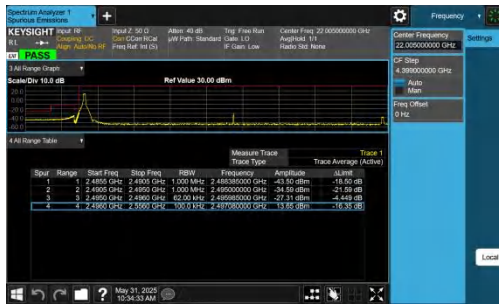
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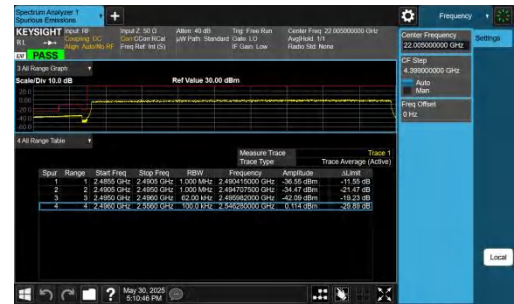
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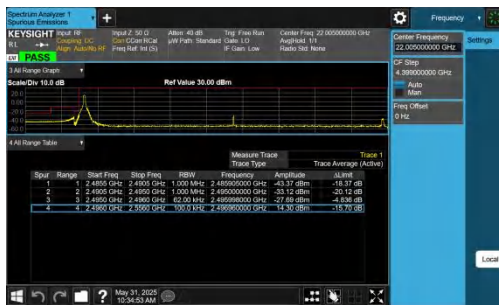
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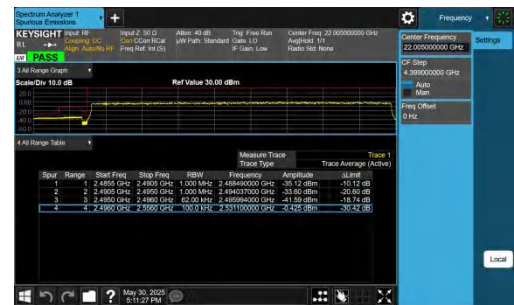
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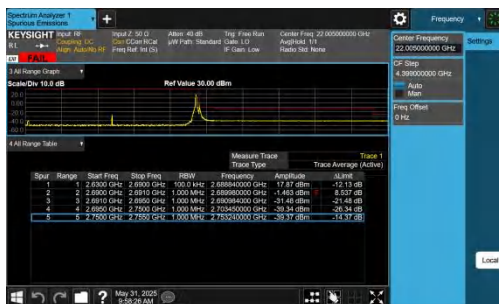
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NTNV-DC_2A-n41A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-Edge_1RB_Left-PASS



NTNV-DC_2A-n41A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS



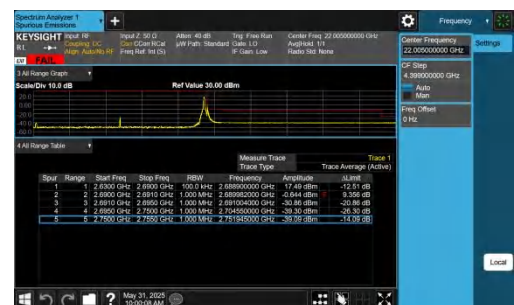
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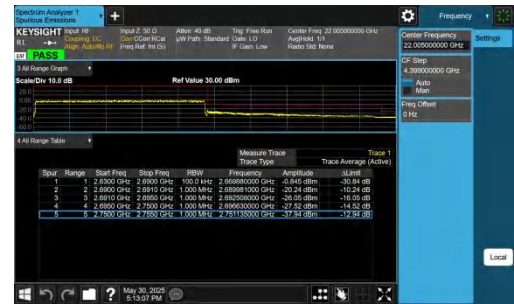
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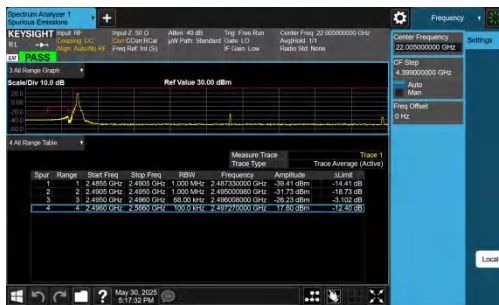
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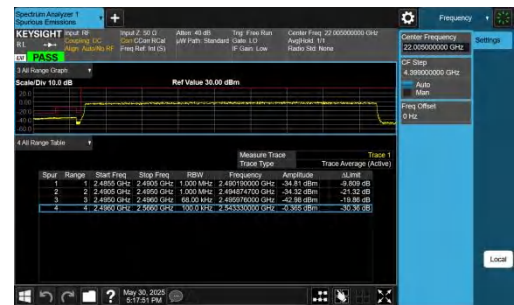
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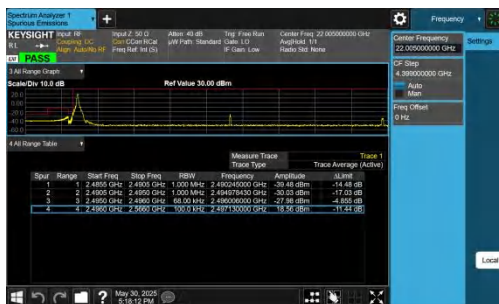
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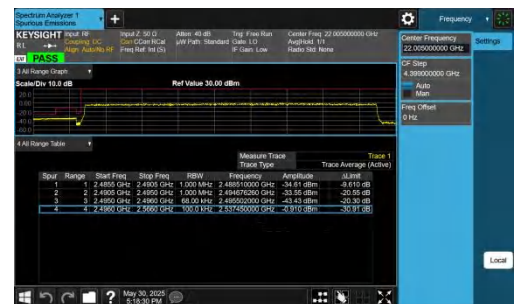
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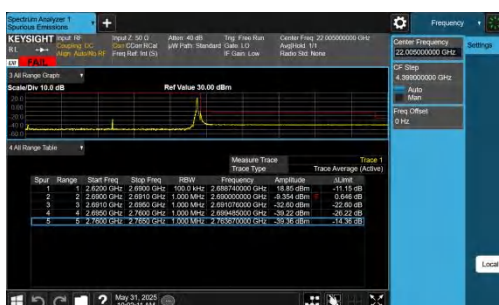
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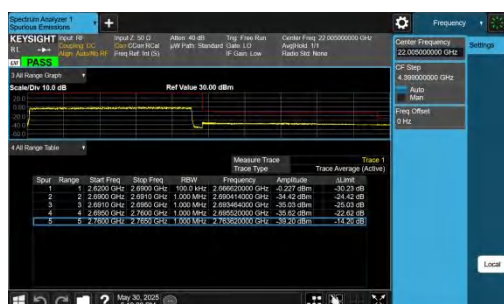
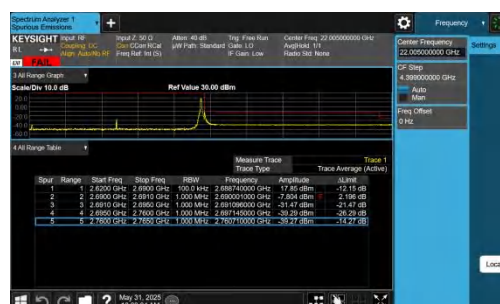
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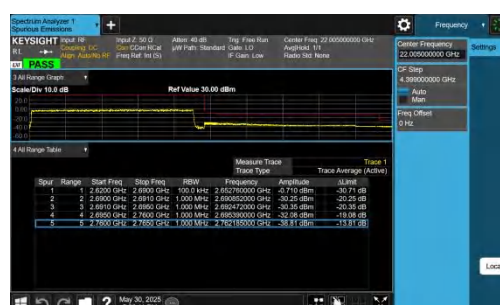
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ter Full-PASS

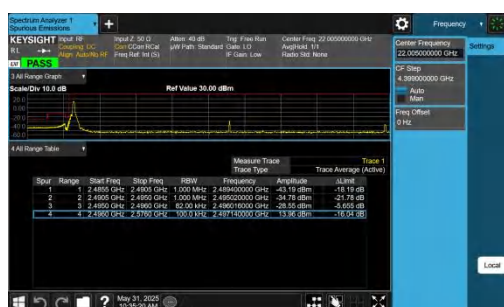
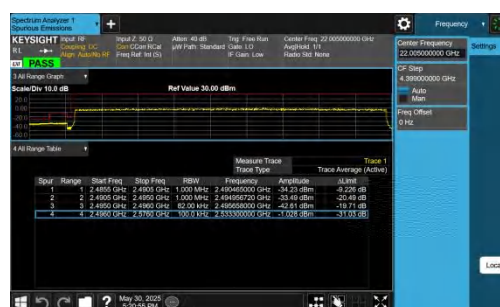
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dqe 1RB Right



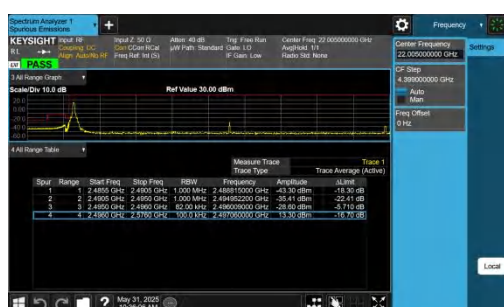
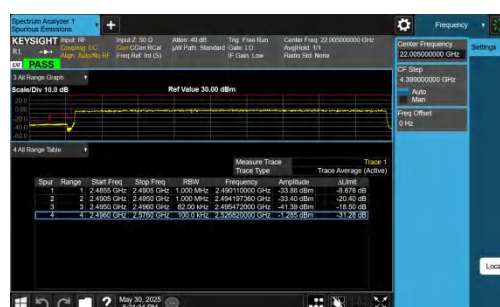
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NTNV-DC_2A-n41A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-QPSK-O
uter Full-PASS

NTNV-DC_2A-n41A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-Ed
ge 1RB Left-PASS

NTNV-DC_2A-n41A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-O
uter Full-PASS

NTNV-DC_2A-n41A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-QPSK-Ed
ge 1RB Left-PASS

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uter Full-PASS



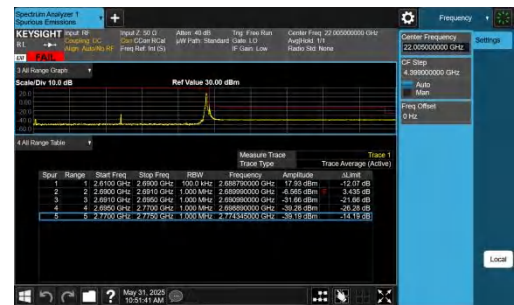
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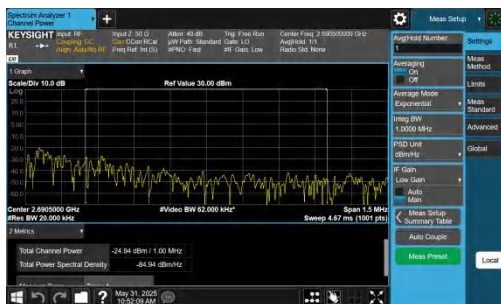
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NTNV-DC_2A-n41A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



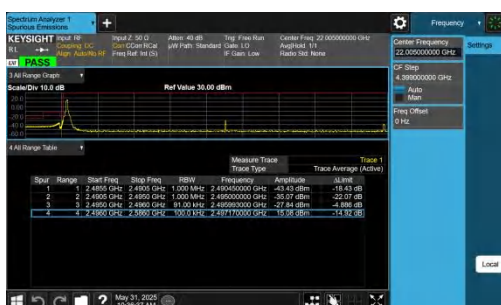
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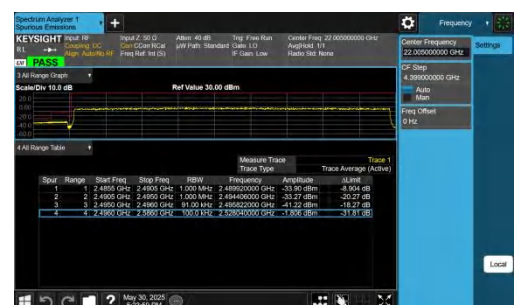
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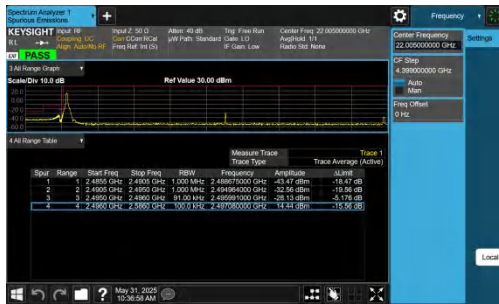
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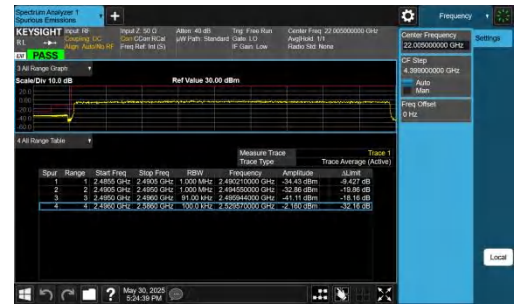
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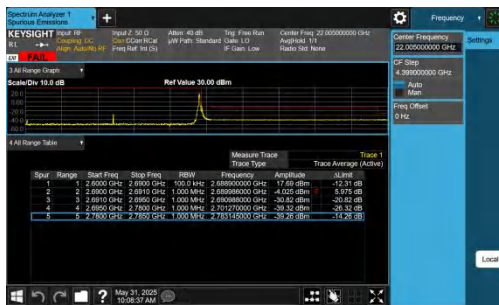
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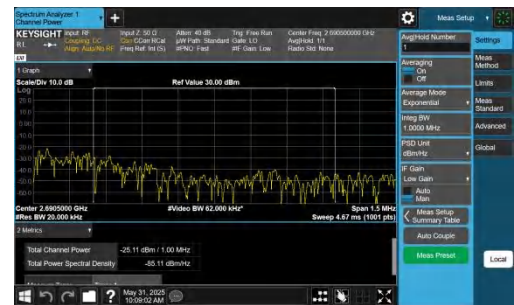
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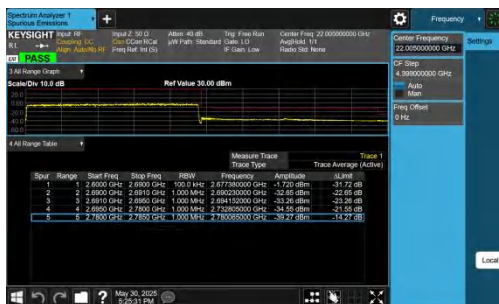
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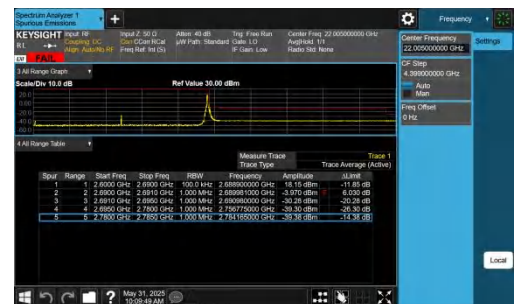
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NTNV-DC_2A-n41A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-Edge_1RB_Right-PASS



NTNV-DC_2A-n41A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



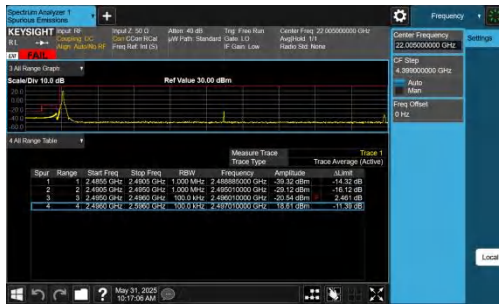
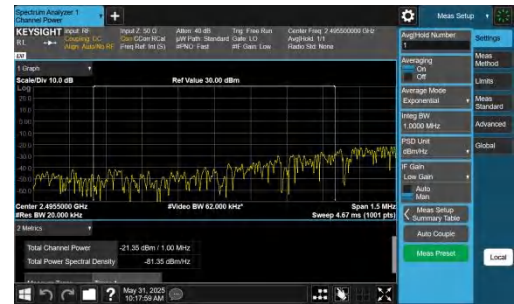
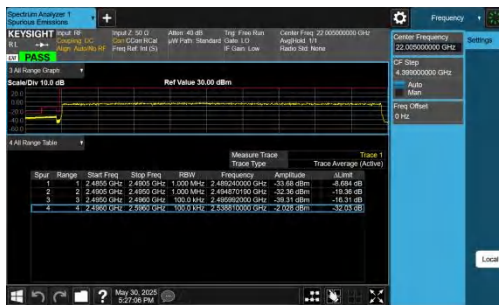
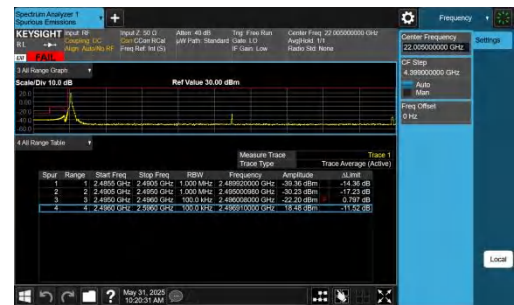
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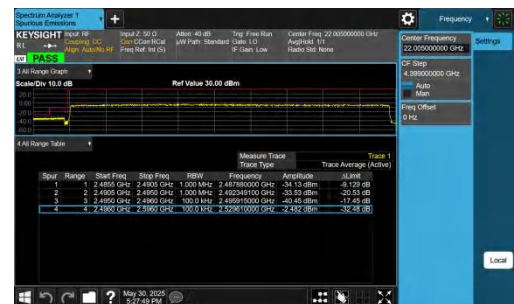
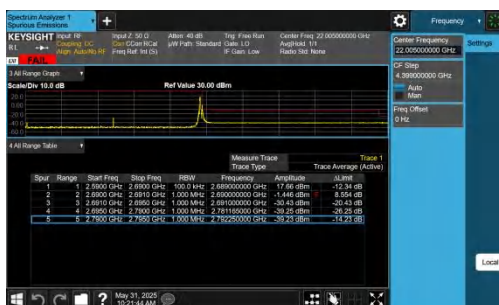


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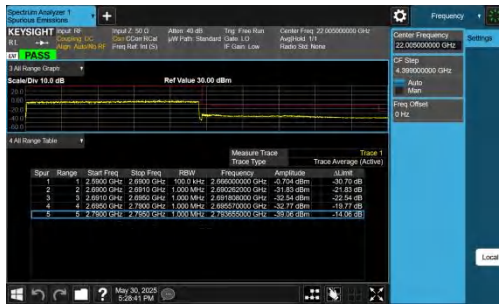


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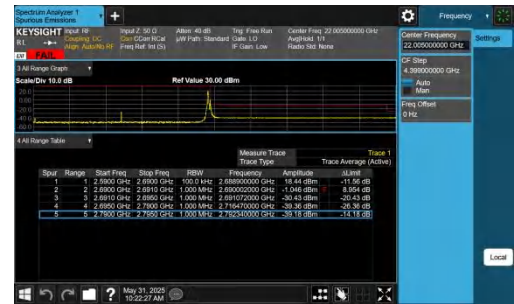

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS_Final1

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-
Edge_1RB_Left

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_2A-n41A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-
Outer_Full-PASS

NTNV-DC_2A-n41A-PC2-30-5+100-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right

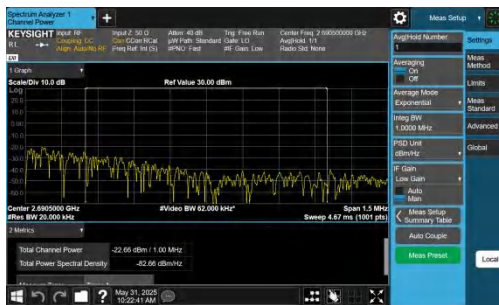
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Edge_1RB_Right-PASS



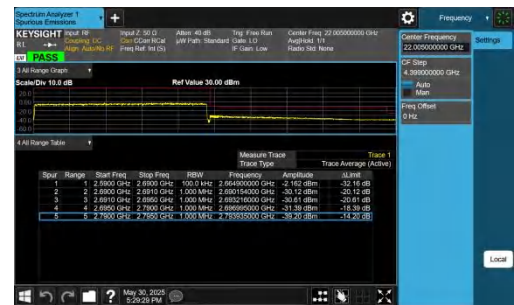
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uter_Full-PASS



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Edge_1RB_Right



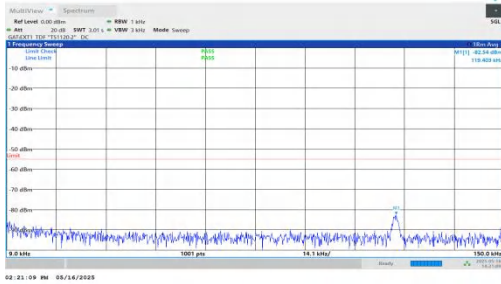
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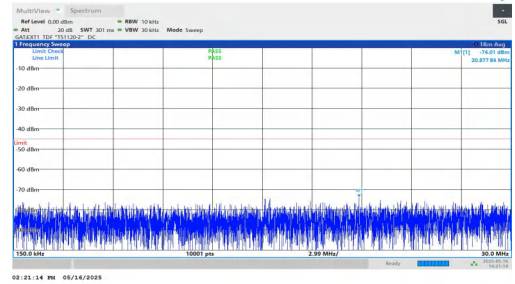
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Outer_Full-PASS

Conducted Spurious Emission for ENDC

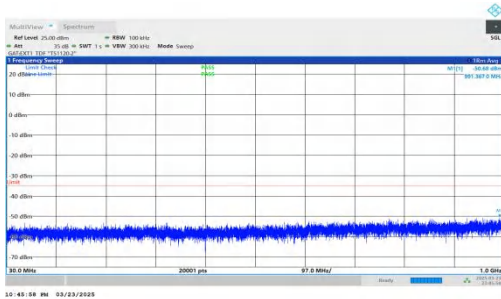
Test Graphs



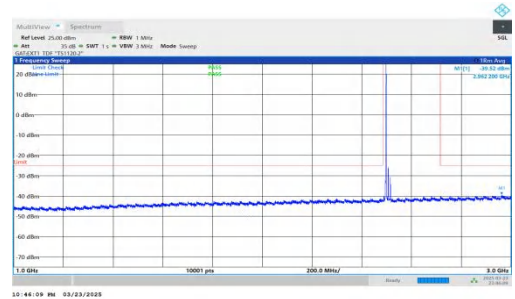
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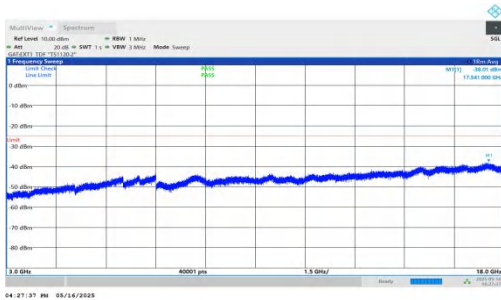
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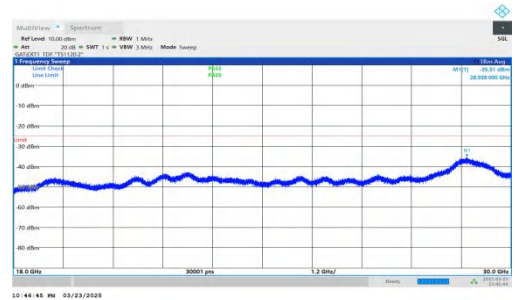
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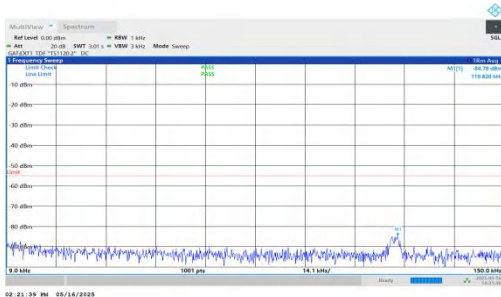
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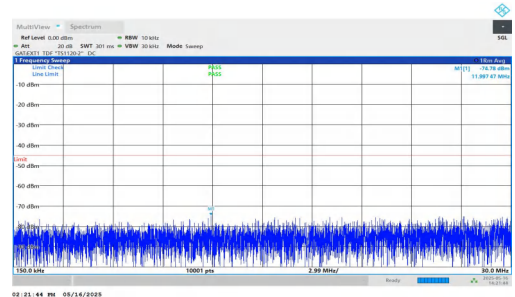
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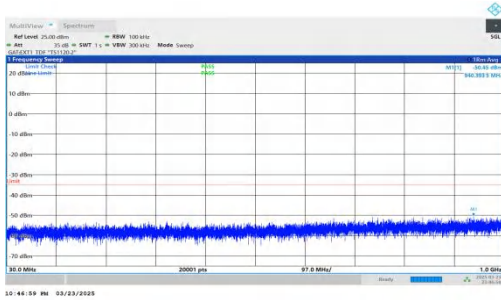
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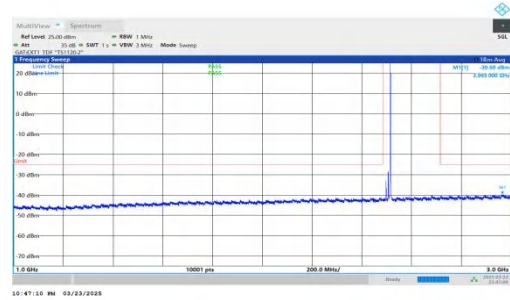
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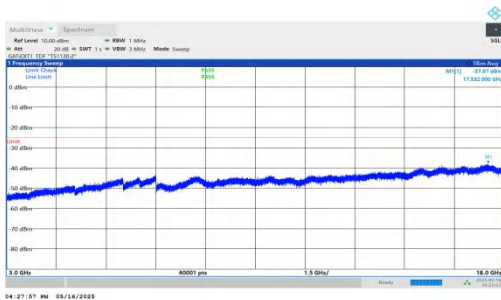
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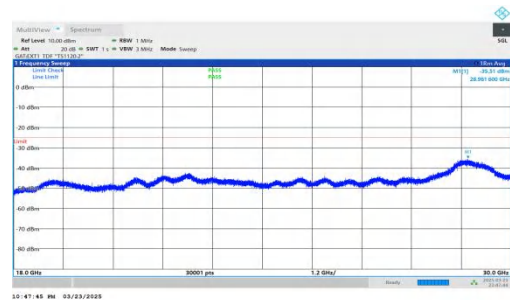
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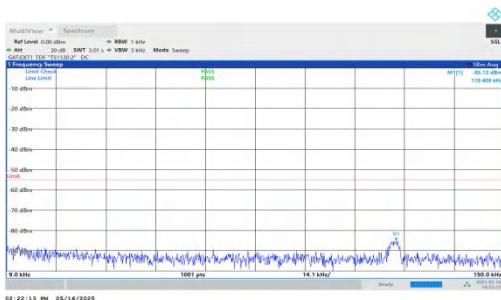
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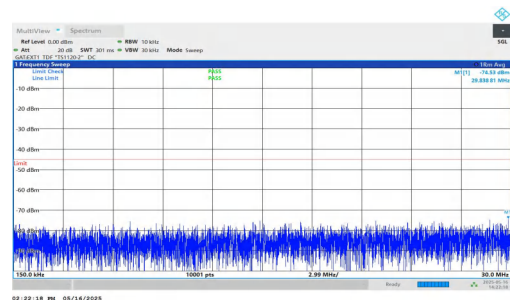
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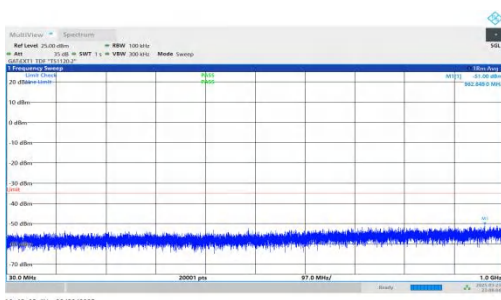
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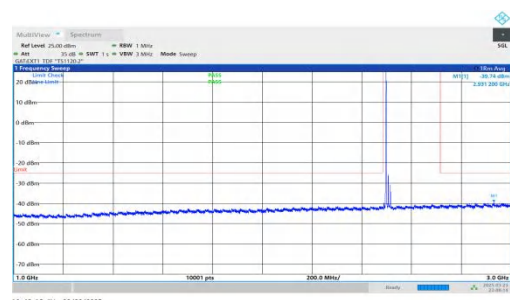
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NTNV-DC_2A-n41A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-0.15-30-PASS



NTNV-DC_2A-n41A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-30-1000-PASS



NTNV-DC_2A-n41A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-Inner_1RB_Left-1000-3000-PASS