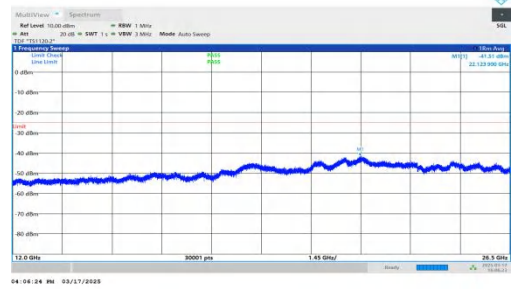
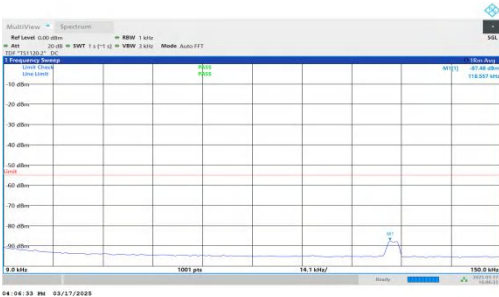


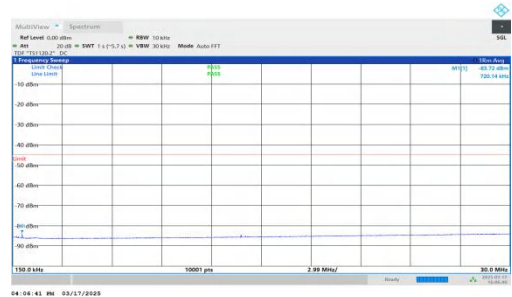
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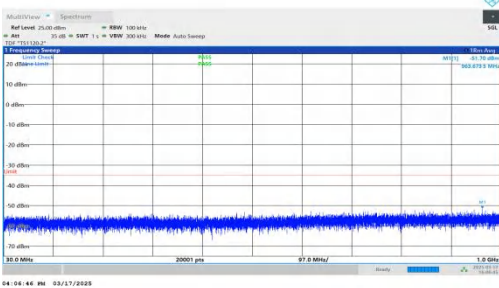
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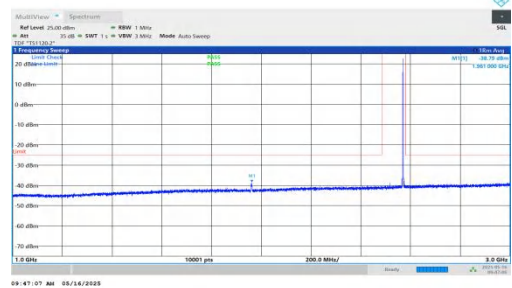
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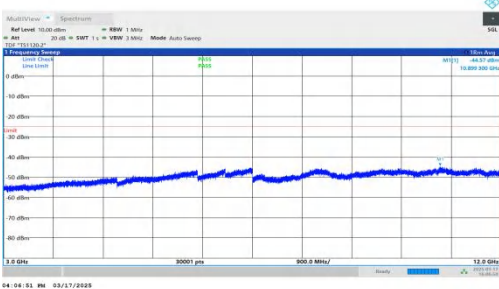
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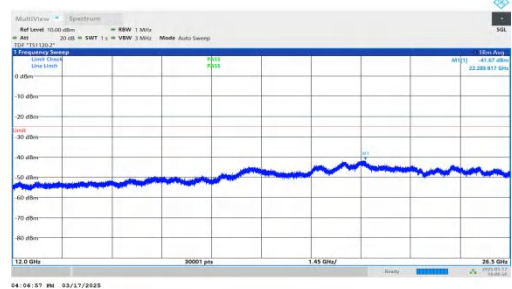
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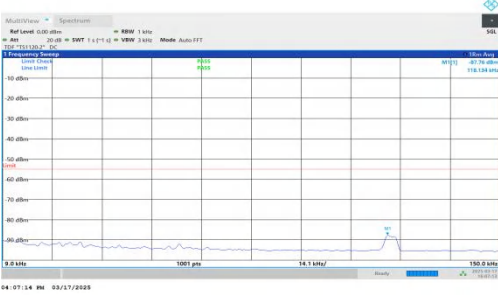
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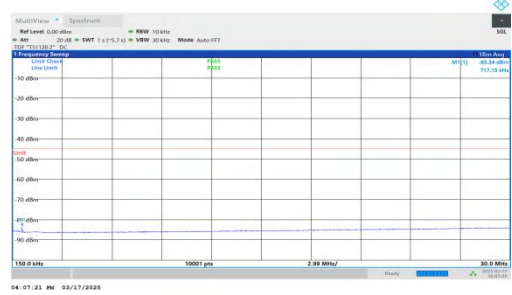
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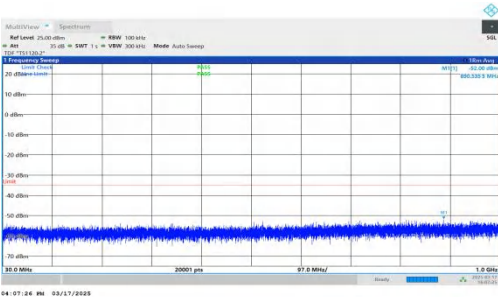
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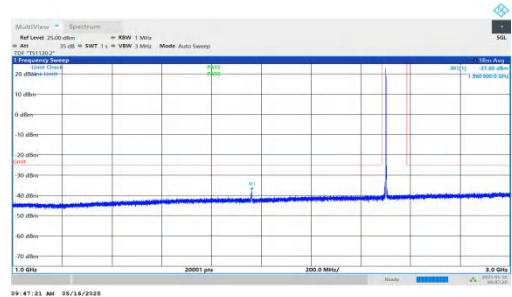
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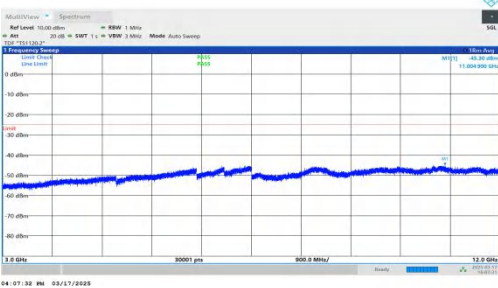
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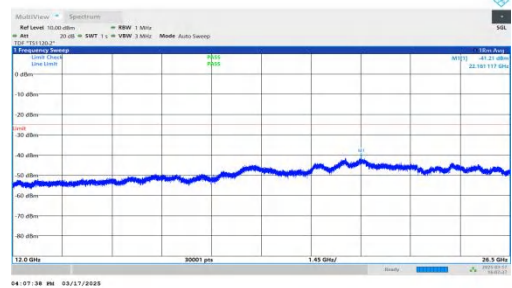
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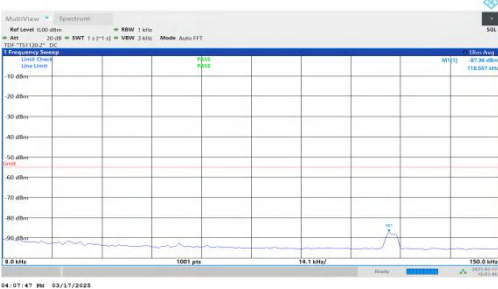
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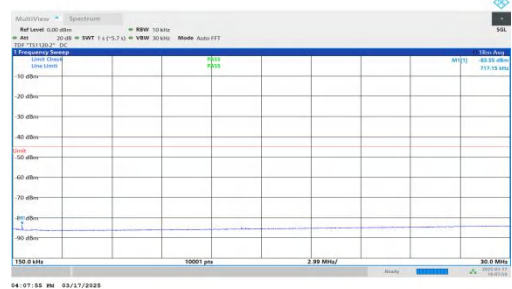
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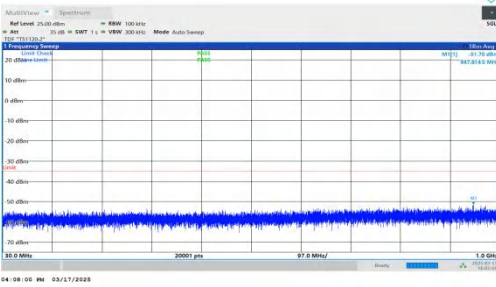
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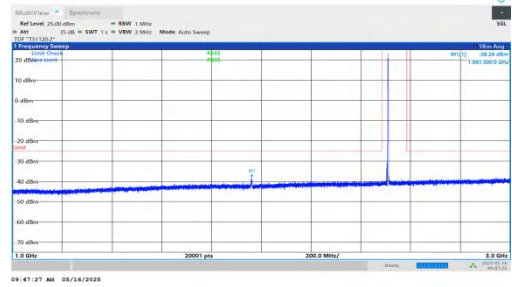
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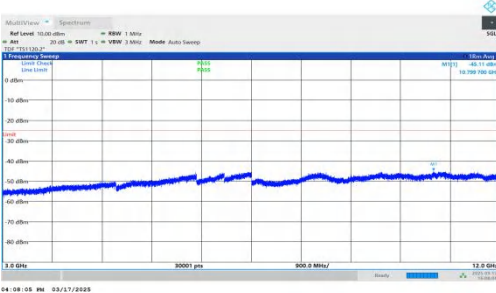
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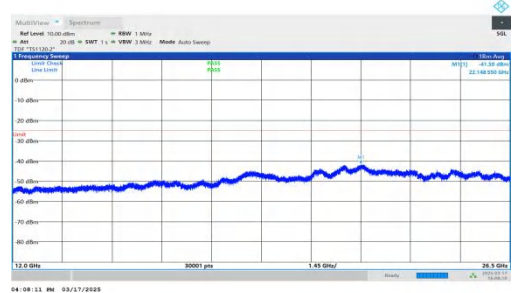
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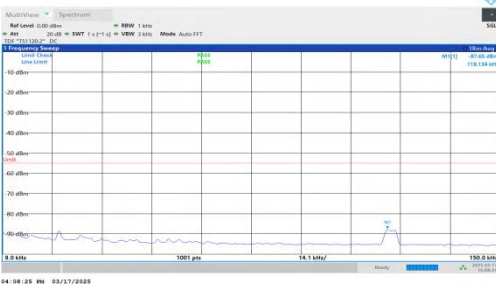
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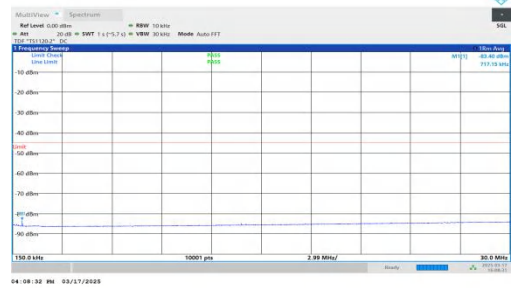
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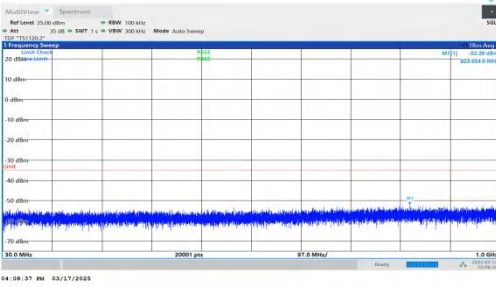
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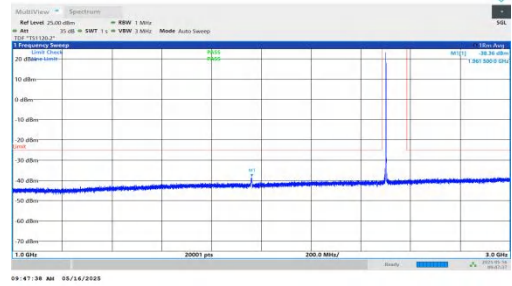
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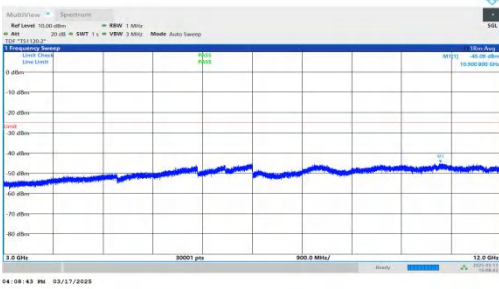
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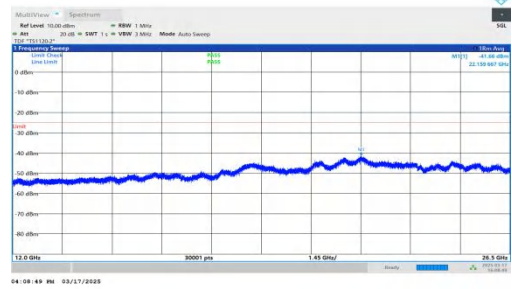
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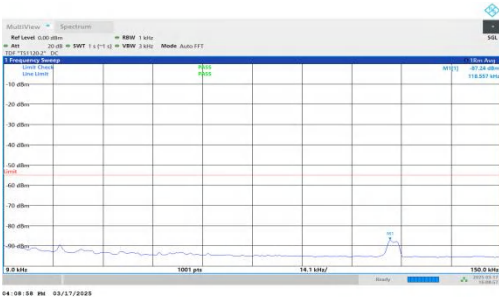
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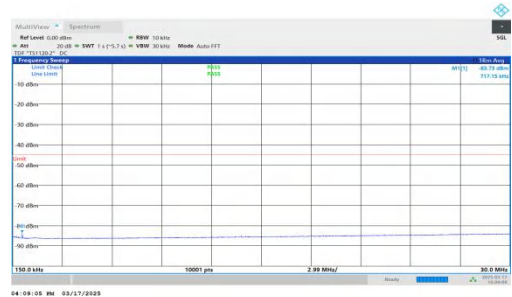
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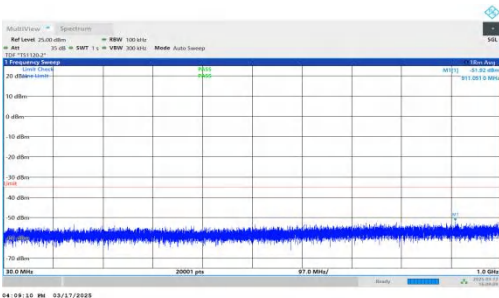
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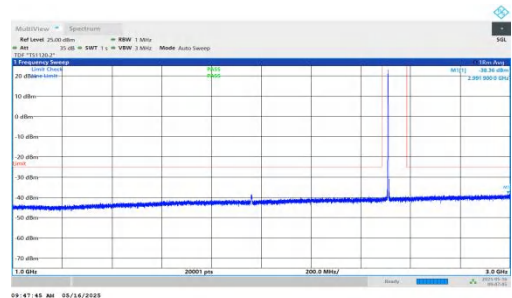
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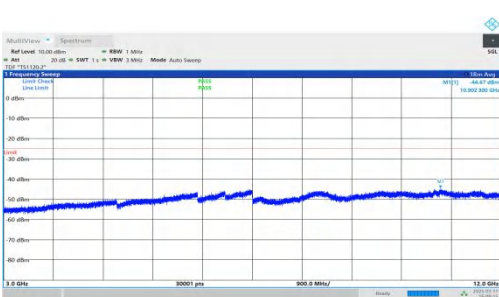
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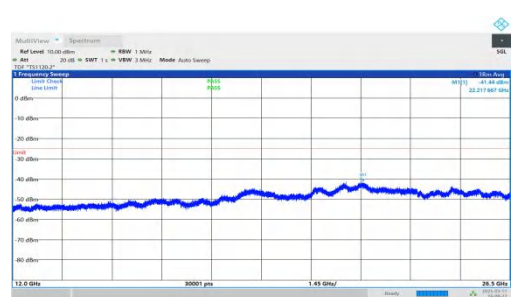
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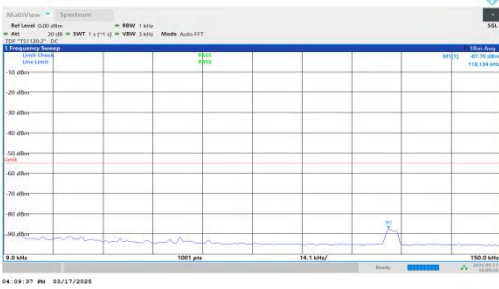
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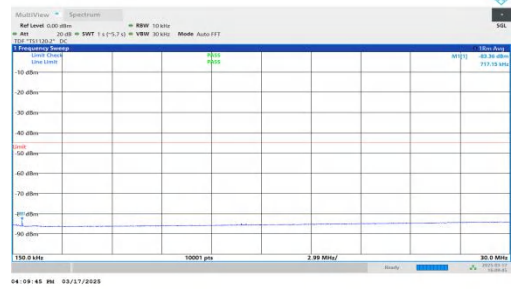
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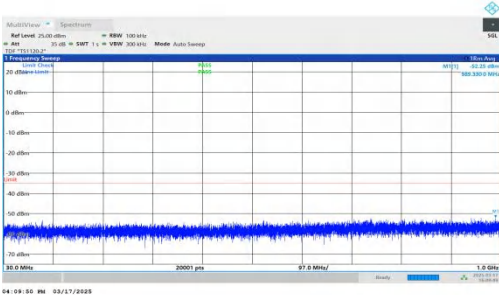
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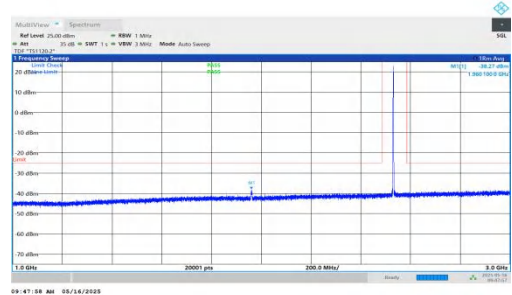
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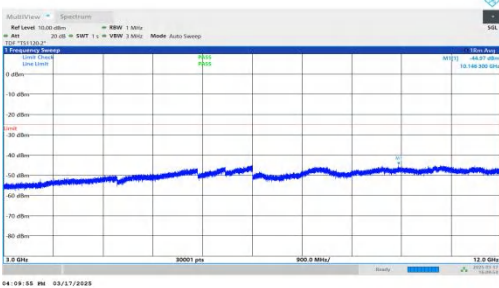
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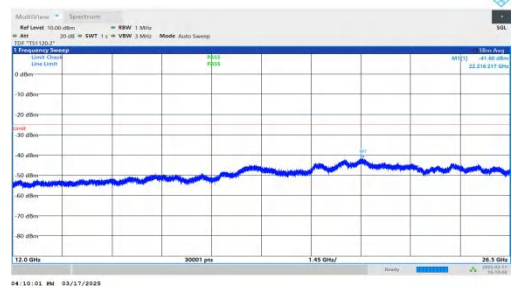
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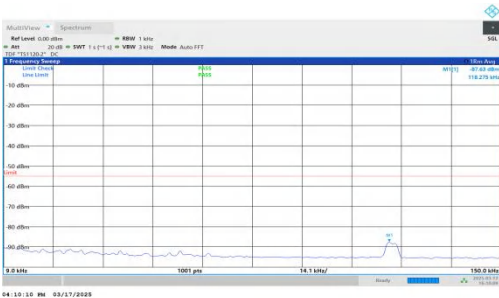
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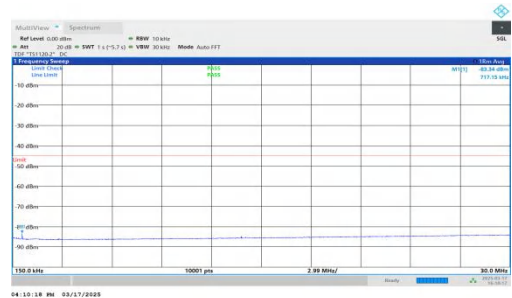
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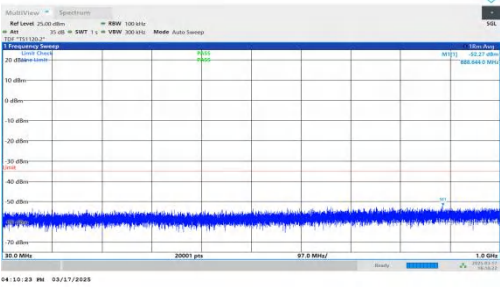
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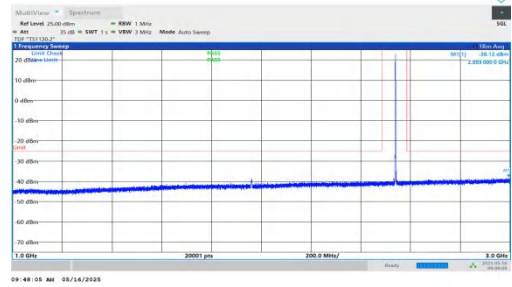
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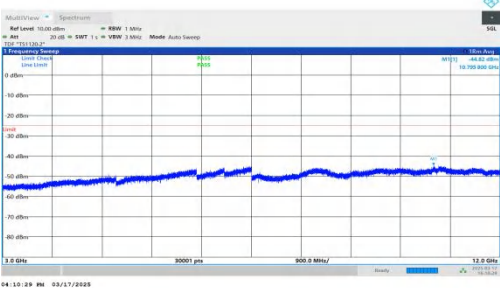
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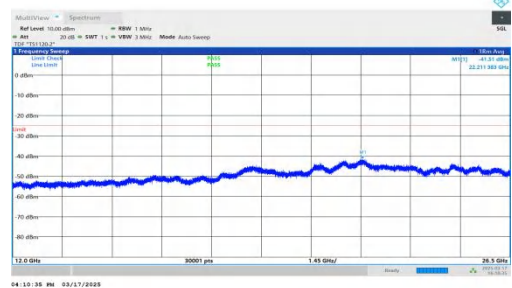
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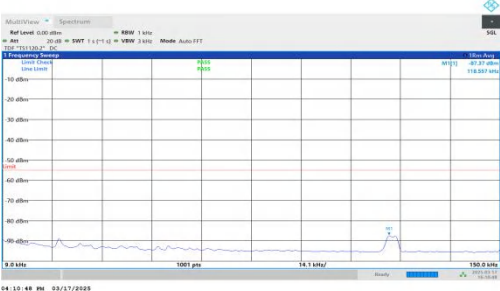
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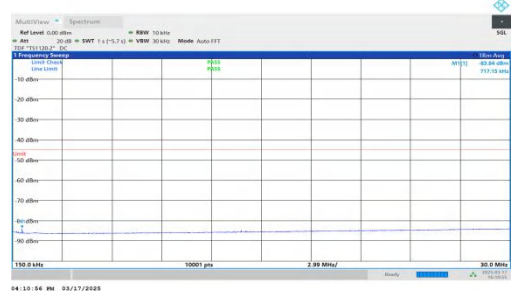
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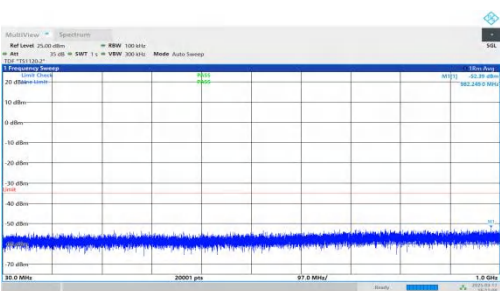
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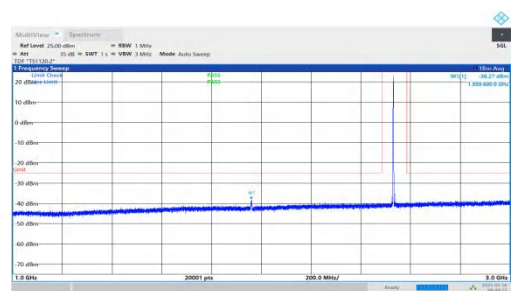
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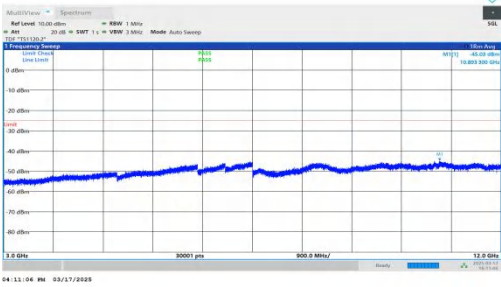
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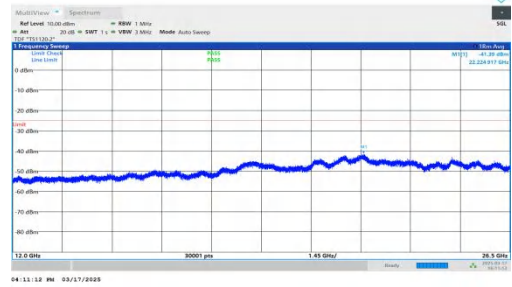
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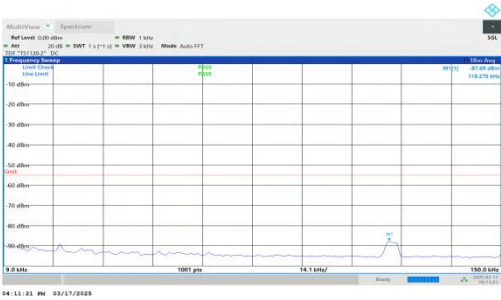
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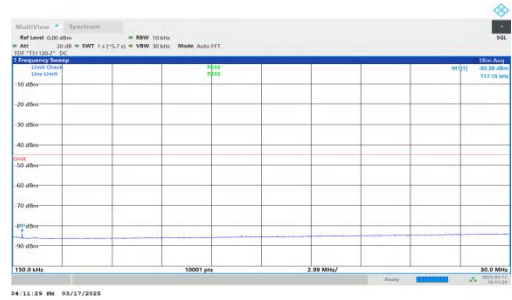
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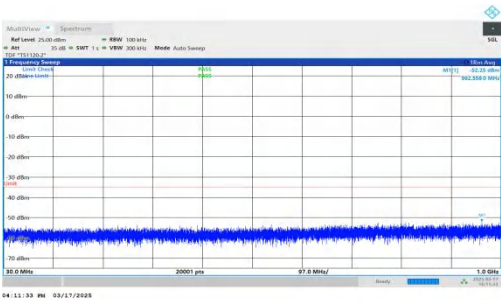
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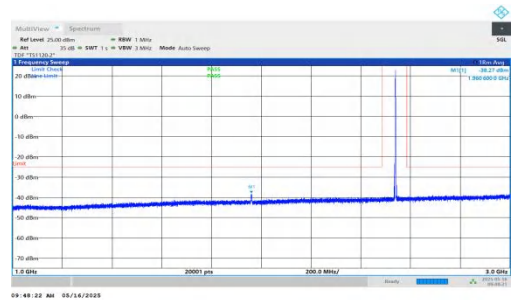
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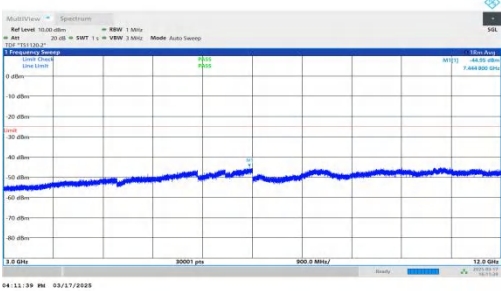
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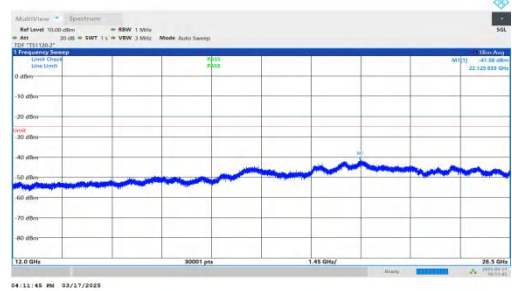
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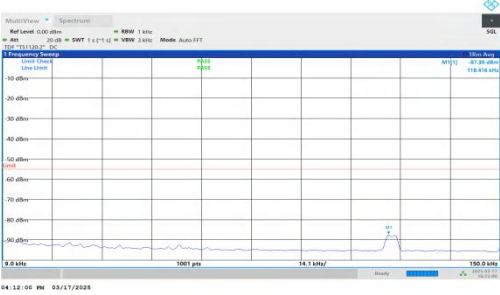
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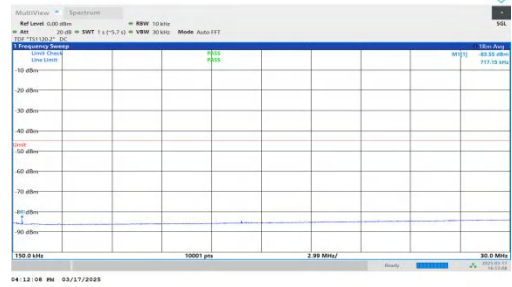
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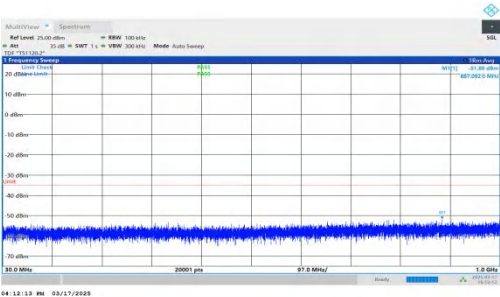
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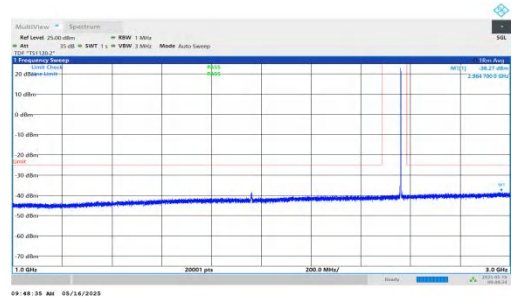
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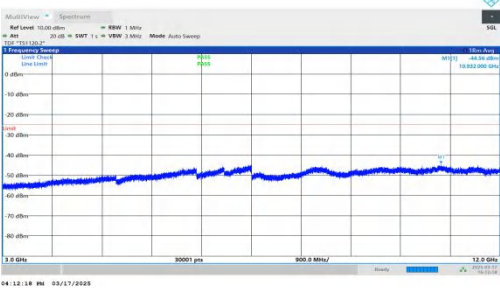
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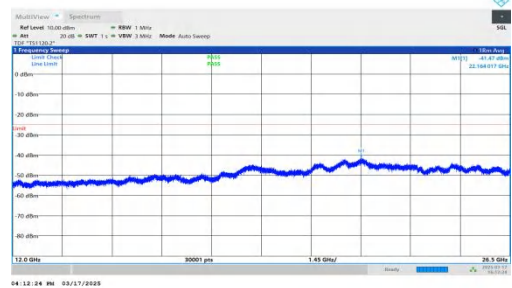
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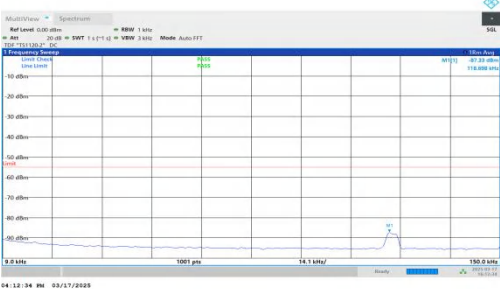
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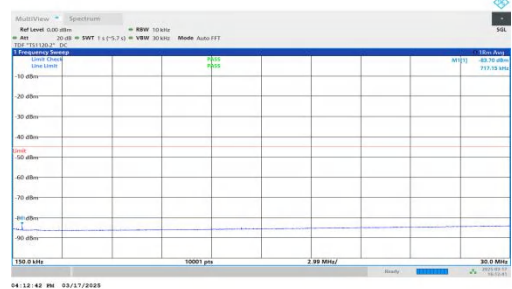
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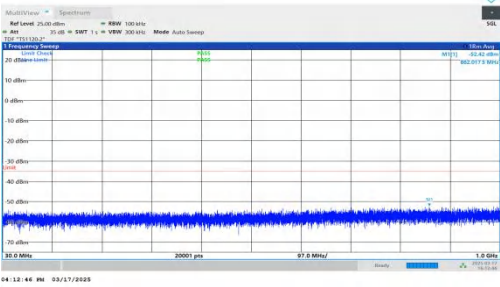
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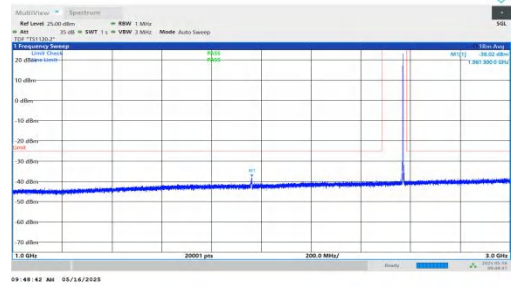
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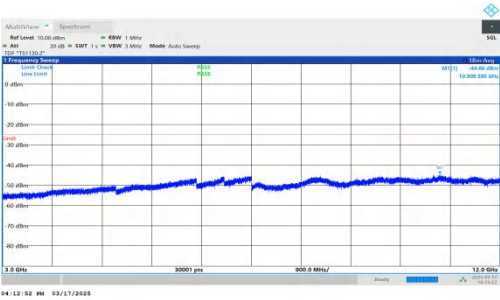
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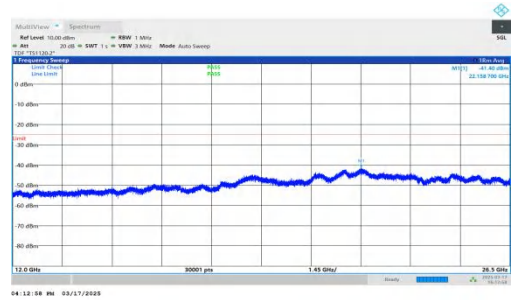
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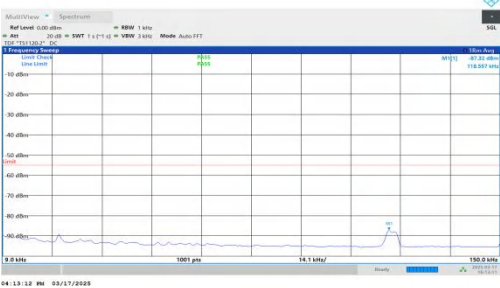
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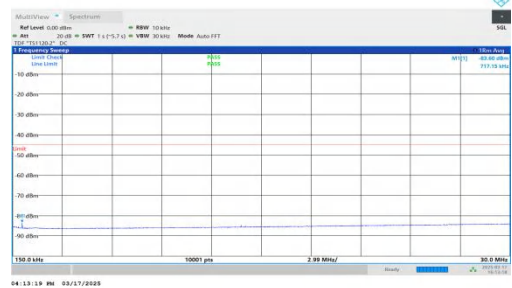
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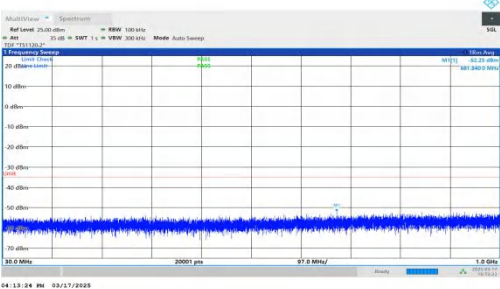
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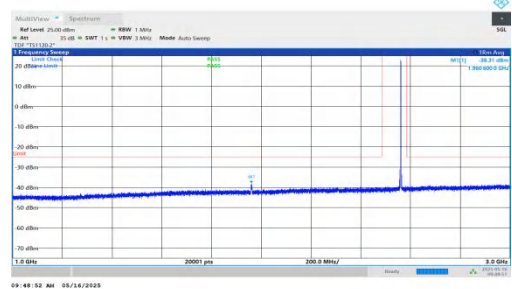
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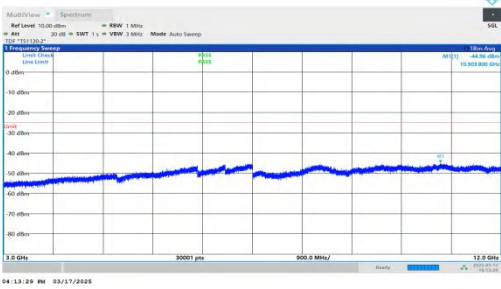
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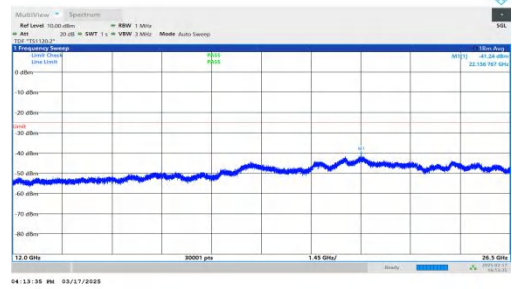
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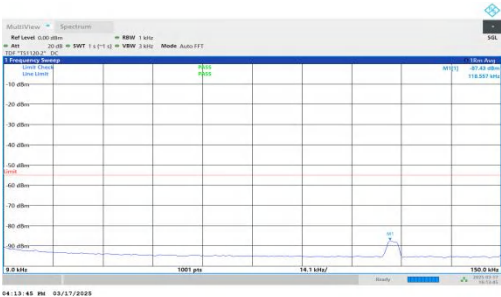
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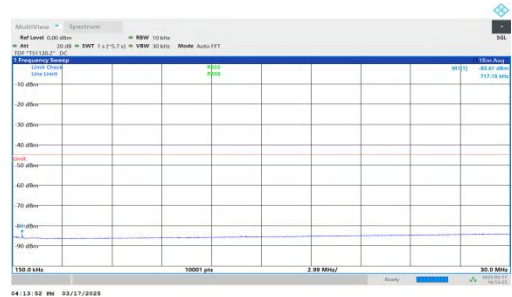
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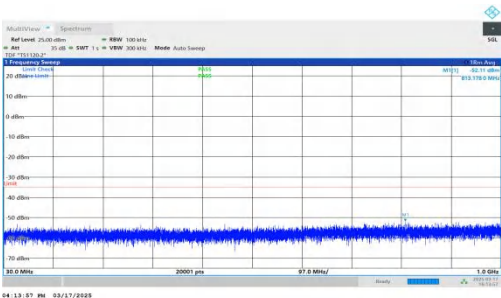
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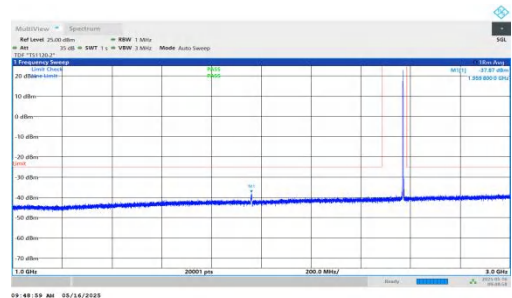
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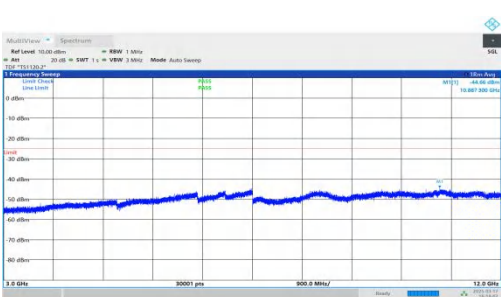
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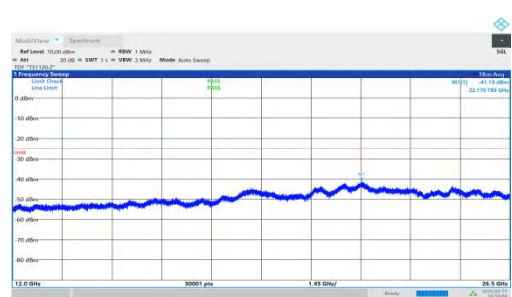
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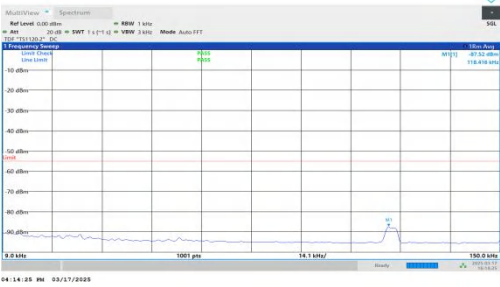
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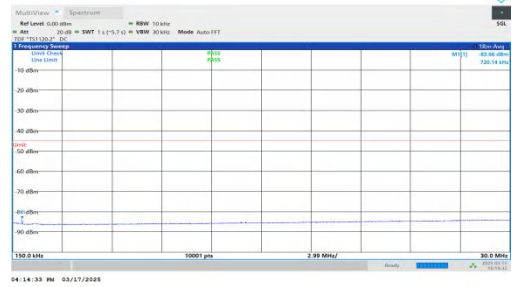
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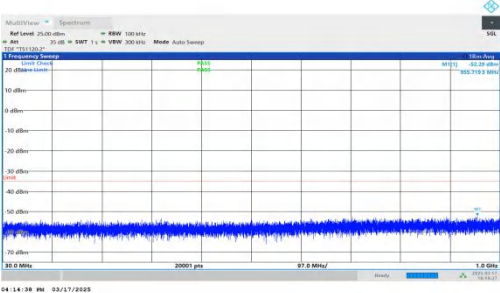
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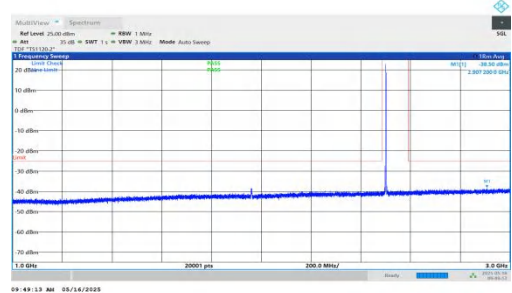
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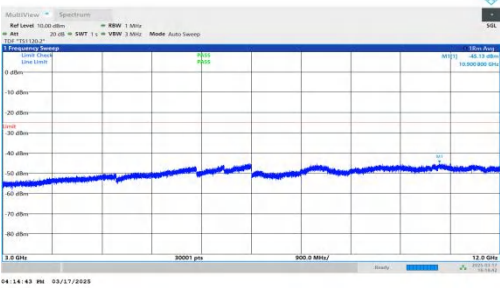
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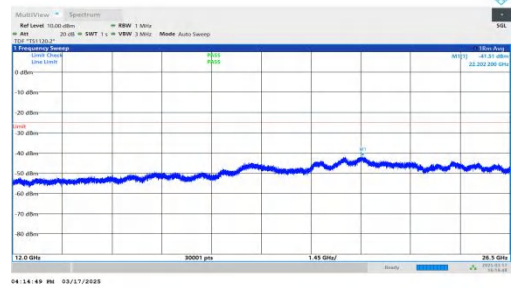
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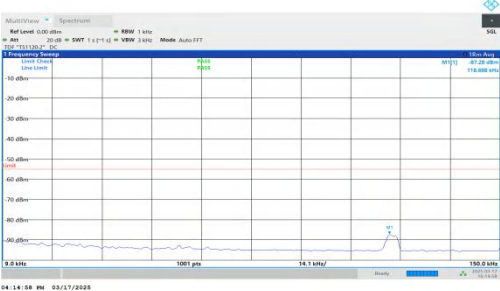
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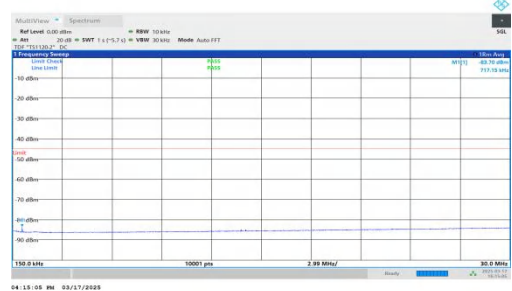
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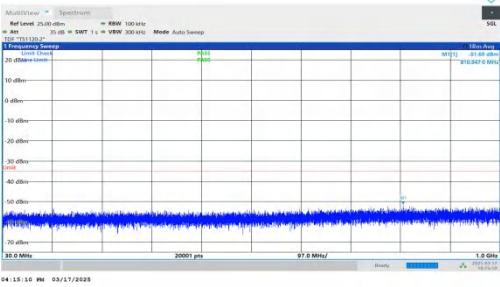
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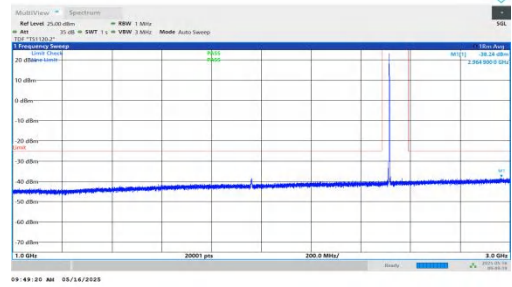
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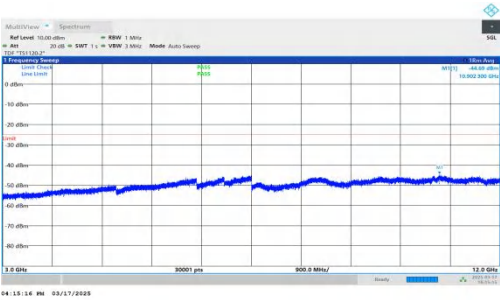
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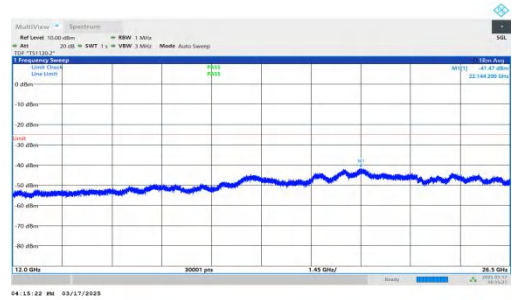
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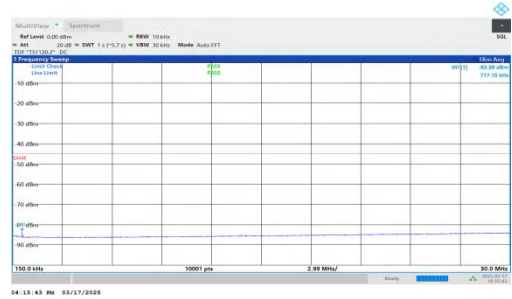
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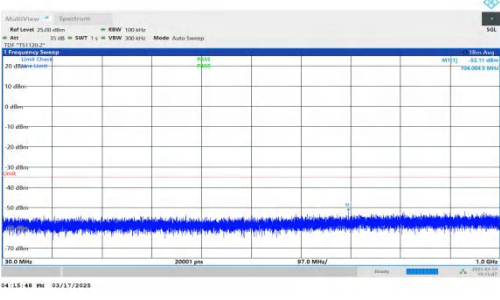
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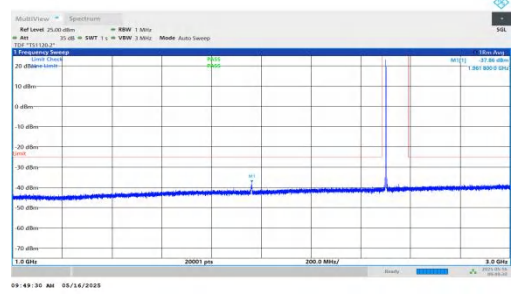
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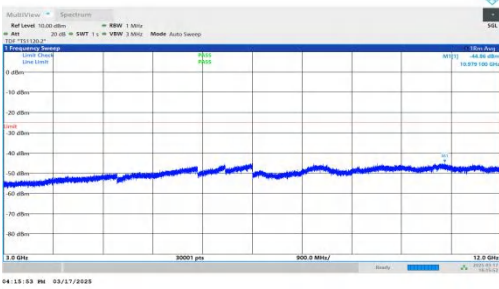
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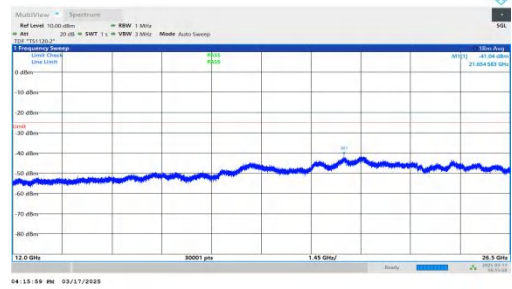
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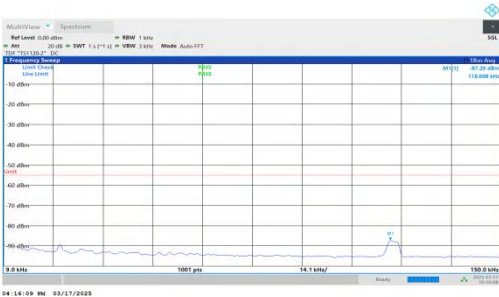
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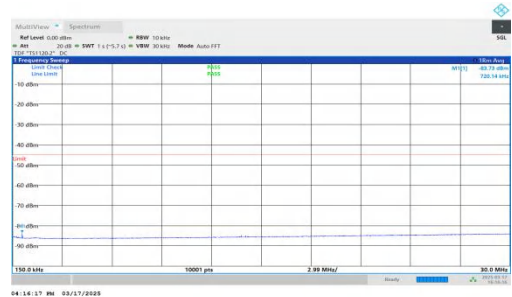
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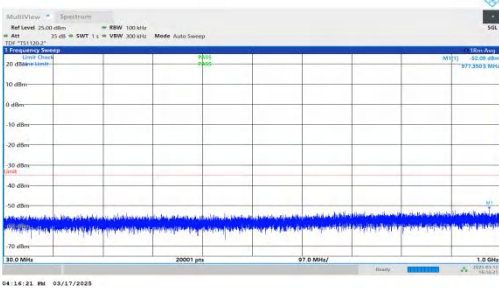
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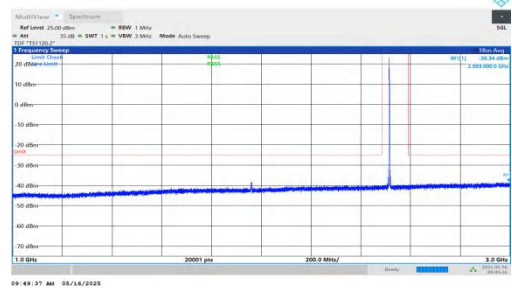
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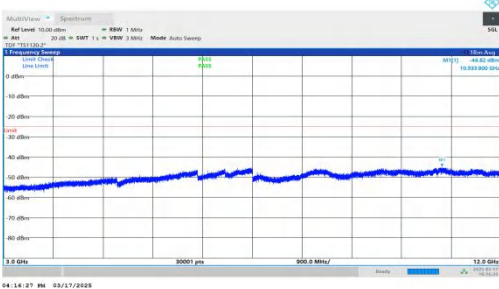
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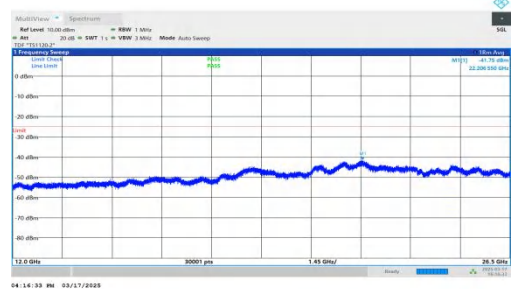
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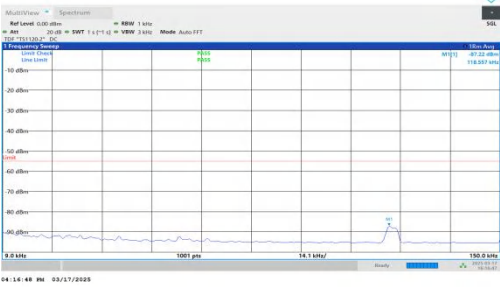
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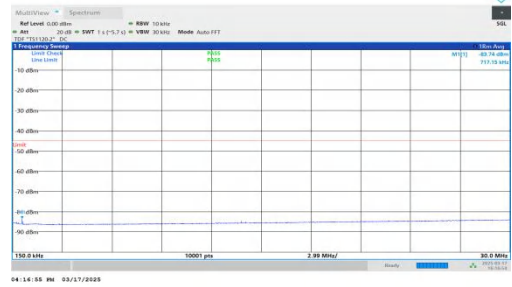
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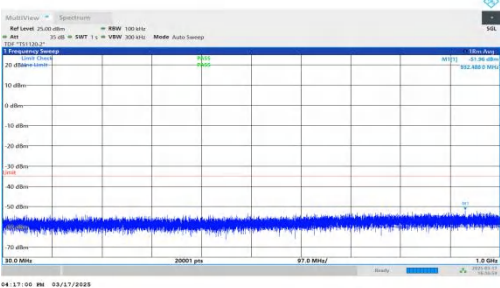
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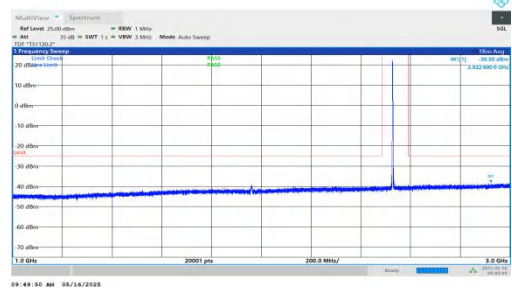
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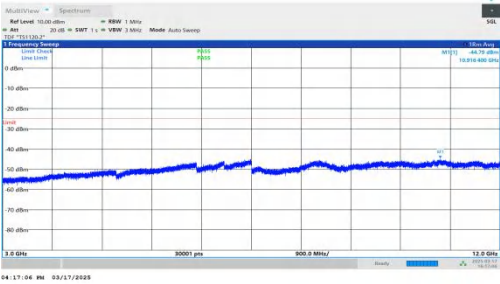
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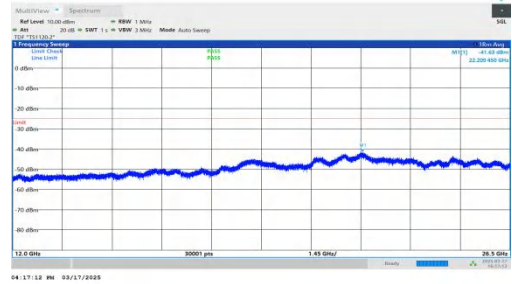
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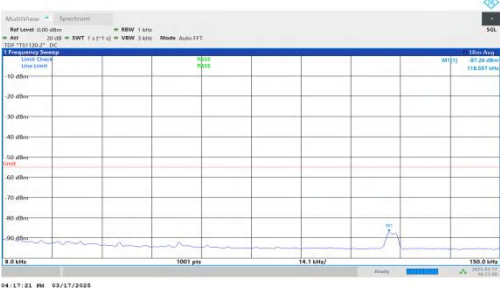
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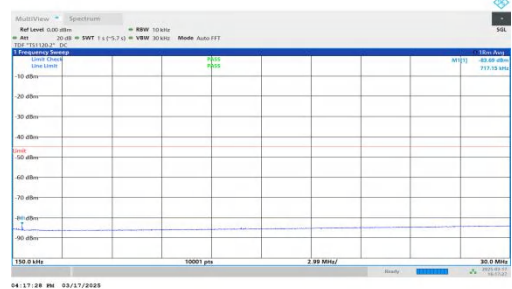
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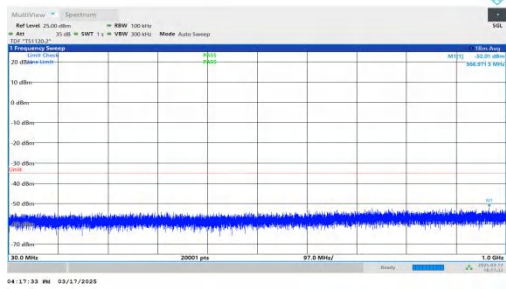
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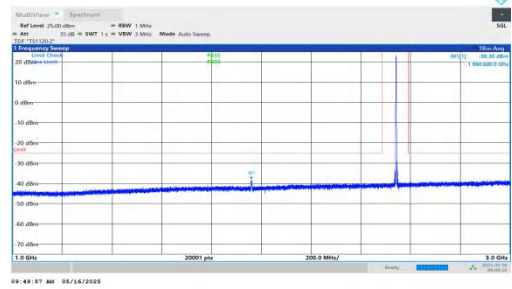
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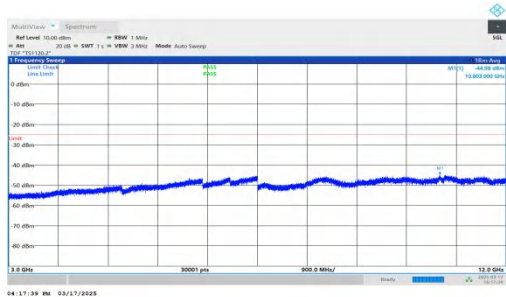
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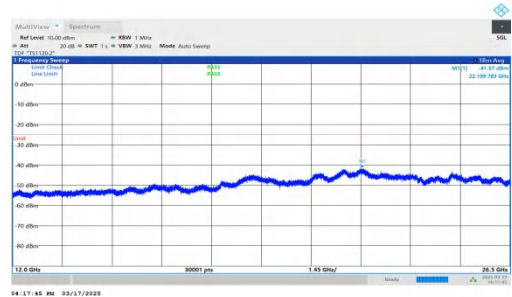
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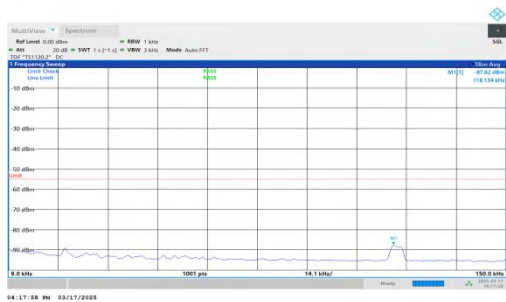
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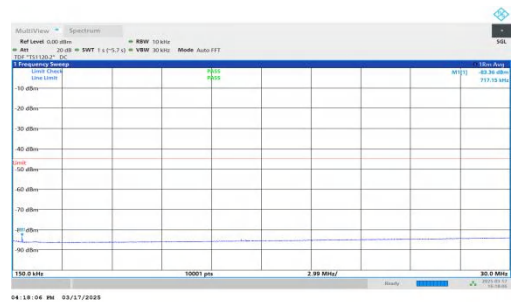
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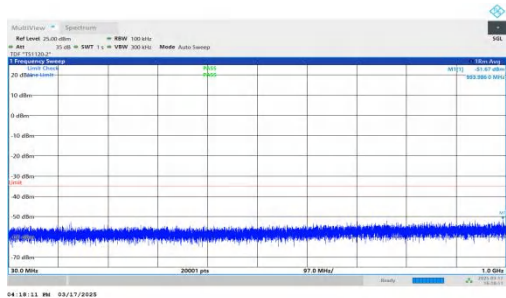
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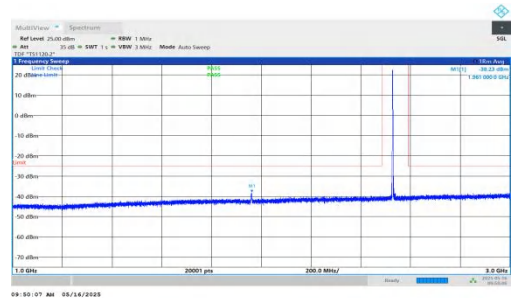
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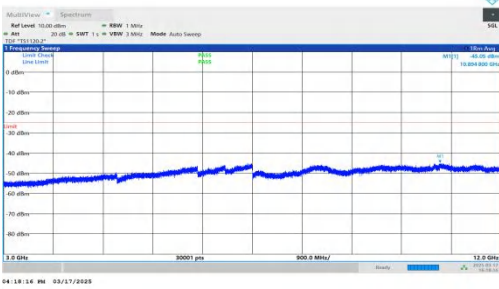
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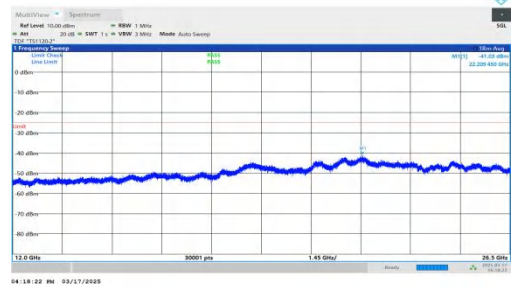
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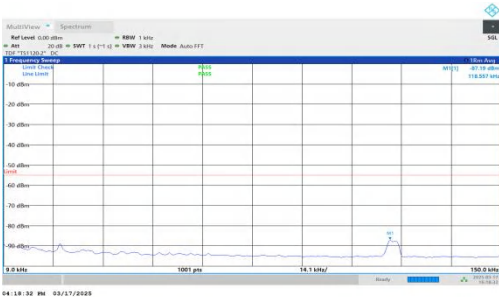
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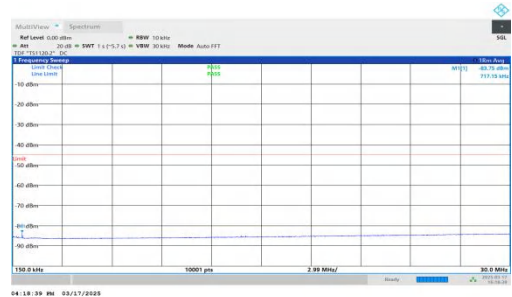
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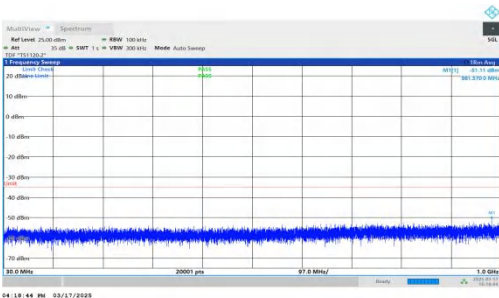
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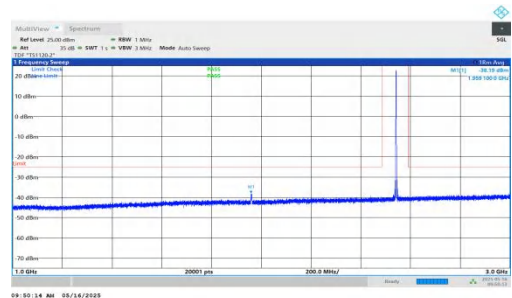
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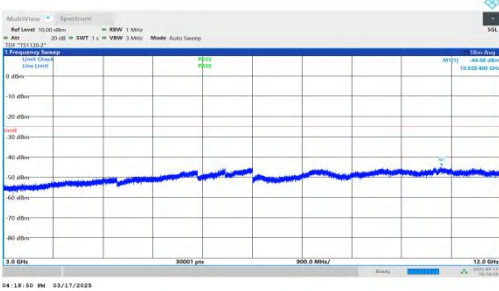
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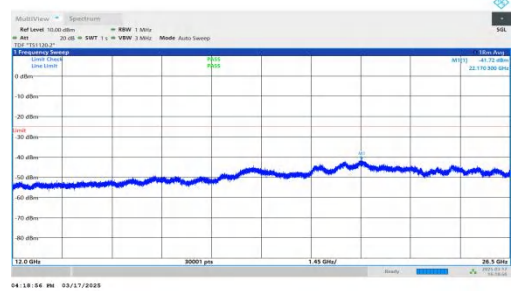
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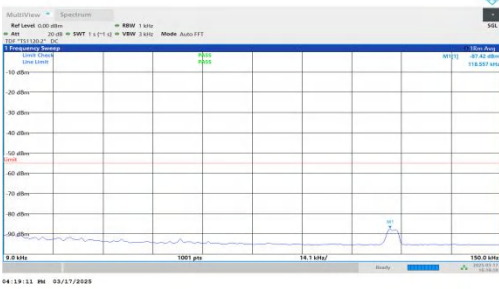
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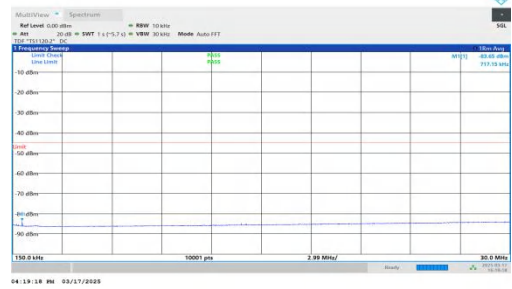
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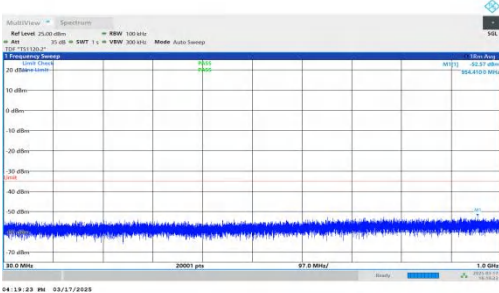
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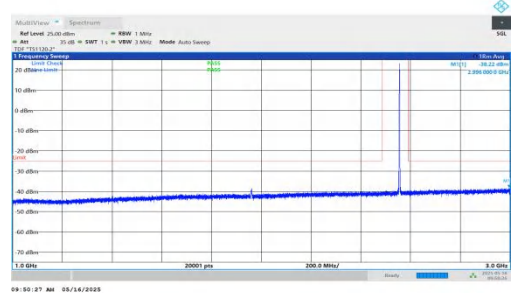
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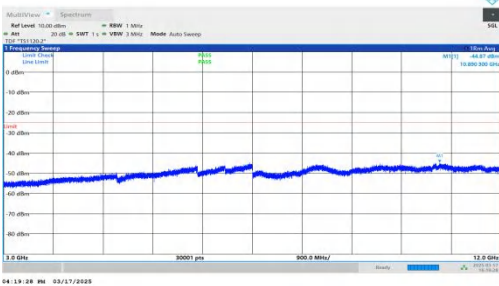
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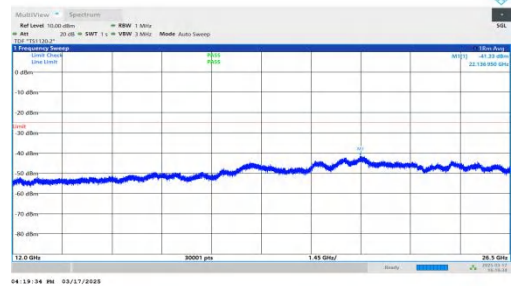
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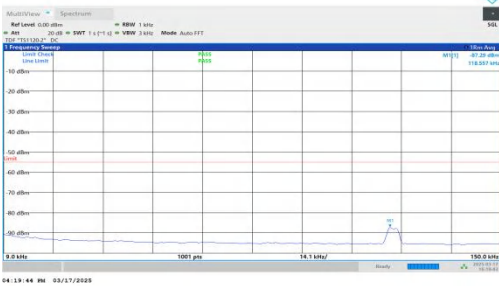
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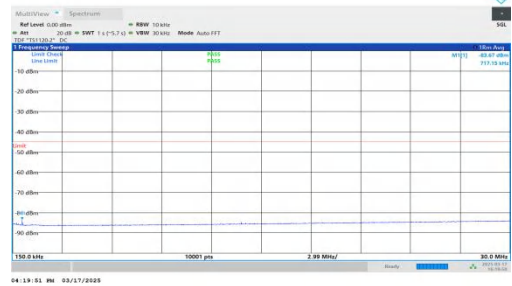
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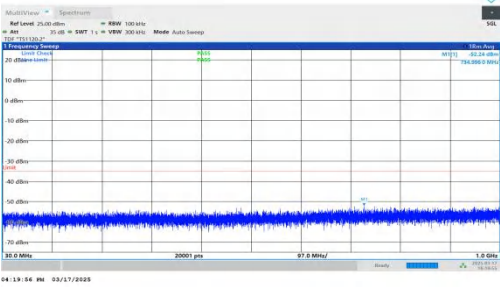
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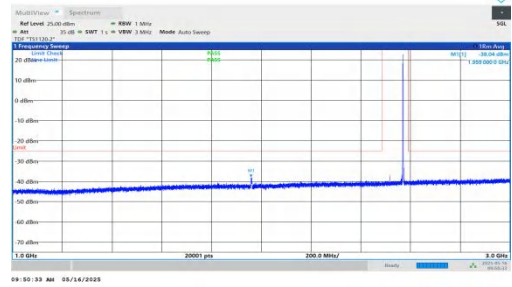
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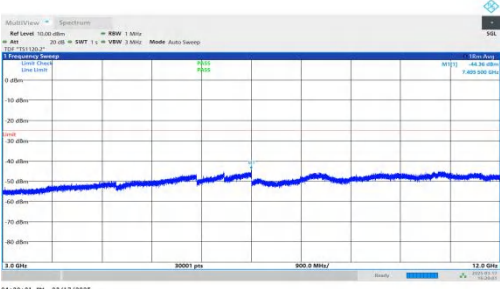
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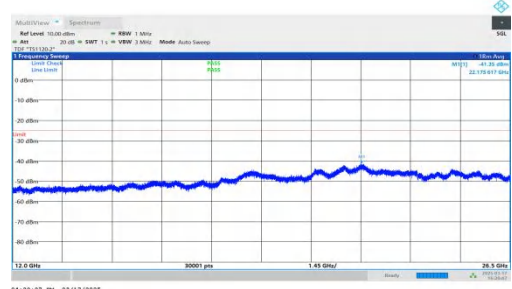
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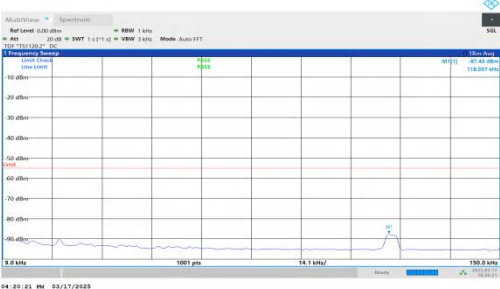
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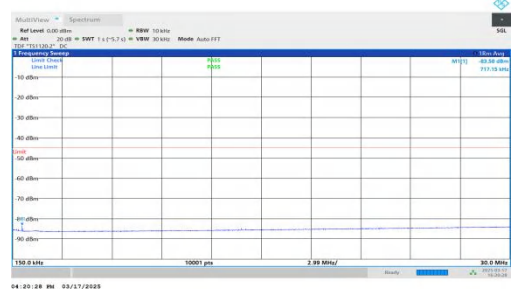
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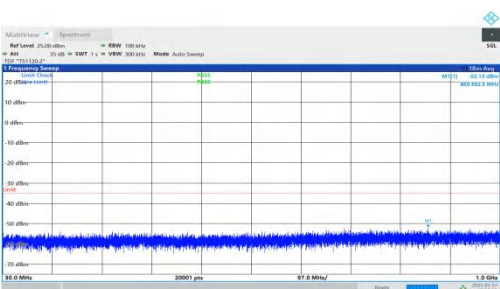
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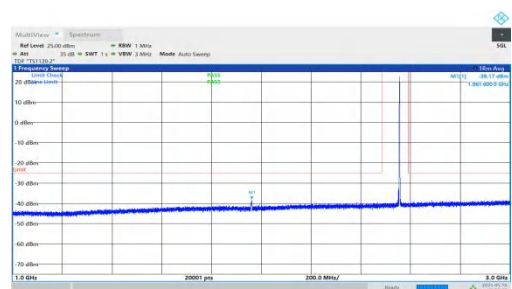
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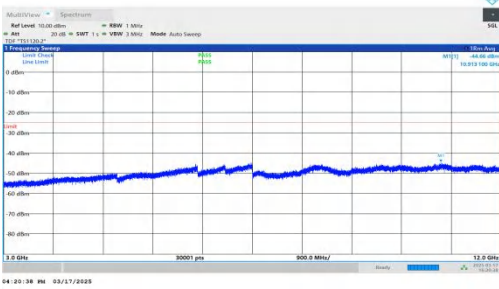
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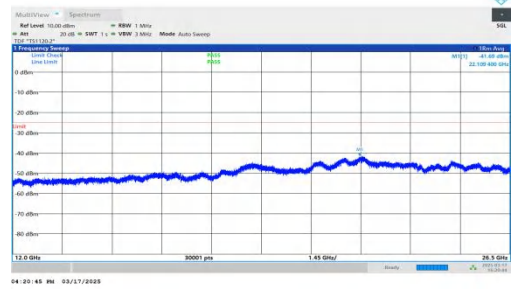
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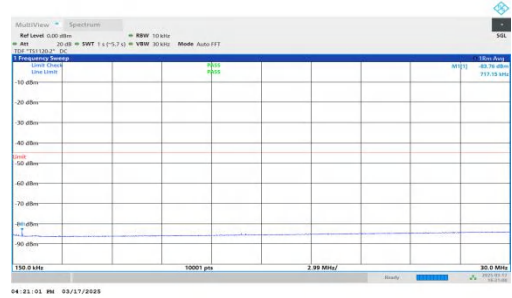
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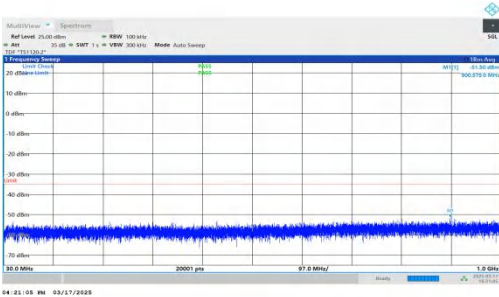
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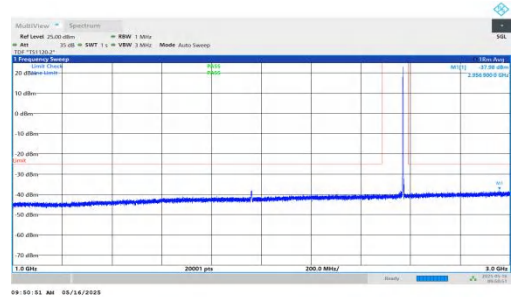
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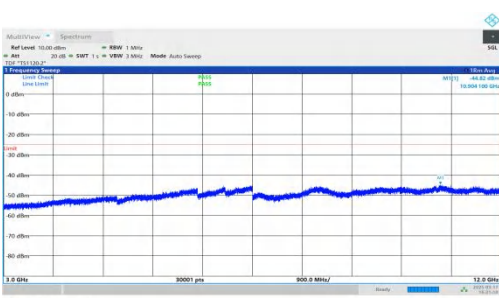
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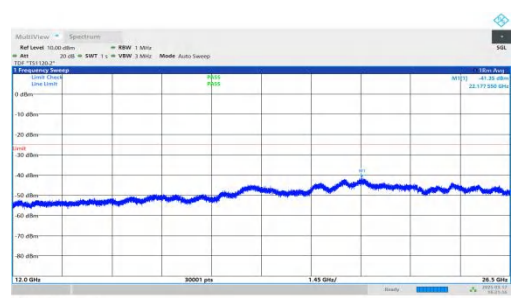
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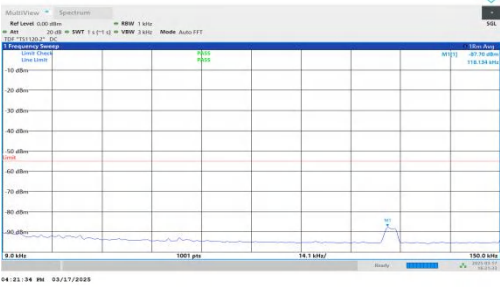
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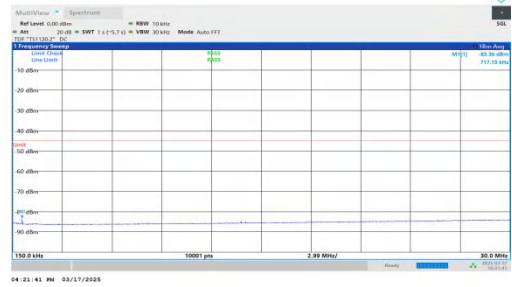
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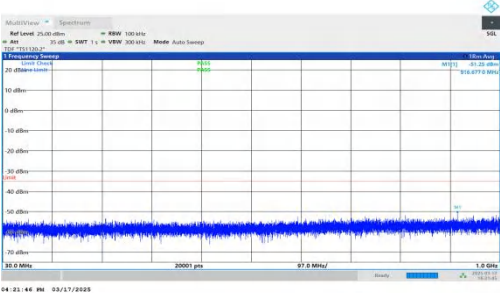
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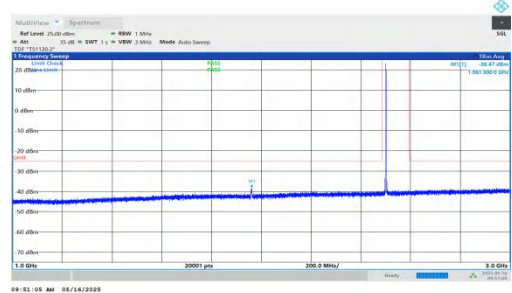
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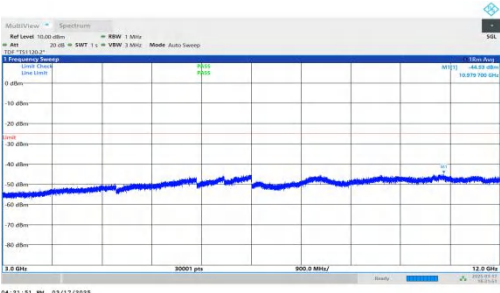
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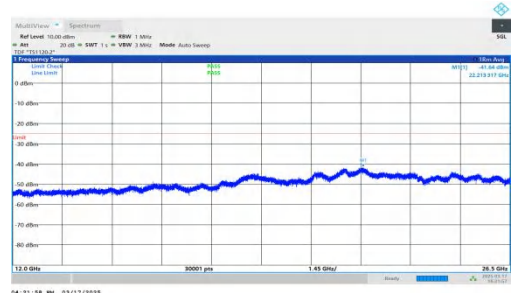
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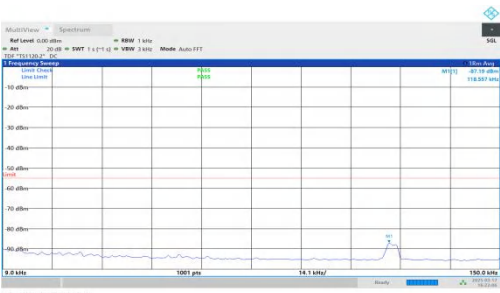
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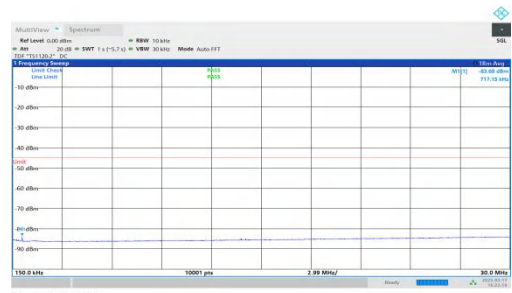
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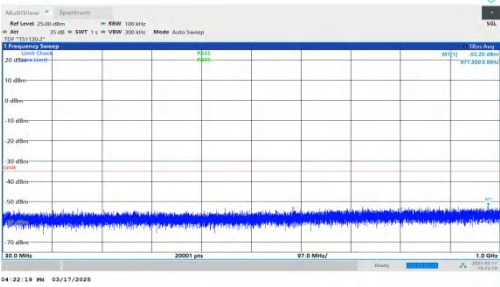
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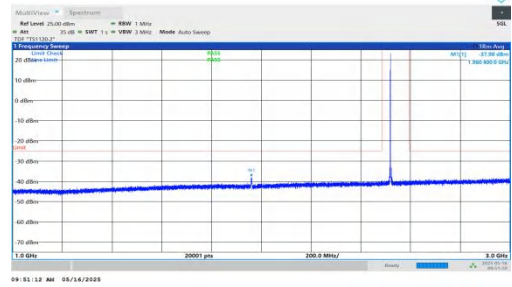
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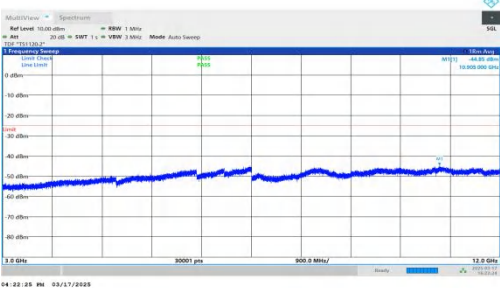
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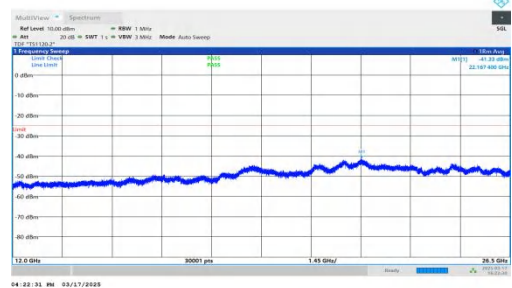
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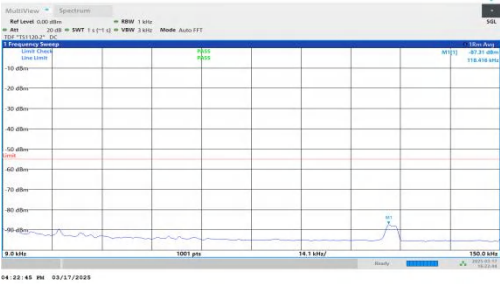
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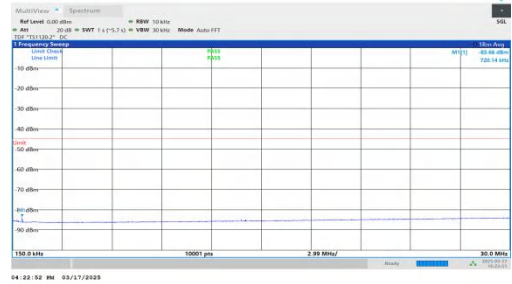
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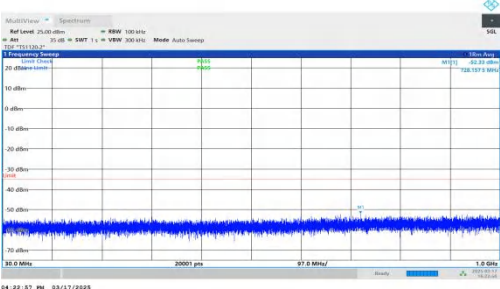
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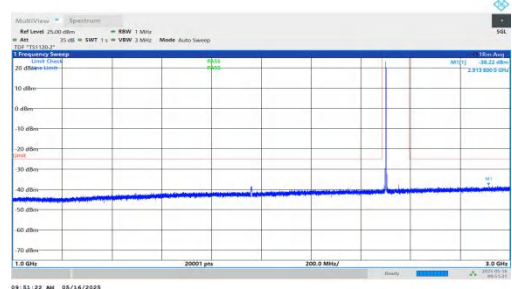
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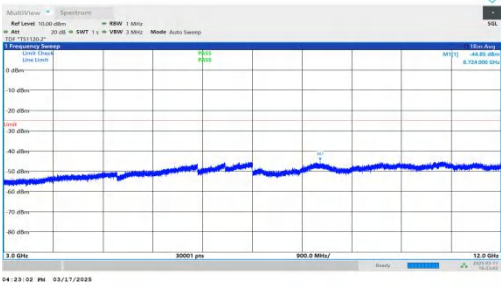
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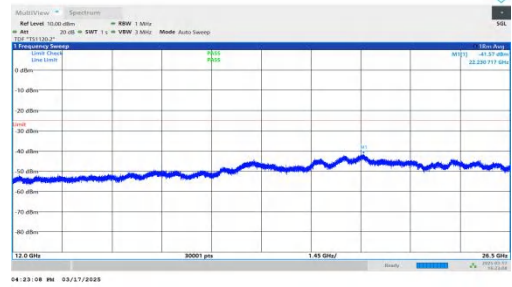
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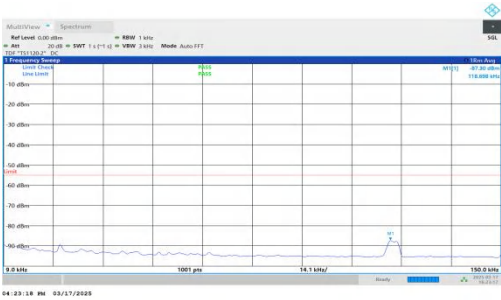
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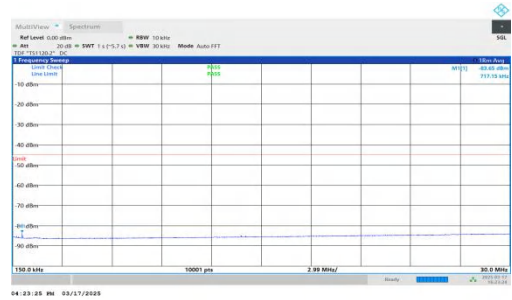
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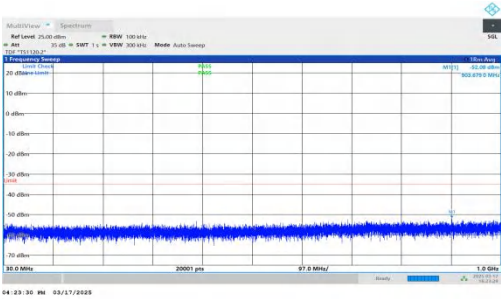
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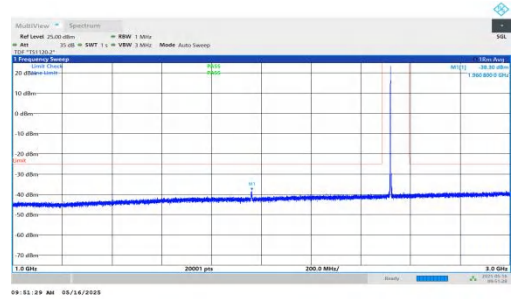
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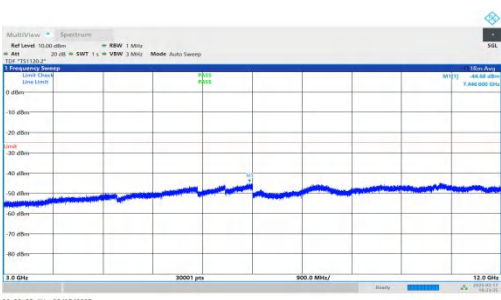
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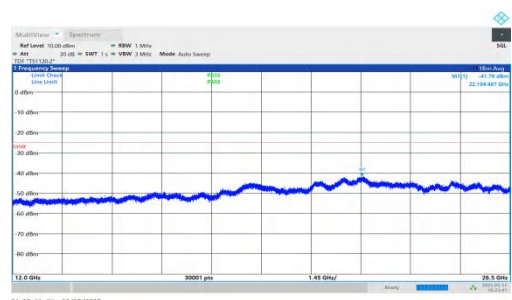
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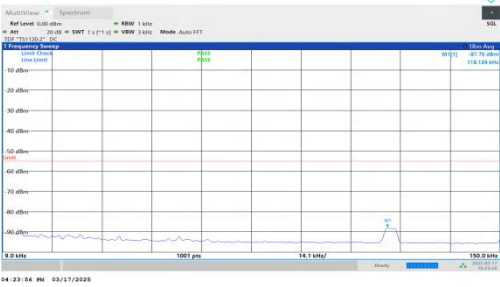
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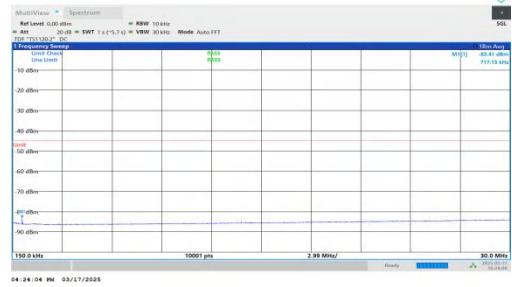
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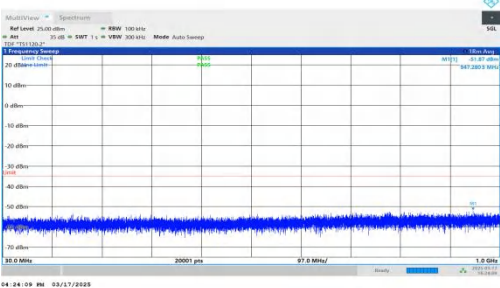
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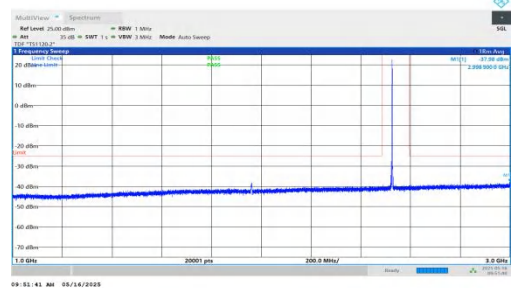
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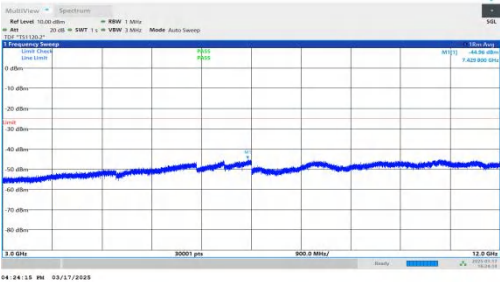
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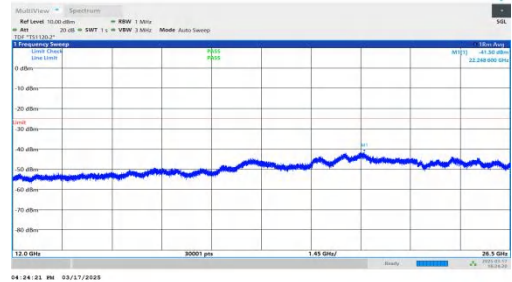
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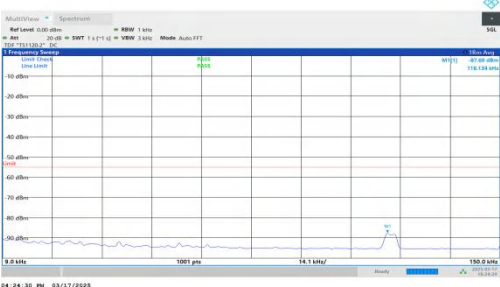
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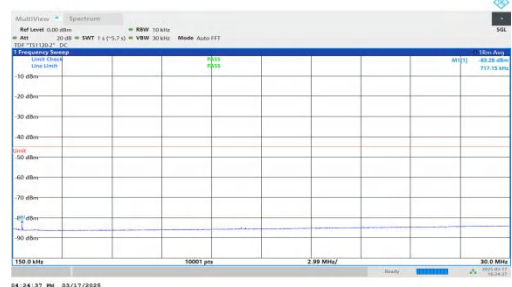
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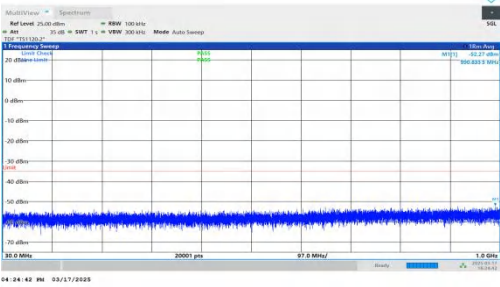
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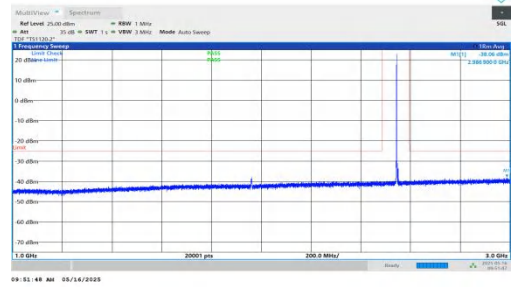
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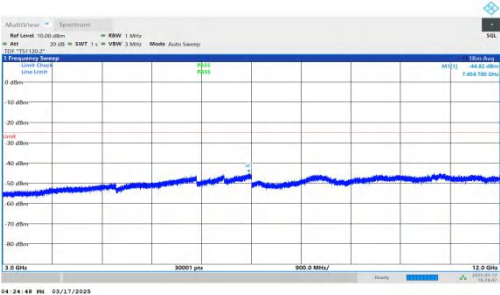
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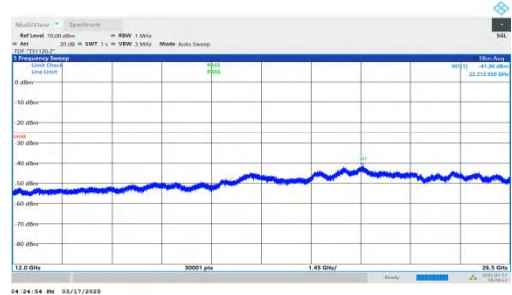
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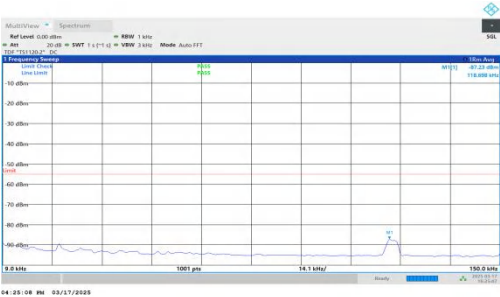
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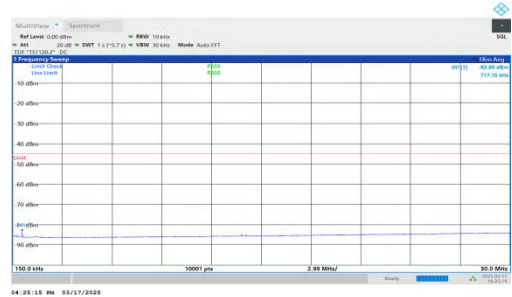
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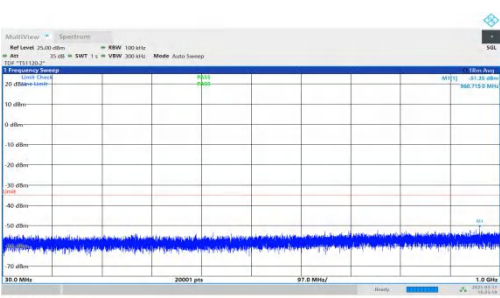
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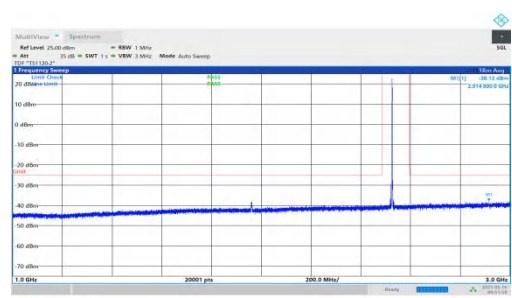
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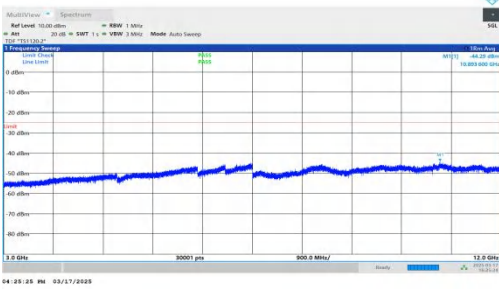
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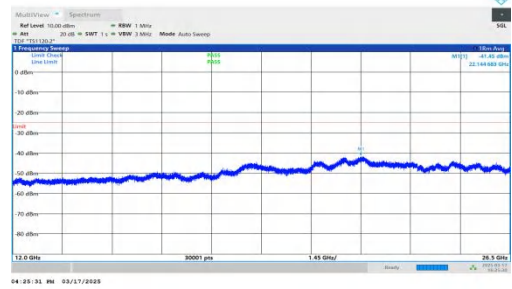
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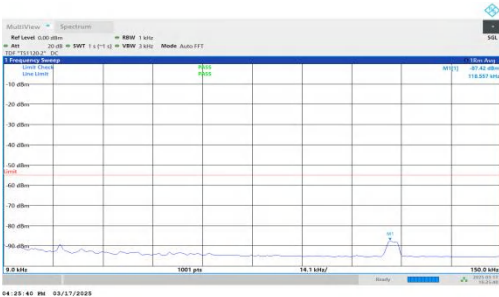
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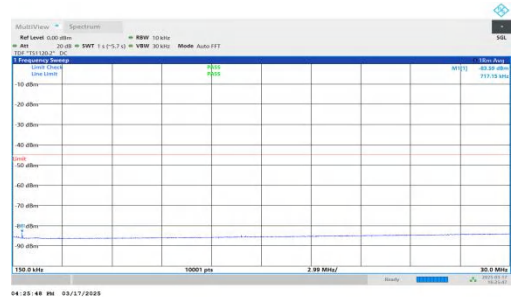
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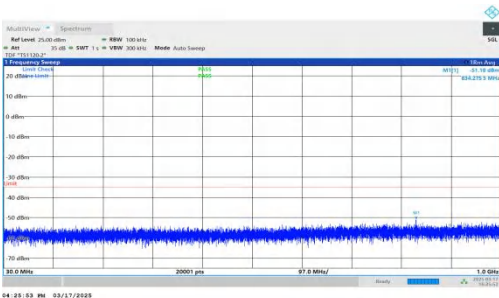
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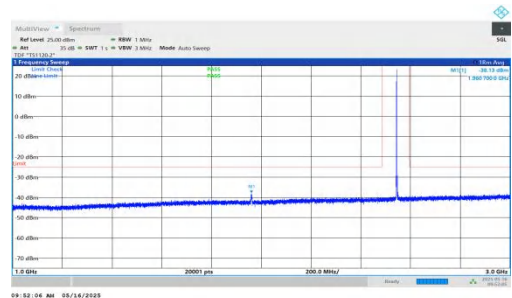
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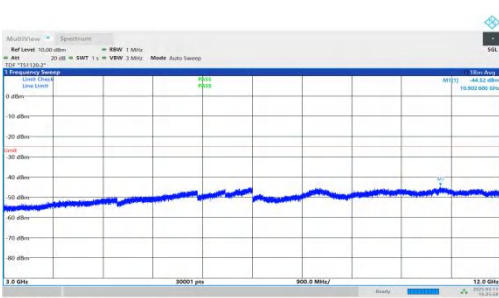
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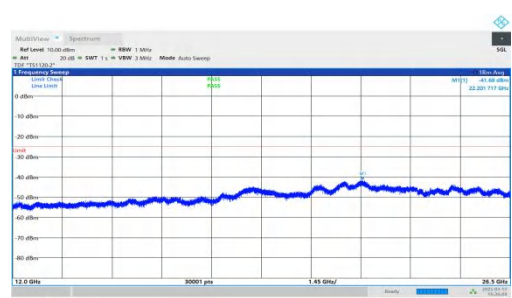
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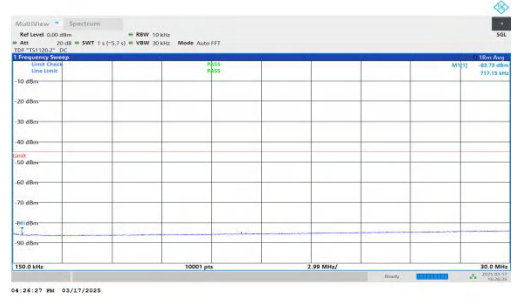
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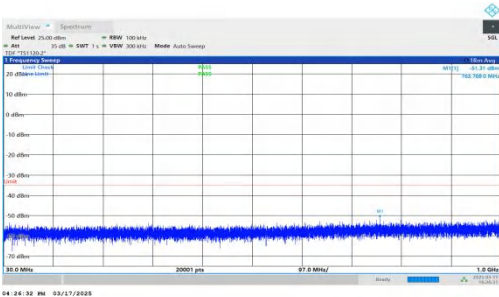
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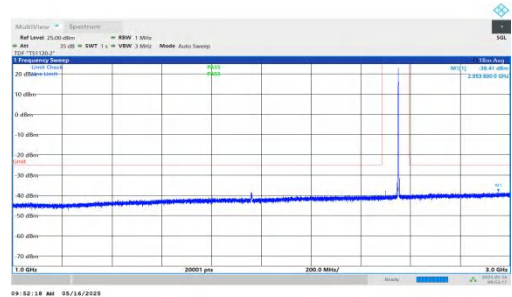
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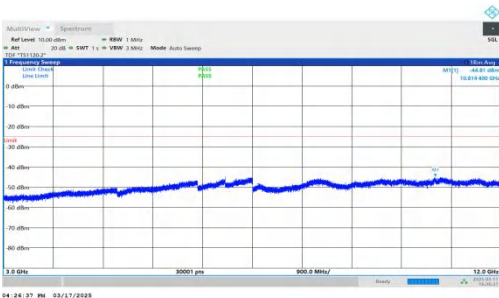
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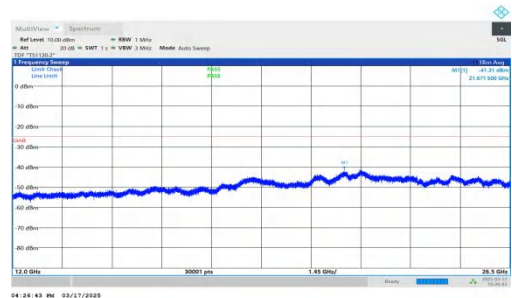
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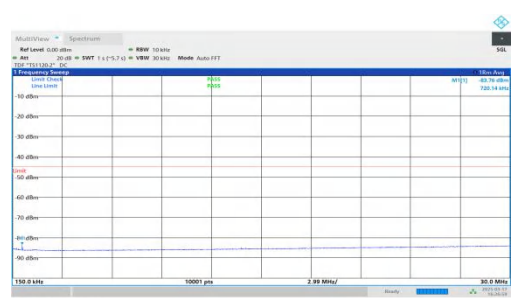
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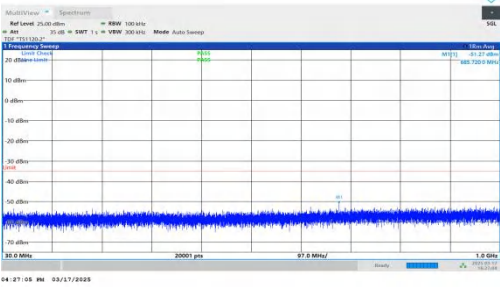
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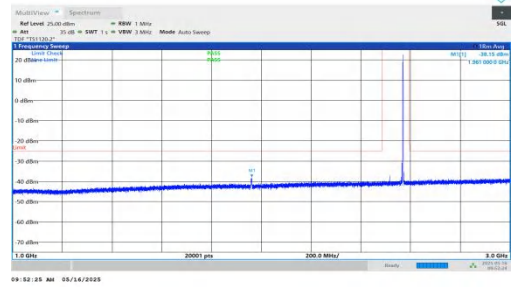
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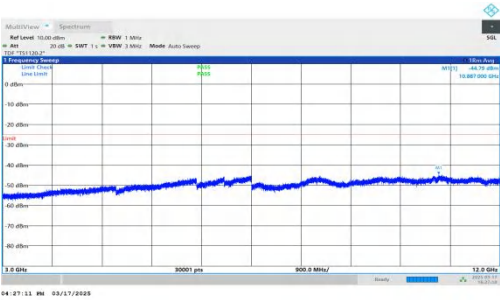
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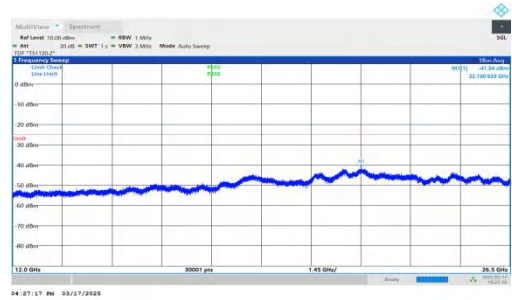
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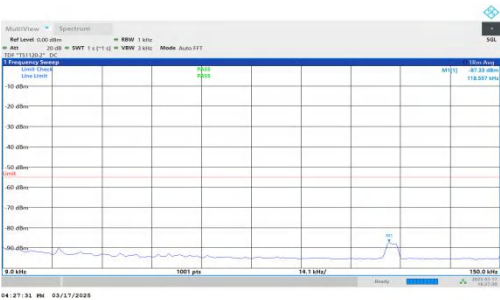
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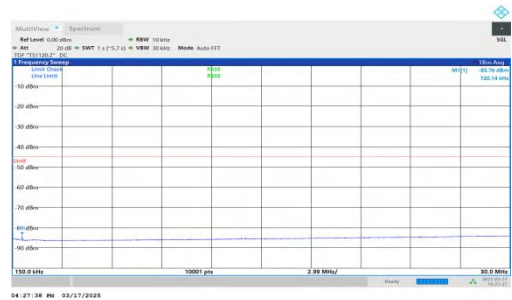
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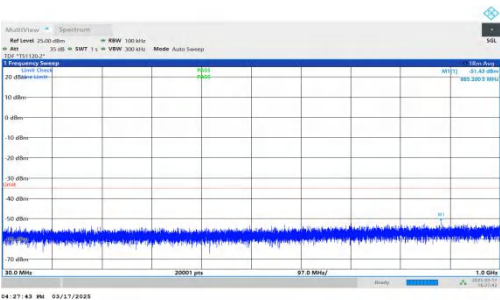
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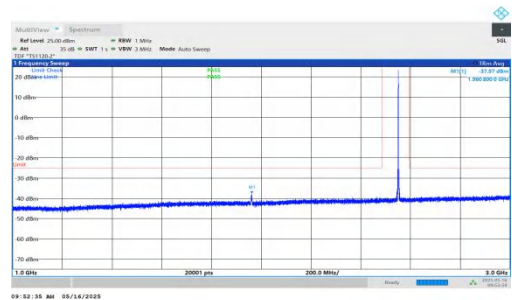
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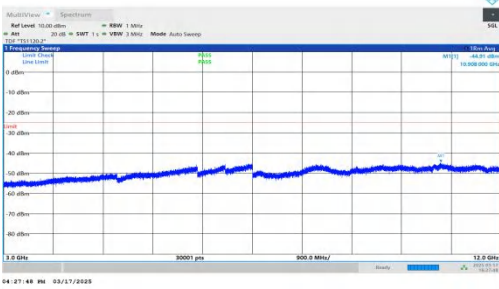
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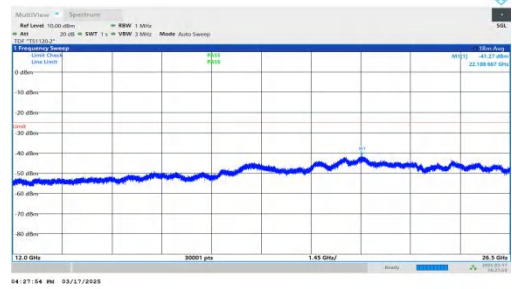
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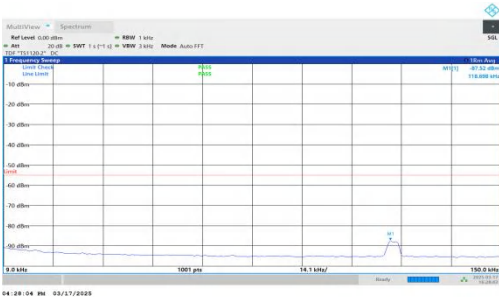
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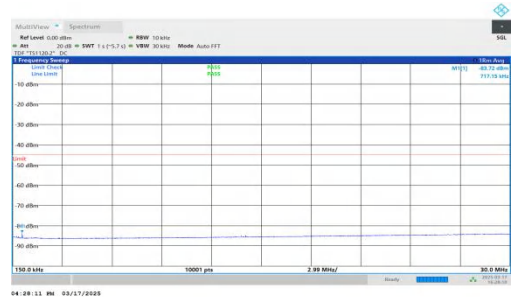
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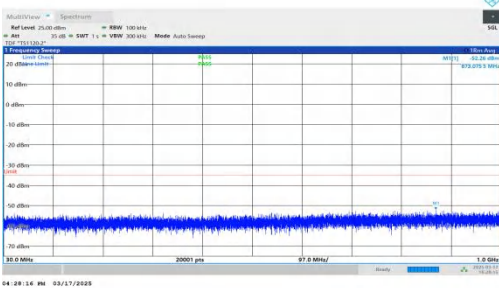
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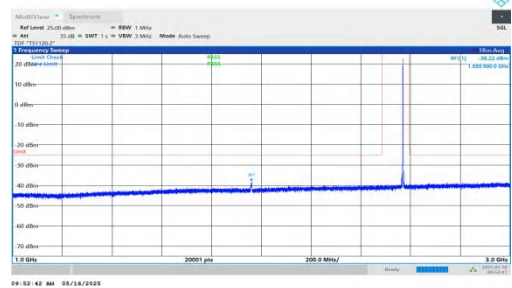
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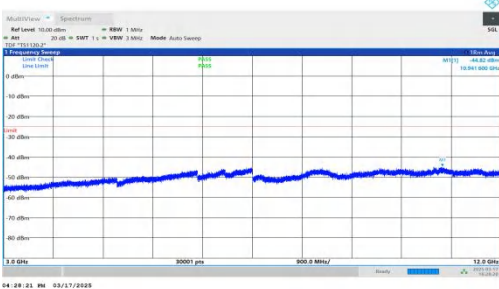
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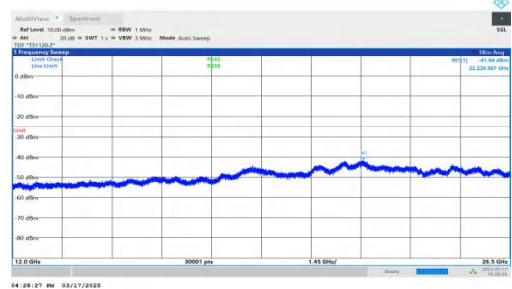
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NTNV-DC_2A-n7A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-QPSK-Inner_1RB_Right-3000-12000-PASS



NTNV-DC_2A-n7A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-QPSK-Inner_1RB_Right-12000-26500-PASS

Frequency Stability for ENDC

Test Result

Voltage											
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage (Vdc)	Temperature(°C)	FL(MHz)	FH(MHz)	Limit (MHz)	Verdict
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	VL	NT	2525.54	2543.41	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	VN	NT	2525.53	2543.41	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	VH	NT	2525.54	2543.42	2500-2570	PASS

Temperature											
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage (Vdc)	Temperature(°C)	FL(MHz)	FH(MHz)	Limit (MHz)	Verdict
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	-30	2525.55	2543.43	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	-20	2525.52	2543.40	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	-10	2525.53	2543.41	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	0	2525.54	2543.39	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	10	2525.54	2543.43	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	20	2525.54	2543.41	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	30	2525.53	2543.40	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	40	2525.56	2543.43	2500-2570	PASS
DC_2A-n7A	15	5+20	DFT-QPSK	M+M	Outer_FuII	NV	50	2525.53	2543.41	2500-2570	PASS