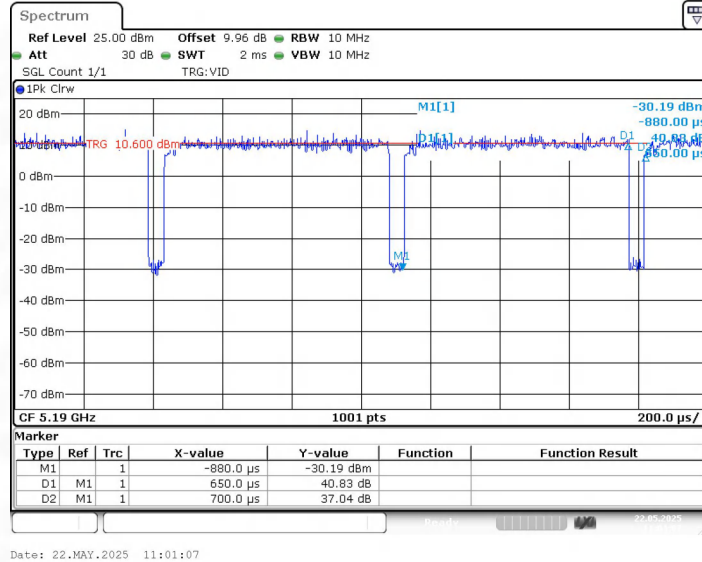
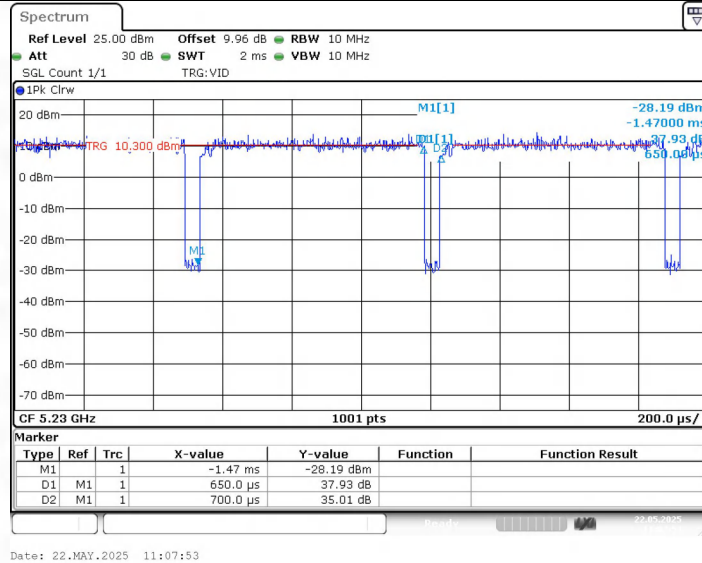
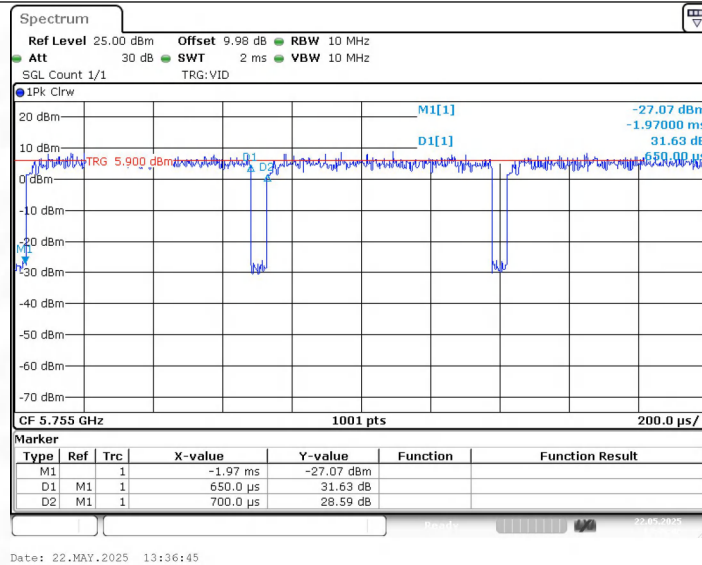




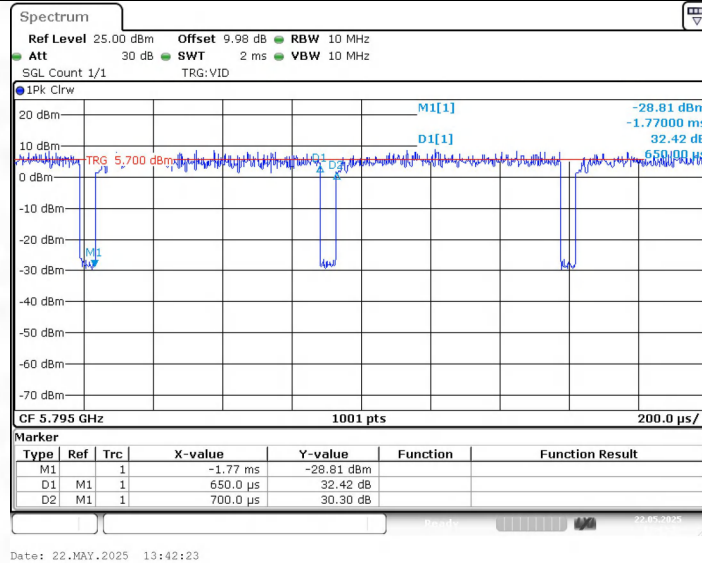
11N40SISO Ant1 5230



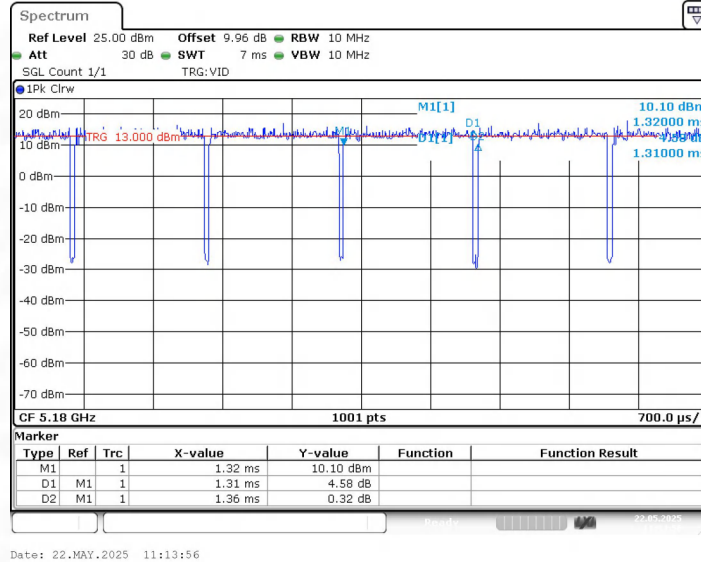
11N40SISO Ant1 5755



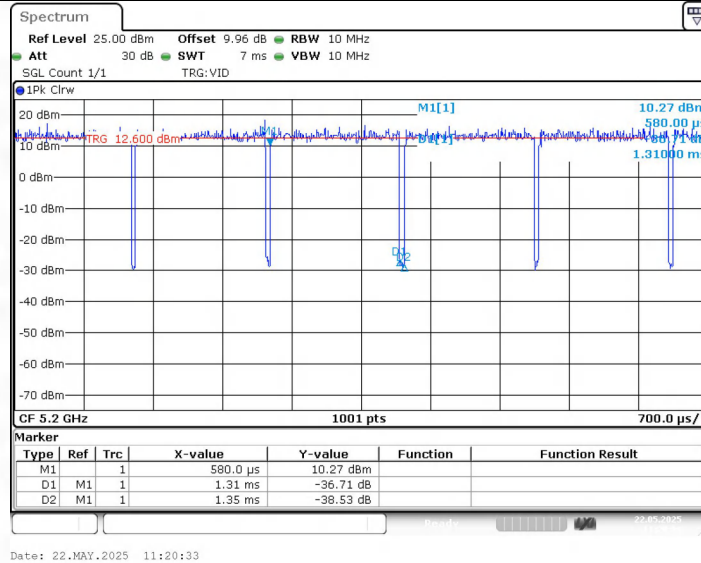
11N40SISO_Ant1_5795



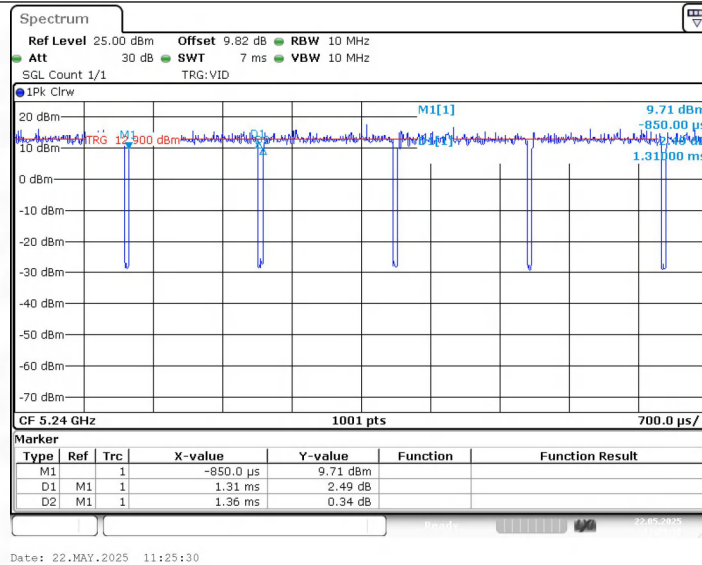
11AC20SISO_Ant1_5180



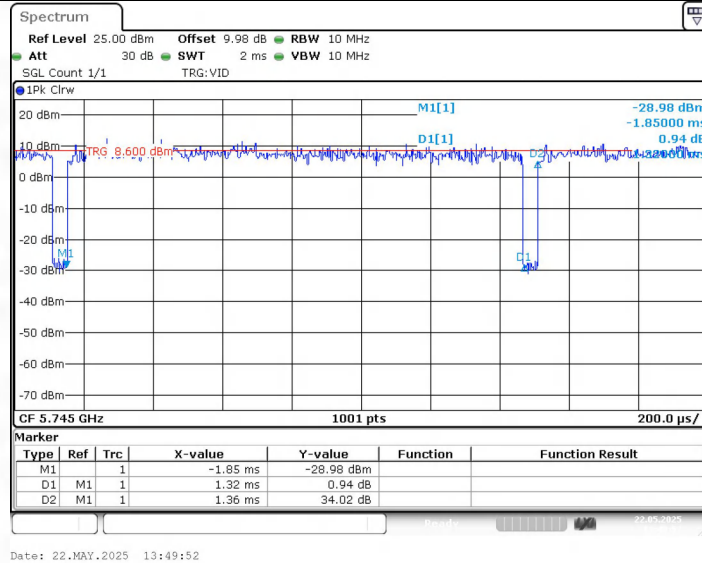
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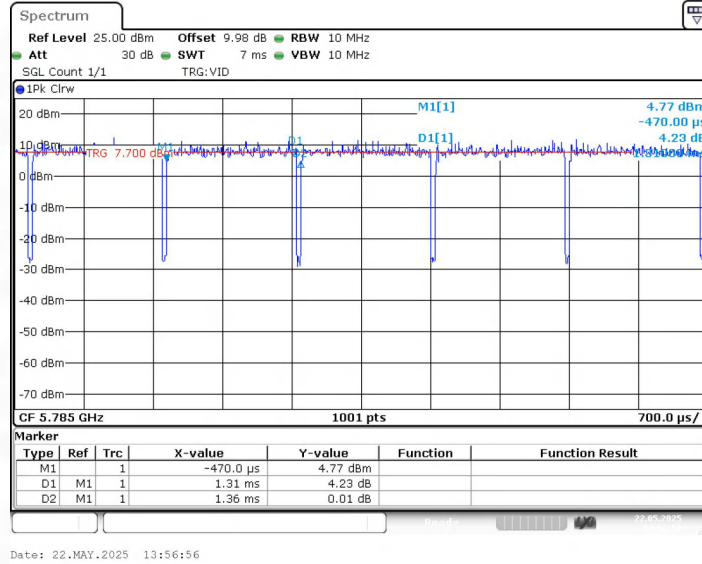
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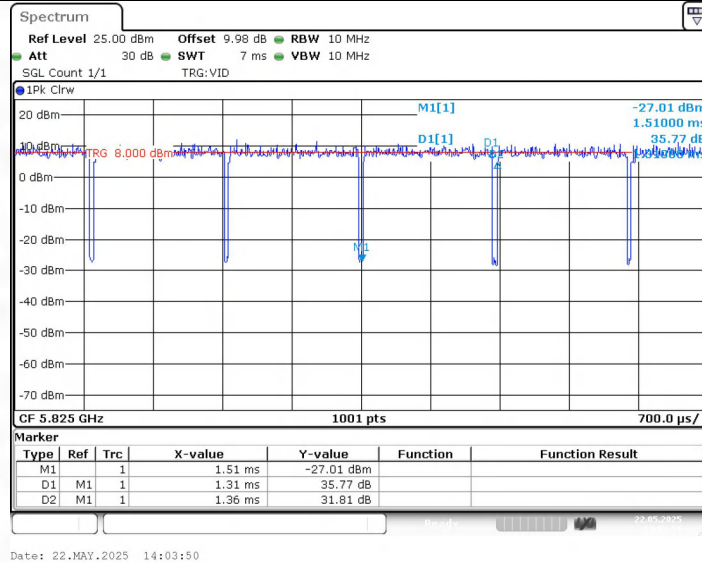
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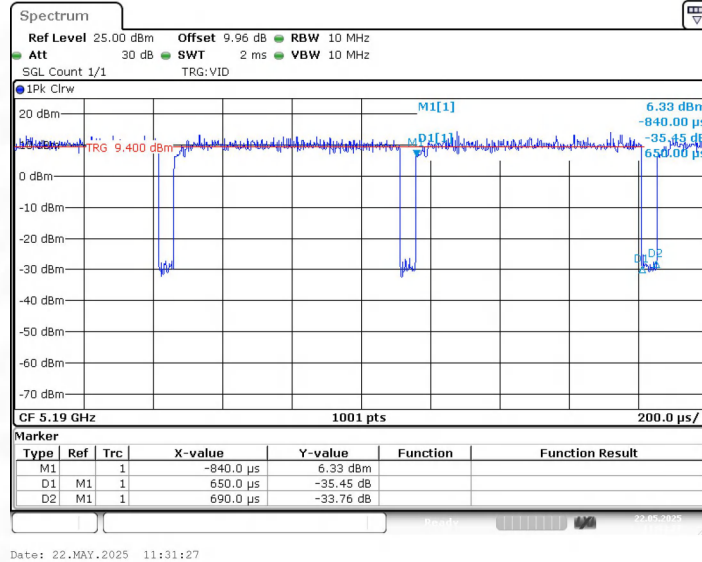
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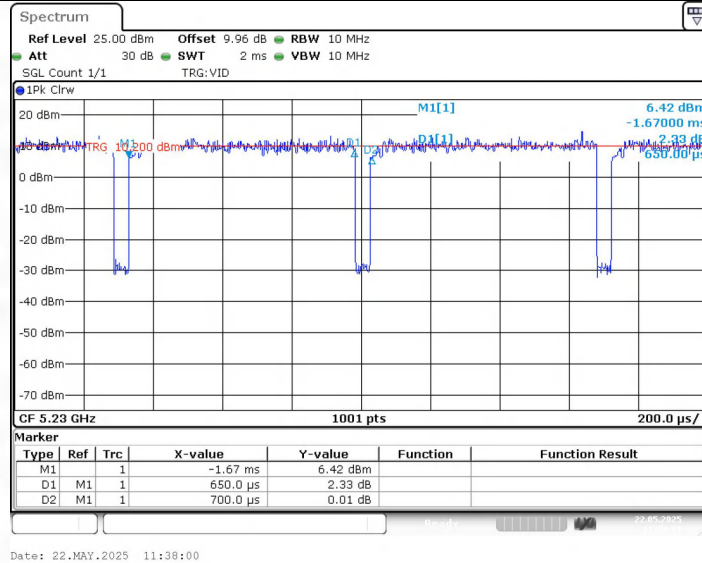
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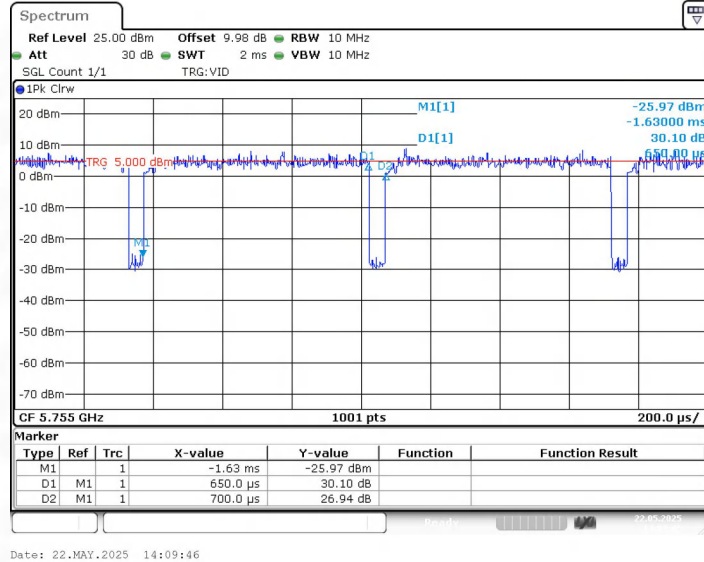
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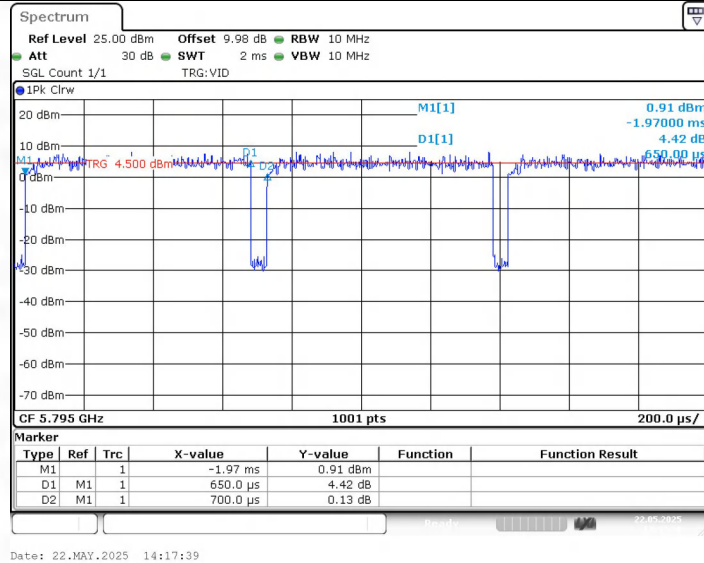
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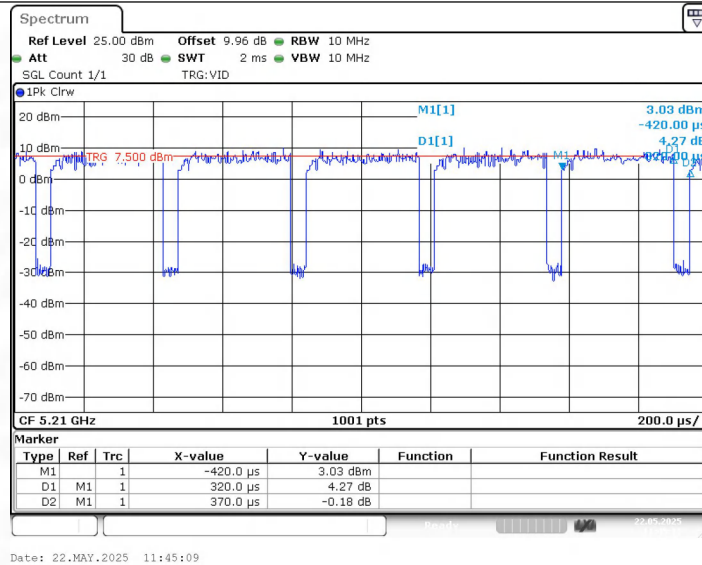
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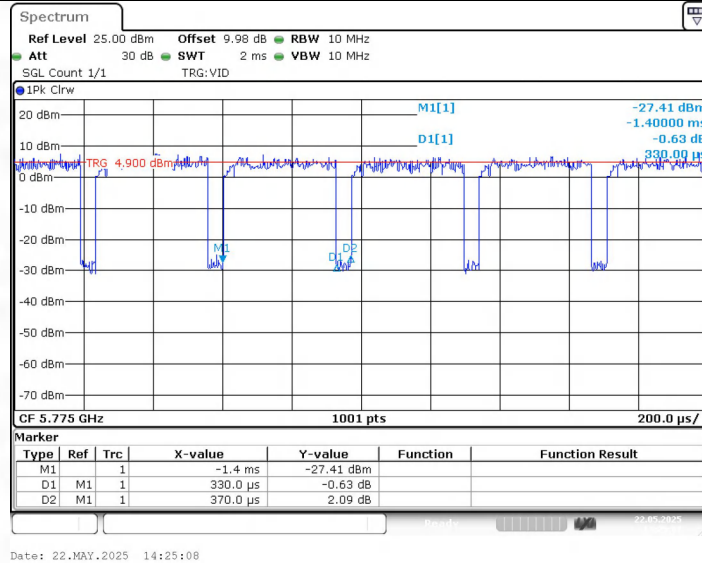
11AC40SISO_Ant1_5795



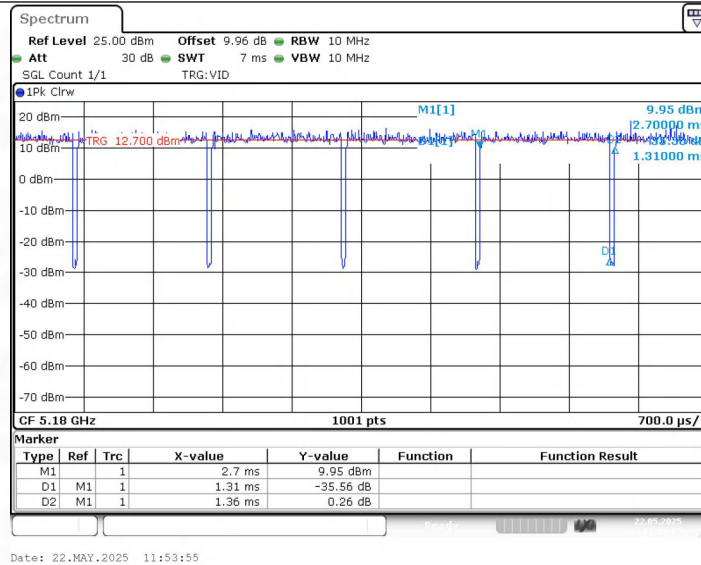
11AC80SISO_Ant1_5210



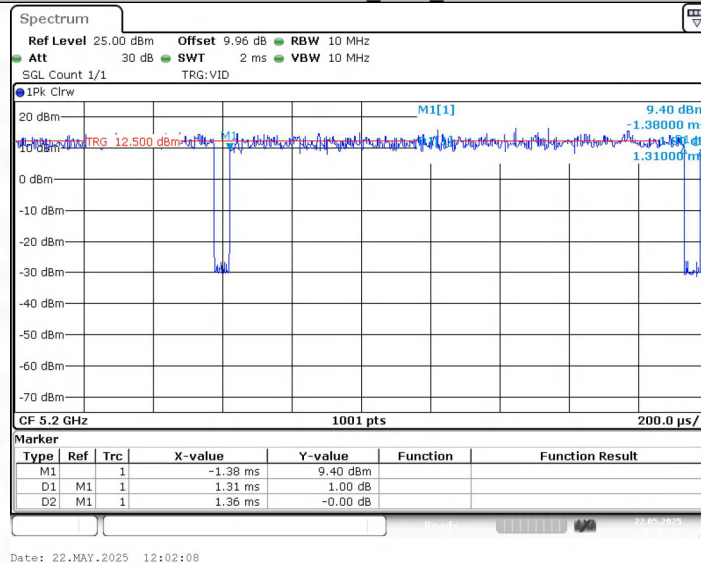
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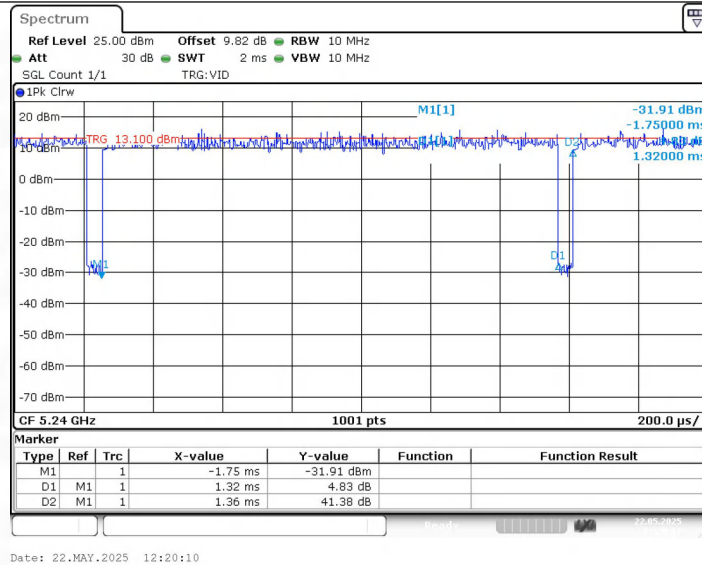
11AX20SISO_Ant1_5180



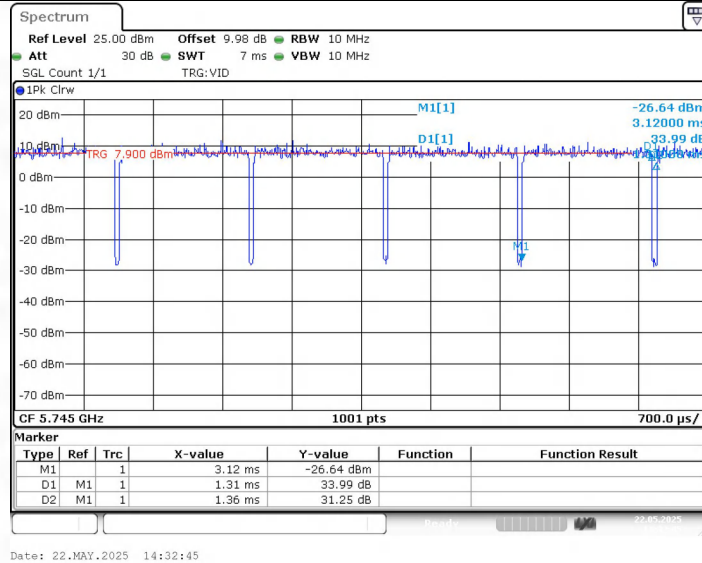
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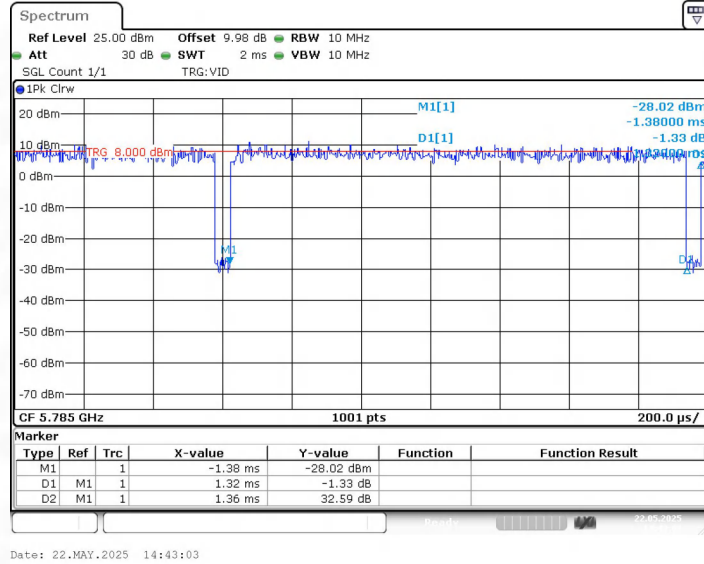
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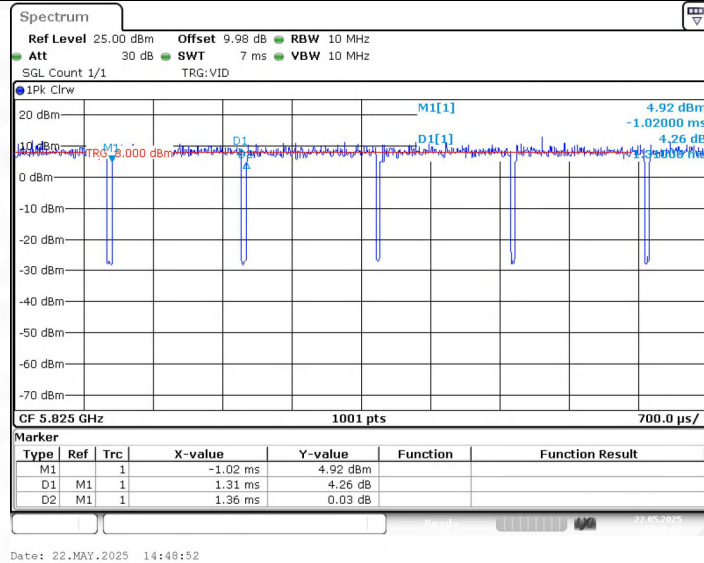
11AX20SISO_Ant1_5745



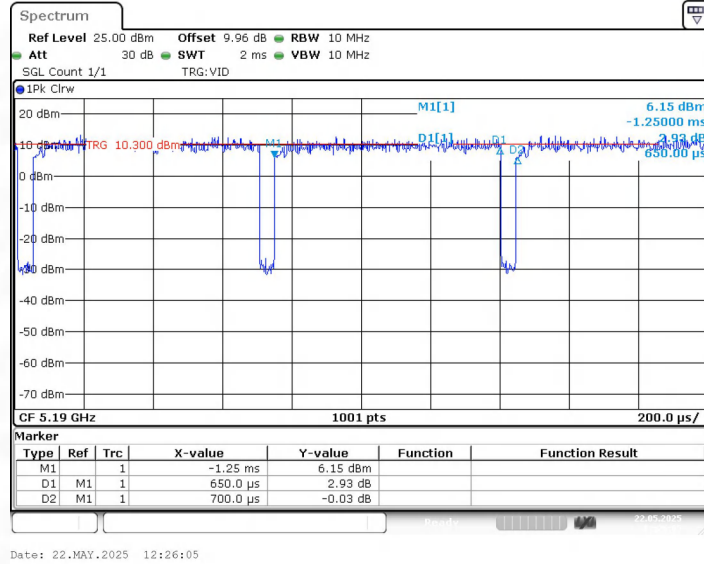
11AX20SISO_Ant1_5785



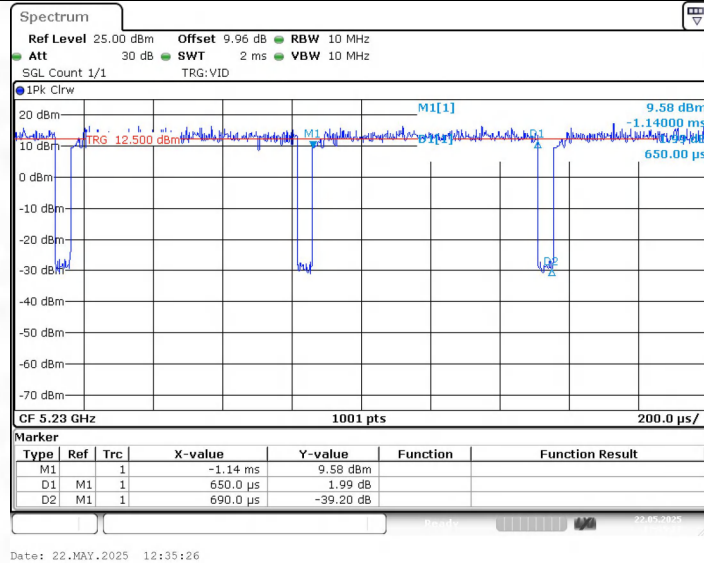
11AX20SISO Ant1_5825



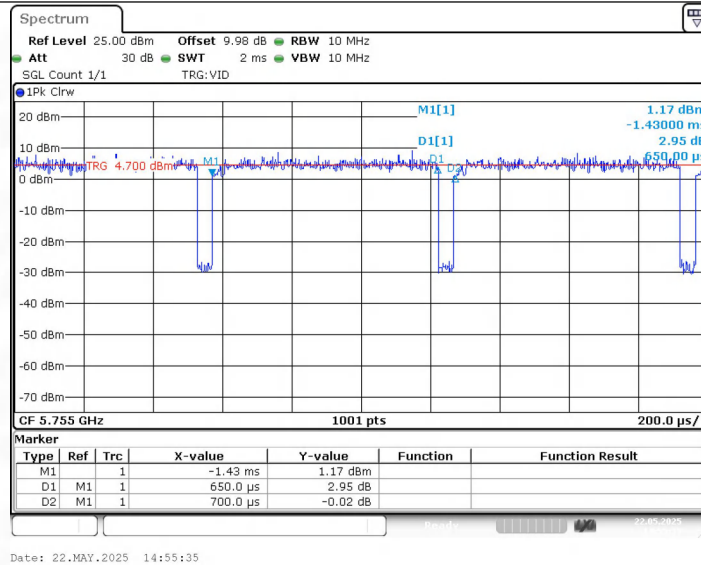
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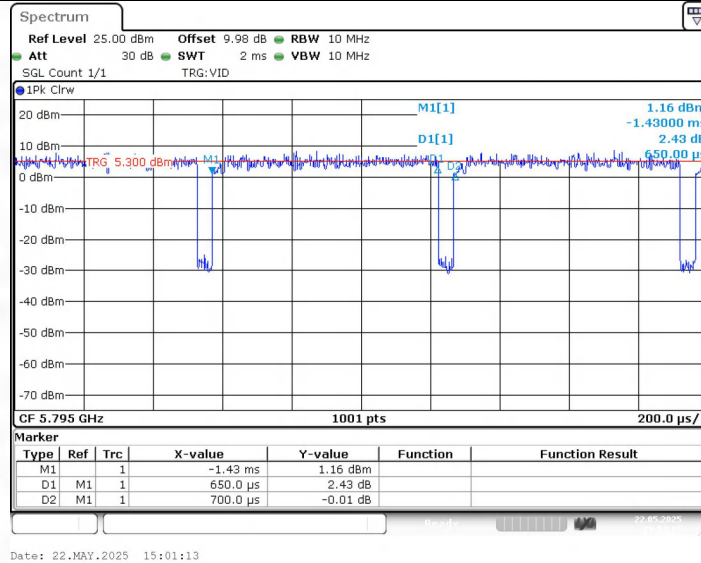
11AX40SISO_Ant1_5230



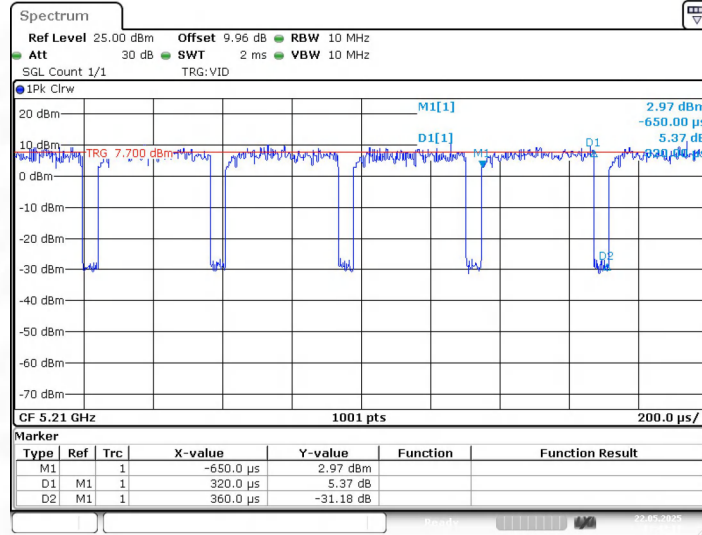
11AX40SISO_Ant1_5755



11AX40SISO_Ant1_5795

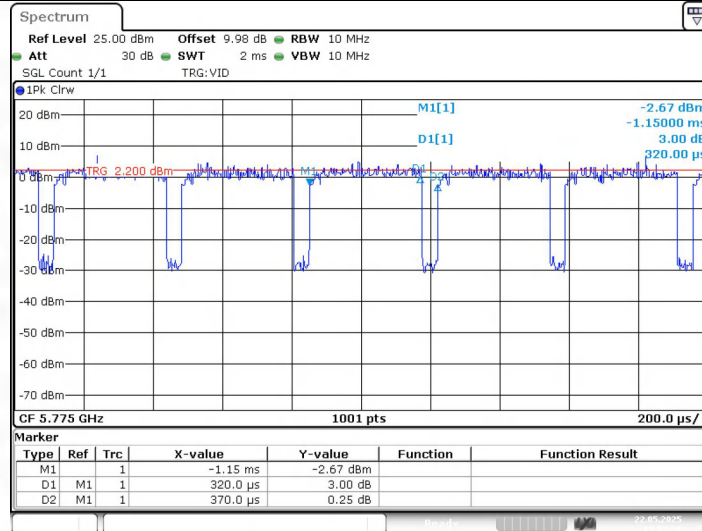


11AX80SISO_Ant1_5210



Date: 22.MAY.2025 12:42:18

11AX80SISO Ant1 5775



Date: 22.MAY.2025 15:07:38

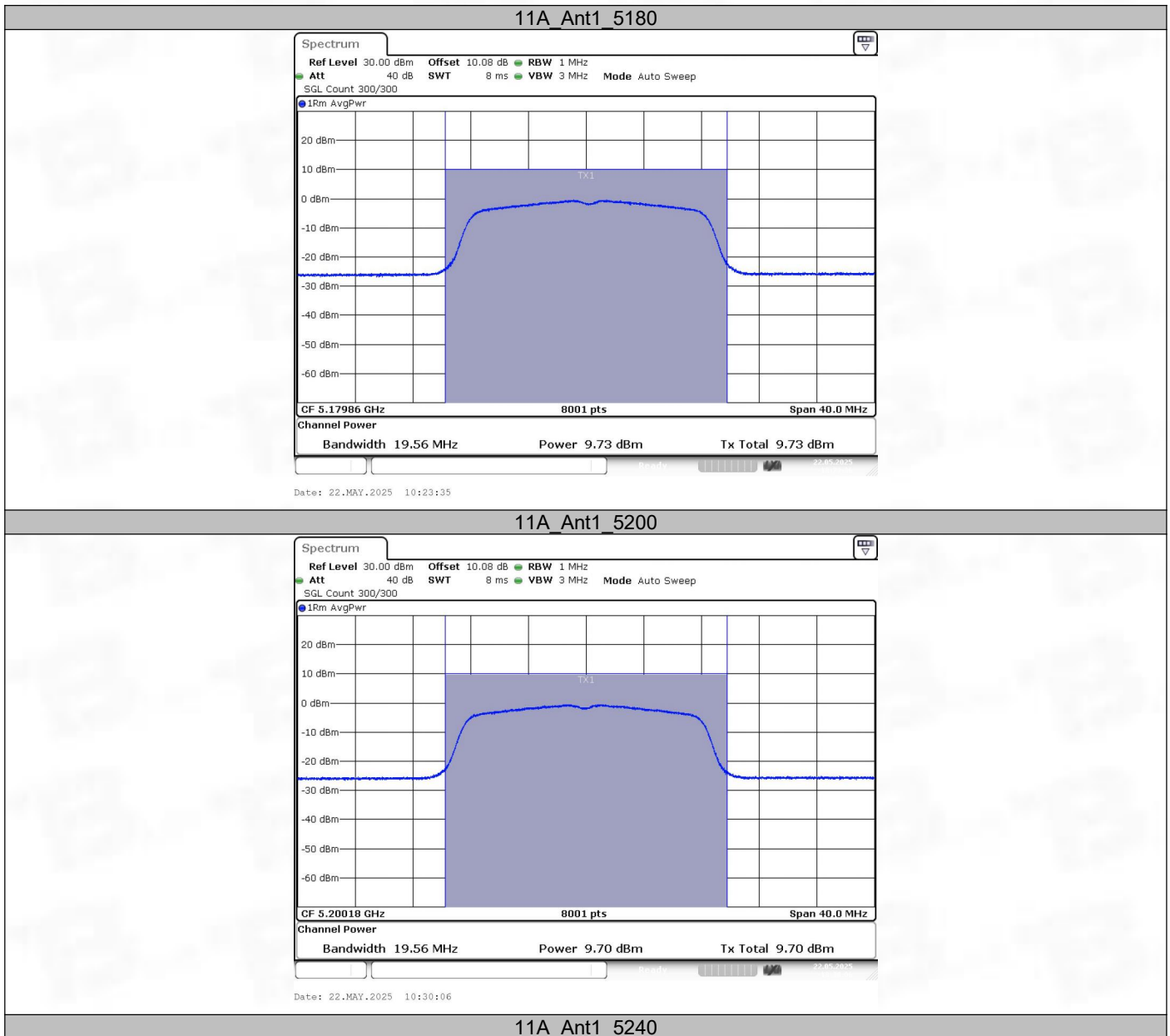
1.5 Appendix C: Maximum conducted output power

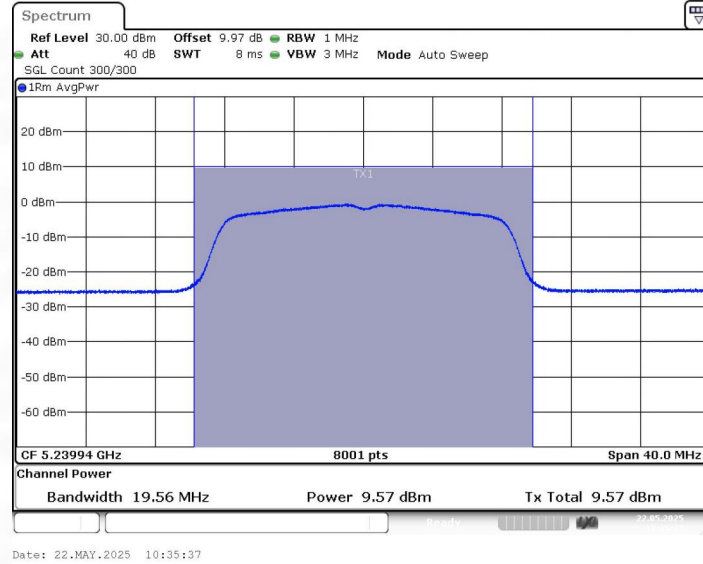
1.5.1 Test Result Channel Power

Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	9.61	97.20	0.12	9.73	≤23.98	PASS
		5200	9.58	97.20	0.12	9.70	≤23.98	PASS
		5240	9.42	96.55	0.15	9.57	≤23.98	PASS
		5745	4.06	97.20	0.12	4.18	≤30.00	PASS
		5785	4.06	96.53	0.15	4.21	≤30.00	PASS
		5825	4.44	96.53	0.15	4.59	≤30.00	PASS
11N20SISO	Ant1	5180	9.10	97.01	0.13	9.23	≤23.98	PASS
		5200	9.64	97.01	0.13	9.77	≤23.98	PASS
		5240	9.23	97.01	0.13	9.36	≤23.98	PASS
		5745	4.26	97.04	0.13	4.39	≤30.00	PASS
		5785	4.32	97.04	0.13	4.45	≤30.00	PASS
		5825	4.62	97.04	0.13	4.75	≤30.00	PASS
11N40SISO	Ant1	5190	9.53	92.86	0.32	9.85	≤23.98	PASS
		5230	9.26	92.86	0.32	9.58	≤23.98	PASS
		5755	4.34	92.86	0.32	4.66	≤30.00	PASS
		5795	4.21	92.86	0.32	4.53	≤30.00	PASS
11AC20SISO	Ant1	5180	8.92	96.32	0.16	9.08	≤23.98	PASS
		5200	8.95	97.04	0.13	9.08	≤23.98	PASS
		5240	8.74	96.32	0.16	8.90	≤23.98	PASS
		5745	3.93	97.06	0.13	4.06	≤30.00	PASS
		5785	3.56	96.32	0.16	3.72	≤30.00	PASS
		5825	3.51	96.32	0.16	3.67	≤30.00	PASS
11AC40SISO	Ant1	5190	9.01	94.20	0.26	9.27	≤23.98	PASS
		5230	8.92	92.86	0.32	9.24	≤23.98	PASS
		5755	3.47	92.86	0.32	3.79	≤30.00	PASS
		5795	3.25	92.86	0.32	3.57	≤30.00	PASS
11AC80SISO	Ant1	5210	8.83	86.49	0.63	9.46	≤23.98	PASS
		5775	6.01	89.19	0.50	6.51	≤30.00	PASS

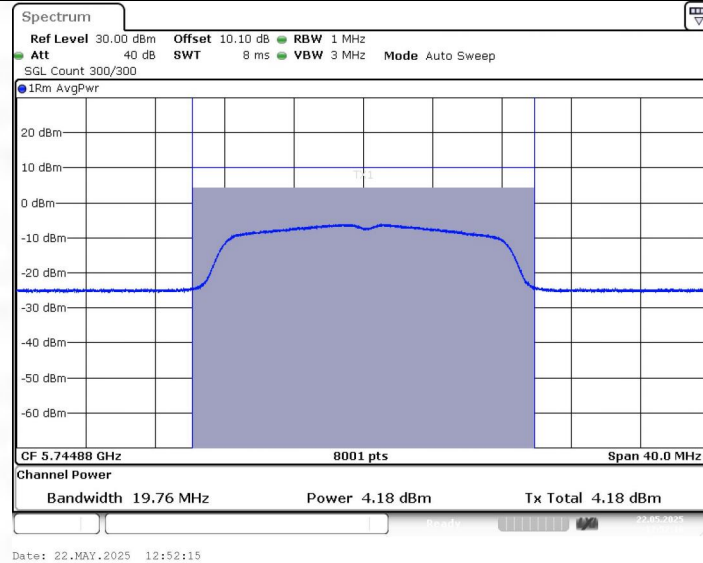
Note: The Duty Cycle Factor is compensated in the graph.

1.5.2 Test Graphs Channel Power

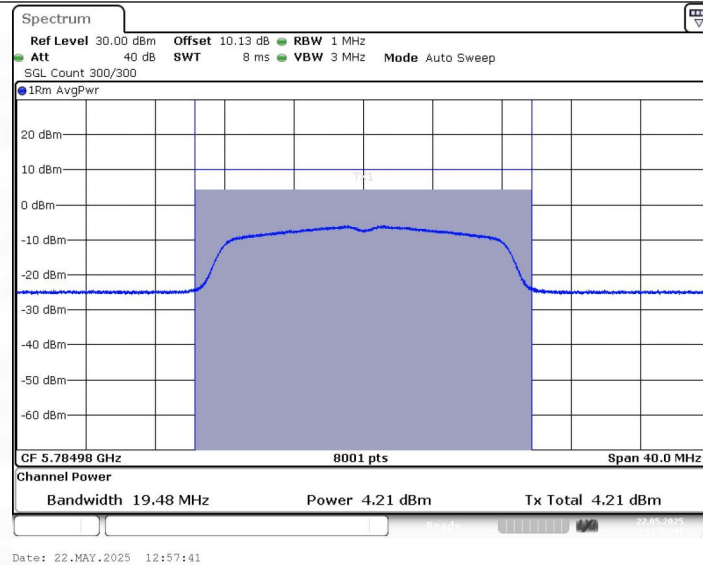




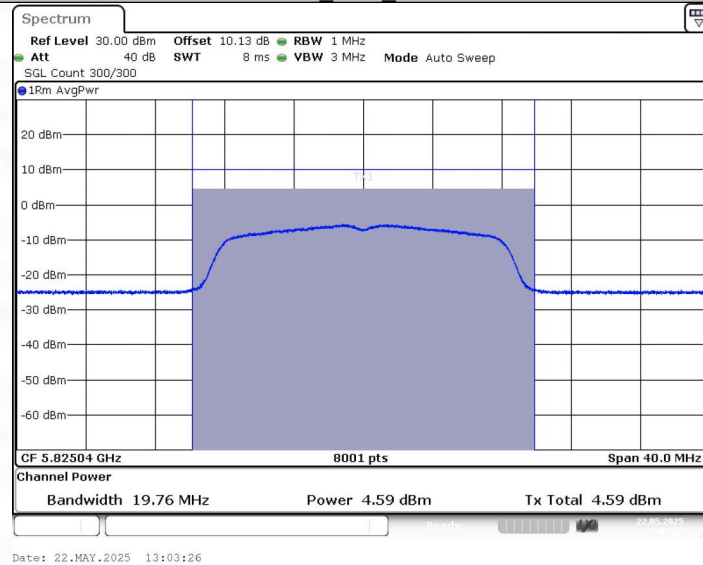
11A_Ant1_5745



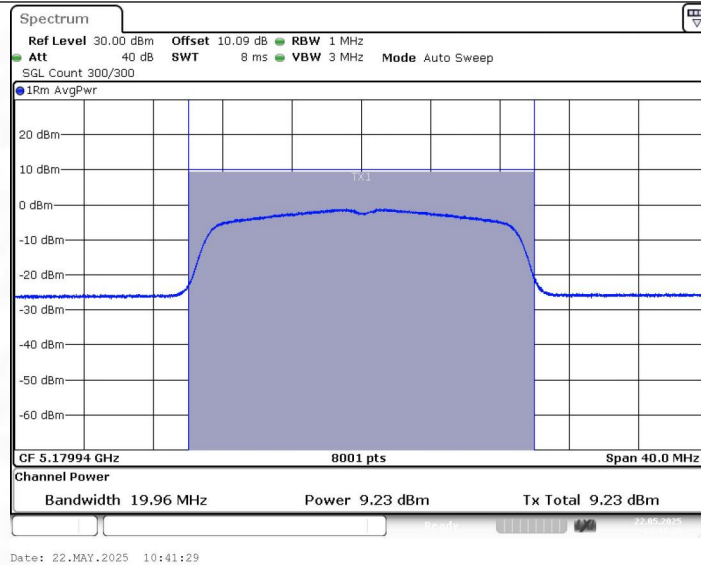
11A_Ant1_5785



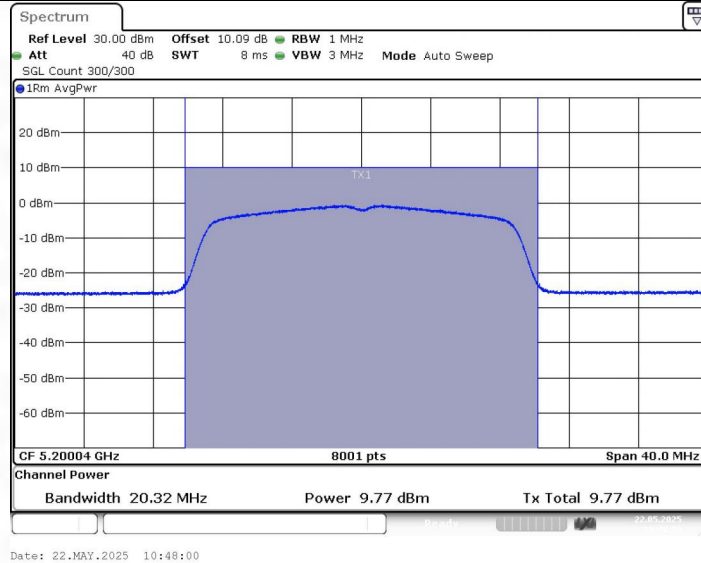
11A Ant1 5825



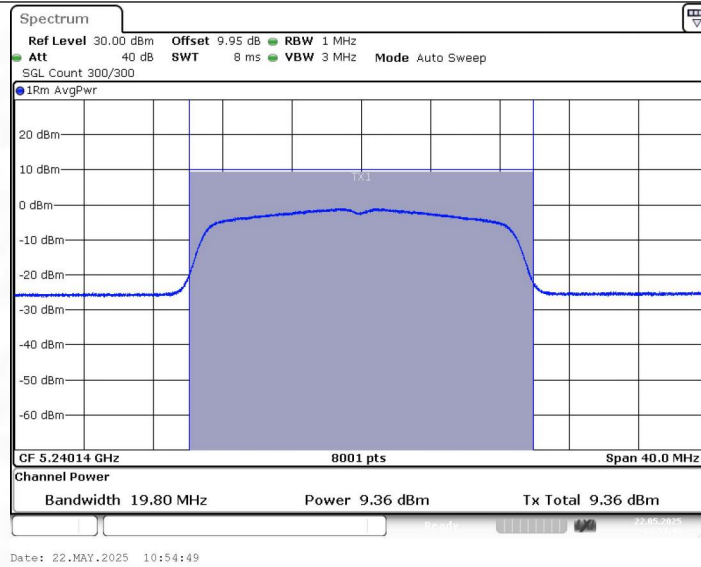
11N20SISO Ant1 5180



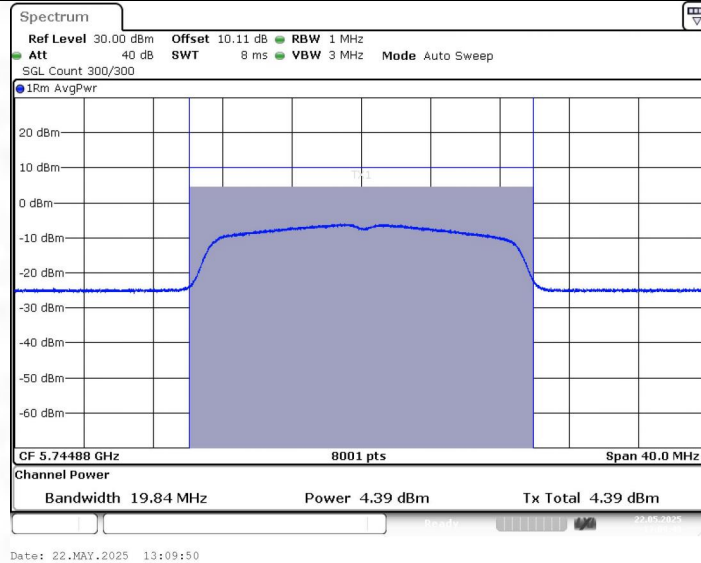
11N20SISO_Ant1_5200



11N20SISO_Ant1_5240



11N20SISO_Ant1_5745



11N20SISO_Ant1_5785