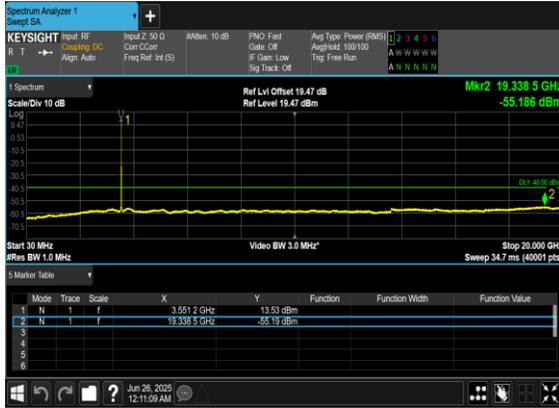
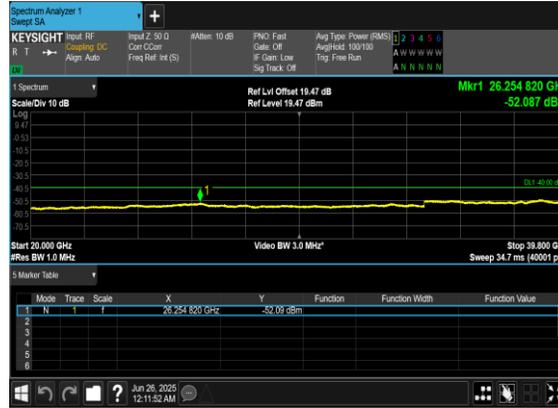




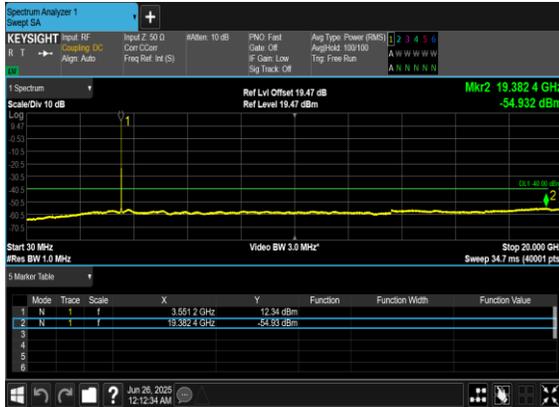
N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





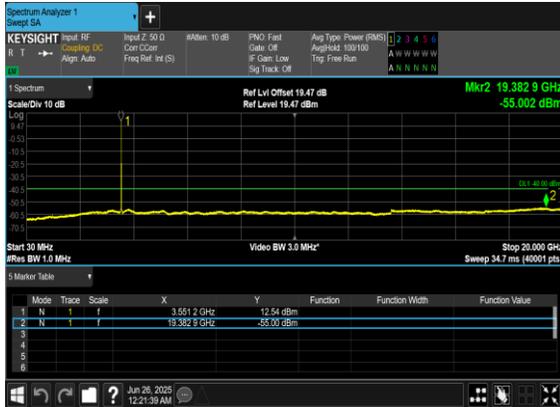
N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

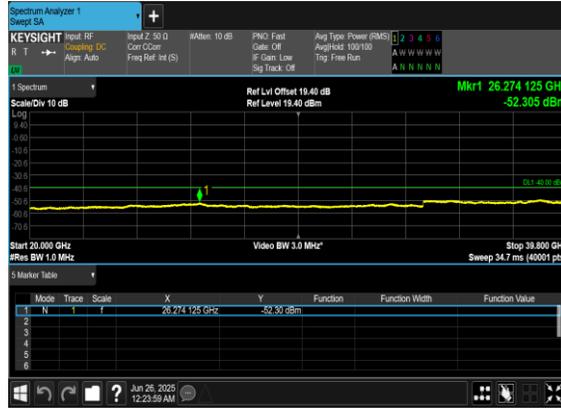




N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

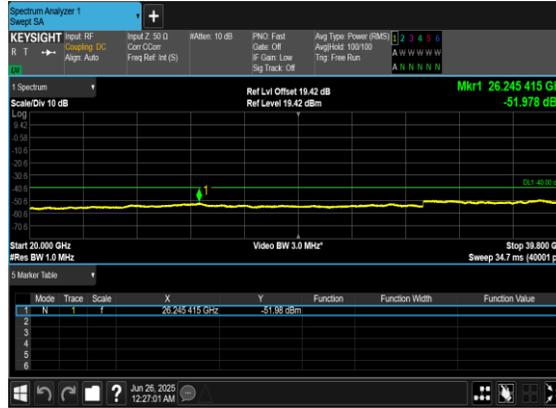




N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

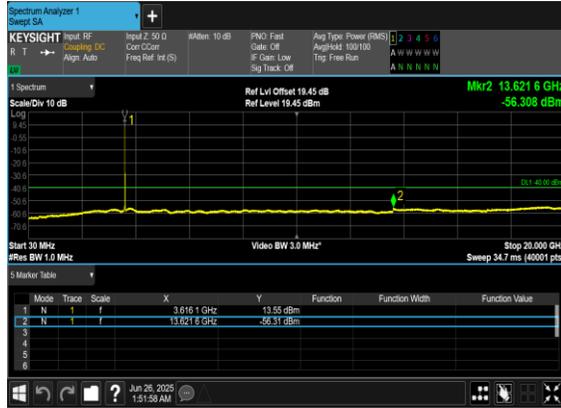


N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(25M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(25M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



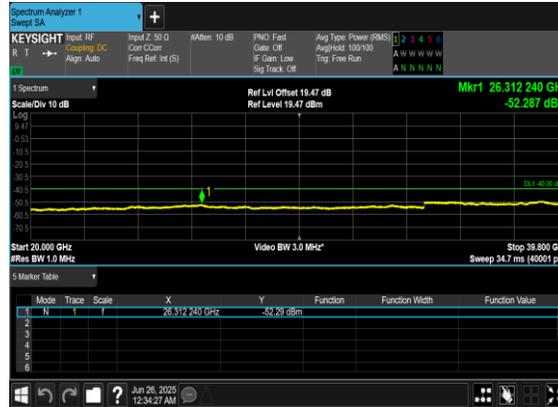
N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

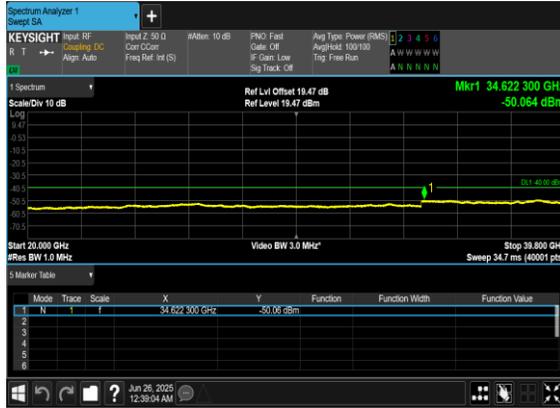




N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

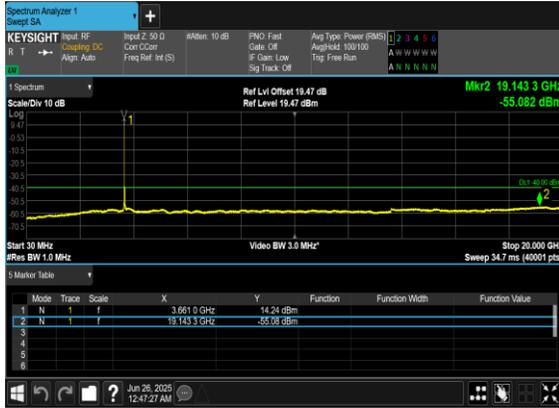


N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

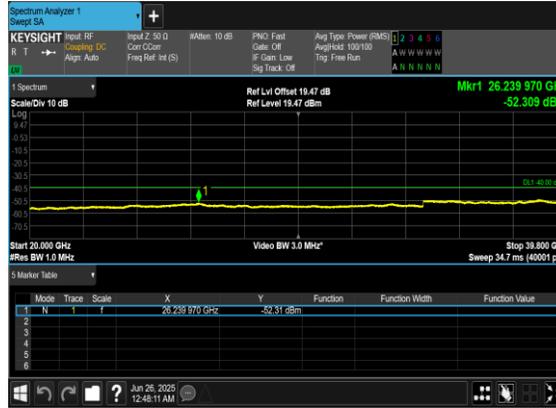




N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

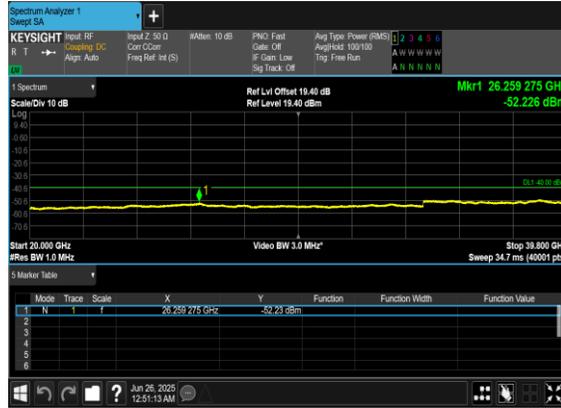




N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

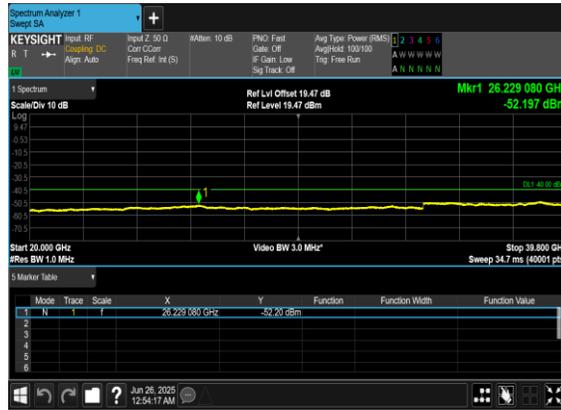




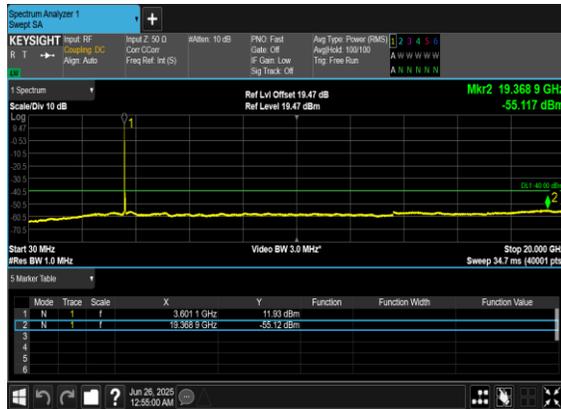
N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

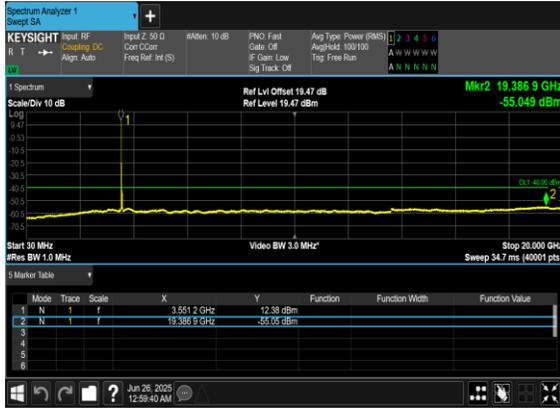


N77(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



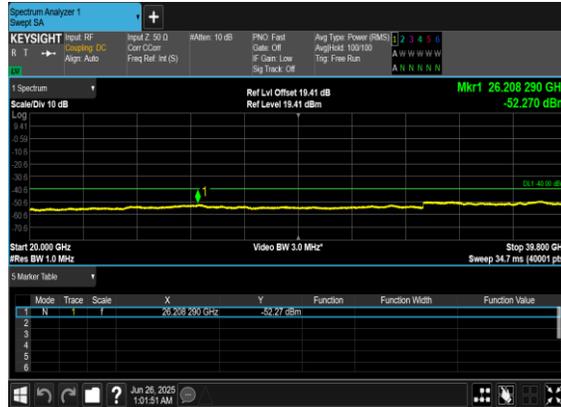
N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





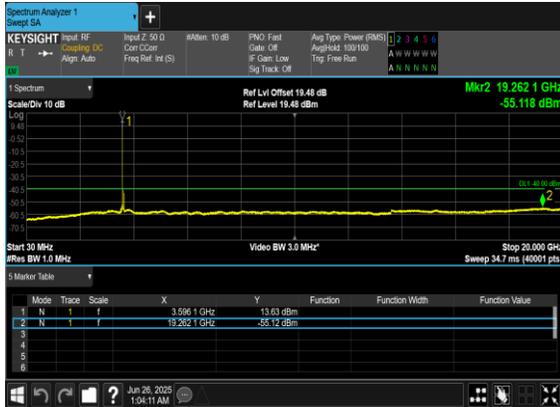
N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

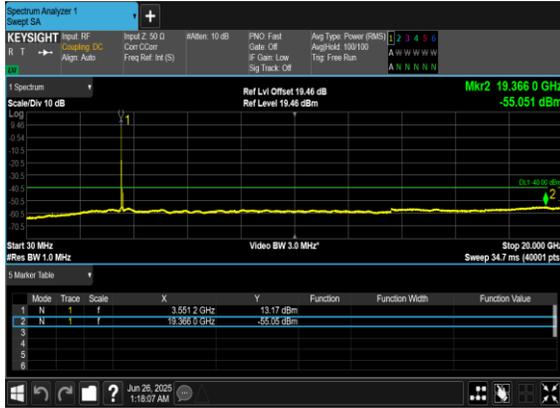


N77(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

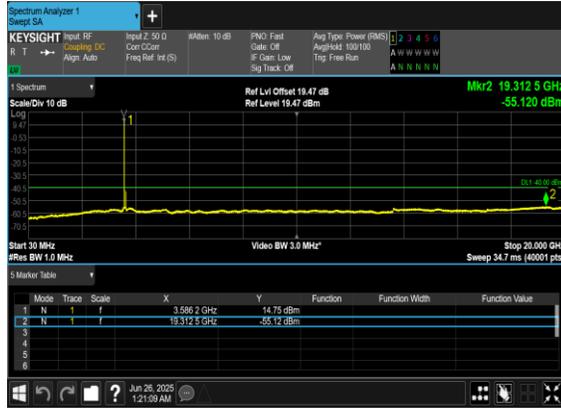


N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

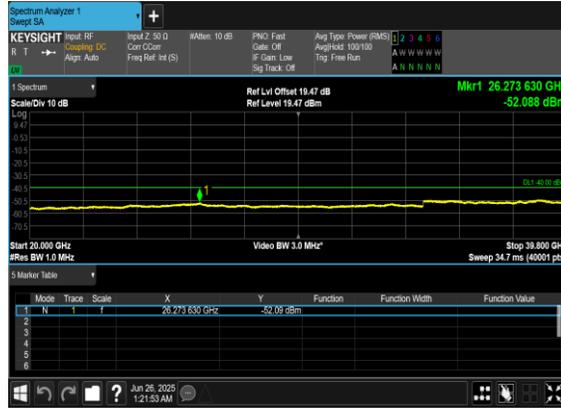




N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

