

## Vision 312

# Technical Report according to 47 CFR § 2.1033 - Application for certification (b) - Declaration of Model Differences Letter

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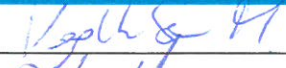

**Project Name:** Vision 312**Document Title:** Technical Report according to 47 CFR § 2.1033 - Application for certification (b) - Declaration of Model Differences Letter

Version: 1.0.0

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## Approval

Project Name  
Technical Report according to 47 CFR § 2.1033 - Application for certification (b) -  
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## History of Changes

Version	Date	Person	Modification
0.0.1	2019	MVO	Creation of TTControl template
1.0.0	2020-03-11	MVO	Initial Version

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## 1 Overview

The purpose of this document is to provide the required data and information as required per FCC Title 47, Chapter I, Subchapter A, Part 2, Subpart J, §2.1033 – “Application for Certification” – Declaration of Model Differences Letter for the product “Vision 312”, that shall be FCC certified.

## 2 Objective

The device that shall be certified is identified as follows:

Device Description: Vision 312

Hardware Identification: H-156-P-02-003

Serial Number: V3xxxxxxxxxxxx

FCC-ID: 2AUIVTTCTVISION312

Date of Manufacture: xx/20xx

Device that the certification is applied for:



Figure 1: Product Vision 312

## 3 47 CFR § 2.1033 (b)

### 3.1 Declaration of Model Differences

There are two different variants of the product. The following two tables show the most different components and interfaces of the Vision 312 product.

### 3.1.1 System Components

	Vision 312	Vision 312Plus
CPU	i.MX 6Solo Processor 800 MHz - ARM Cortex-A9 (single core)	i.MX 6QuadPlus Processor 1 GHz - ARM Cortex-A9 (quad core)
DDR3 RAM	512 MB	2 GB
eMMC Flash	4 GB	8 GB
Display	12.1", 1280x800, 500 cd/m2 luminance, 1000:1 contrast ratio	12.1", 1280x800, 1000 cd/m2 luminance, 1000:1 contrast ratio
Touchscreen	Yes	Yes
Bonding	Fully bonded	Fully bonded
Speaker	Integrated	Integrated
Real-time clock	1	1
Non-volatile memory	32 kByte EEPROM	32 kByte EEPROM

Table 1: System Component Difference

### 3.1.2 Interfaces

	Vision 312	Vision 312Plus
CAN	2	4 (1 ISOBUS compliant)
Analog video input	2 (PAL / NTSC)	4 (PAL / NTSC)
USB	1 (OTG)	2 (1 OTG / 1 HOST)
Ethernet	1 (100BASE-T1)	1 (100BASE-TX)
RS-232	1	1
Terminal 15	1	1
Wake-up	1	1
Temperature sensor	2	2
Status LED	1 (RGB)	1 (RGB)
Ambient light sensor	1	1

Table 2: Interface Difference

## Abbreviations and Acronyms

Abbr. / Term	Meaning
<b>100BASE-T1</b>	BroadR-Reach
<b>100BASE-TX</b>	Ethernet
<b>eMMC</b>	Embedded MultiMediaCard
<b>CAN</b>	Controller Area Network
<b>CPU</b>	Central Processing Unit
<b>DDR3</b>	Double Data Rate 3
<b>EEPROM</b>	Electrically Erasable Programmable Read-Only Memory
<b>FCC</b>	Federal Communications Commission
<b>NTSC</b>	National Television System Committee
<b>OTG</b>	On-The-Go
<b>PAL</b>	Phase Alternating Line
<b>USB</b>	Universal Serial Bus

## Glossary

Abbr. / Term	Meaning
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## References

- [1] FCC Title 47 – Part 2 J CFR § 2.1033, Application for certification
- [2] FCC Title 47 - Part 15 B, 47 CFR Ch. I (10–1–10 Edition), Unintentional Radiators