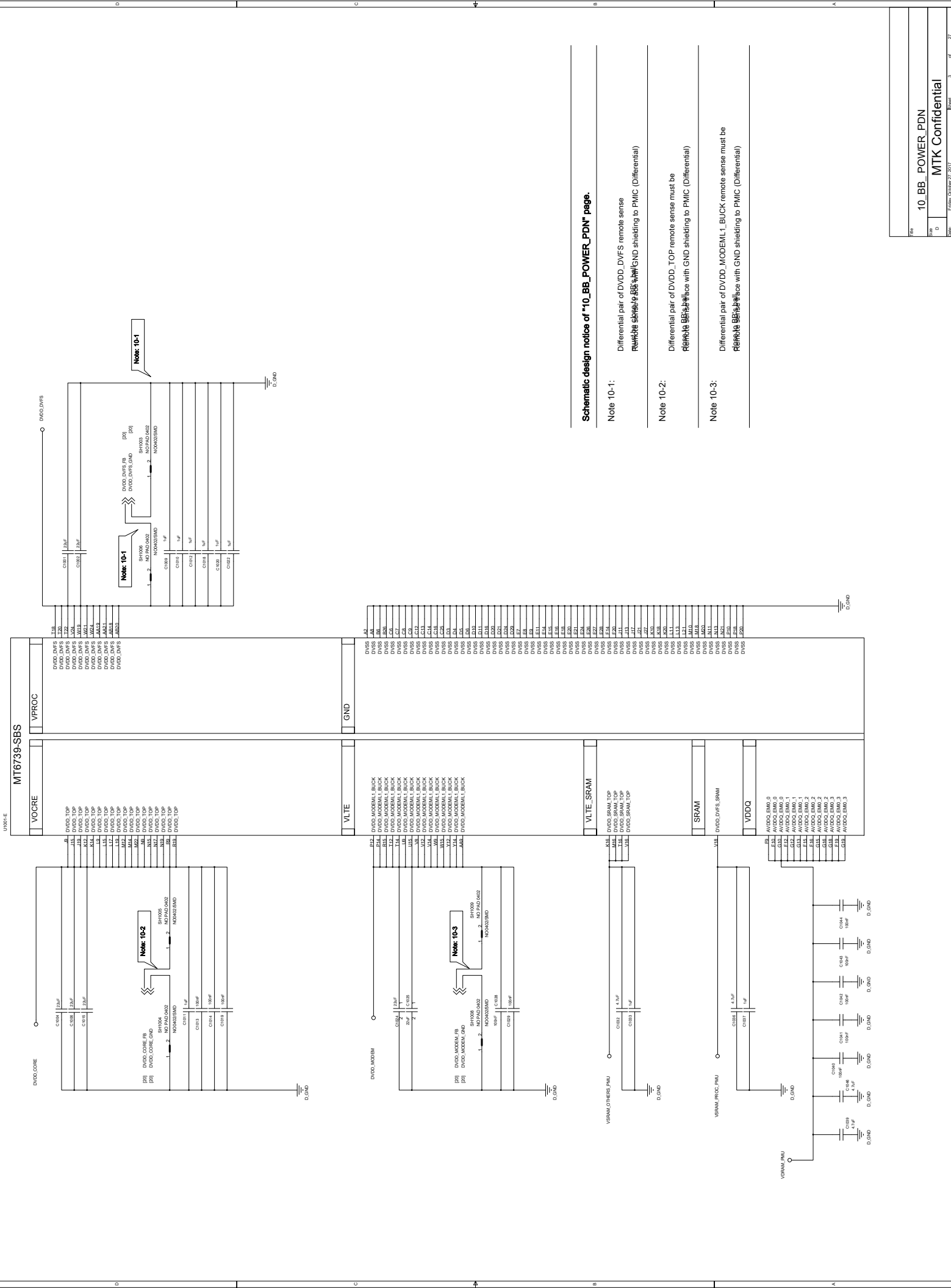
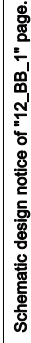
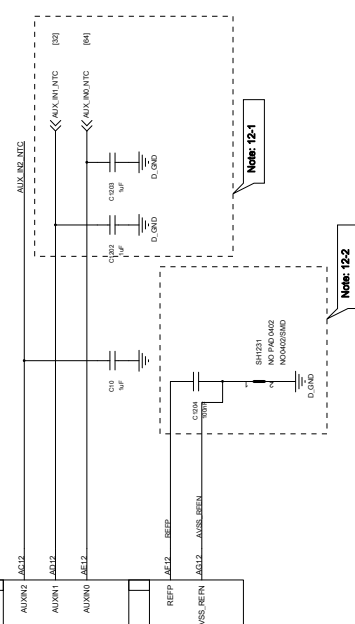
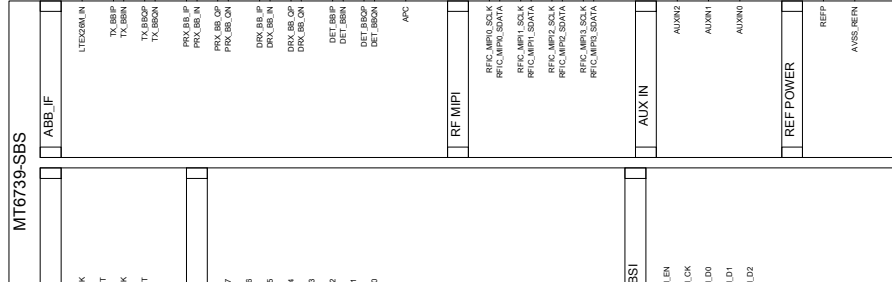
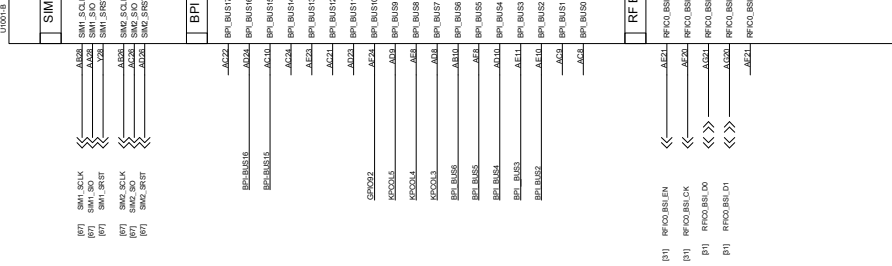
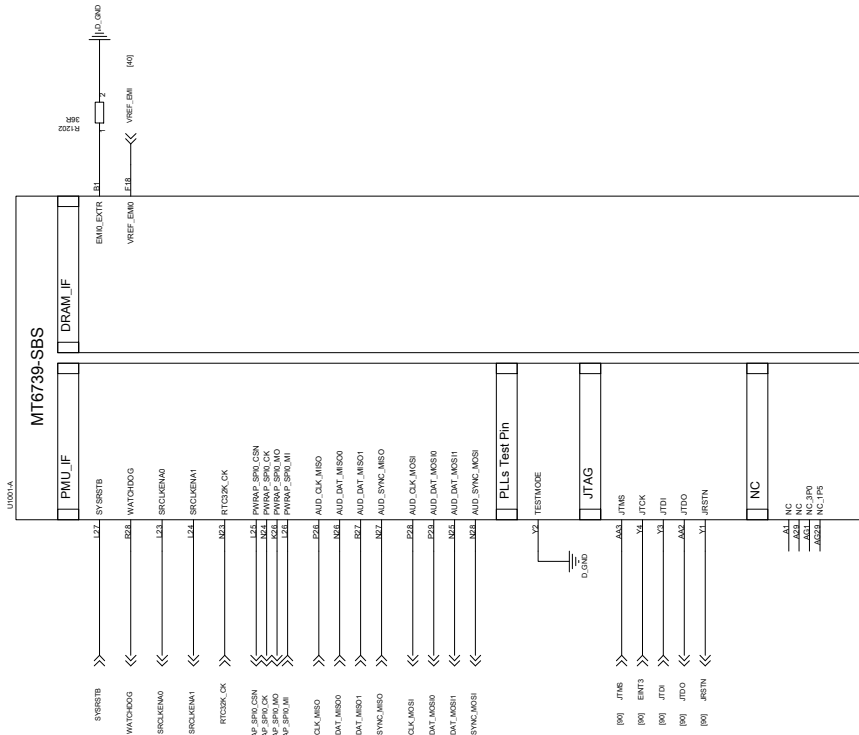
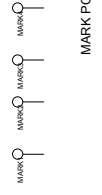






Note 12-1: To shunt a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused AUX ADC input to GND.

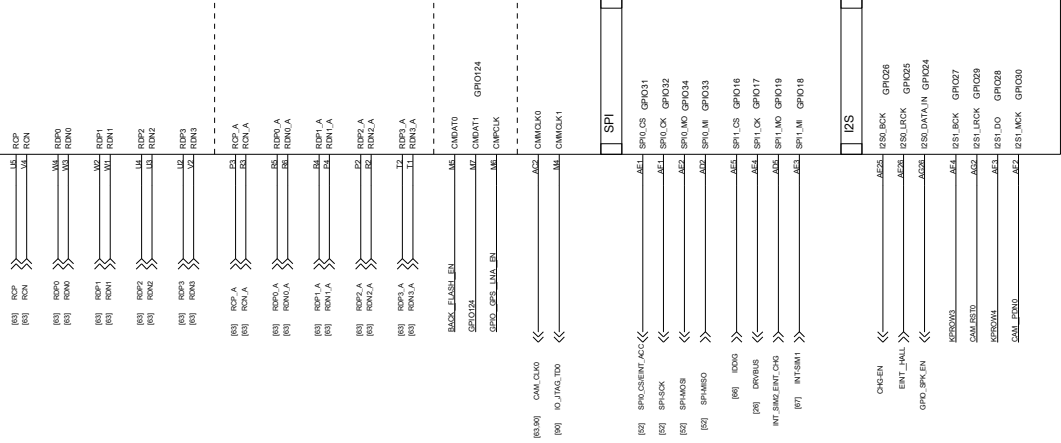
Note 12-2: The de-coupling cap. for REFP (AF12 ball) have to be placed as close to BB as possible.



U1001-C

MT6739-SBS

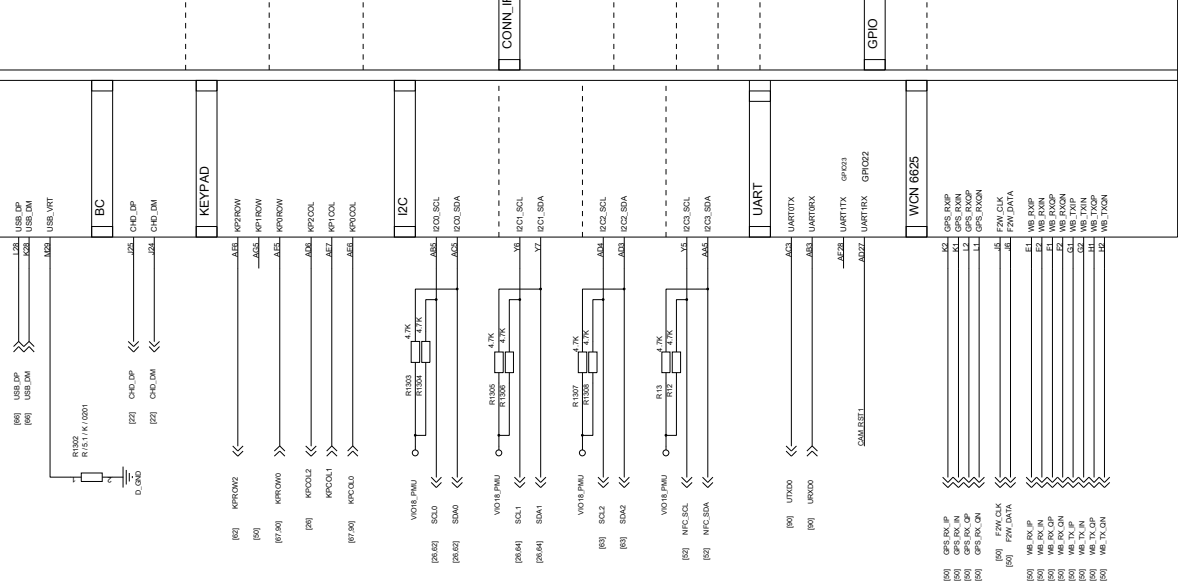
CSI	DSI
-----	-----

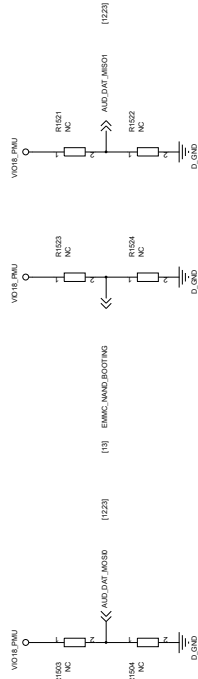


U1001-D

MT6739-SBS

USB	MSDCs
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Schematic design notice of "15_BB_HW_Trap" page.

Note 15-1: AUD_DAT_MISO0 is JTAG feature in bootstrap.

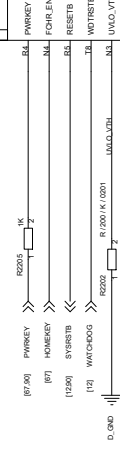
AUD_DAT_MISO0 default status is LO		Note
LO	Default	N/A
HI	Aux Func. trap MD_JTAG_AP_JTAG	MD_JTAG = CMDAT0/CMDAT1/CMPCLK/CMMCLK/CMMCLK1

Note 15-2: ANT_SEL2 and AUD_DAT_MISO1 is storage booting feature in bootstrap.

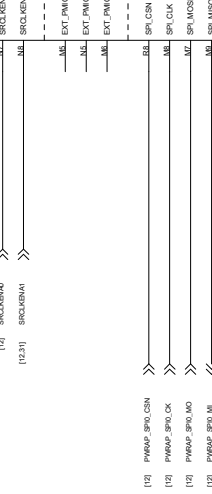
ANT_SEL2 default status is LO		AUD_DAT_MISO1		Storage Booting
AUD_SPAT_MISO1 default status is LO		(EMMC_MAND_BOOTING)		
HI	LO	LO	LO	TLC Boot
LO	HI	HI	HI	SLC Boot
LO	LO	LO	LO	eMMC Boot
LO	HI	HI	HI	N/A

MT6357

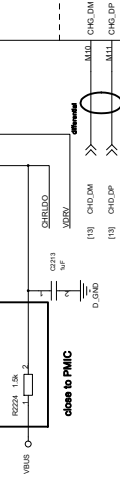
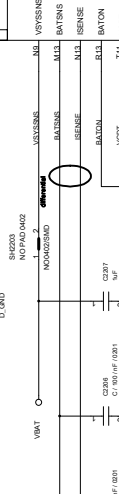
Control I/F	
-------------	--



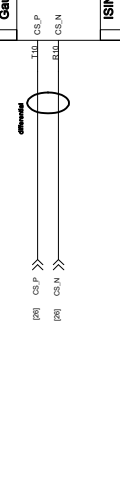
0000-0001-9150-4000

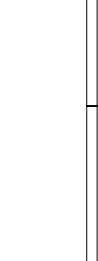
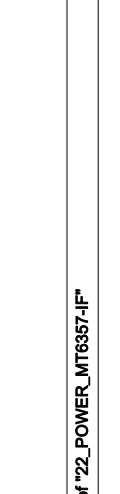


Charger I/F



□

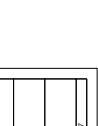




100



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Schematic design notice of "22_POWER_MT6357-IF"

Note 22-1:

Please implement 2520 & 2016 Size TMS PCB co-layout.

Please refer to MT6739 MT6357 Co-Clock Design Notice for co-layout guide

Note 22-2:

Please follow MT6739_MT6357 Co-Clock Design Notice for Layout guide of VAUX18,

then B2236 can use 0 ohm to replace READ

Note: 22-1

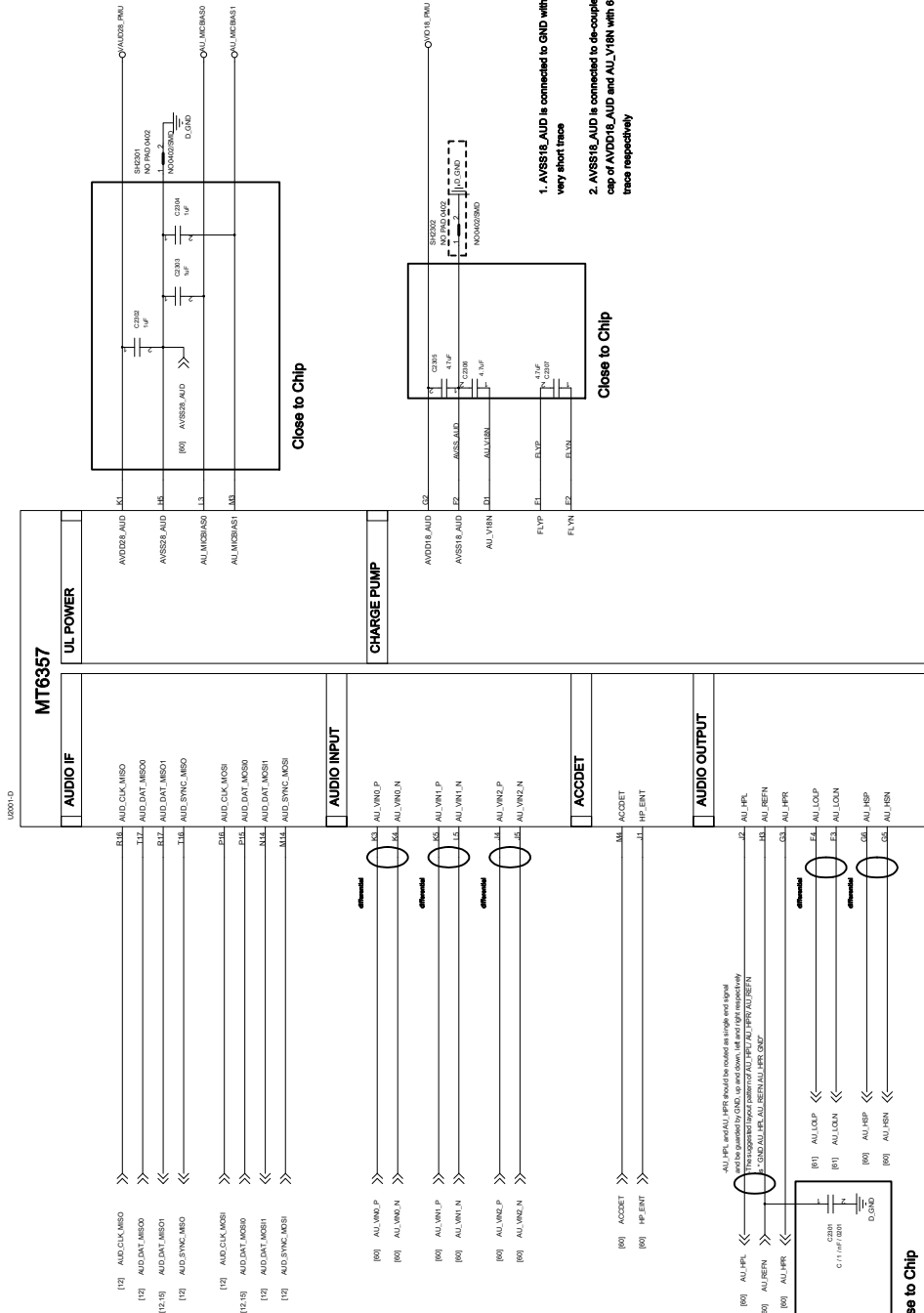
DCXO

Connect to interface for reserving layout area

Route AVDD18_AUXADC1AUXADC_VIN as differential traces (3 mil each) with well GND shielding and route AVSS18_AUXADC with 15mil trace width under AVDD18_AUXADC1AUXADC_VIN traces to provide return current path.

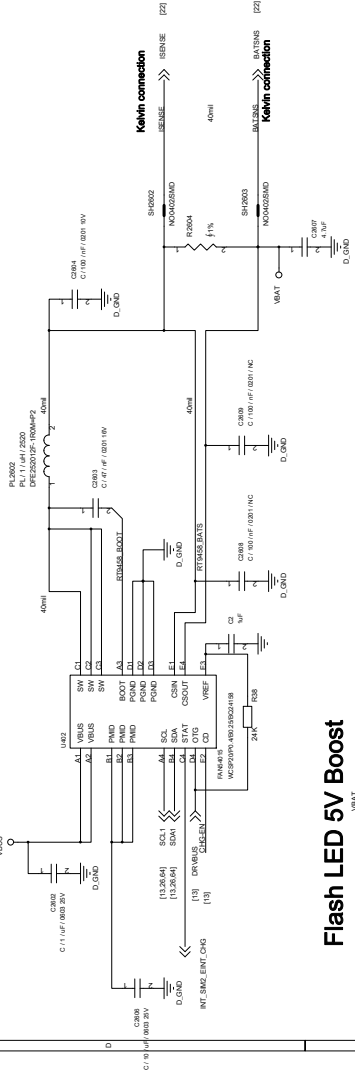
22_POWER_MT6357-IF

MTK Confidential

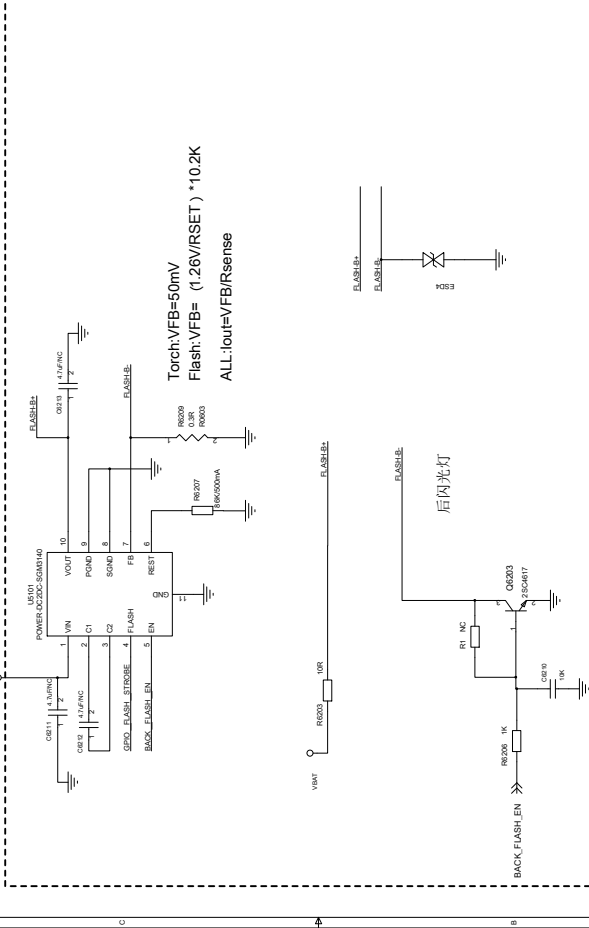


Switching Charger w/o Power Path

SW CHG I2C address: TBD (Write:0x7BC, Read:0x7B0)



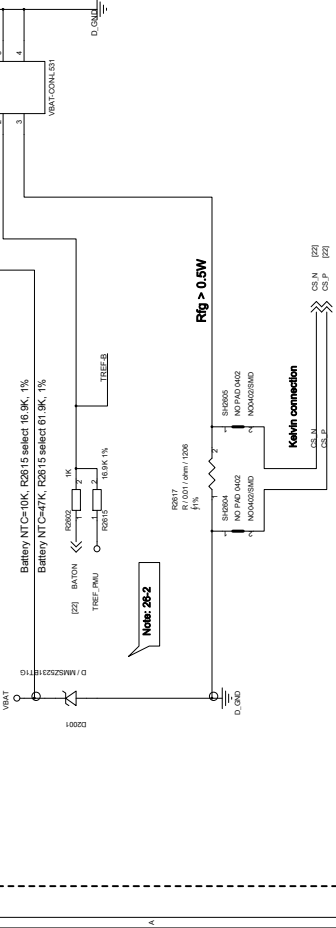
Flash LED 5V Boost



Torch VFB=50mV
Flash VFB= (1.28V/RSET) * 10.2K
ALL Iout=VFB/Rsense

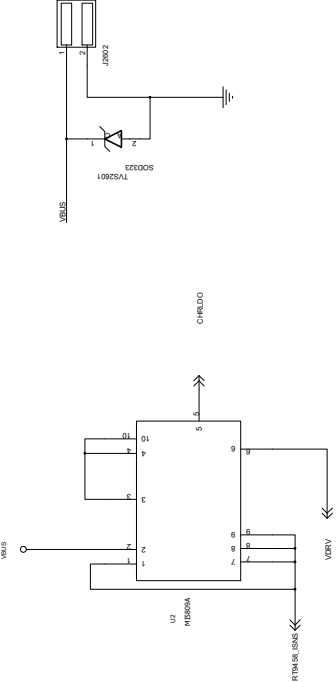
后闪光灯

BATTERY CONNECTOR



Note: 26-2

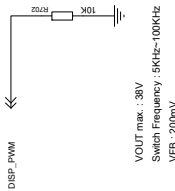
A969-5.1有用过



FLASH LED-FRONT

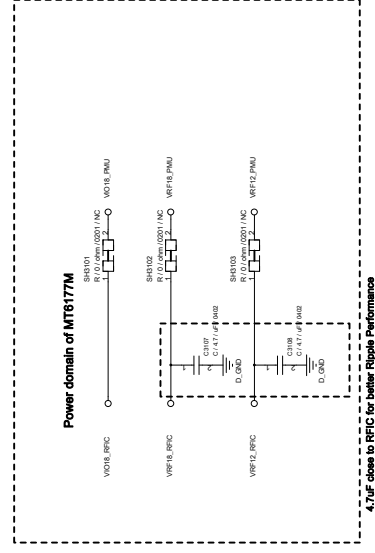
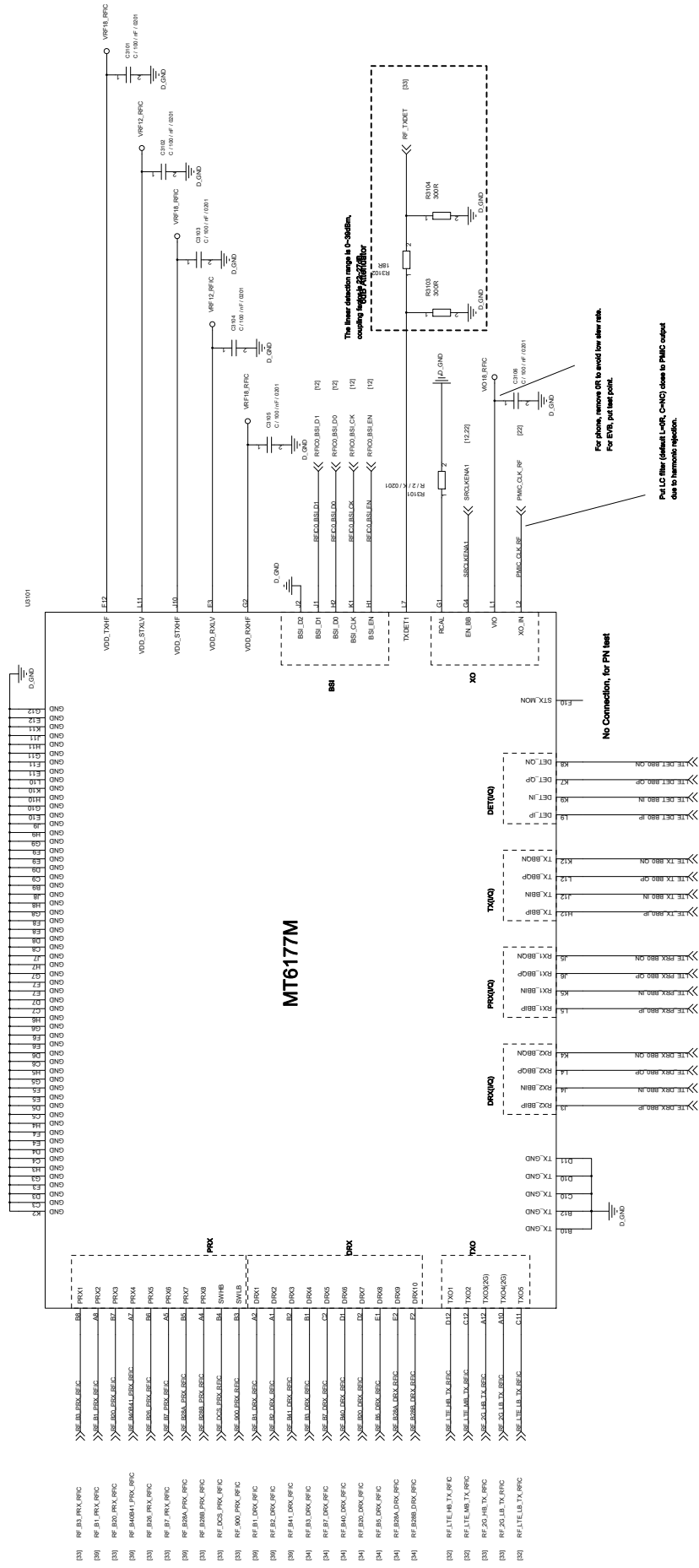
前闪无

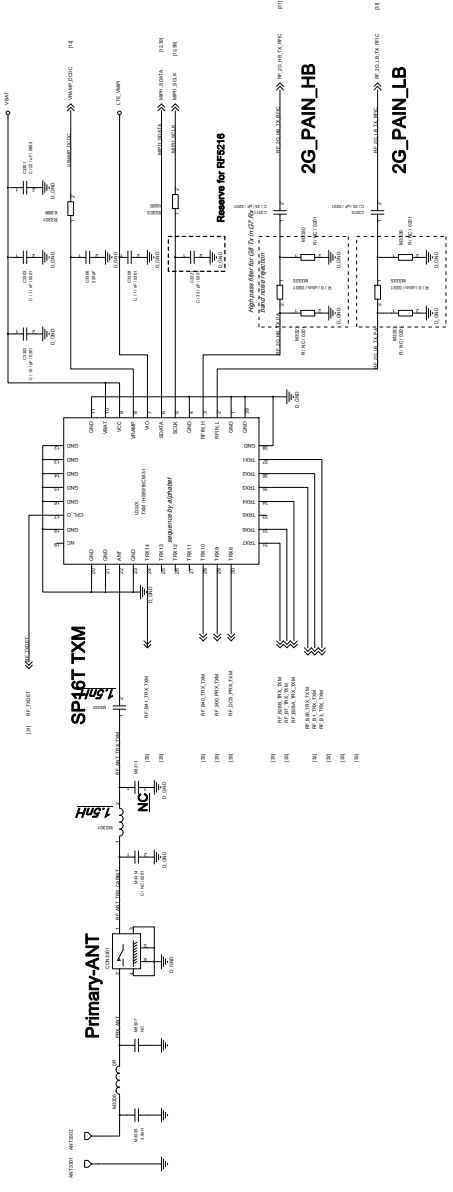
LCM Backlight LED Driver



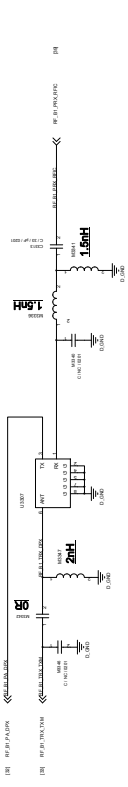
Schematic design notice of "26_POWER_THIRD_PARTY"

- Note 26-1: If LCD module Power requirement is 3 Power mode. AVDDIAVEE of LCM module is powered by 3rd party chip (e.g. RT4801ART5989).
- Note 26-2: For better ESD or surge performance we need choose suitable device for system protection. Please refer to [Surge device selection guide V2.0] provide by MTK.

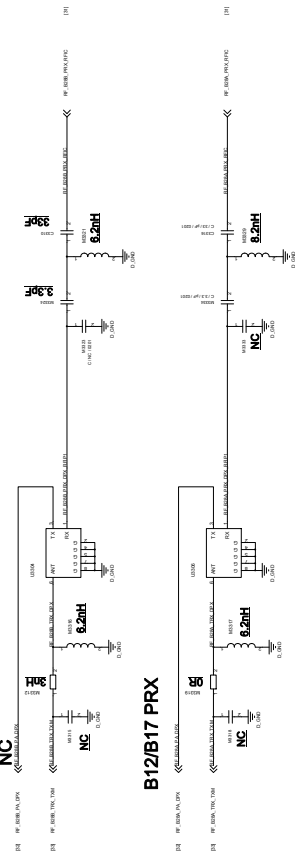




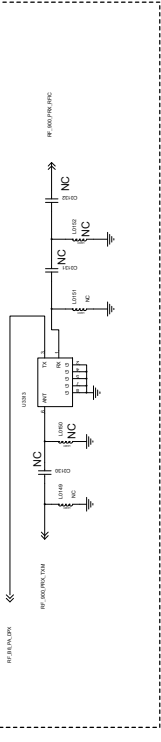
B4/B6 TRX



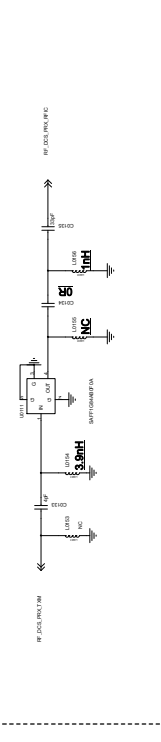
B12/B17 PRX



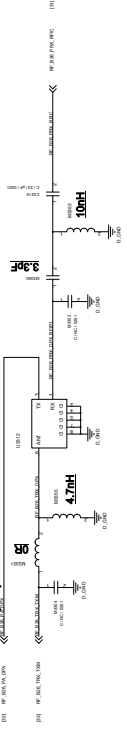
B8 TRX



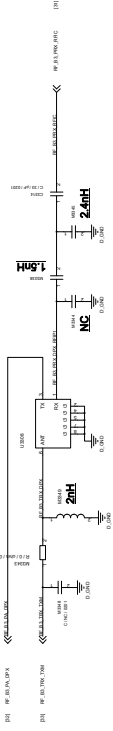
PCS/DCS PRX



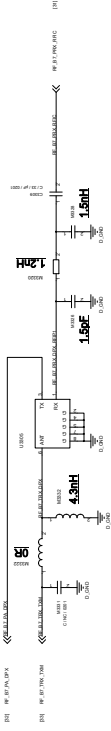
B5(26) TRX

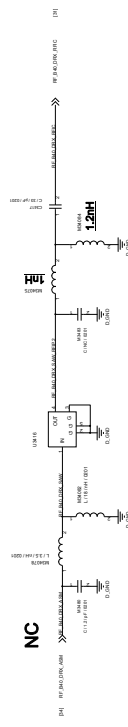
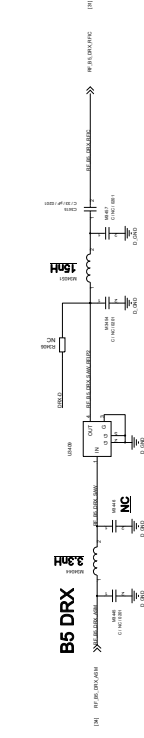
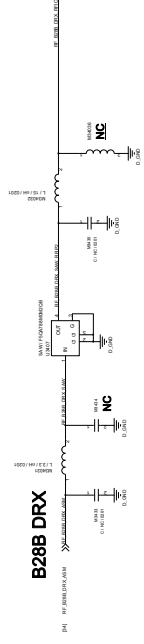
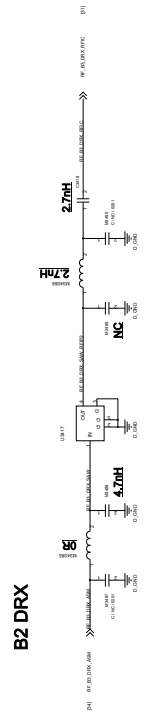
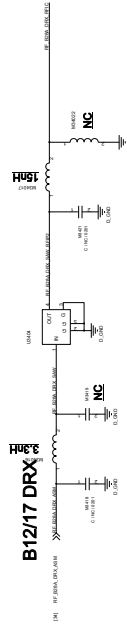
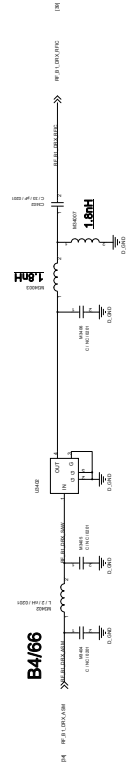
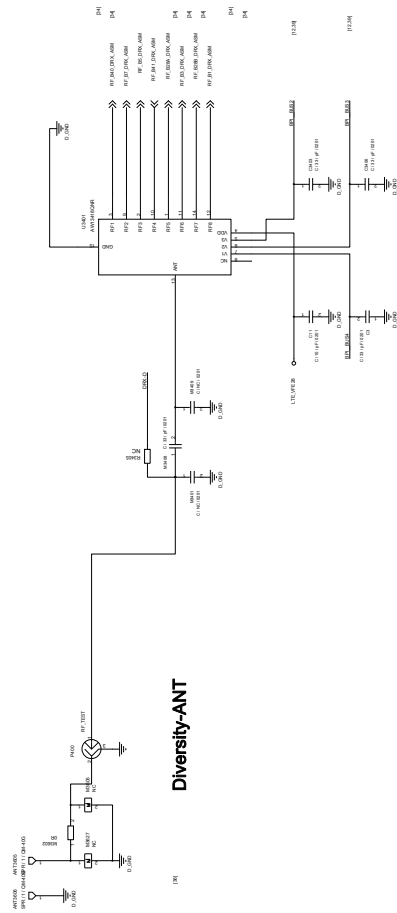


B2 TRX

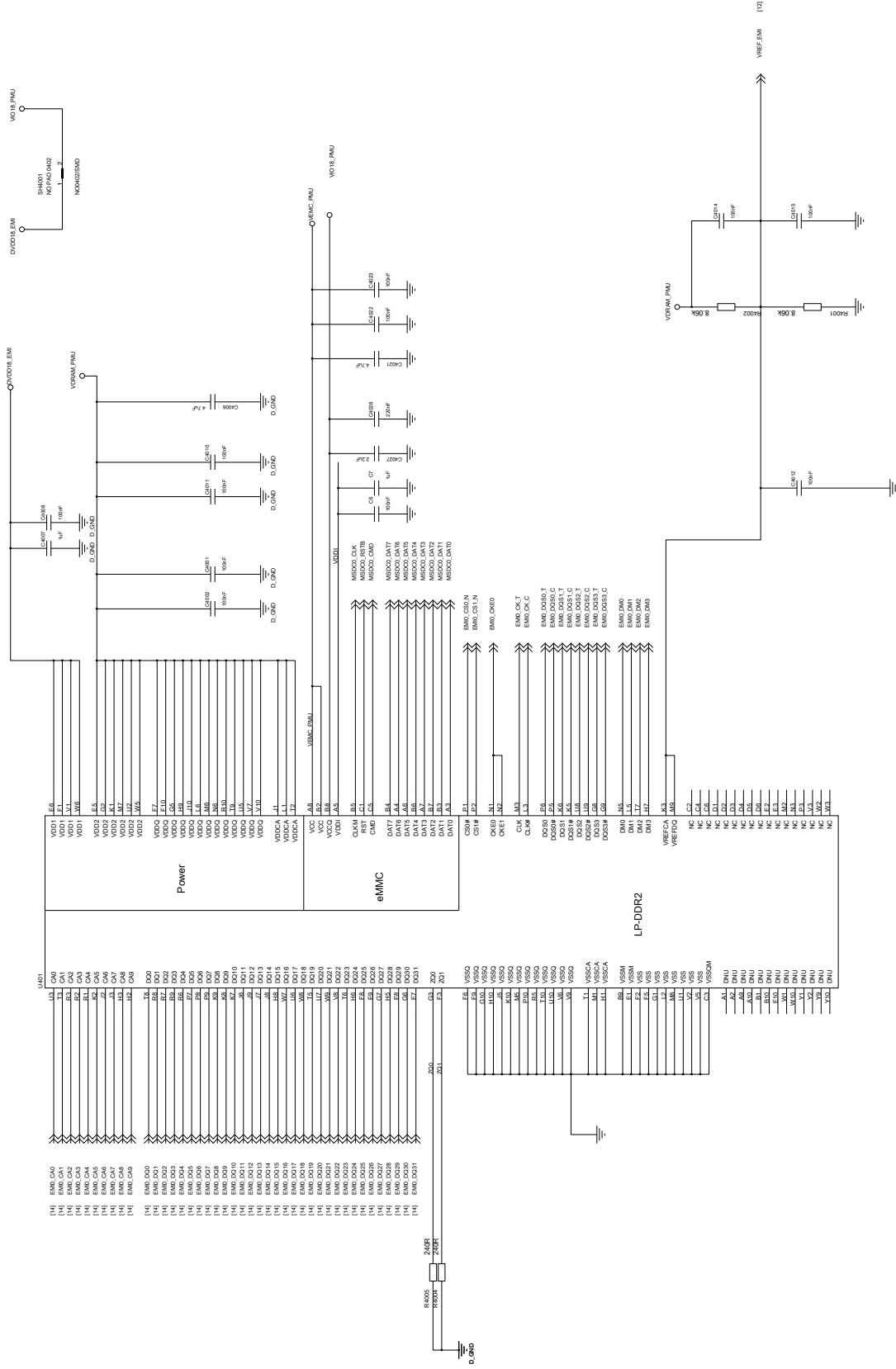


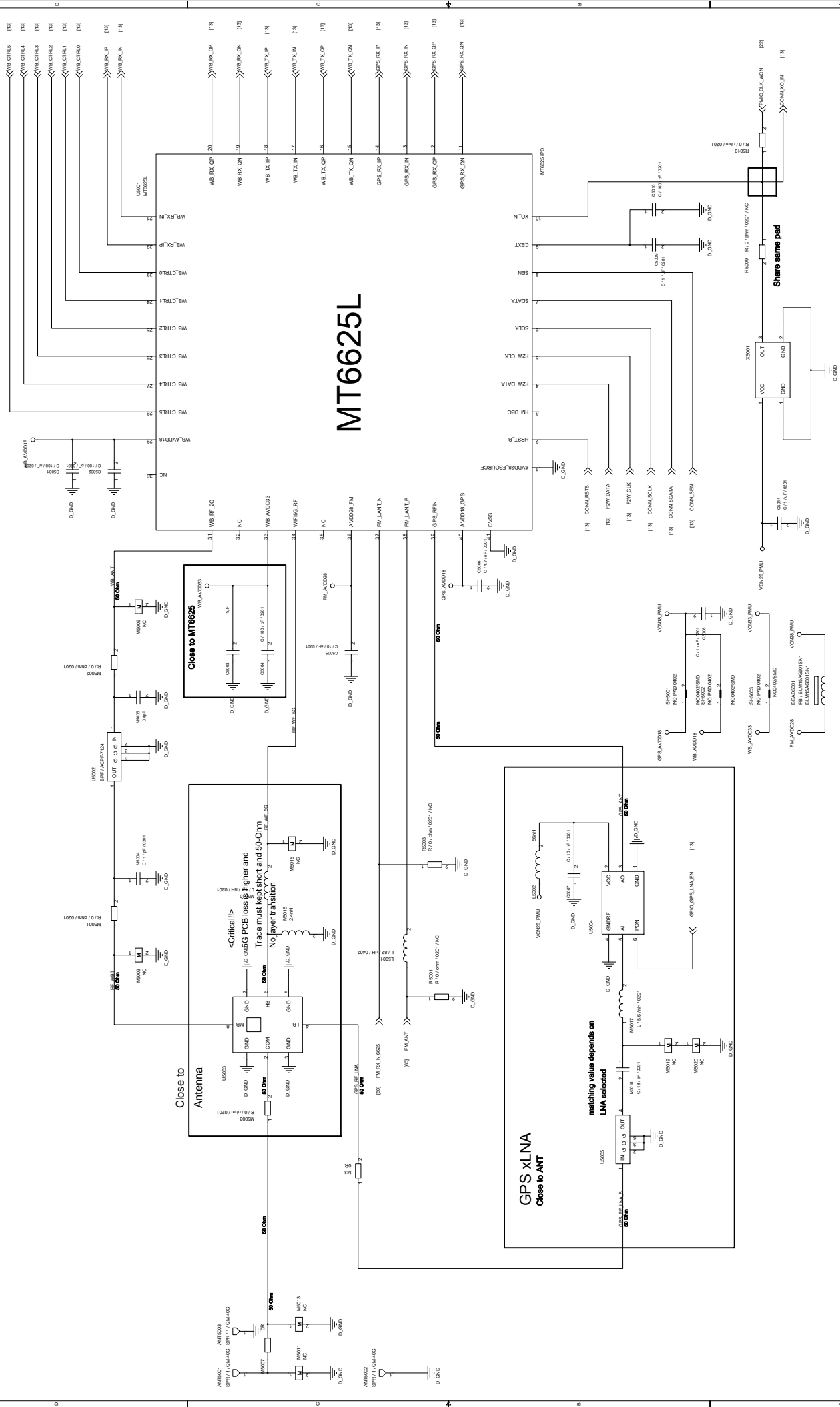
B7 TRX





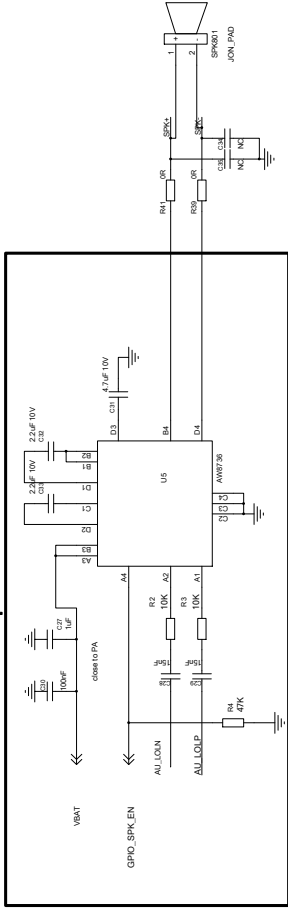
EMCP+LPDDR2



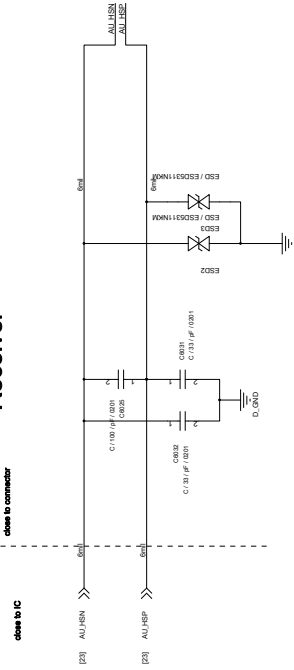


Speaker Amplifier

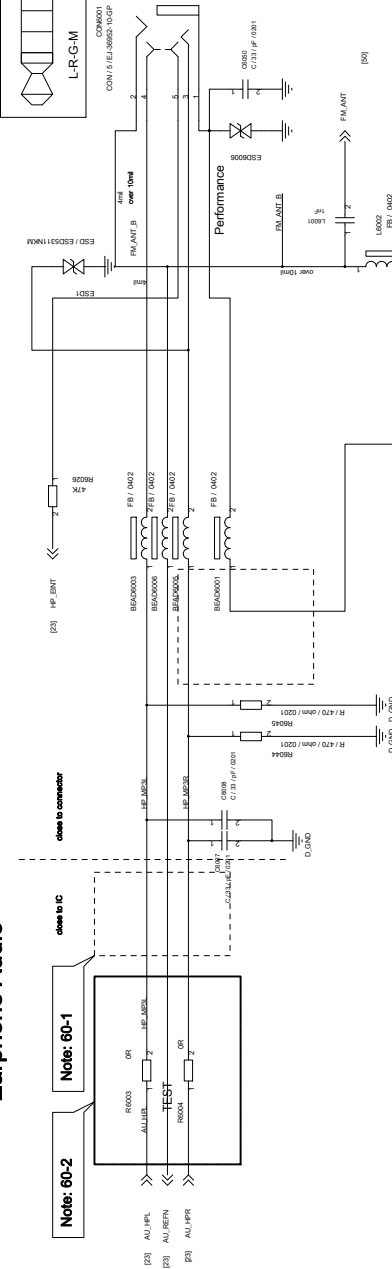
Speaker



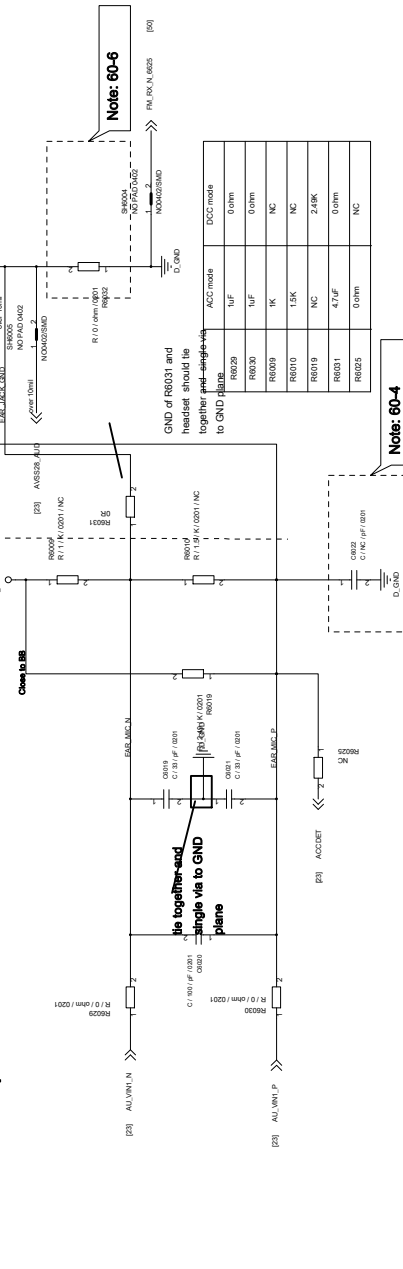
Receiver



Earphone Audio



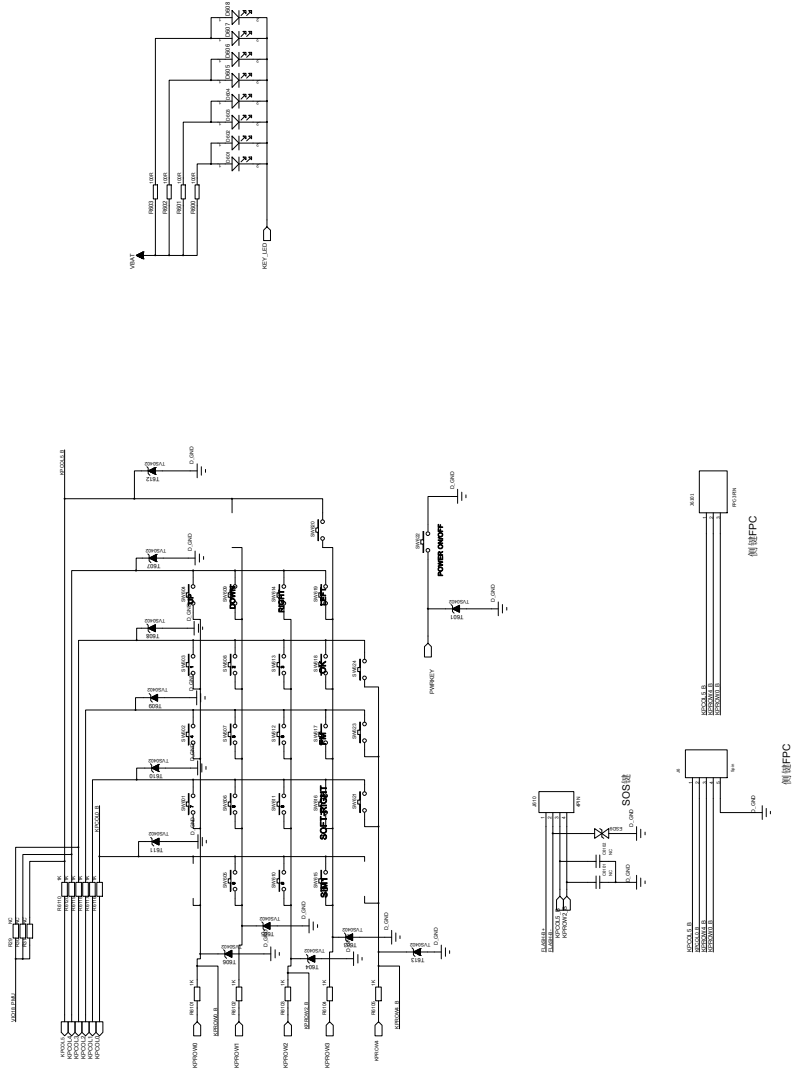
Earphone MICPHONE

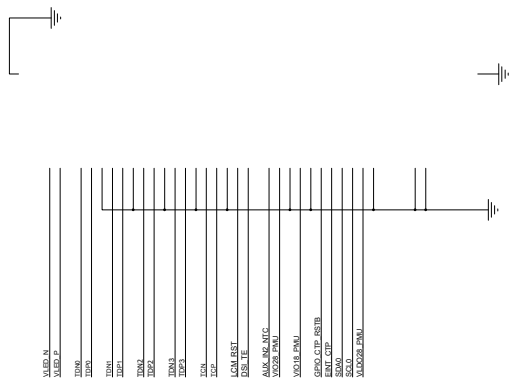


Please refer to [Surge device selection guide V2.0] provide by MTK.

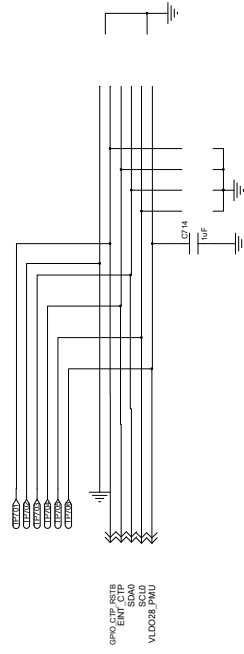
60 PERI_AUDIO_IO
MTK Confidential

KEY

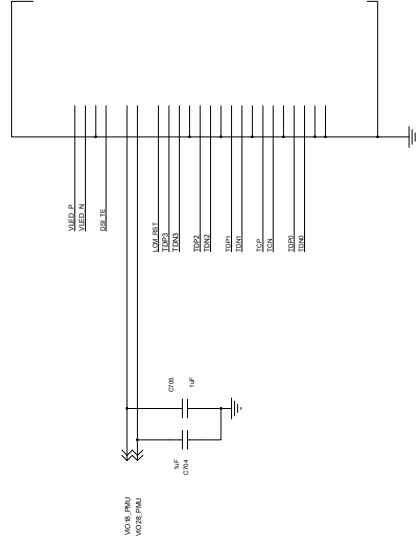
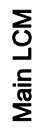




CTP



TEST-LCD-ZIFCONNECT



Schematic design notice of "62_PERI_LCD_CTP" page.

Note 62-1:

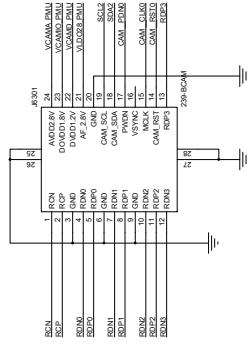
COF GT1151 I2C address: 0X5D (Write:0xBA, Read:0xBB)
Or 0X14 (Write:0x28, Read:0x29)

Note 62-2:

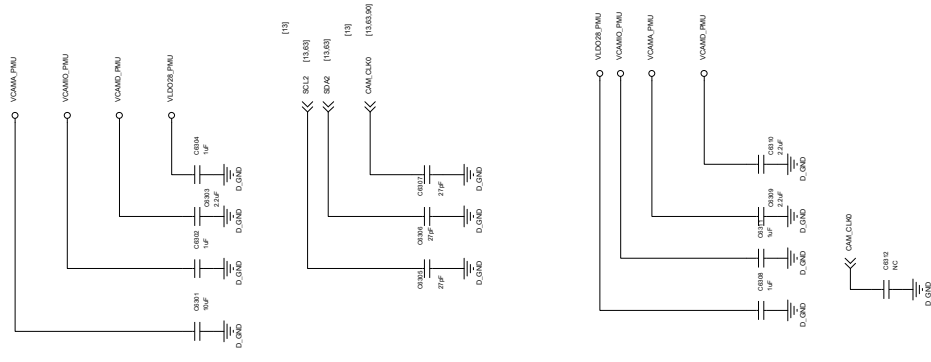
NT35521S 3Lane and 4Lane Selection
4Lane-->R6205=0R
3Lane-->R6205=NC

后Rear Camera - 13M

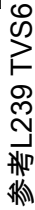
Rear camera (OV13870) I2C address: 0X10 (Write:0x20, Read:0x21)
AF driver (LC-898217) I2C address: 0X72 (Write:0xE4, Read:0xE5)



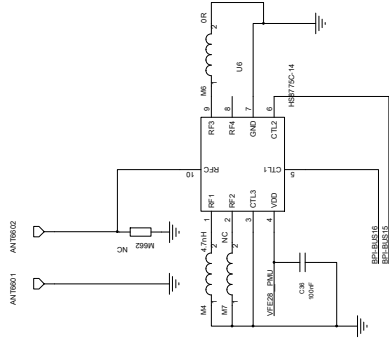
同L239后摄像头



Front camera (OV8856) I2C address: 0X36 (Write:0x6C, Read:0x6D)



VIBRATOR



Note 66-1: For better ESD or surge performance we need choose suitable device for system protection, please refer to [Surge device selection guide V2.0] provide by MTK.

MTK Debug I/O

Schematic design notice of "90_DEBUG_IO" page.

Note 90-1: UART Default support : UART0, MD_UART0
JTAG Default support : AP JTAG

