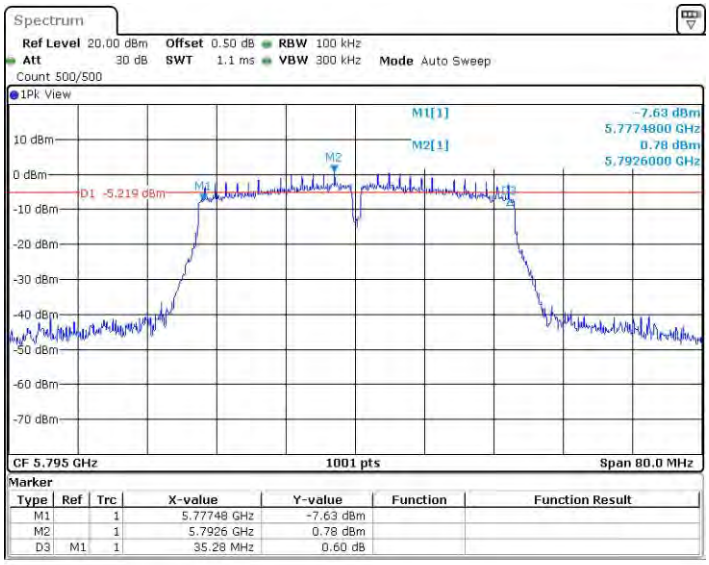
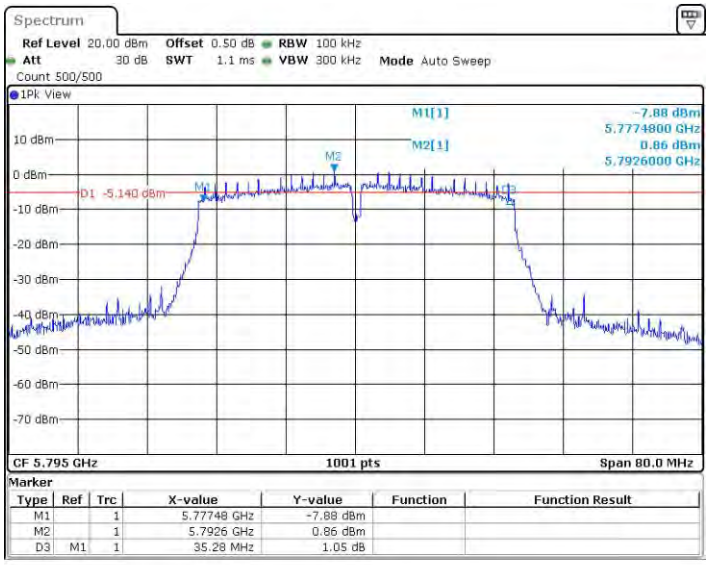
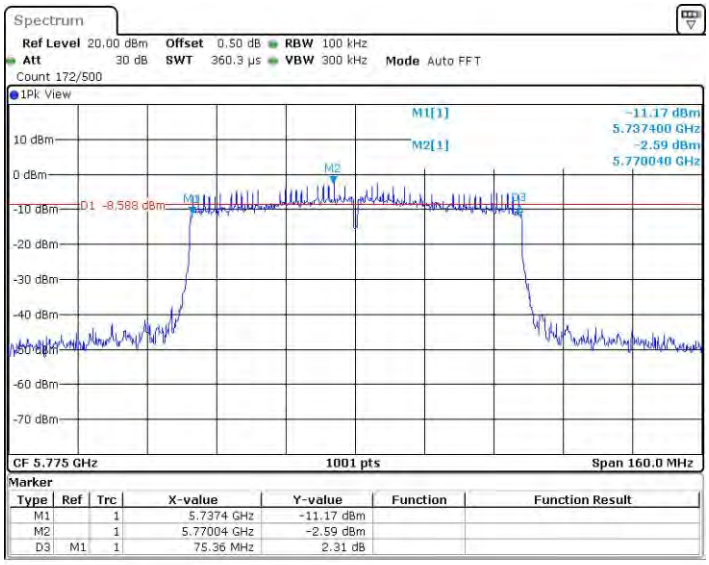


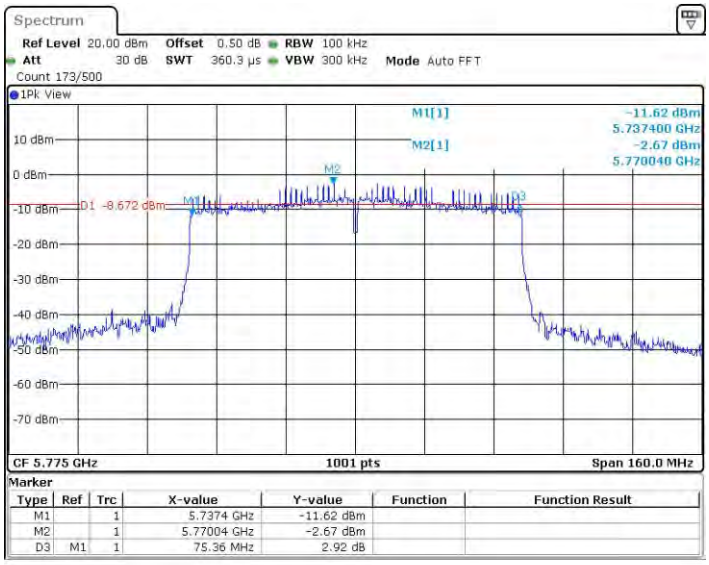
11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix B: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	11.19	<=23.98	PASS
	Ant2	5180	11.34	<=23.98	PASS
	Ant1	5200	11.09	<=23.98	PASS
	Ant2	5200	11.48	<=23.98	PASS
	Ant1	5240	10.80	<=23.98	PASS
	Ant2	5240	11.60	<=23.98	PASS
	Ant1	5260	15.28	<=23.98	PASS
	Ant2	5260	15.15	<=23.98	PASS
	Ant1	5280	15.45	<=23.98	PASS
	Ant2	5280	15.11	<=23.98	PASS
	Ant1	5320	15.36	<=23.98	PASS
	Ant2	5320	15.28	<=23.97	PASS
	Ant1	5500	15.48	<=23.98	PASS
	Ant2	5500	15.66	<=23.98	PASS
	Ant1	5580	15.34	<=23.98	PASS
	Ant2	5580	15.45	<=23.98	PASS
	Ant1	5700	15.57	<=23.98	PASS
	Ant2	5700	15.59	<=23.98	PASS
	Ant1	5745	15.68	<=30	PASS
	Ant2	5745	15.69	<=30	PASS
	Ant1	5785	15.76	<=30	PASS
	Ant2	5785	15.82	<=30	PASS
	Ant1	5825	15.83	<=30	PASS
	Ant2	5825	15.85	<=30	PASS
11N20SISO	Ant1	5180	3.69	<=23.98	PASS
	Ant2	5180	3.92	<=23.98	PASS
	Ant1	5200	3.67	<=23.98	PASS
	Ant2	5200	3.75	<=23.98	PASS
	Ant1	5240	3.85	<=23.98	PASS
	Ant2	5240	3.72	<=23.98	PASS
	Ant1	5260	12.79	<=23.98	PASS
	Ant2	5260	12.64	<=23.98	PASS
	Ant1	5280	12.88	<=23.98	PASS
	Ant2	5280	12.61	<=23.98	PASS
	Ant1	5320	12.91	<=23.98	PASS
	Ant2	5320	12.68	<=23.98	PASS
	Ant1	5500	13.26	<=23.98	PASS

	Ant2	5500	14.56	<=23.98	PASS
	Ant1	5580	13.15	<=23.98	PASS
	Ant2	5580	13.05	<=23.98	PASS
	Ant1	5700	13.39	<=23.98	PASS
	Ant2	5700	13.17	<=23.98	PASS
	Ant1	5745	13.40	<=30	PASS
	Ant2	5745	13.30	<=30	PASS
	Ant1	5785	13.48	<=30	PASS
	Ant2	5785	13.46	<=30	PASS
	Ant1	5825	13.52	<=30	PASS
	Ant2	5825	13.56	<=30	PASS
11N40SISO	Ant1	5190	6.61	<=23.98	PASS
	Ant2	5190	6.54	<=23.98	PASS
	Ant1	5230	6.74	<=23.98	PASS
	Ant2	5230	6.42	<=23.98	PASS
	Ant1	5270	13.11	<=23.98	PASS
	Ant2	5270	12.93	<=23.98	PASS
	Ant1	5310	13.13	<=23.98	PASS
	Ant2	5310	12.97	<=23.98	PASS
	Ant1	5510	13.45	<=23.98	PASS
	Ant2	5510	13.44	<=23.98	PASS
	Ant1	5550	13.46	<=23.98	PASS
	Ant2	5550	13.34	<=23.98	PASS
	Ant1	5670	13.55	<=23.98	PASS
	Ant2	5670	13.34	<=23.98	PASS
	Ant1	5755	13.65	<=30	PASS
	Ant2	5755	13.51	<=30	PASS
	Ant1	5795	13.77	<=30	PASS
	Ant2	5795	13.76	<=30	PASS
11AC20SISO	Ant1	5180	3.65	<=23.98	PASS
	Ant2	5180	3.84	<=23.98	PASS
	Ant1	5200	3.68	<=23.98	PASS
	Ant2	5200	3.63	<=23.98	PASS
	Ant1	5240	3.96	<=23.98	PASS
	Ant2	5240	3.64	<=23.98	PASS
	Ant1	5260	12.96	<=23.98	PASS
	Ant2	5260	12.91	<=23.98	PASS
	Ant1	5280	13.04	<=23.98	PASS
	Ant2	5280	12.97	<=23.98	PASS
	Ant1	5320	12.99	<=23.98	PASS
	Ant2	5320	12.95	<=23.98	PASS
	Ant1	5500	13.36	<=23.98	PASS

	Ant2	5500	13.46	<=23.98	PASS
	Ant1	5580	13.23	<=23.98	PASS
	Ant2	5580	13.38	<=23.98	PASS
	Ant1	5700	13.51	<=23.98	PASS
	Ant2	5700	13.50	<=23.98	PASS
	Ant1	5745	13.52	<=30	PASS
	Ant2	5745	13.61	<=30	PASS
	Ant1	5785	13.58	<=30	PASS
	Ant2	5785	13.67	<=30	PASS
	Ant1	5825	13.65	<=30	PASS
	Ant2	5825	13.74	<=30	PASS
11AC40SISO	Ant1	5190	6.45	<=23.98	PASS
	Ant2	5190	6.28	<=23.98	PASS
	Ant1	5230	6.61	<=23.98	PASS
	Ant2	5230	6.31	<=23.98	PASS
	Ant1	5270	13.14	<=23.98	PASS
	Ant2	5270	13.01	<=23.98	PASS
	Ant1	5310	13.16	<=23.98	PASS
	Ant2	5310	12.95	<=23.98	PASS
	Ant1	5510	13.36	<=23.98	PASS
	Ant2	5510	13.49	<=23.98	PASS
	Ant1	5550	13.32	<=23.98	PASS
	Ant2	5550	13.33	<=23.98	PASS
	Ant1	5670	13.41	<=23.98	PASS
	Ant2	5670	13.37	<=23.98	PASS
	Ant1	5755	13.58	<=30	PASS
	Ant2	5755	13.58	<=30	PASS
	Ant1	5795	13.62	<=30	PASS
	Ant2	5795	13.69	<=30	PASS
11AC80SISO	Ant1	5210	9.11	<=23.98	PASS
	Ant2	5210	9.34	<=23.98	PASS
	Ant1	5290	13.04	<=23.98	PASS
	Ant2	5290	12.92	<=23.98	PASS
	Ant1	5530	13.30	<=23.98	PASS
	Ant2	5530	13.39	<=23.98	PASS
	Ant1	5610	13.27	<=23.98	PASS
	Ant2	5610	13.18	<=23.98	PASS
	Ant1	5775	13.59	<=30	PASS
	Ant2	5775	13.57	<=30	PASS
11N20MIMO	Ant1	5180	3.07	<=23.98	PASS
	Ant2	5180	3.95	<=23.98	PASS
	total	5180	6.5	<=23.98	PASS

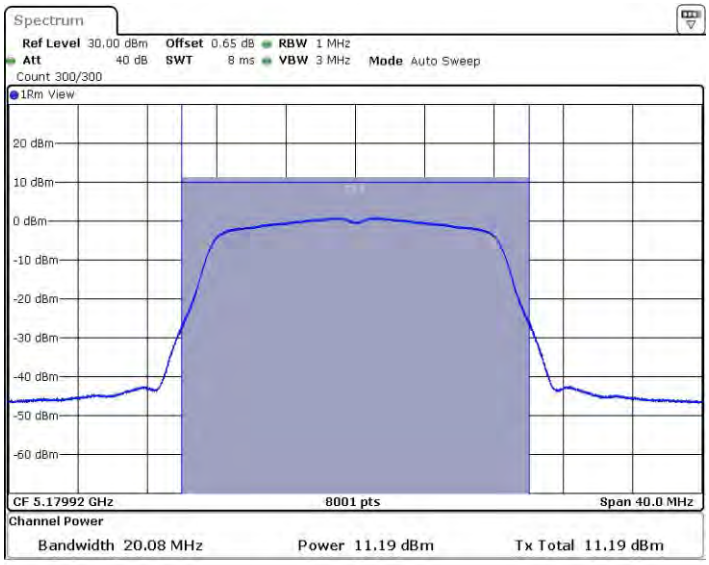
	Ant1	5200	3.41	<=23.98	PASS
	Ant2	5200	3.90	<=23.98	PASS
	total	5200	6.7	<=23.98	PASS
	Ant1	5240	3.38	<=23.98	PASS
	Ant2	5240	3.82	<=23.98	PASS
	total	5240	6.6	<=23.98	PASS
	Ant1	5260	12.28	<=23.98	PASS
	Ant2	5260	11.84	<=23.98	PASS
	total	5260	15.4	<=23.98	PASS
	Ant1	5280	13.21	<=23.98	PASS
	Ant2	5280	11.26	<=23.98	PASS
	total	5280	15.2	<=23.98	PASS
	Ant1	5320	12.82	<=23.98	PASS
	Ant2	5320	11.44	<=23.98	PASS
	total	5320	15.3	<=23.98	PASS
	Ant1	5500	13.09	<=23.98	PASS
	Ant2	5500	11.77	<=23.98	PASS
	total	5500	15.3	<=23.98	PASS
	Ant1	5580	12.96	<=23.98	PASS
	Ant2	5580	11.13	<=23.98	PASS
	total	5580	15.0	<=23.98	PASS
	Ant1	5700	13.08	<=23.98	PASS
	Ant2	5700	11.18	<=23.98	PASS
	total	5700	15.0	<=23.98	PASS
	Ant1	5745	13.23	<=30	PASS
	Ant2	5745	13.35	<=30	PASS
	total	5745	16.3	<=30	PASS
	Ant1	5785	13.24	<=30	PASS
	Ant2	5785	13.53	<=30	PASS
	total	5785	16.4	<=30	PASS
	Ant1	5825	13.25	<=30	PASS
	Ant2	5825	13.68	<=30	PASS
	total	5825	16.5	<=30	PASS
11N40MIMO	Ant1	5190	6.61	<=23.98	PASS
	Ant2	5190	6.83	<=23.98	PASS
	total	5190	9.7	<=23.98	PASS
	Ant1	5230	6.78	<=23.98	PASS
	Ant2	5230	6.79	<=23.98	PASS
	total	5230	9.8	<=23.98	PASS
	Ant1	5270	13.29	<=23.98	PASS
	Ant2	5270	12.89	<=23.98	PASS
	total	5270	16.1	<=23.98	PASS

	Ant1	5310	12.78	<=23.98	PASS
	Ant2	5310	12.92	<=23.98	PASS
	total	5310	15.9	<=23.98	PASS
	Ant1	5510	13.12	<=23.98	PASS
	Ant2	5510	13.34	<=23.98	PASS
	total	5510	16.2	<=23.98	PASS
	Ant1	5550	13.01	<=23.98	PASS
	Ant2	5550	13.28	<=23.98	PASS
	total	5550	16.2	<=23.98	PASS
	Ant1	5670	12.96	<=23.98	PASS
	Ant2	5670	13.17	<=23.98	PASS
	total	5670	16.1	<=23.98	PASS
	Ant1	5755	13.42	<=30	PASS
	Ant2	5755	13.45	<=30	PASS
	total	5755	16.4	<=30	PASS
	Ant1	5795	13.50	<=30	PASS
	Ant2	5795	13.59	<=30	PASS
	total	5795	16.6	<=30	PASS
11AC20MIMO	Ant1	5180	3.64	<=23.98	PASS
	Ant2	5180	3.90	<=23.98	PASS
	total	5180	6.8	<=23.98	PASS
	Ant1	5200	3.64	<=23.98	PASS
	Ant2	5200	3.97	<=23.98	PASS
	total	5200	6.8	<=23.98	PASS
	Ant1	5240	3.83	<=23.98	PASS
	Ant2	5240	3.81	<=23.98	PASS
	total	5240	6.8	<=23.98	PASS
	Ant1	5260	12.66	<=23.98	PASS
	Ant2	5260	10.75	<=23.98	PASS
	total	5260	14.8	<=23.98	PASS
	Ant1	5280	12.65	<=23.98	PASS
	Ant2	5280	10.74	<=23.98	PASS
	total	5280	14.8	<=23.98	PASS
	Ant1	5320	13.16	<=23.98	PASS
	Ant2	5320	10.79	<=23.98	PASS
	total	5320	15.1	<=23.98	PASS
	Ant1	5500	12.92	<=23.98	PASS
	Ant2	5500	11.18	<=23.98	PASS
	total	5500	15.1	<=23.98	PASS
	Ant1	5580	12.78	<=23.98	PASS
	Ant2	5580	11.13	<=23.98	PASS
	total	5580	15.0	<=23.98	PASS

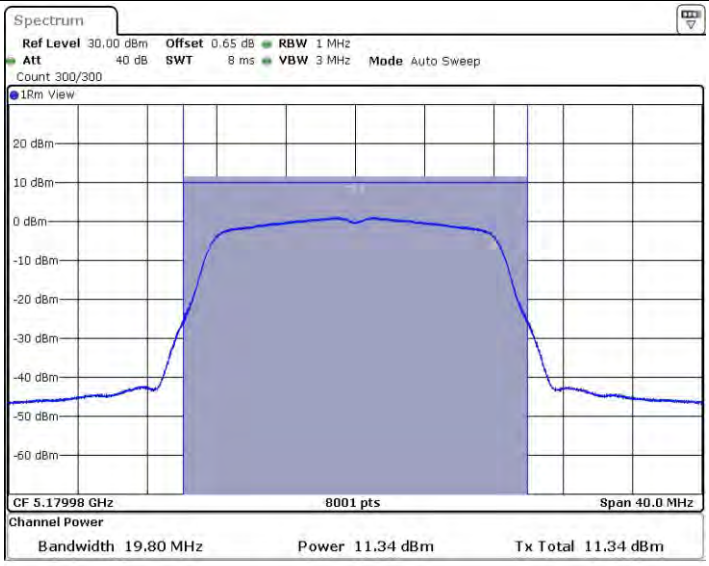
	Ant1	5700	12.80	<=23.98	PASS
	Ant2	5700	11.13	<=23.98	PASS
	total	5700	15.1	<=23.98	PASS
	Ant1	5745	13.23	<=30	PASS
	Ant2	5745	13.34	<=30	PASS
	total	5745	16.3	<=30	PASS
	Ant1	5785	13.21	<=30	PASS
	Ant2	5785	13.44	<=30	PASS
	total	5785	16.3	<=30	PASS
	Ant1	5825	13.30	<=30	PASS
	Ant2	5825	13.55	<=30	PASS
	total	5825	16.4	<=30	PASS
11AC40MIMO	Ant1	5190	6.44	<=23.98	PASS
	Ant2	5190	6.65	<=23.98	PASS
	total	5190	9.6	<=23.98	PASS
	Ant1	5230	6.57	<=23.98	PASS
	Ant2	5230	6.49	<=23.98	PASS
	total	5230	9.5	<=23.98	PASS
	Ant1	5270	13.06	<=23.98	PASS
	Ant2	5270	12.71	<=23.98	PASS
	total	5270	15.9	<=23.98	PASS
	Ant1	5310	13.13	<=23.98	PASS
	Ant2	5310	12.76	<=23.98	PASS
	total	5310	16.0	<=23.98	PASS
	Ant1	5510	13.35	<=23.98	PASS
	Ant2	5510	13.15	<=23.98	PASS
	total	5510	16.3	<=23.98	PASS
	Ant1	5550	13.23	<=23.98	PASS
	Ant2	5550	13.13	<=23.98	PASS
	total	5550	16.2	<=23.98	PASS
	Ant1	5670	13.26	<=23.98	PASS
	Ant2	5670	13.06	<=23.98	PASS
	total	5670	16.2	<=23.98	PASS
	Ant1	5755	13.20	<=30	PASS
	Ant2	5755	13.30	<=30	PASS
	total	5755	16.3	<=30	PASS
	Ant1	5795	13.18	<=30	PASS
	Ant2	5795	13.46	<=30	PASS
	total	5795	16.3	<=30	PASS
11AC80MIMO	Ant1	5210	9.22	<=23.98	PASS
	Ant2	5210	9.35	<=23.98	PASS
	total	5210	12.3	<=23.98	PASS

	Ant1	5290	13.07	<=23.98	PASS
	Ant2	5290	12.80	<=23.98	PASS
	total	5290	15.9	<=23.98	PASS
	Ant1	5530	13.30	<=23.98	PASS
	Ant2	5530	12.95	<=23.98	PASS
	total	5530	16.1	<=23.98	PASS
	Ant1	5610	12.69	<=23.98	PASS
	Ant2	5610	12.92	<=23.98	PASS
	total	5610	15.8	<=23.98	PASS
	Ant1	5775	12.95	<=30	PASS
	Ant2	5775	13.25	<=30	PASS
	total	5775	16.1	<=30	PASS

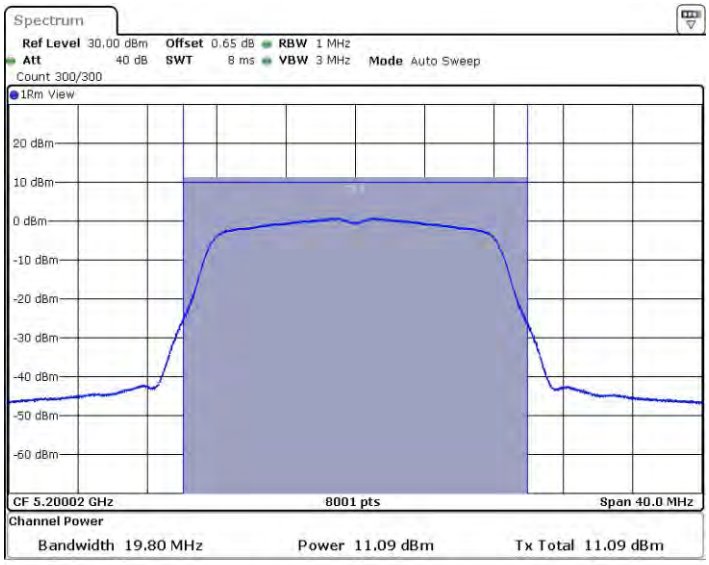
Test Graphs



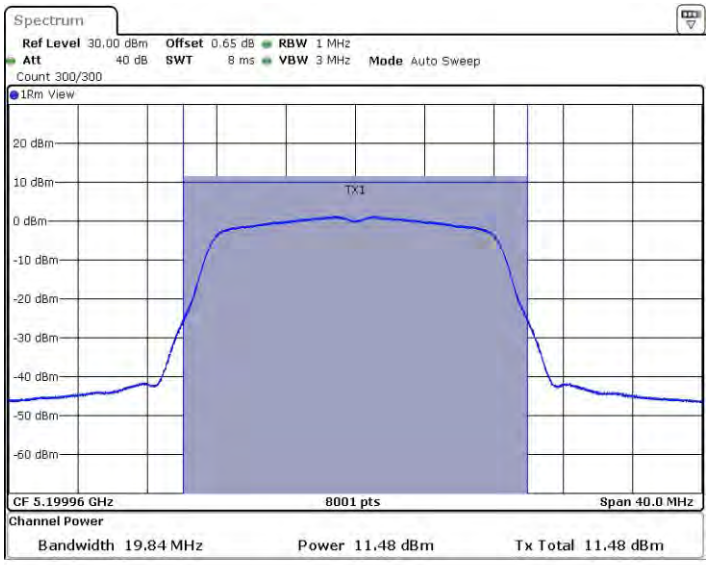
11A_Ant1_5180



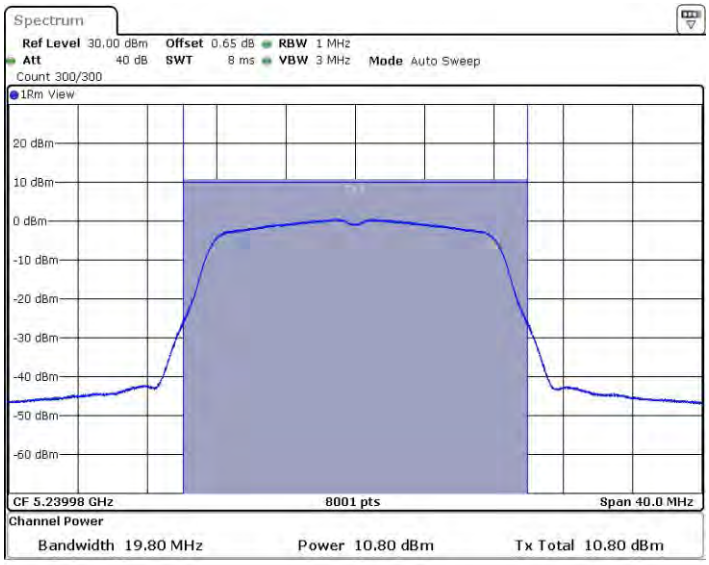
11A_Ant2_5180



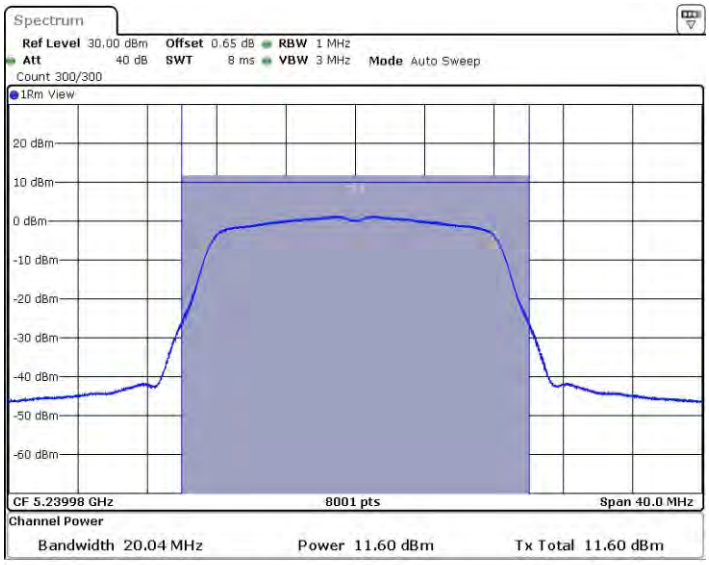
11A_Ant1_5200



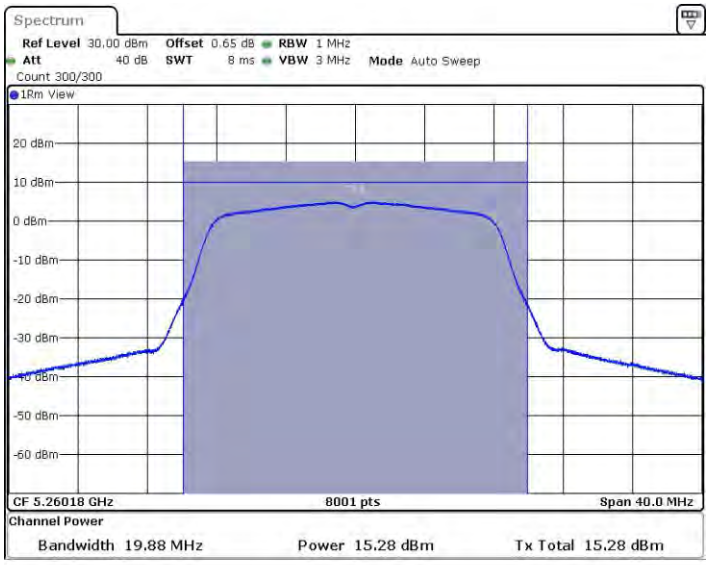
11A_Ant2_5200



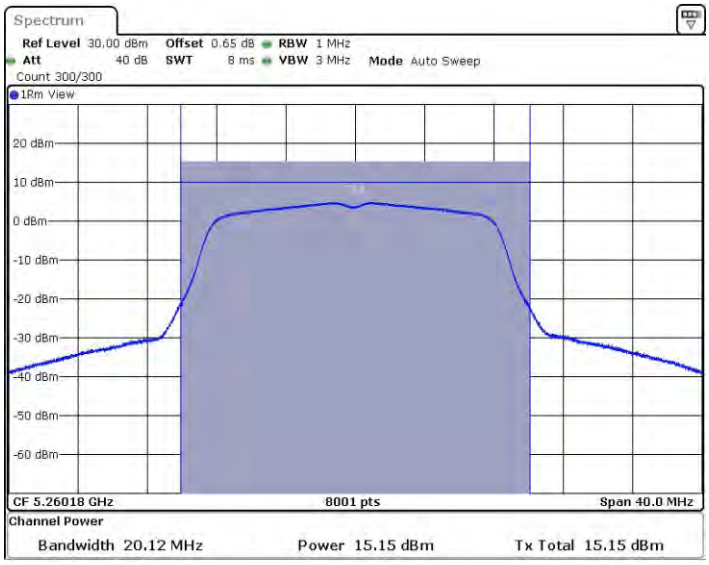
11A_Ant1_5240



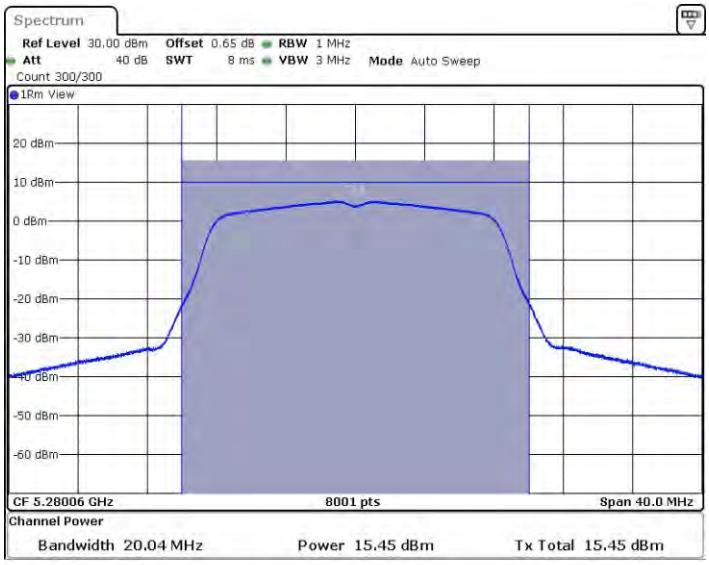
11A_Ant2_5240



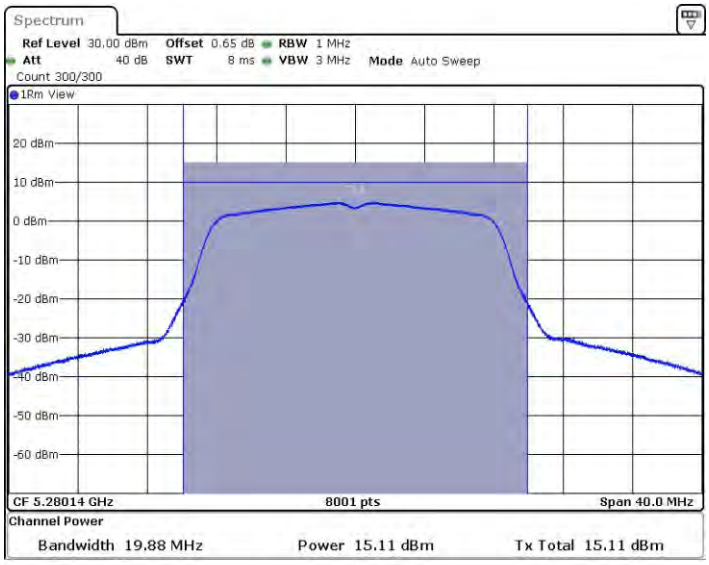
11A_Ant1_5260



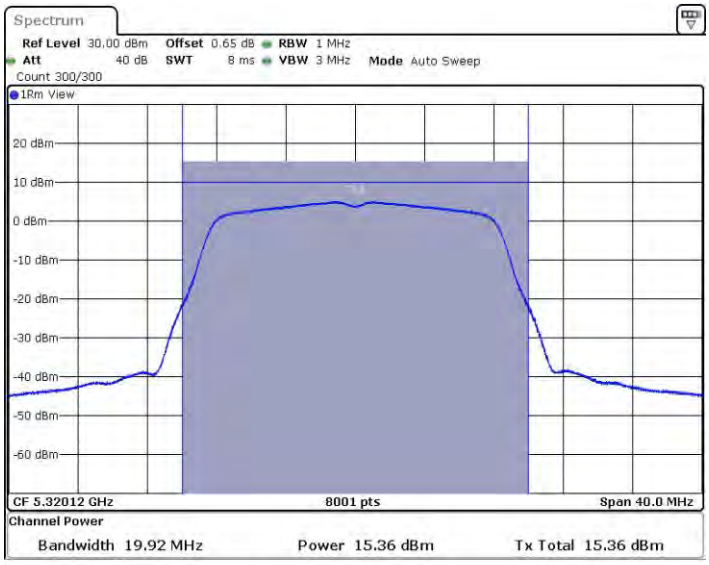
11A_Ant2_5260



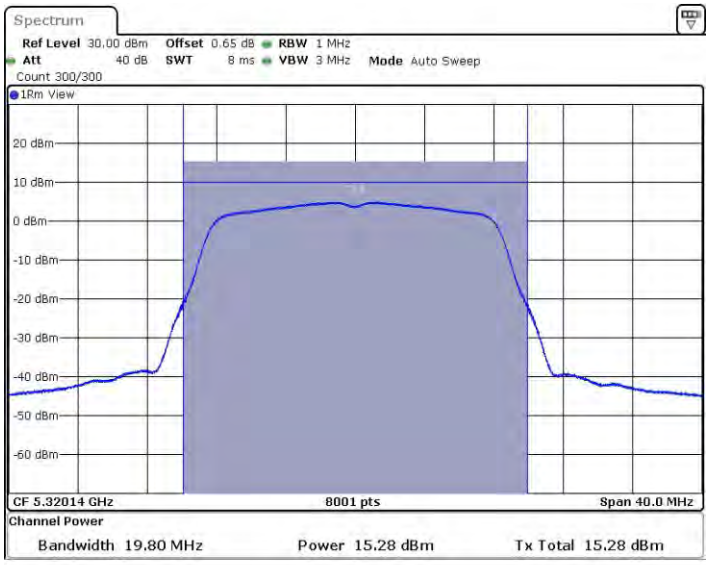
11A_Ant1_5280



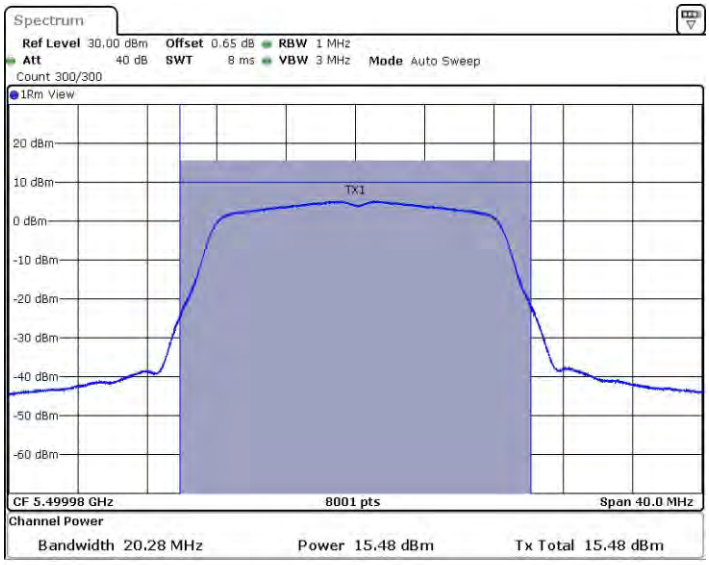
11A_Ant2_5280



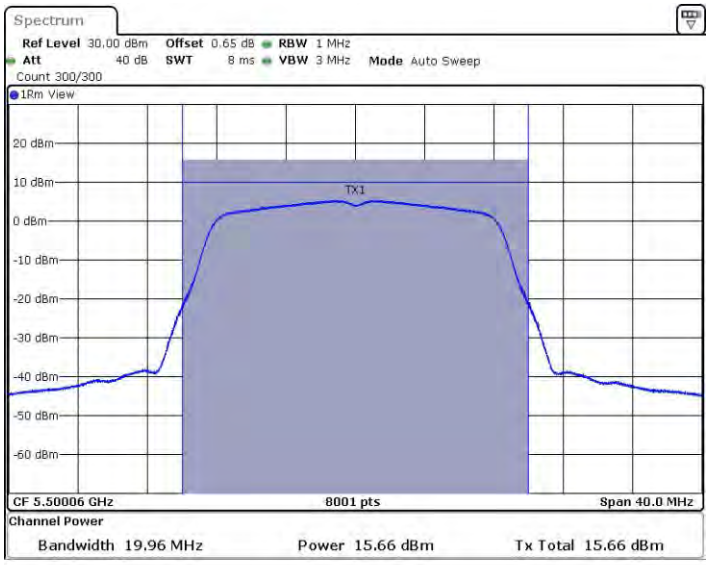
11A_Ant1_5320



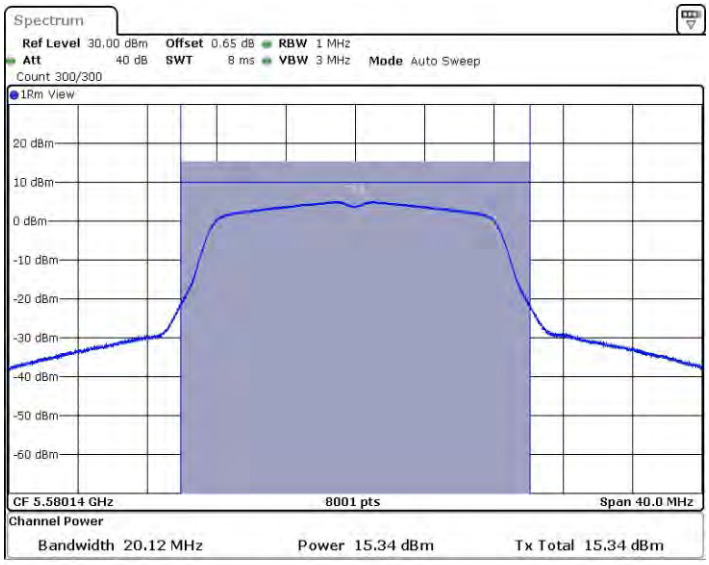
11A_Ant2_5320



11A_Ant1_5500



11A_Ant2_5500



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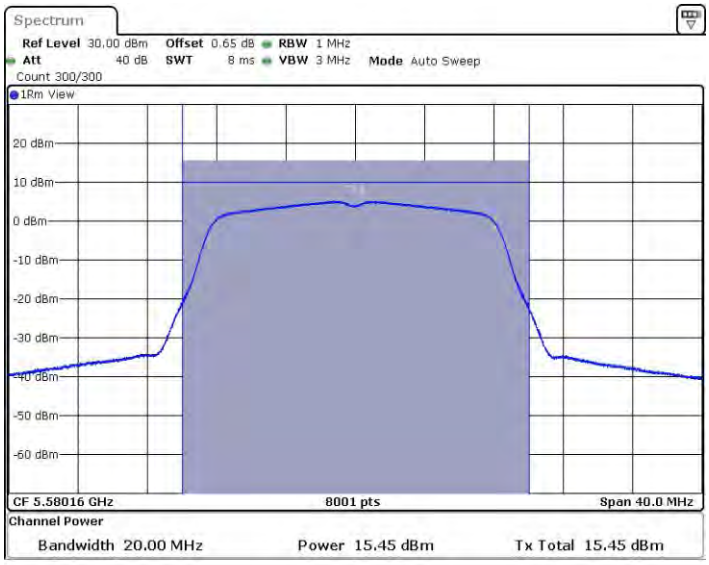
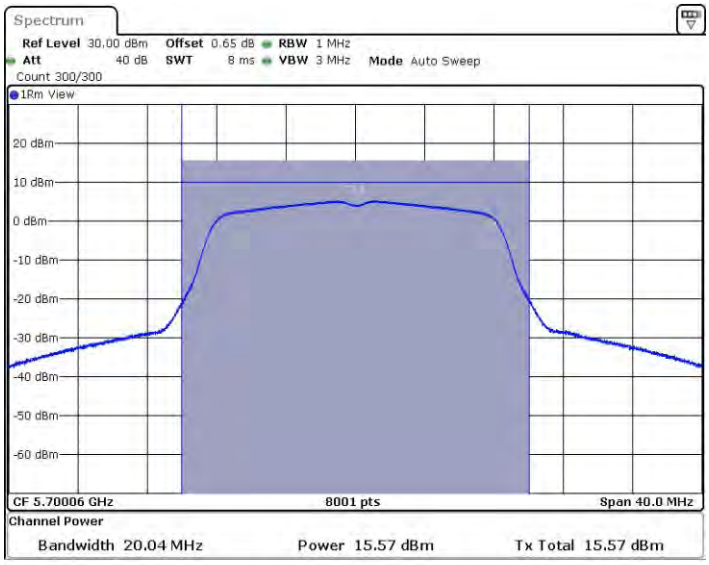
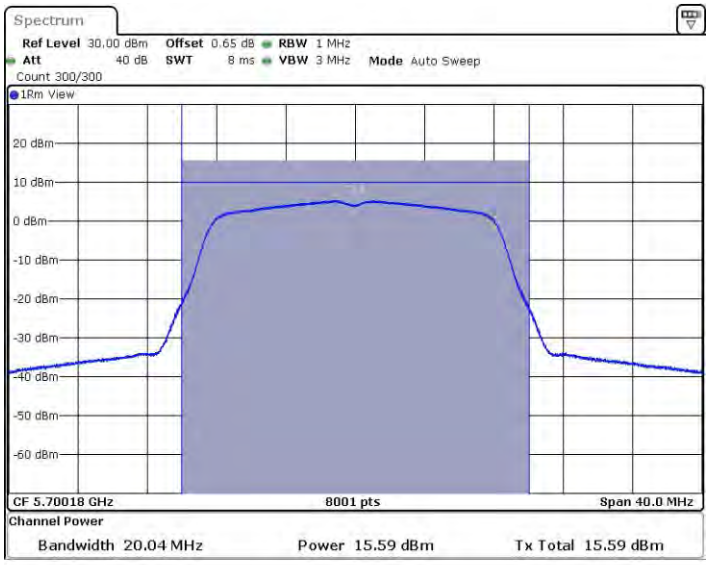


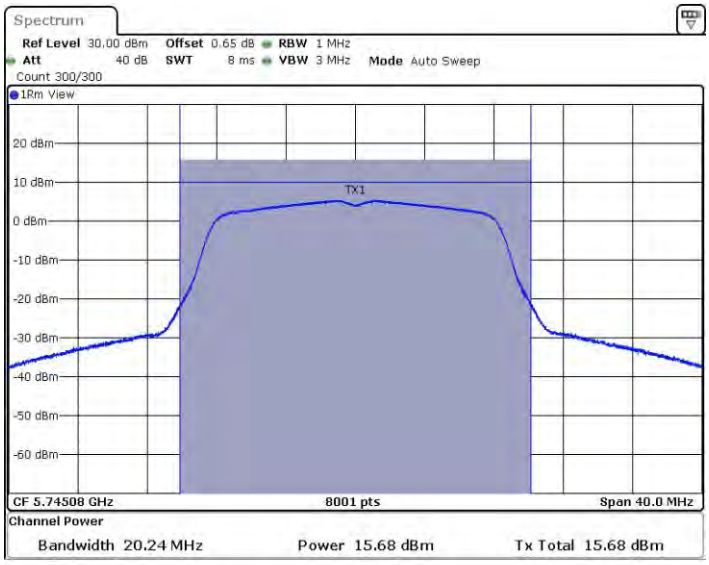
11A_Ant2_5580



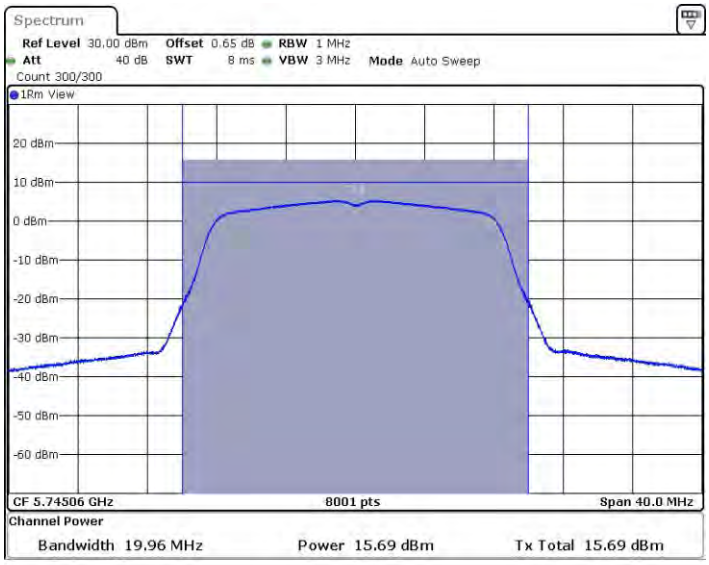
11A_Ant1_5700



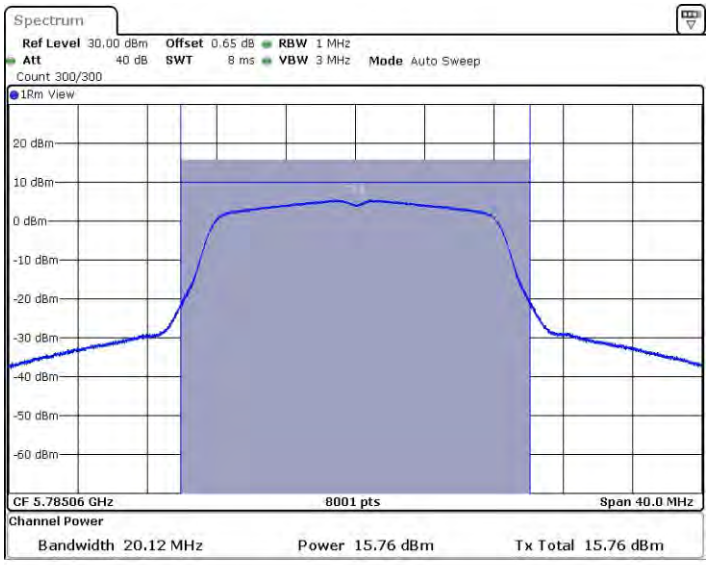
11A_Ant2_5700



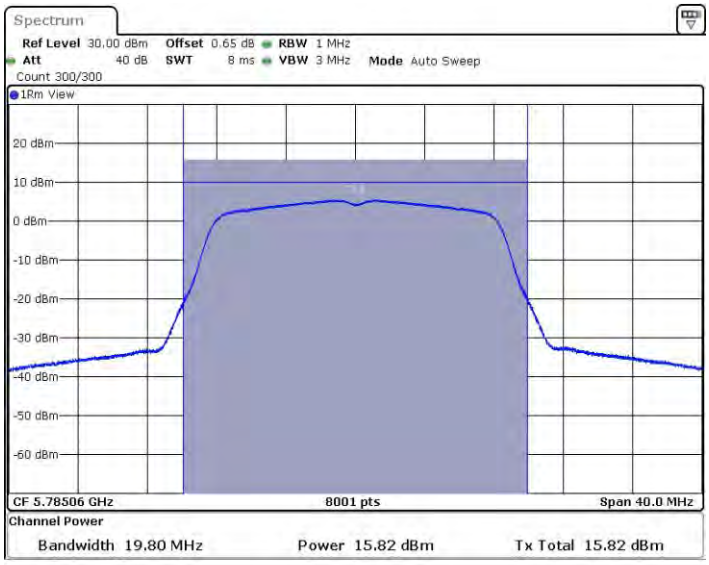
11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



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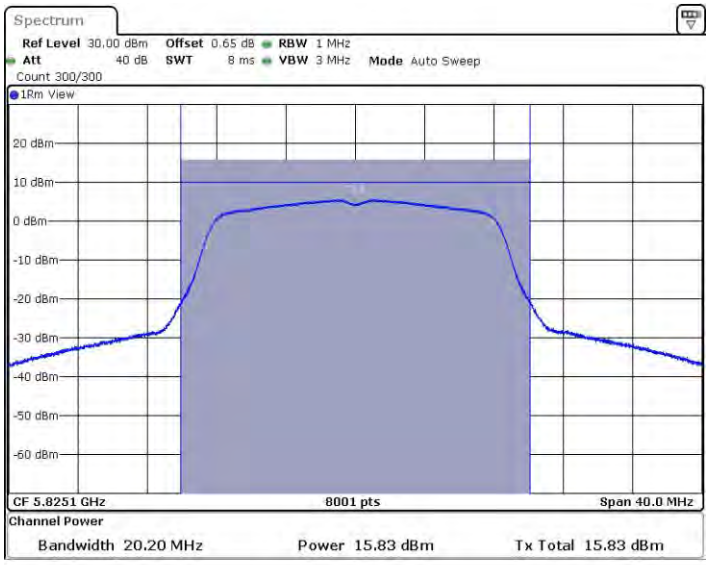

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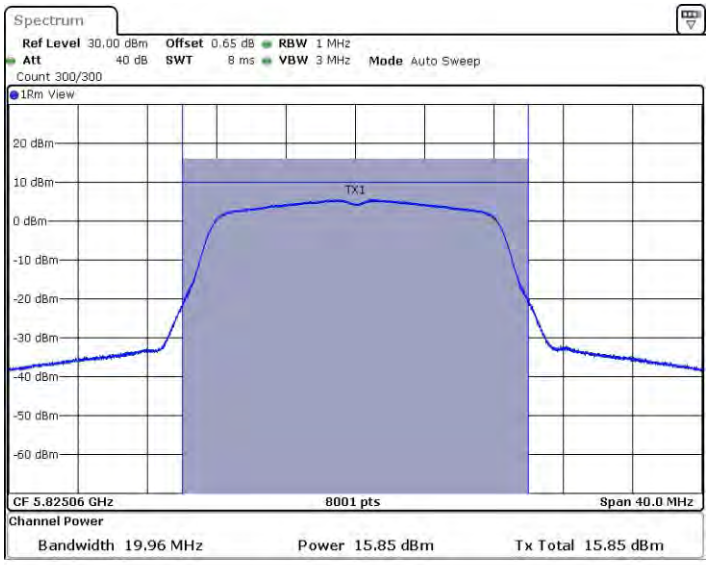

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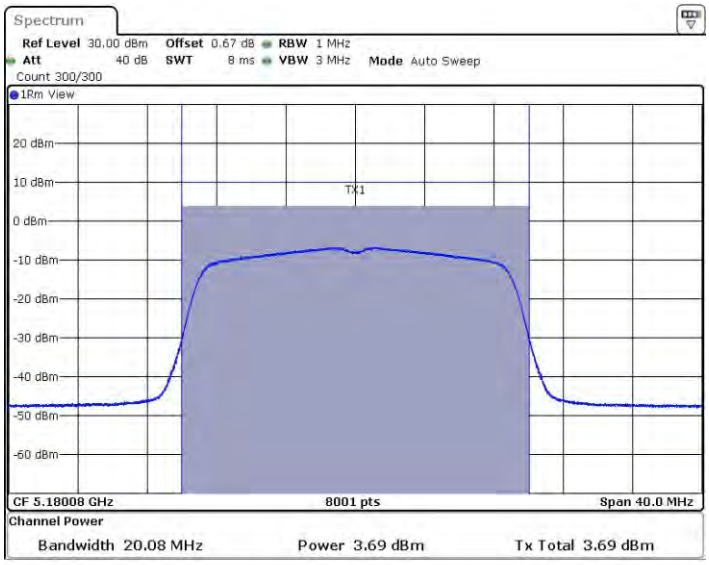

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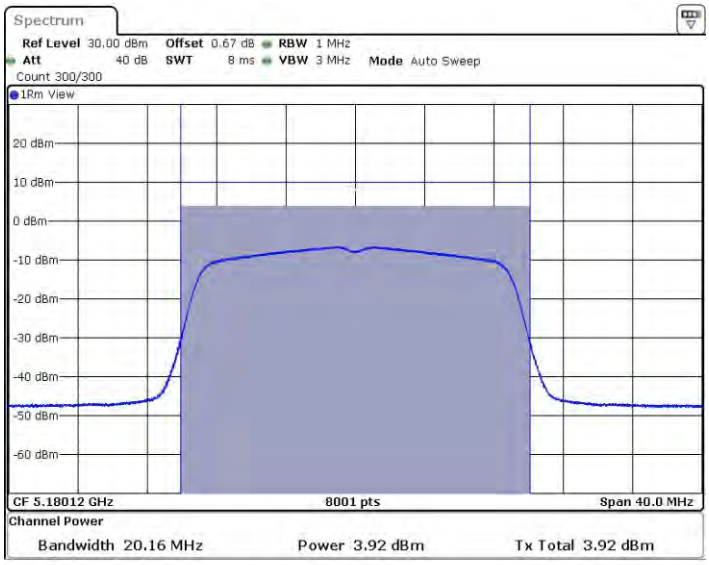

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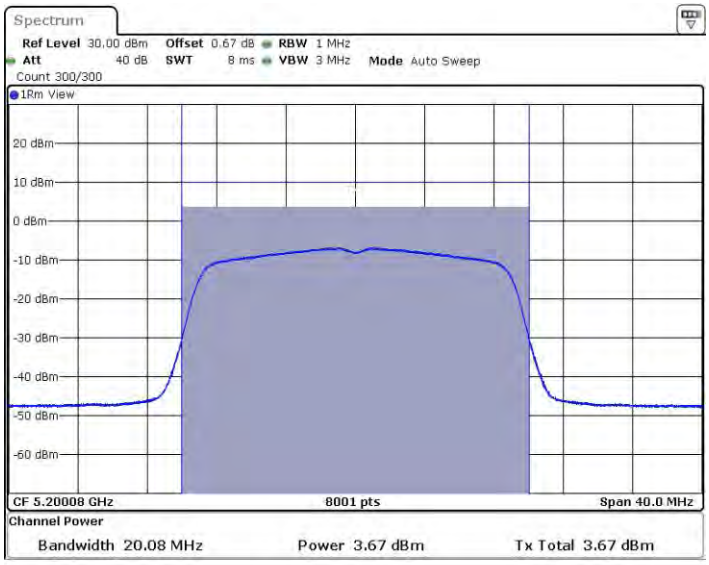

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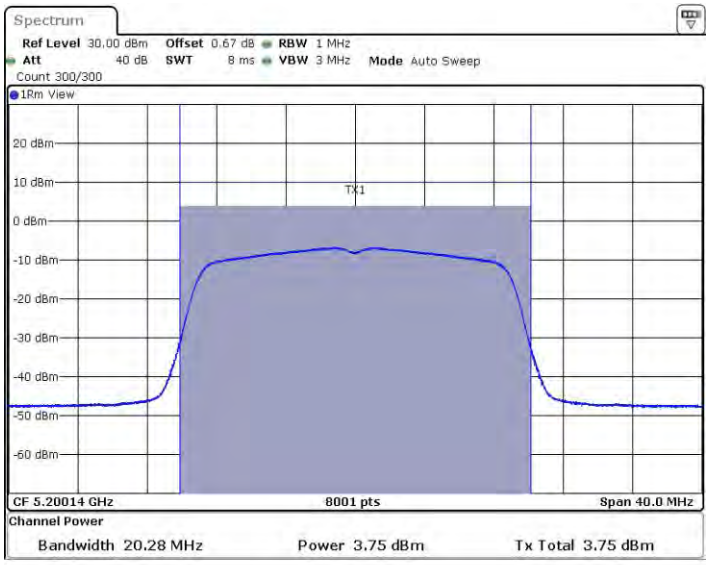

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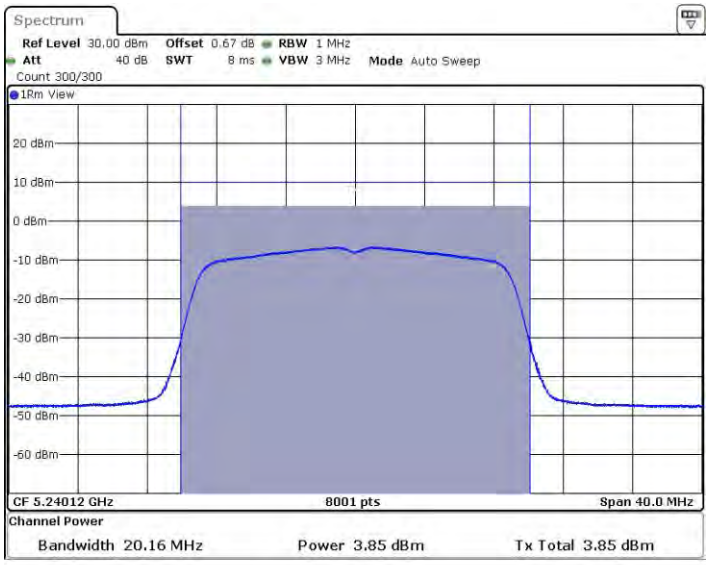

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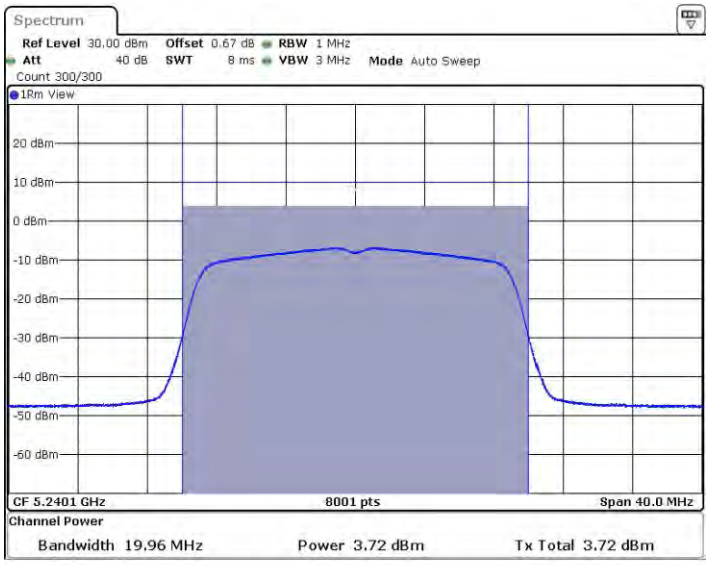

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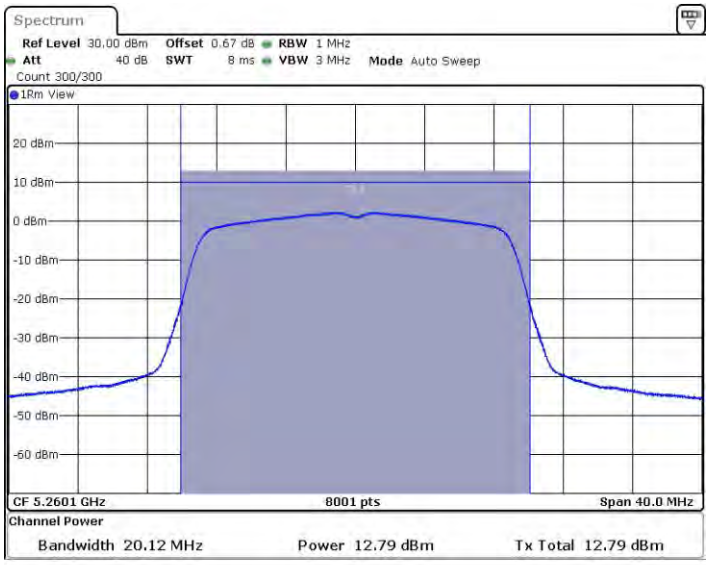

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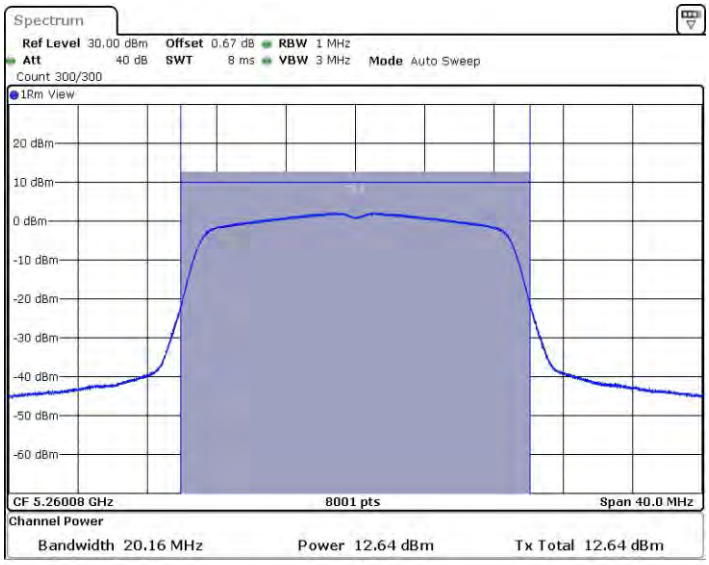

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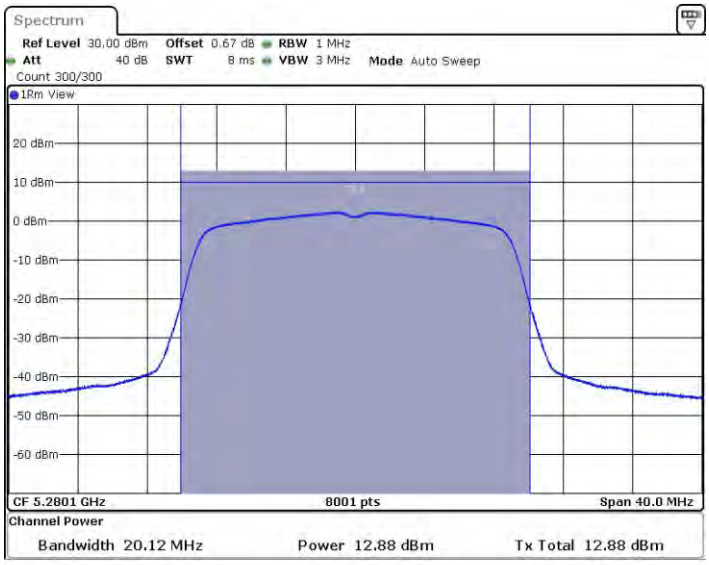

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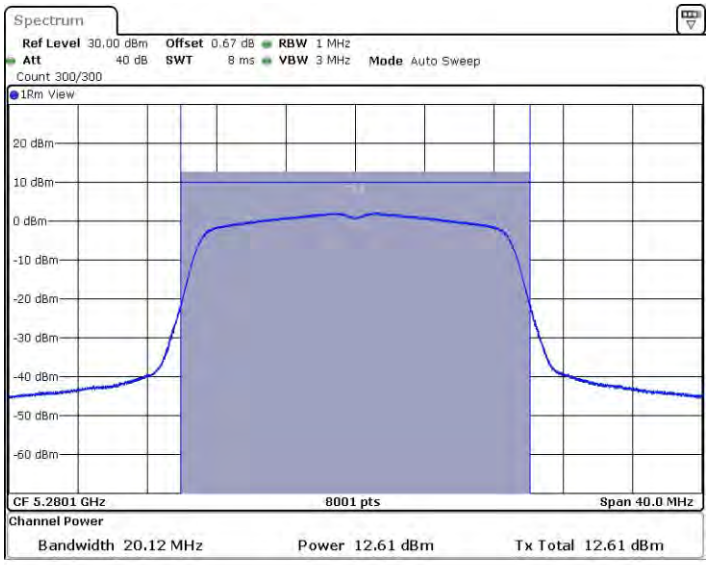

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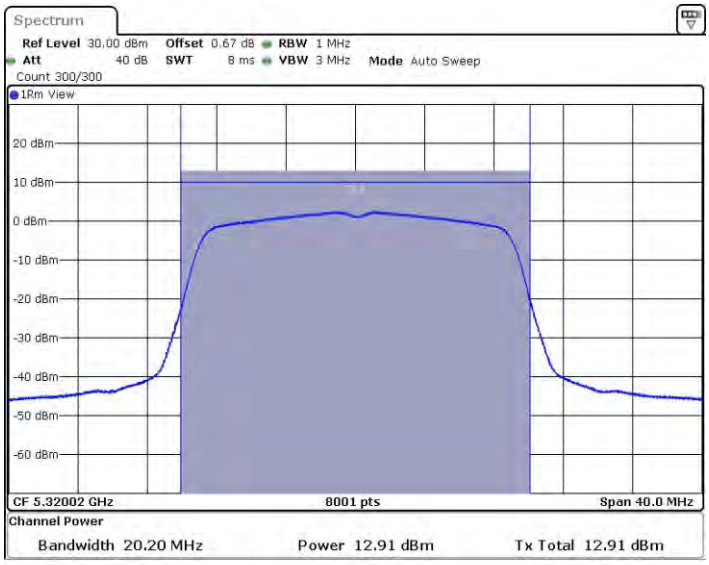

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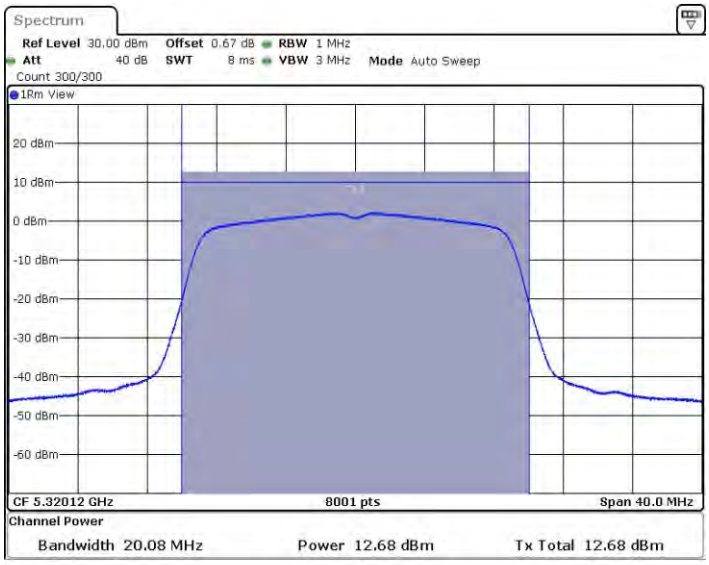

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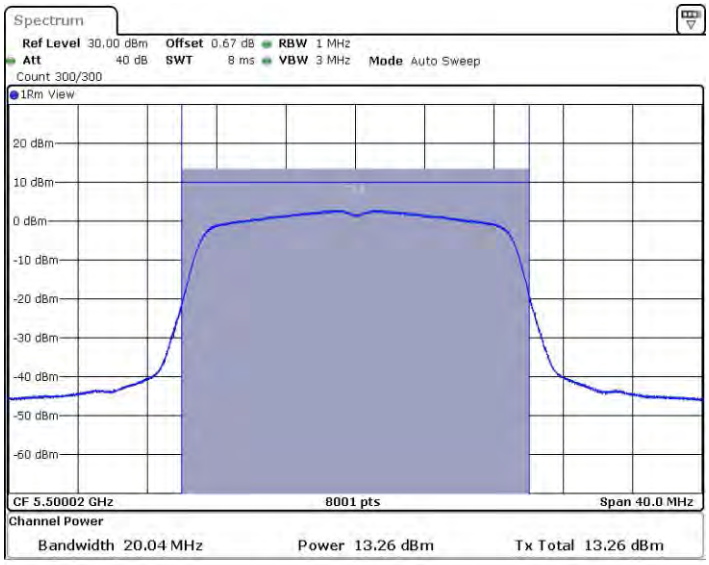

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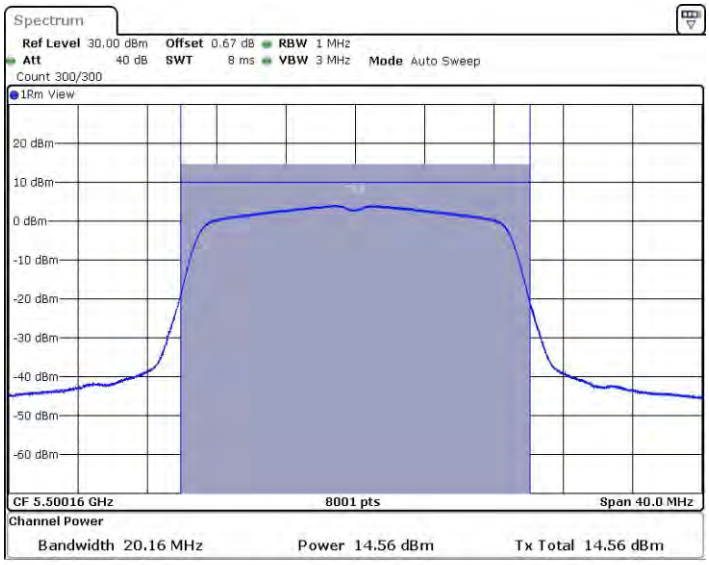

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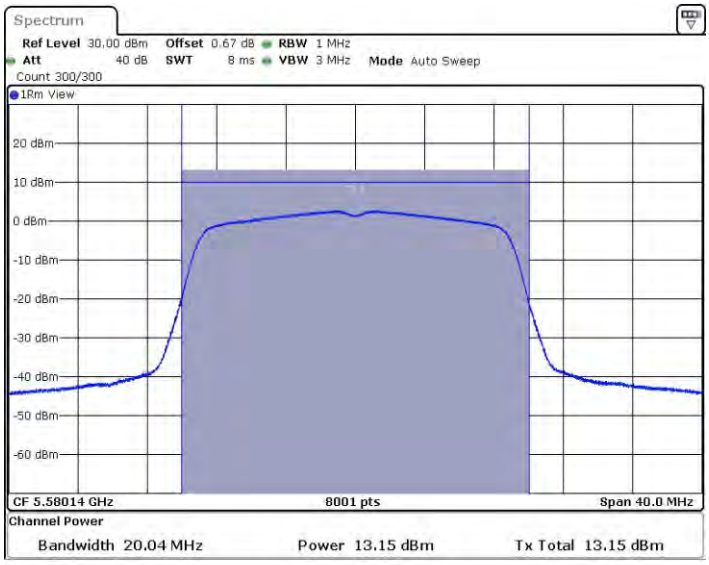

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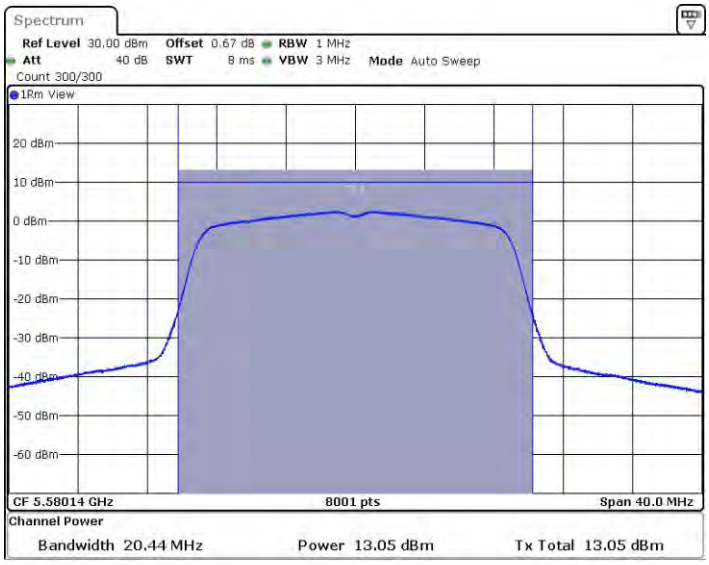

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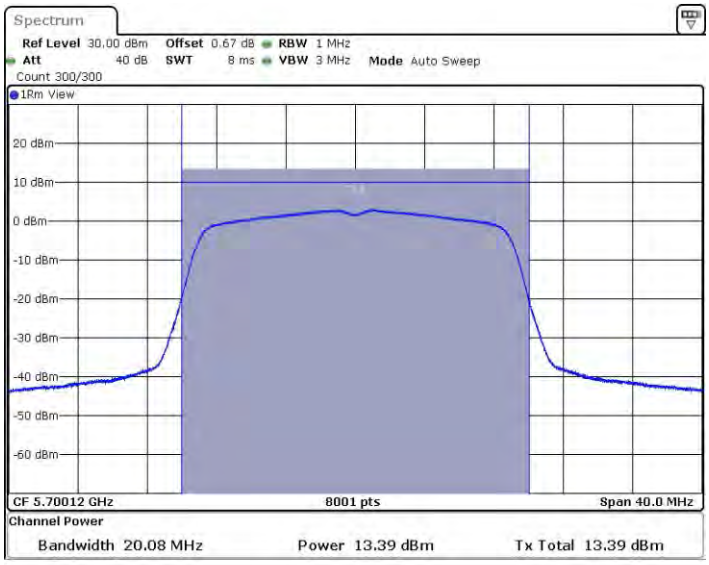

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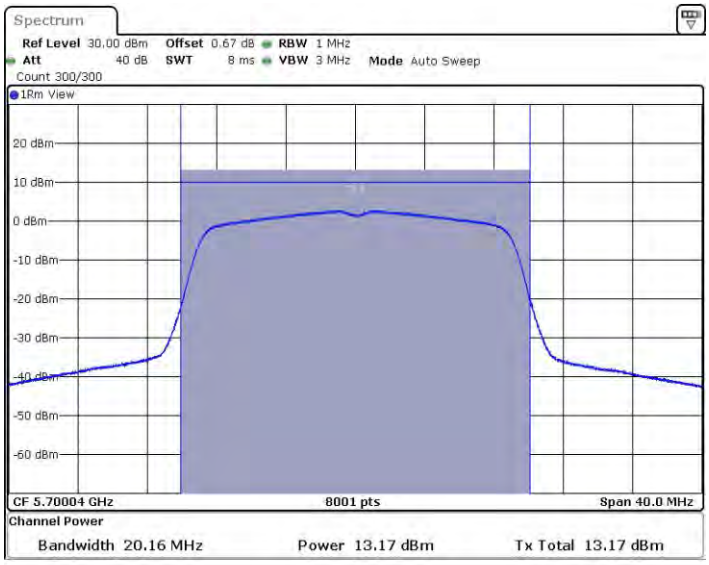

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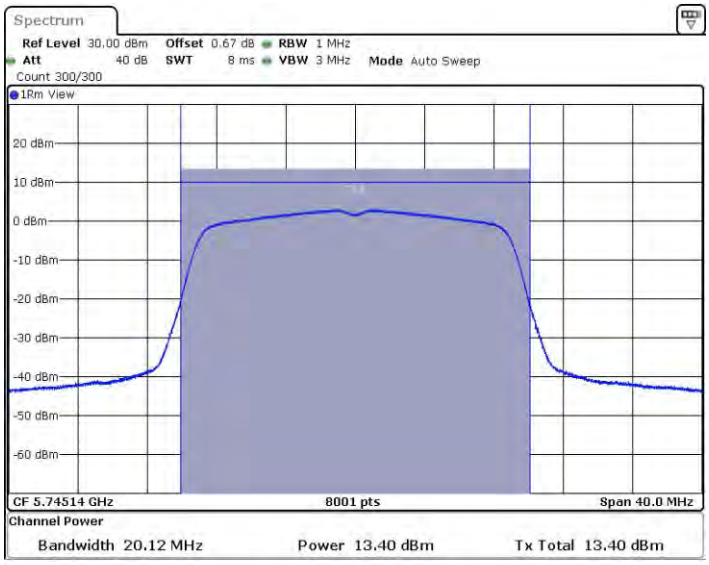

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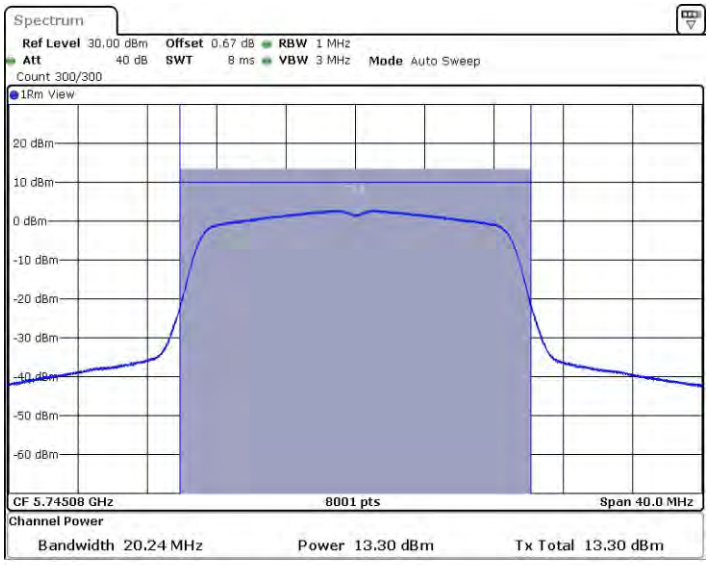

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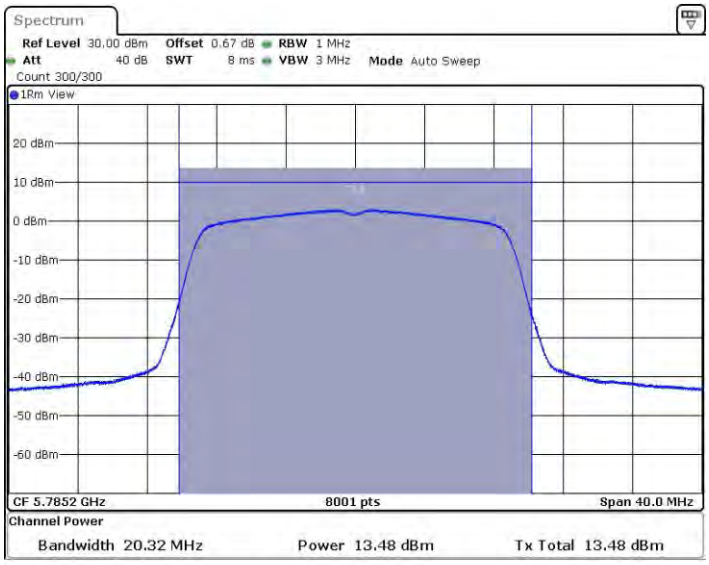

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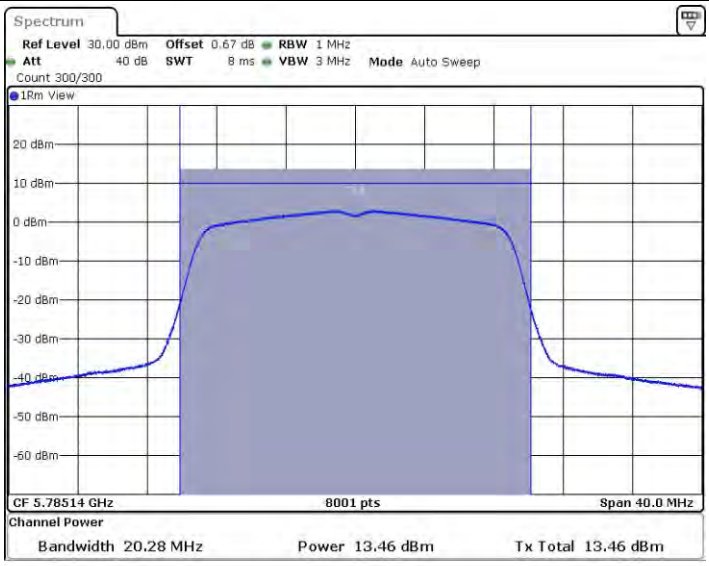

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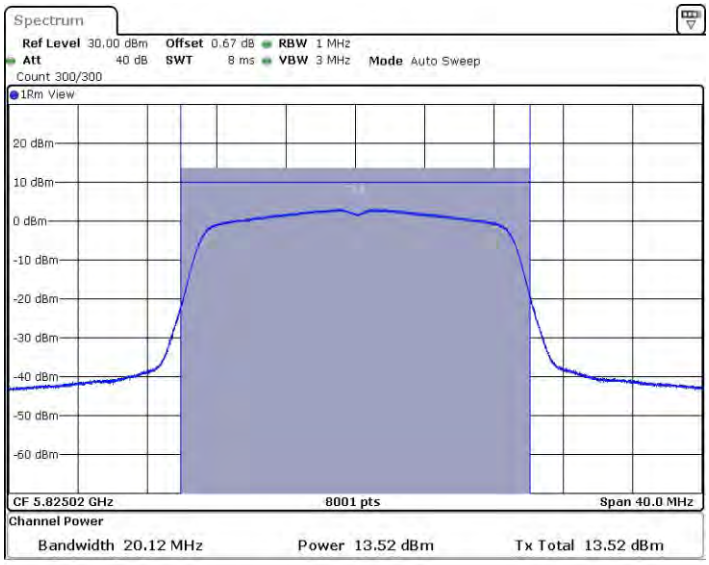

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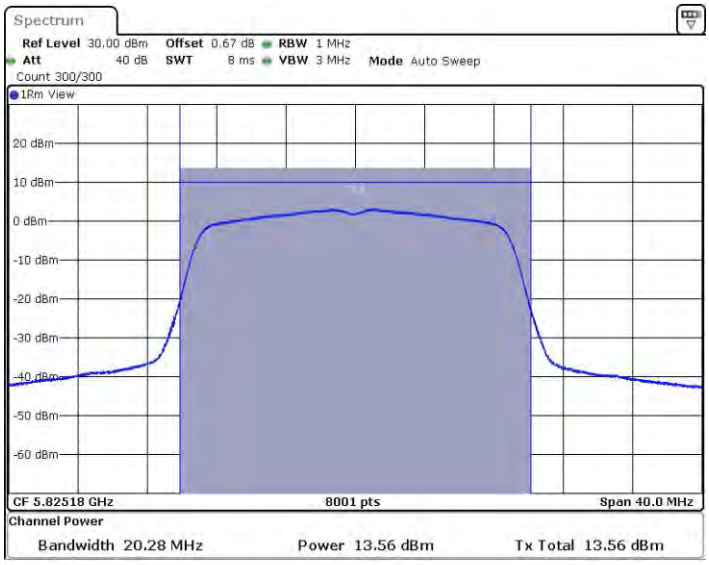

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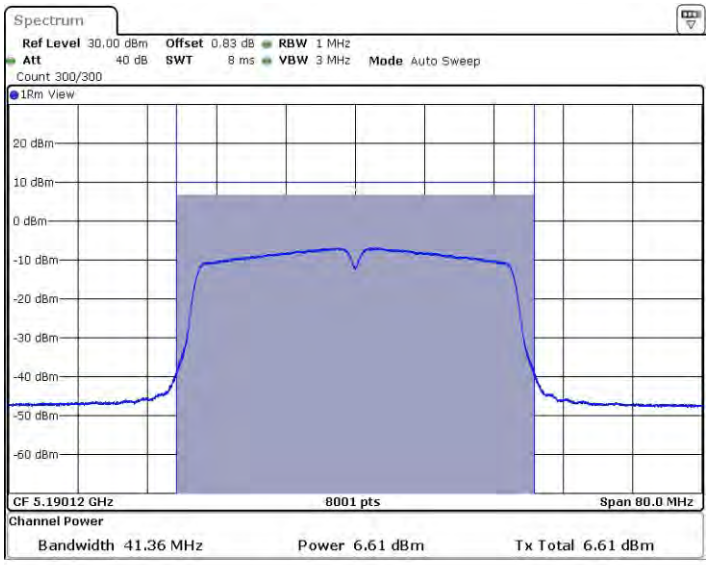

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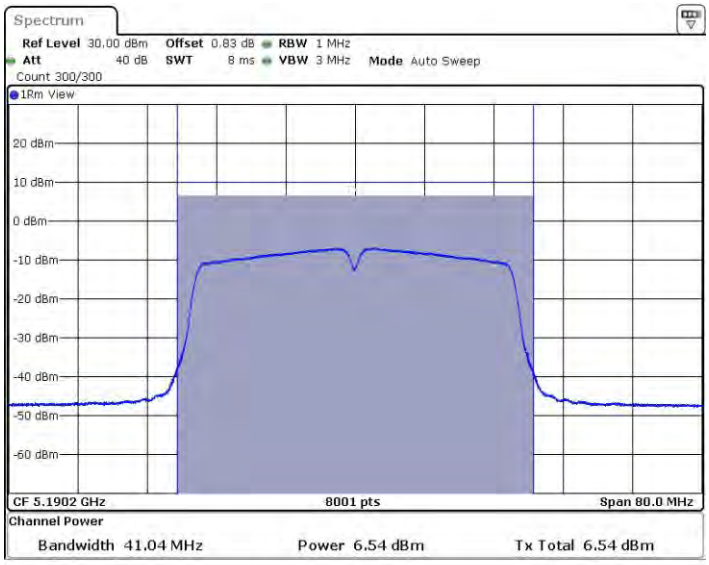

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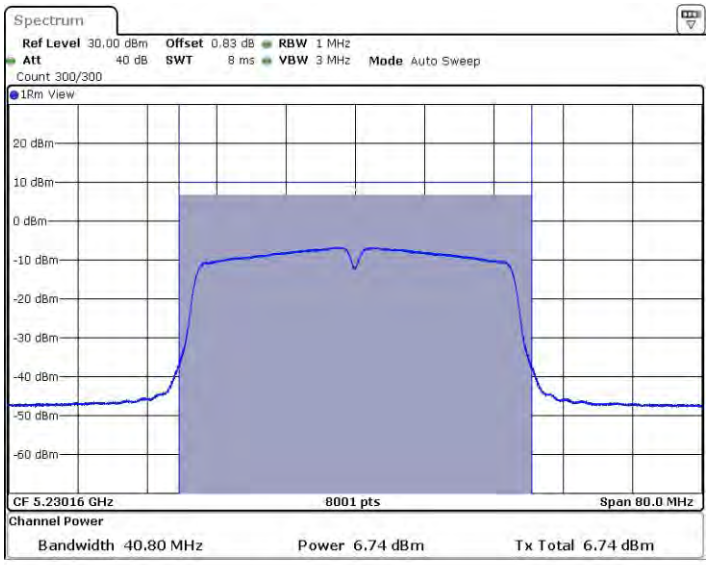

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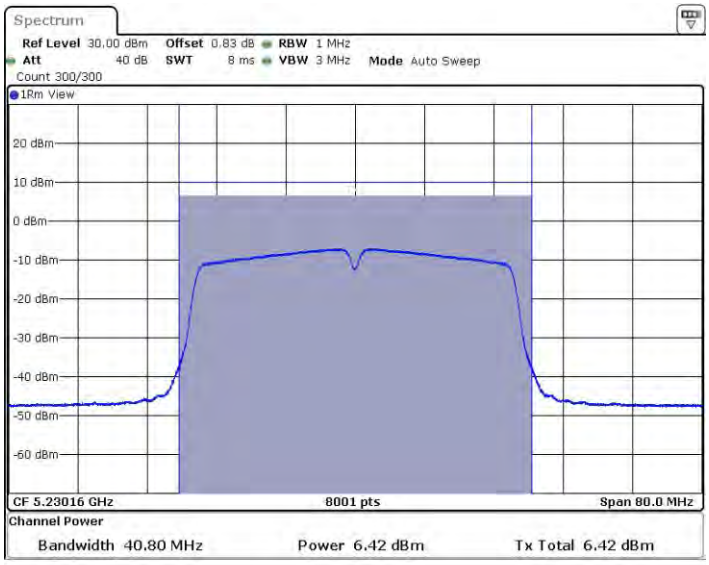

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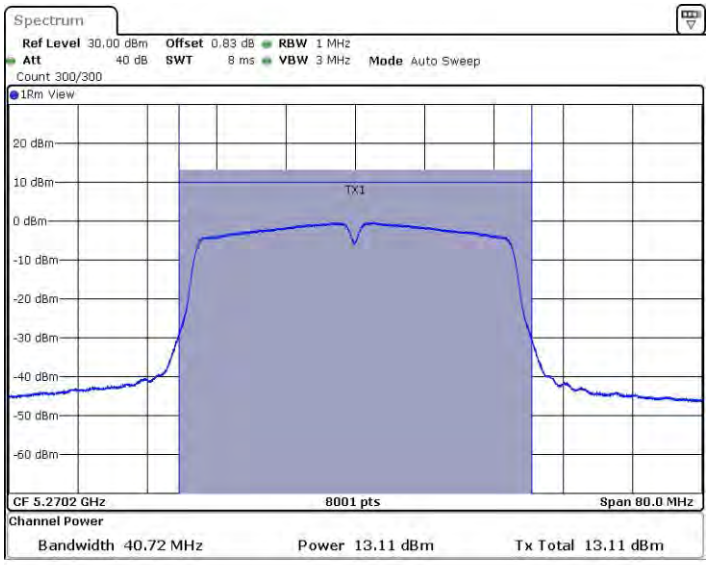

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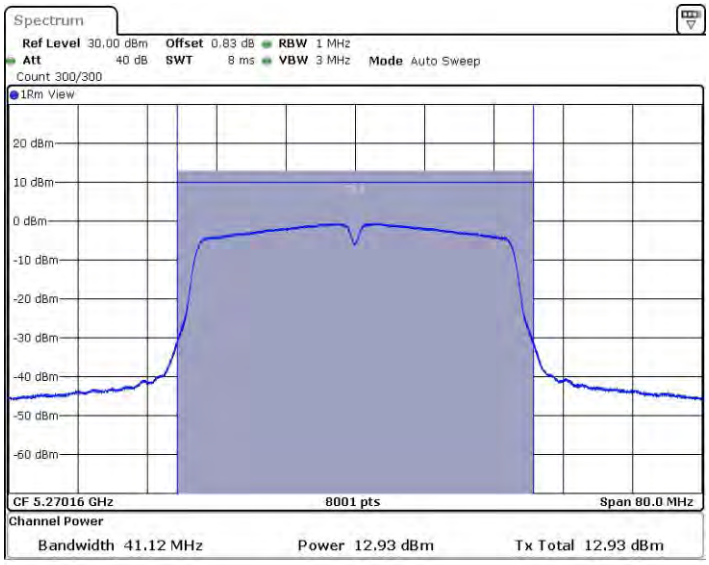

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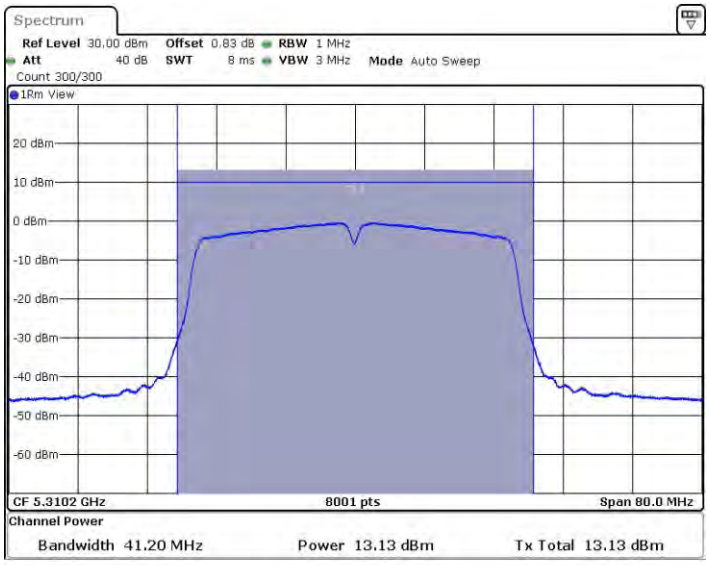

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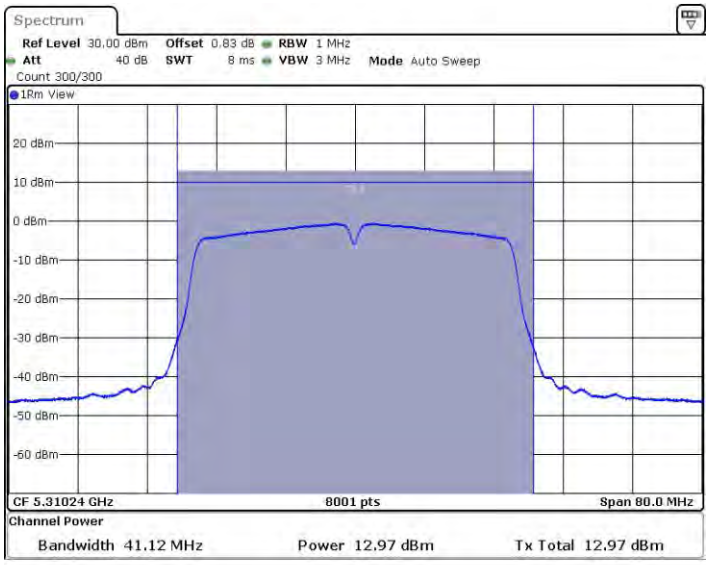

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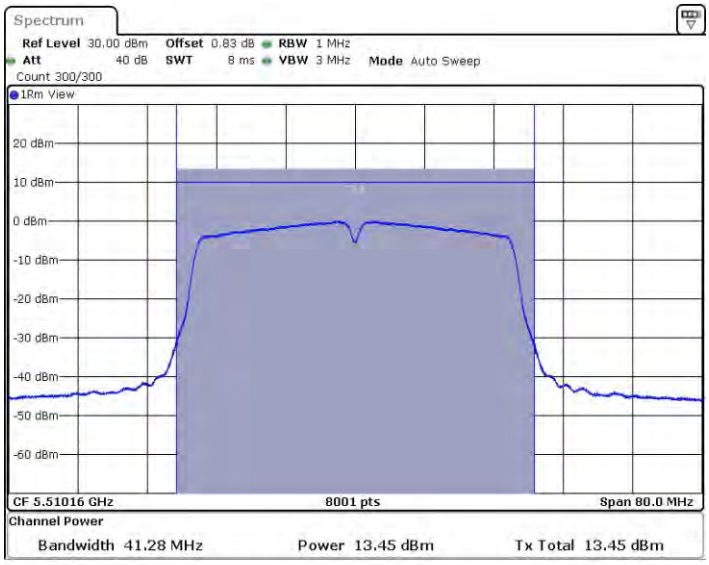

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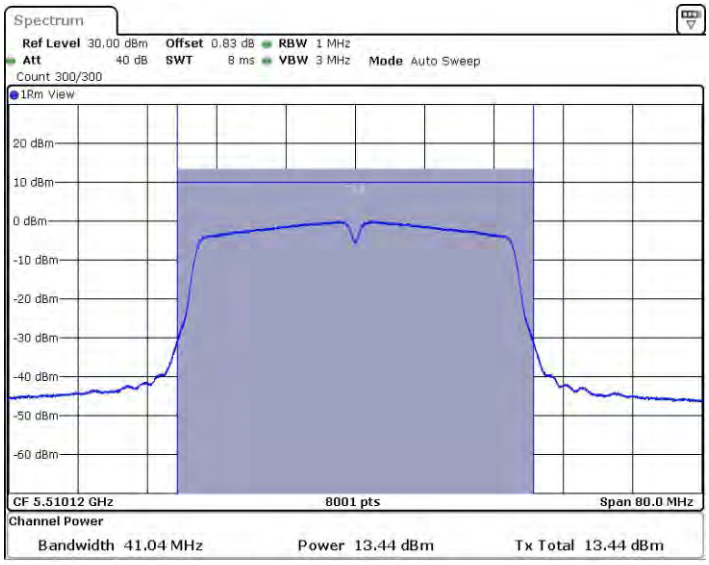

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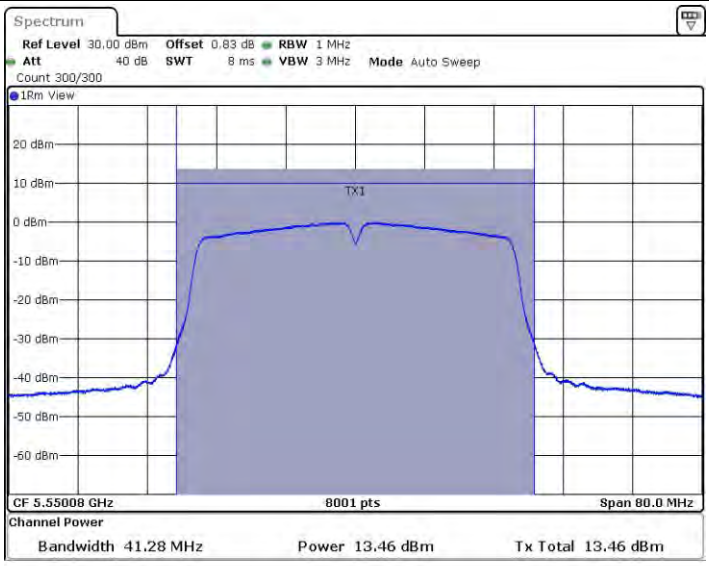

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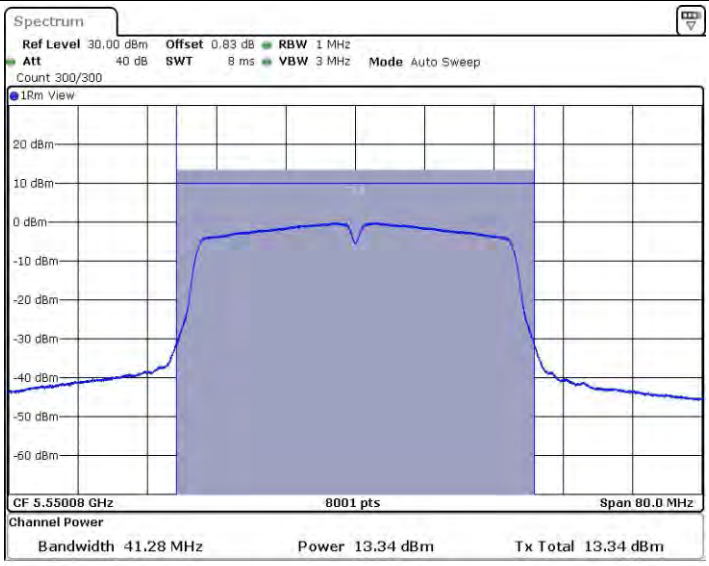

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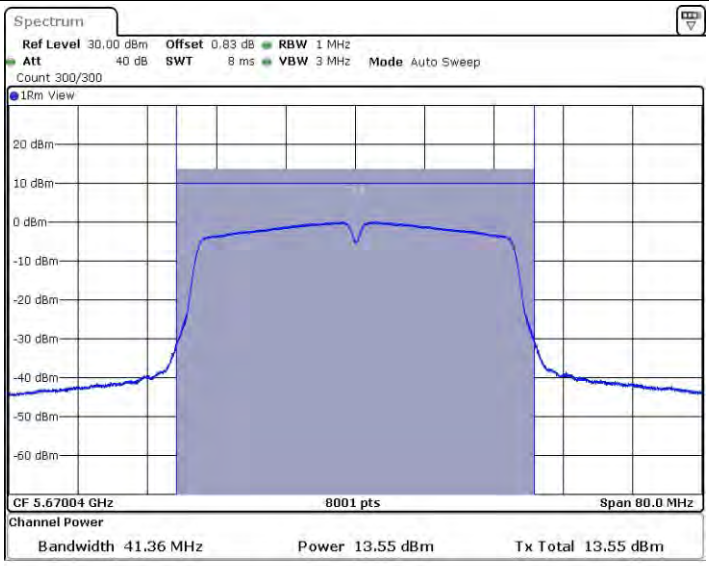

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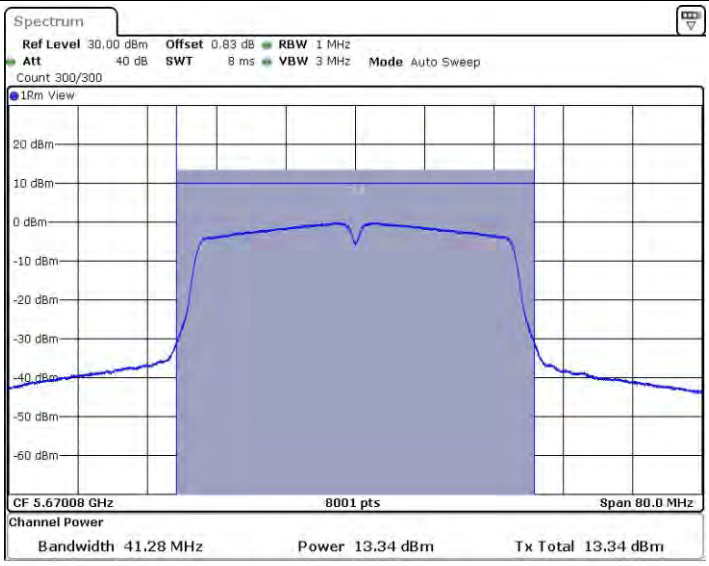

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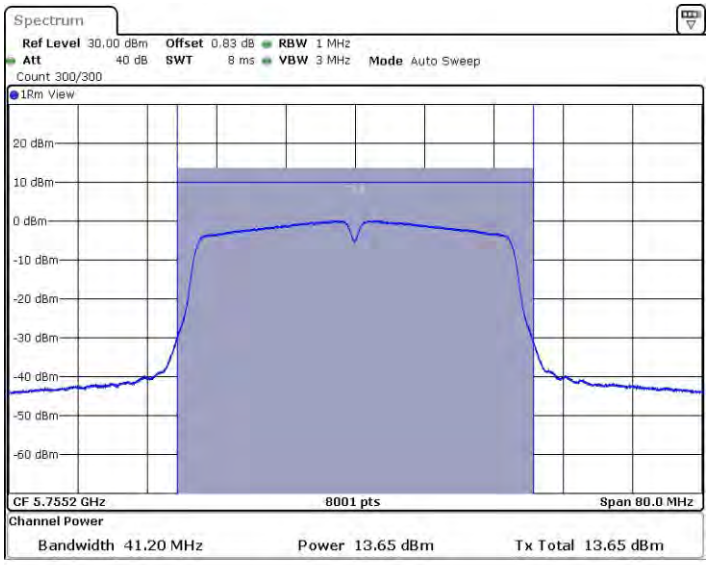

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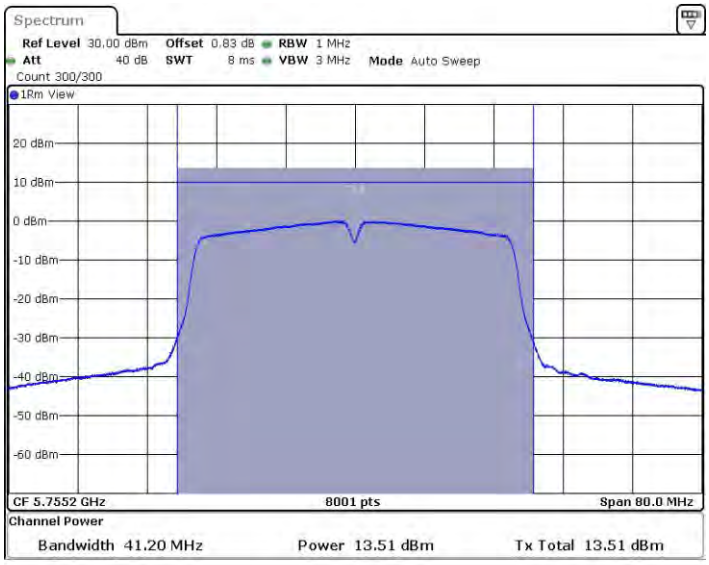

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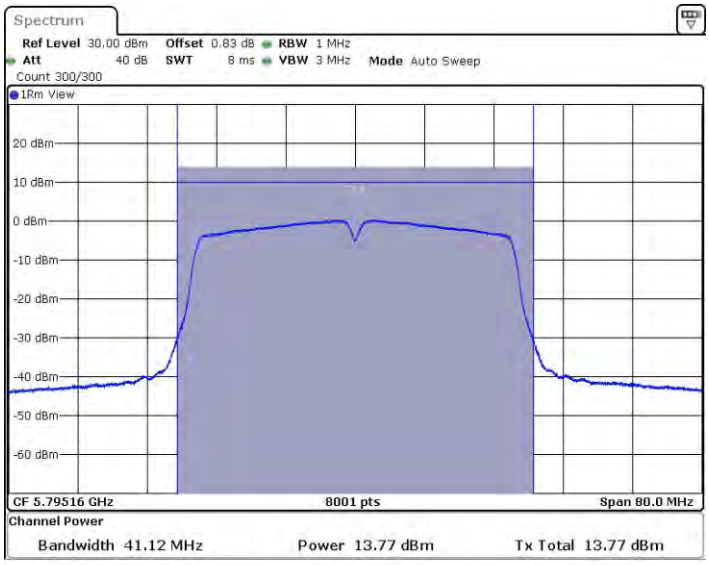

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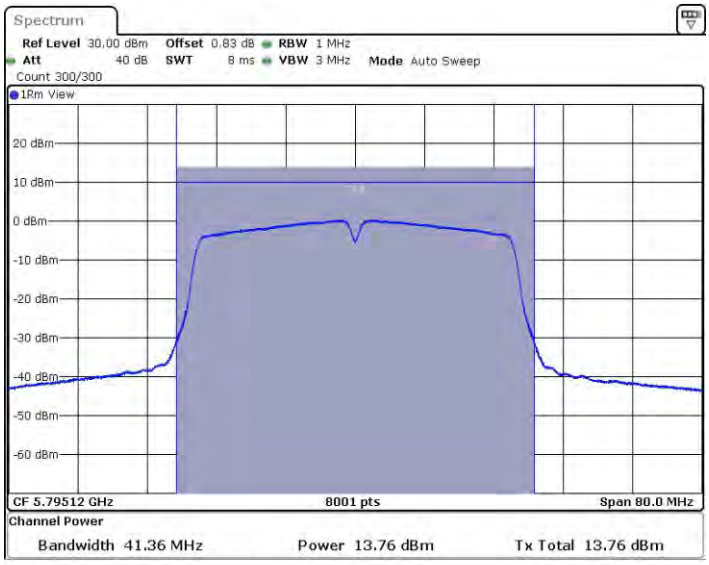

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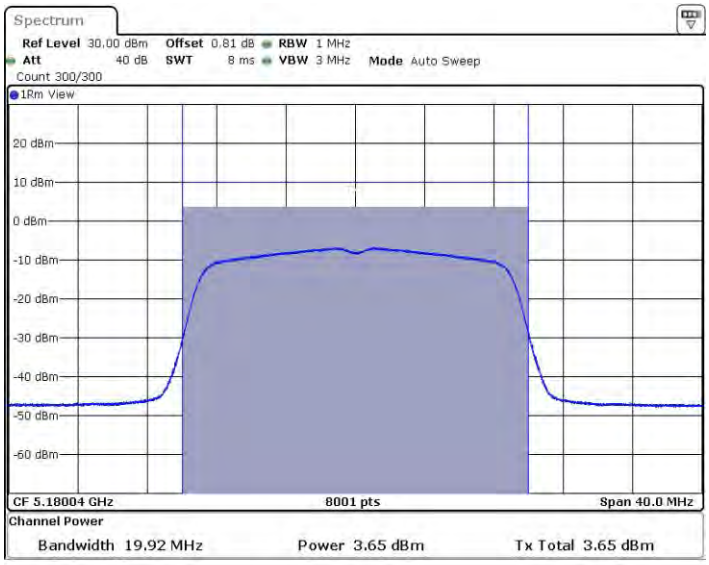

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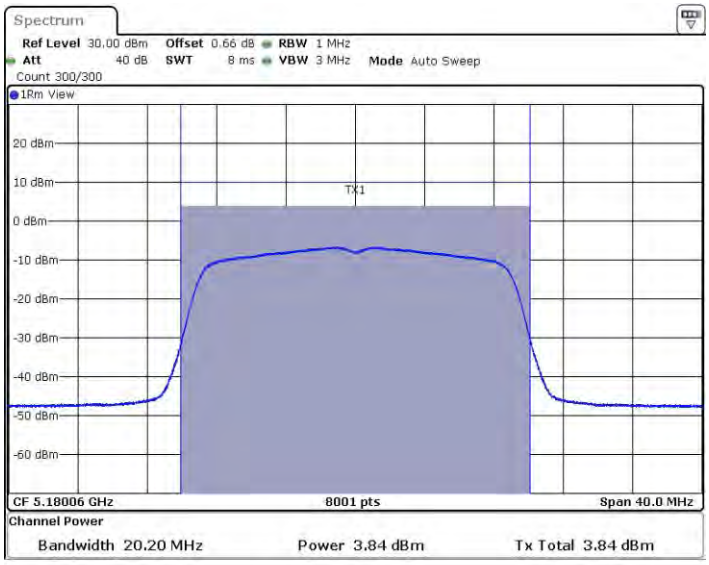

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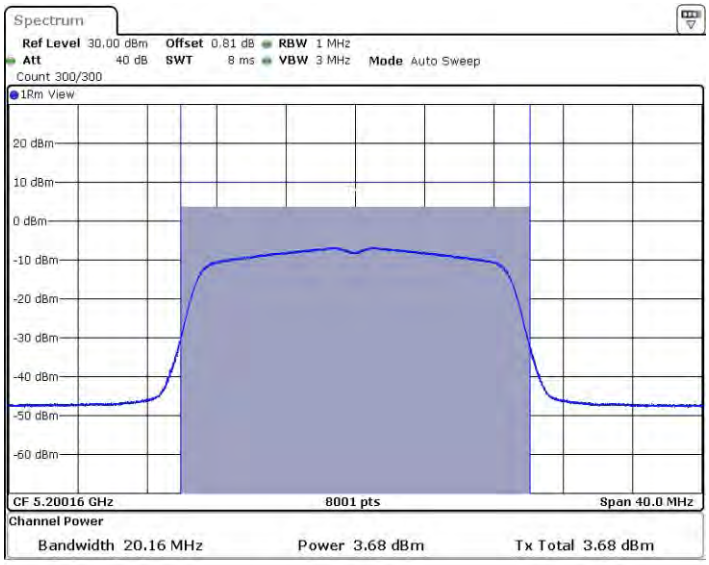

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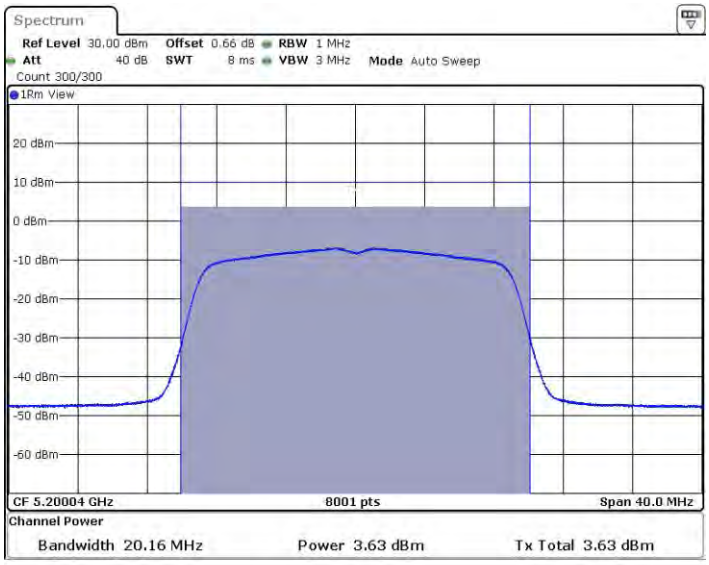

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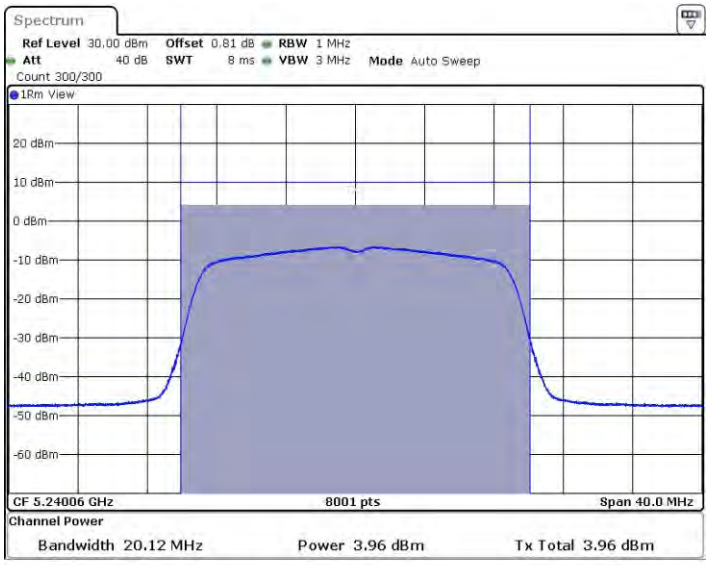

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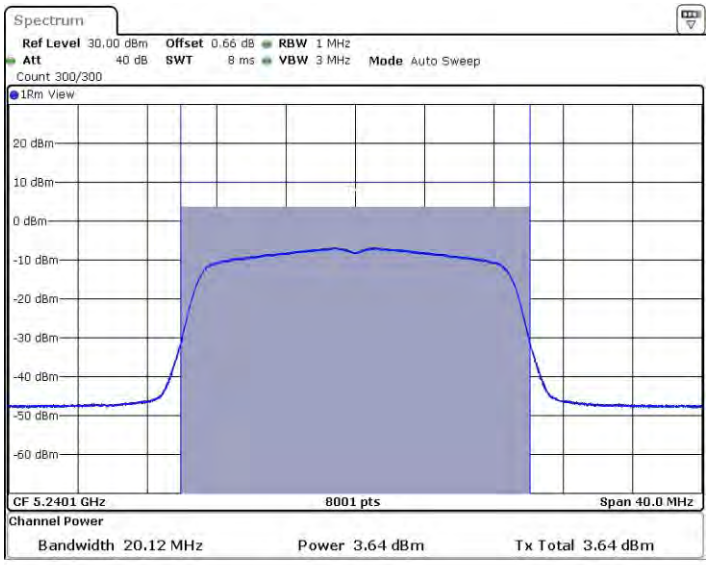

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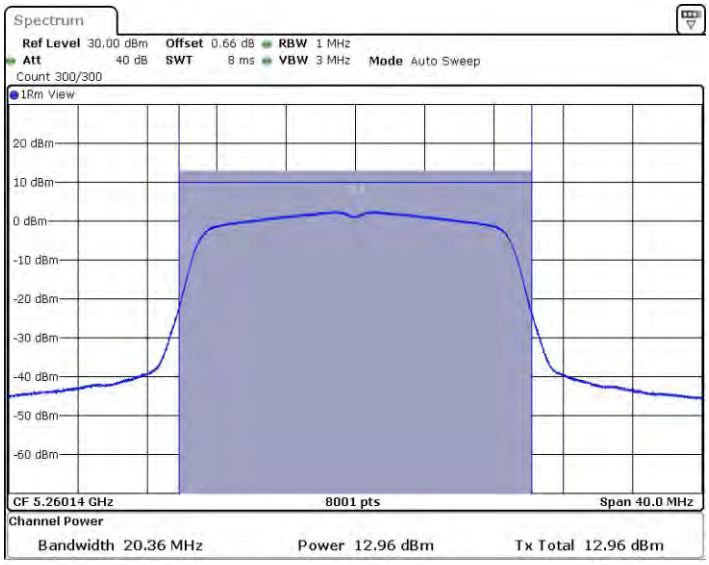

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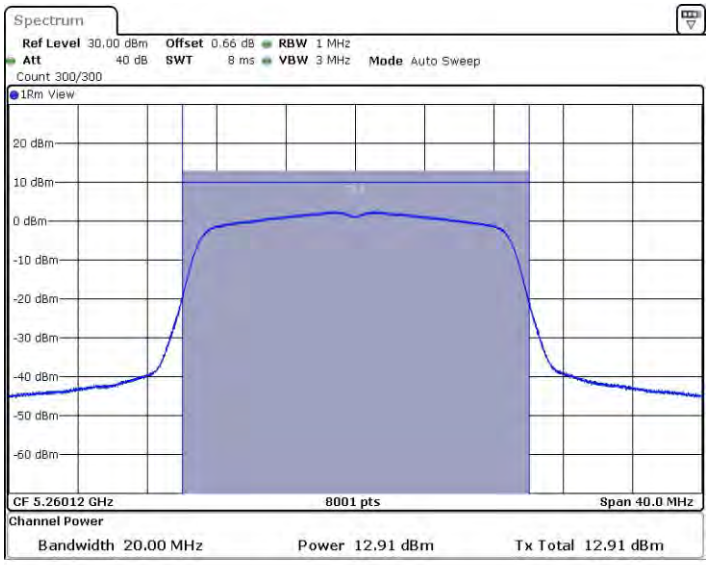

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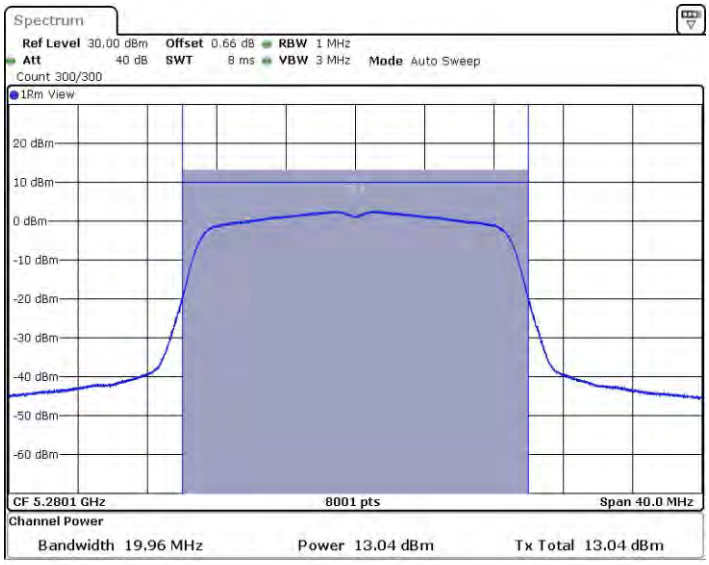

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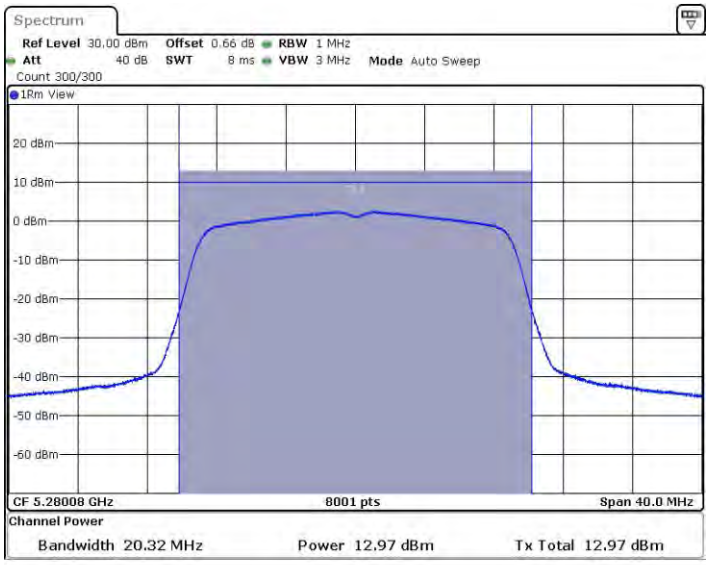
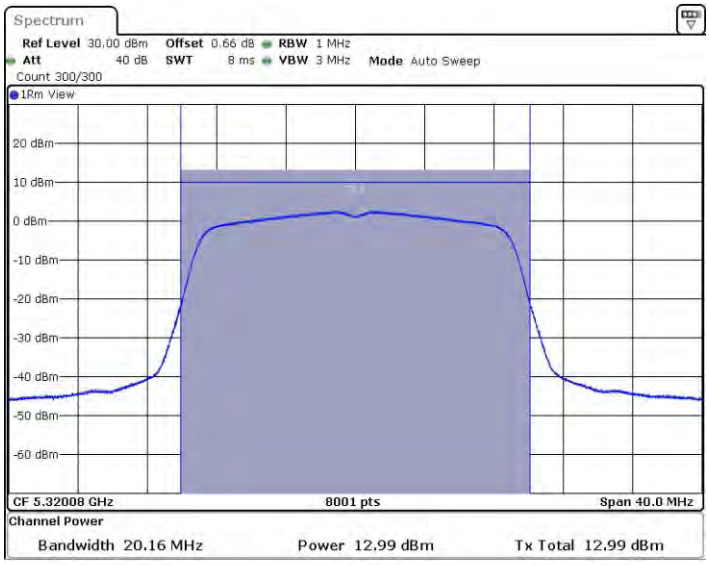


11AC20SISO_Ant2_5280



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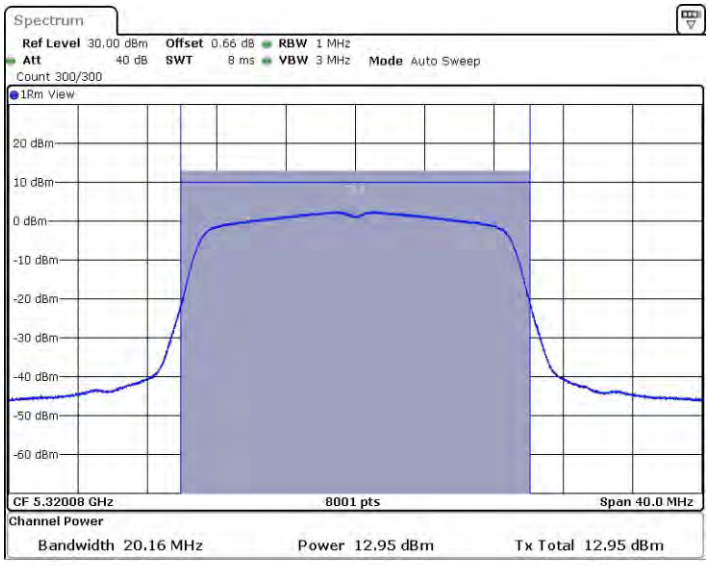

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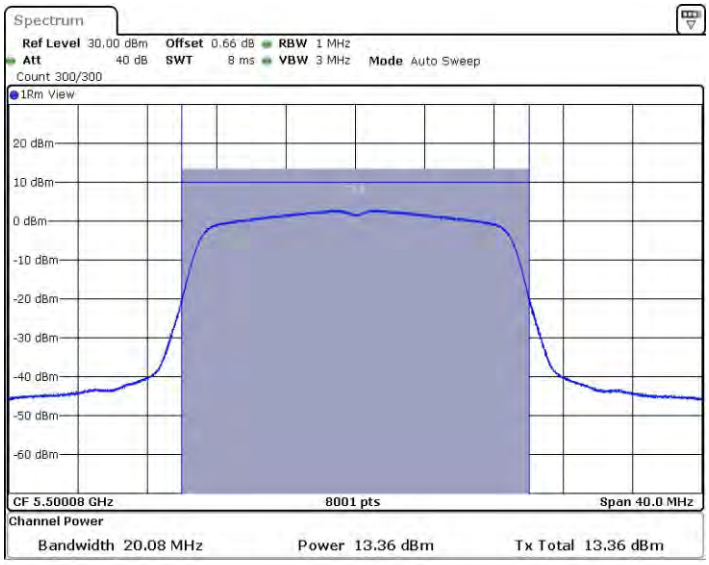

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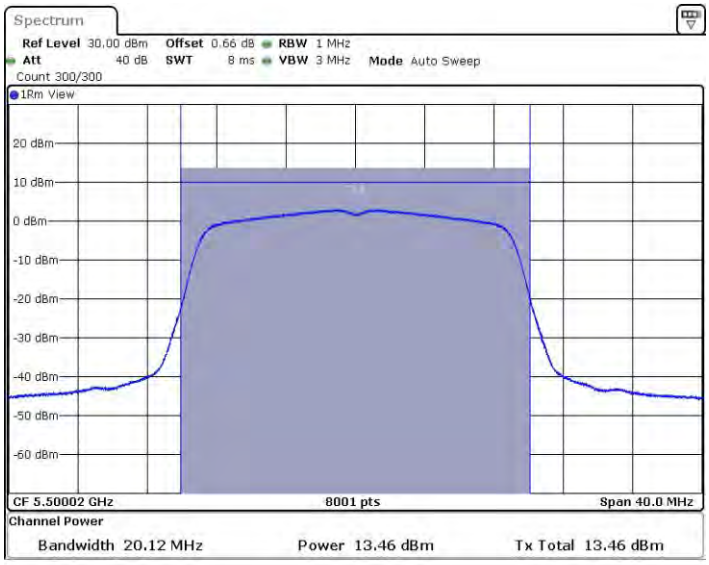

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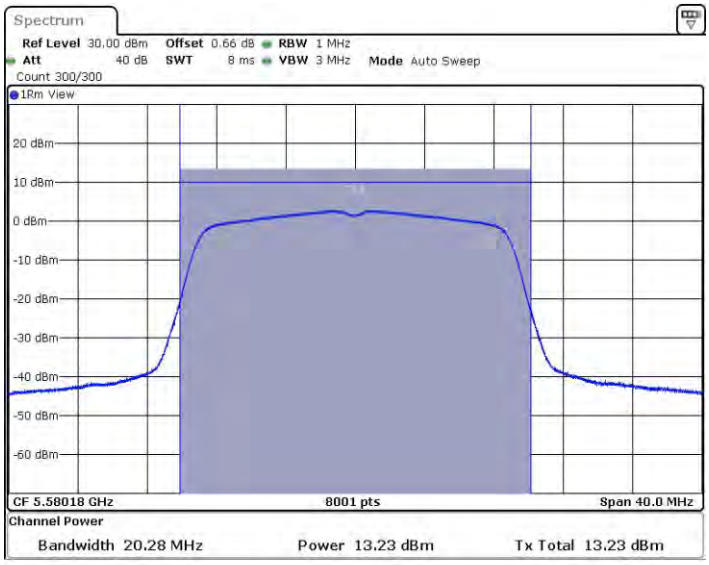

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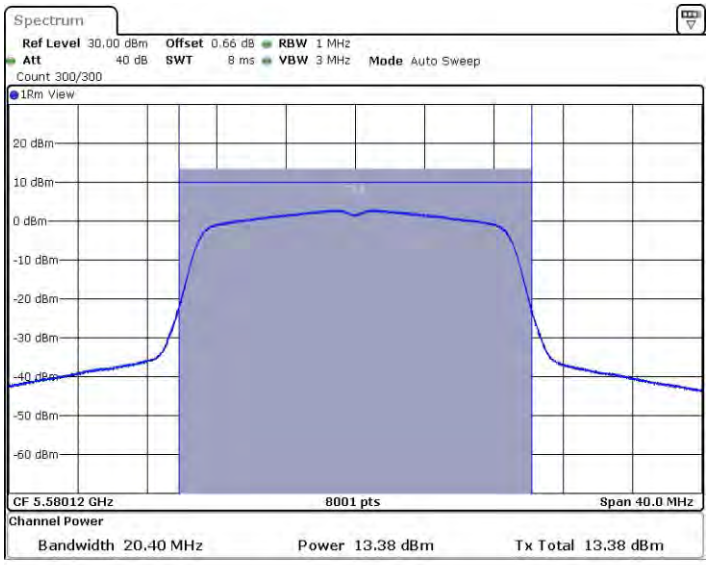

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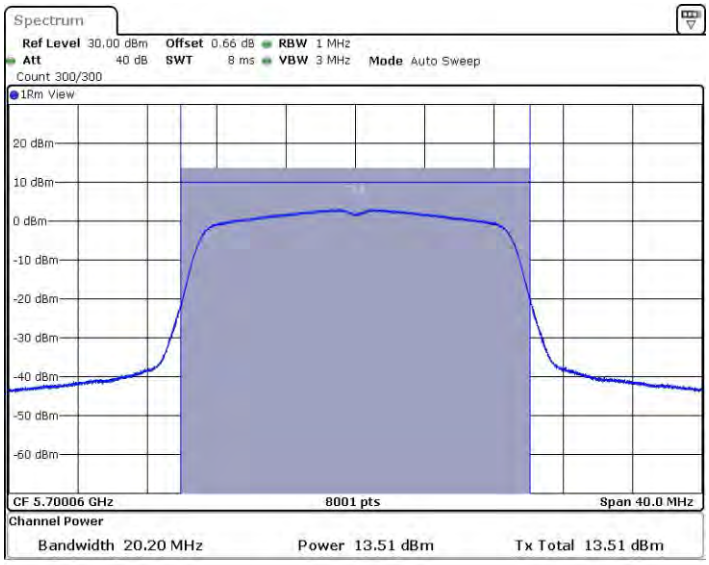
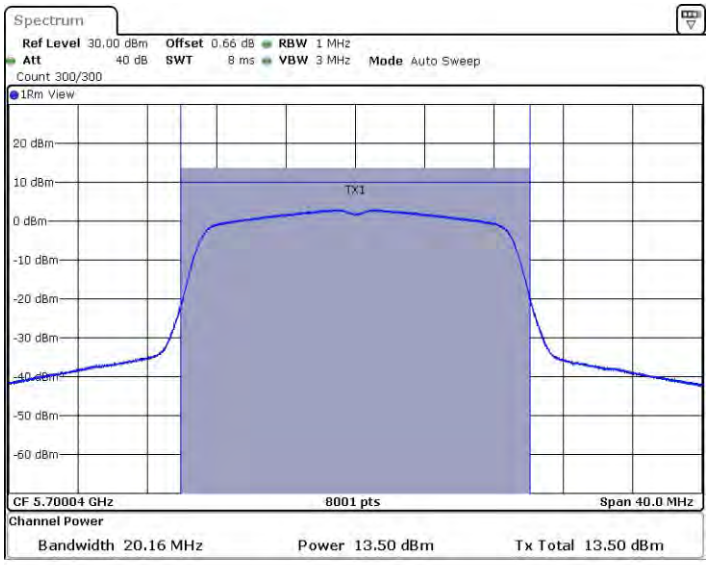


11AC20SISO_Ant1_5700



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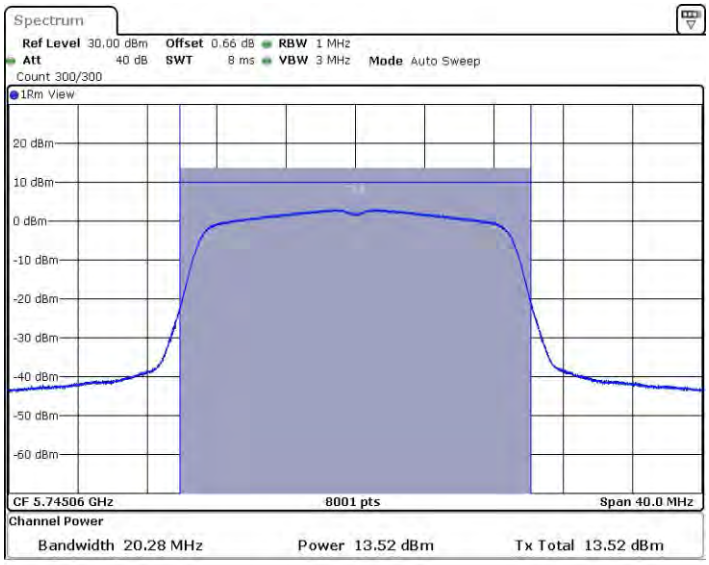

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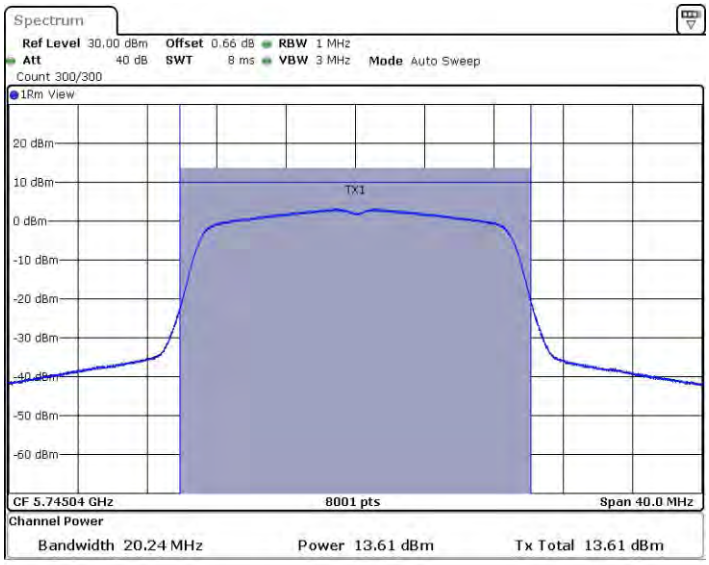

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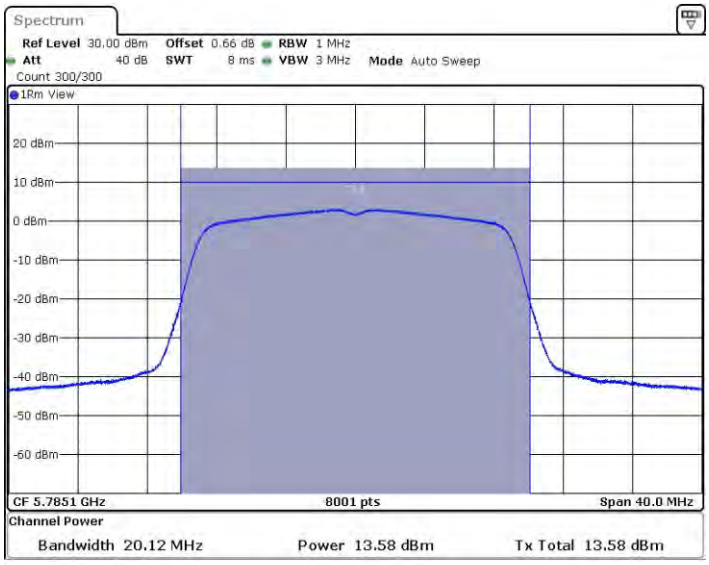
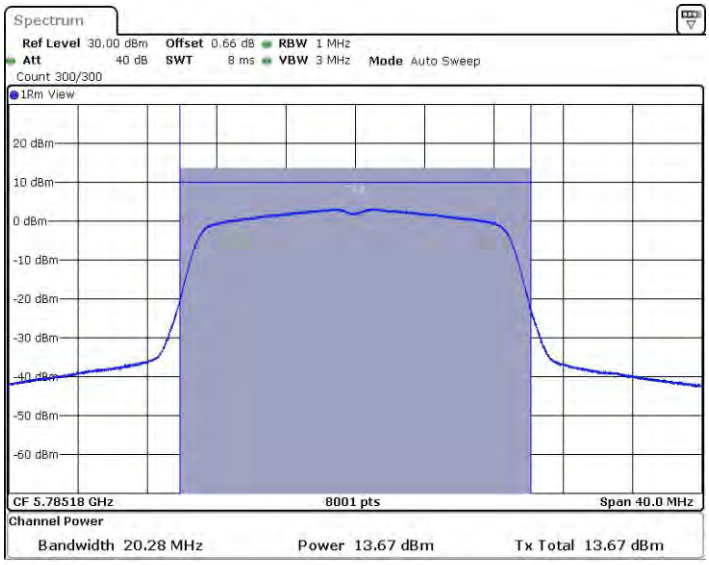


11AC20SISO_Ant1_5785



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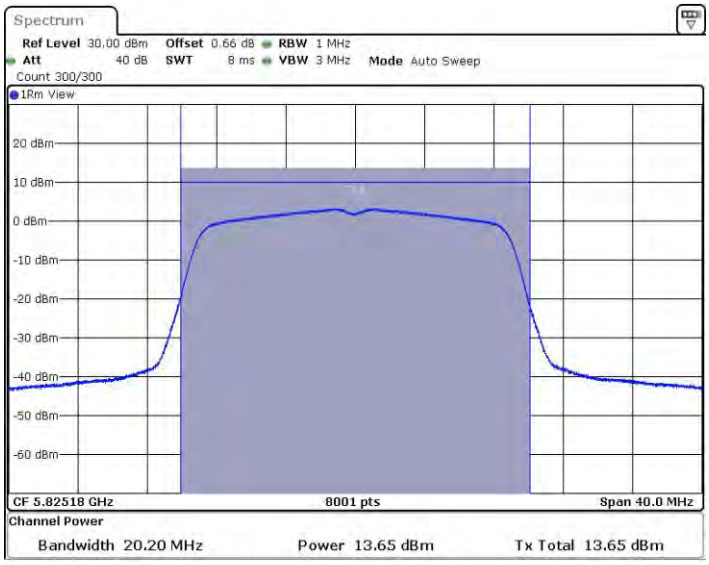

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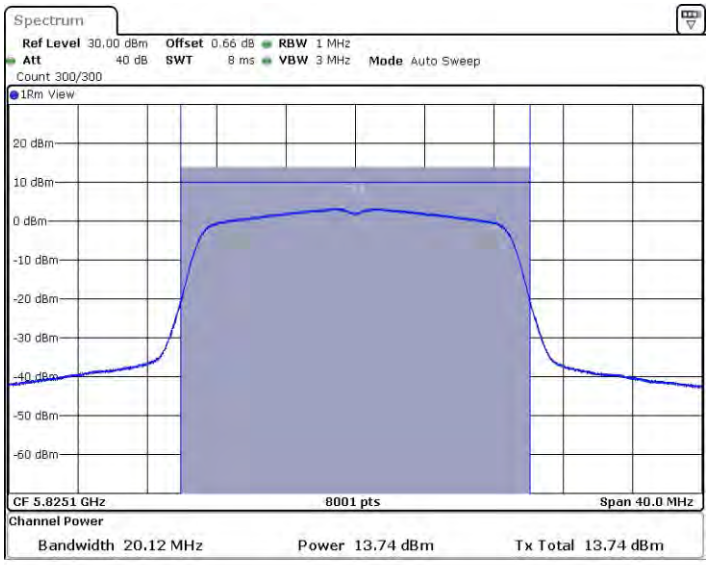

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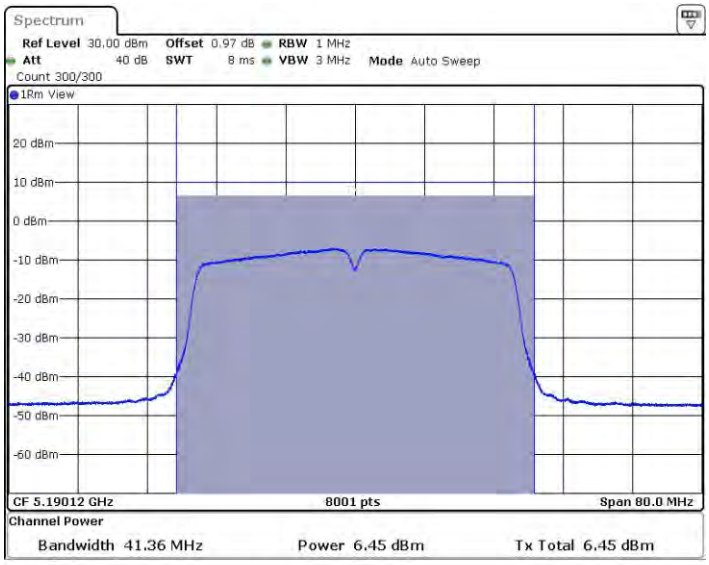
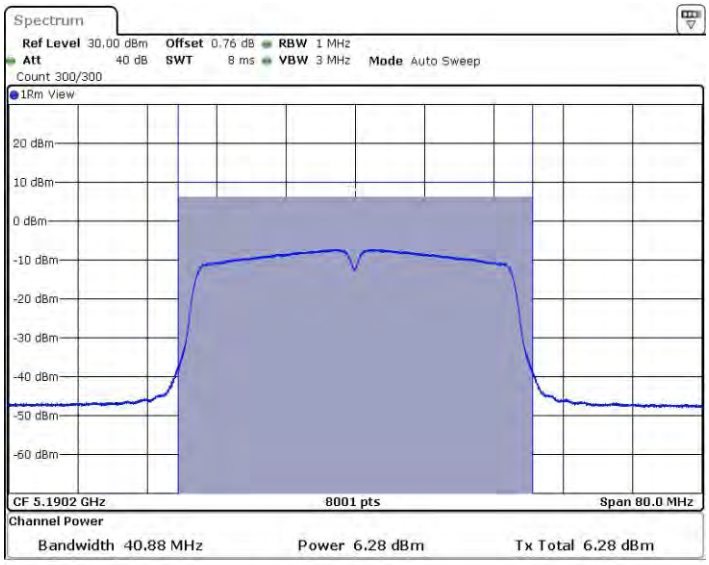


11AC40SISO_Ant1_5190



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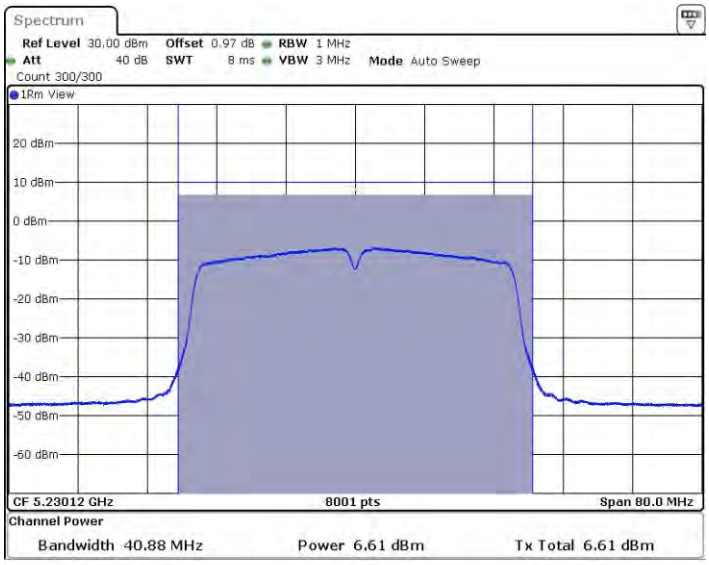

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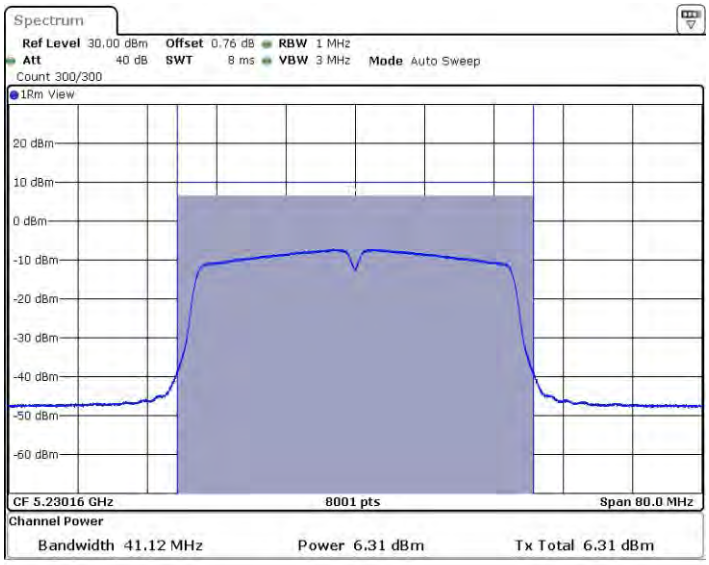

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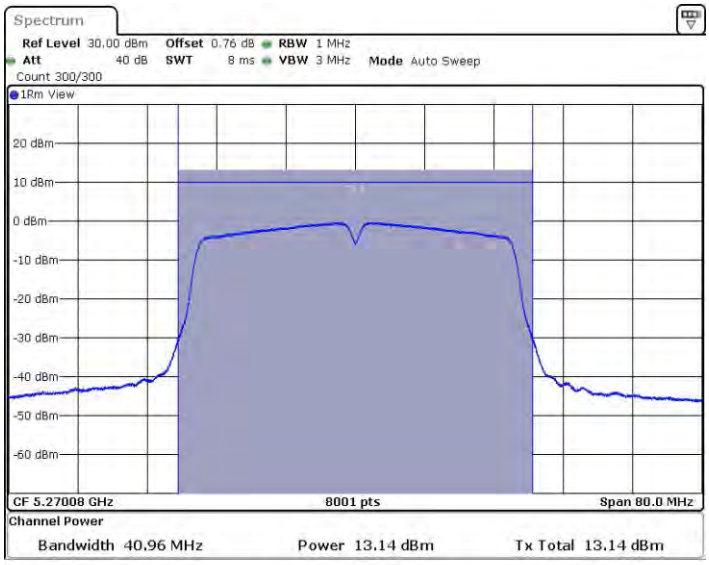

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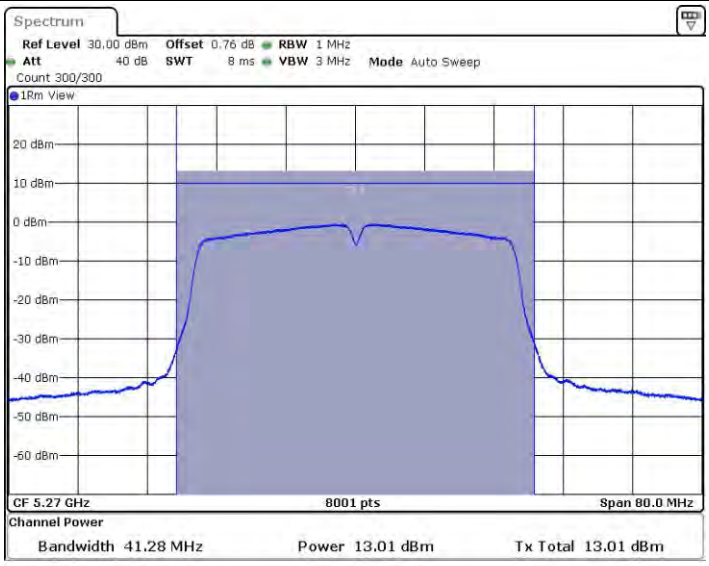

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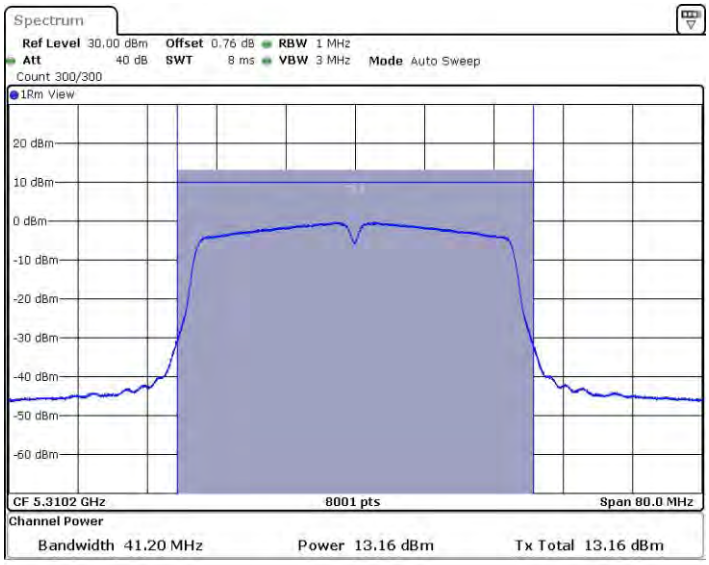

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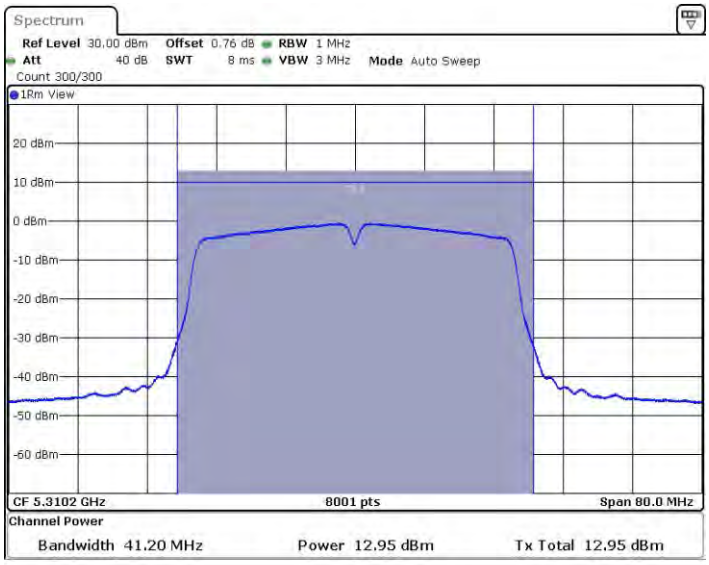

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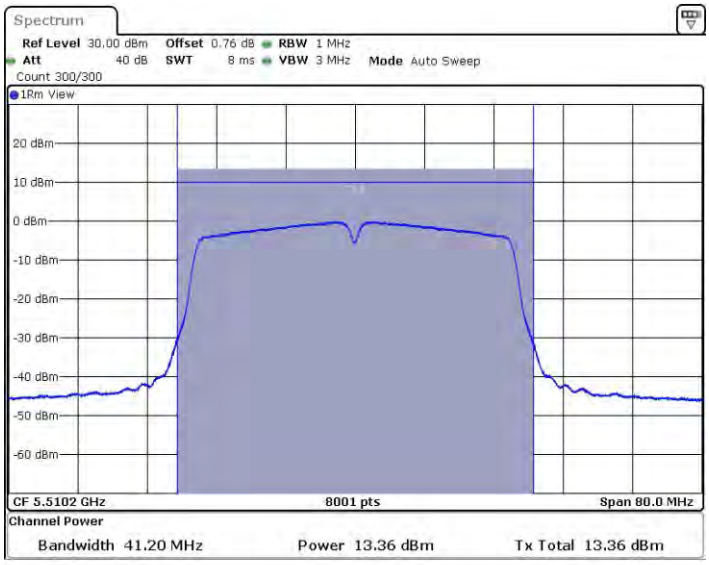

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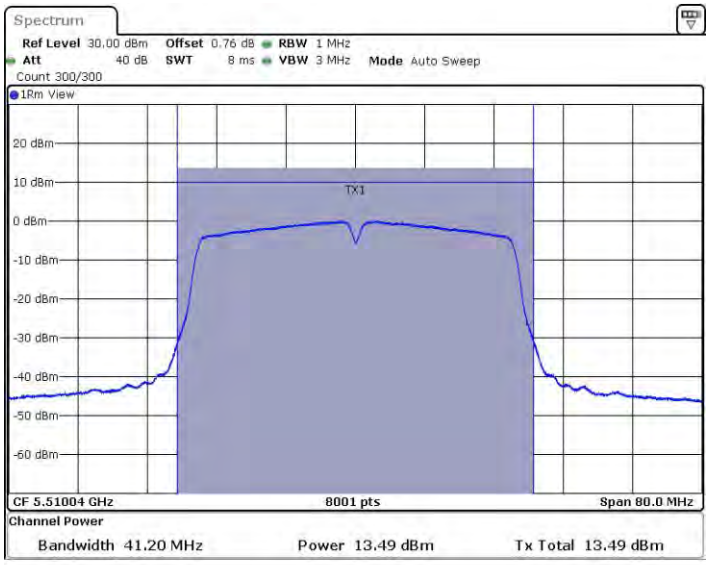

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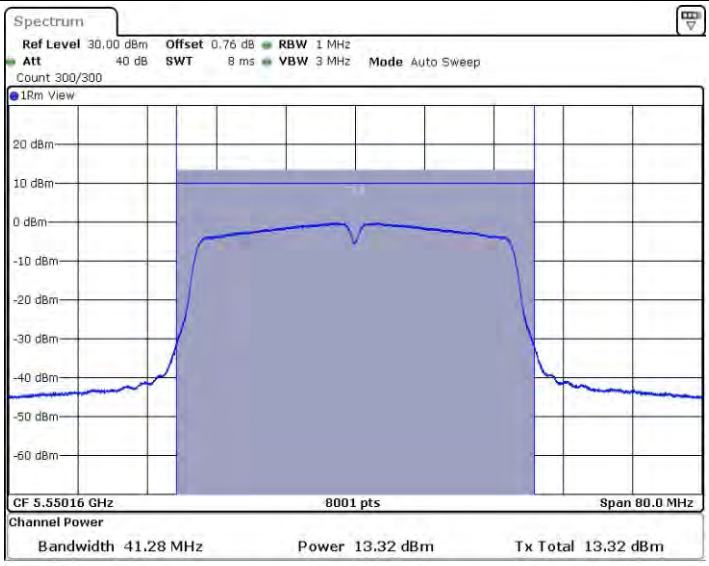

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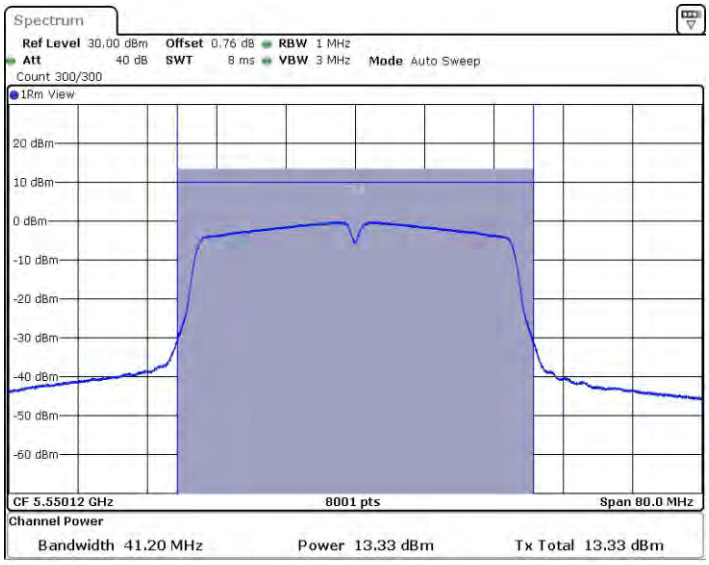

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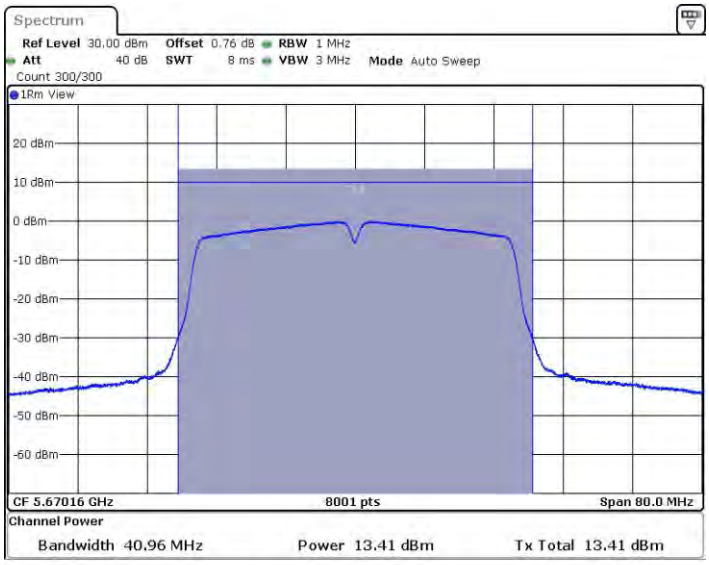

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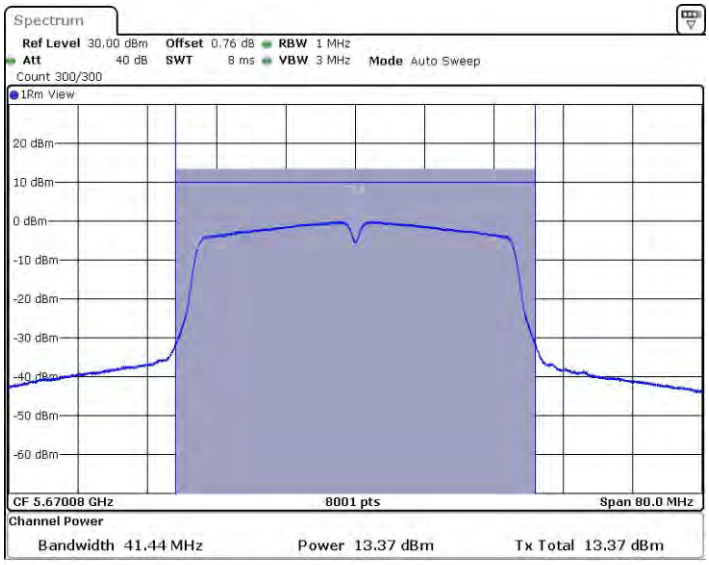

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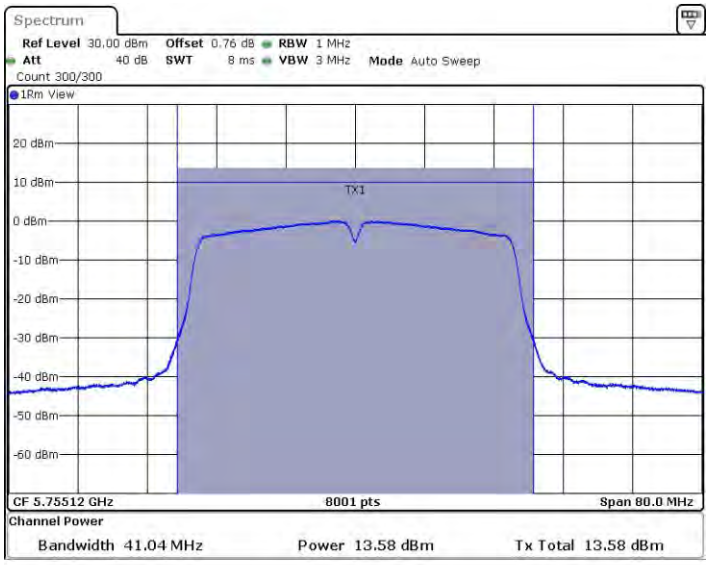

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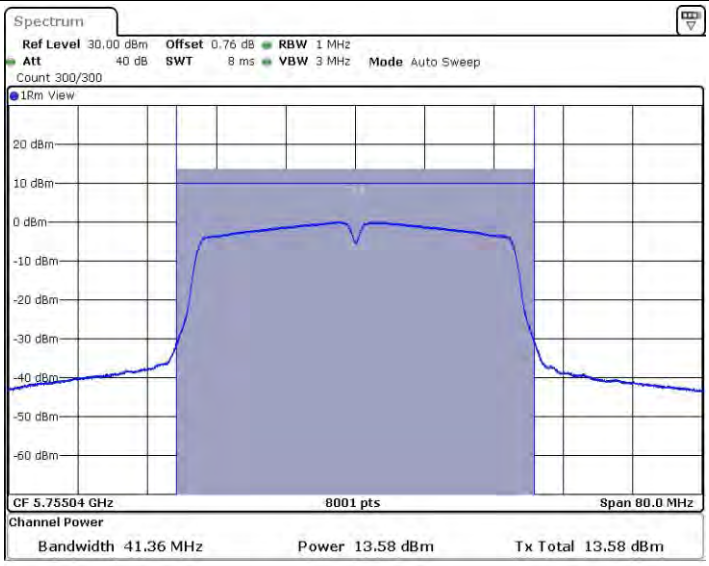

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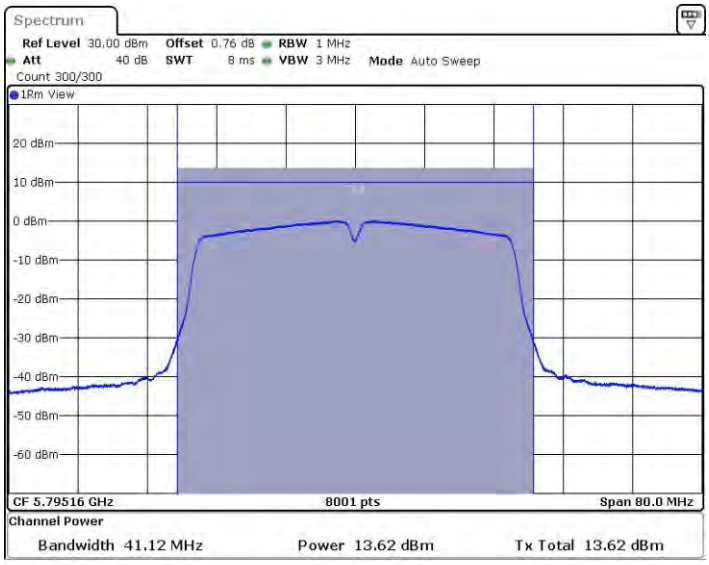

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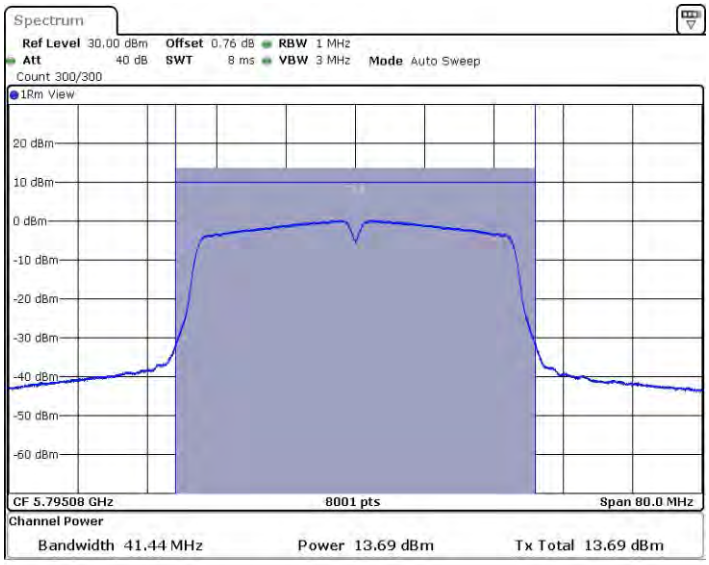

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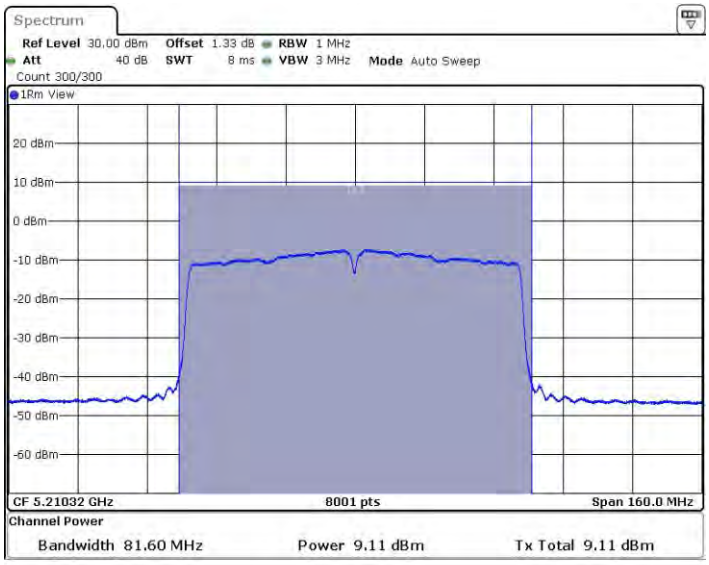
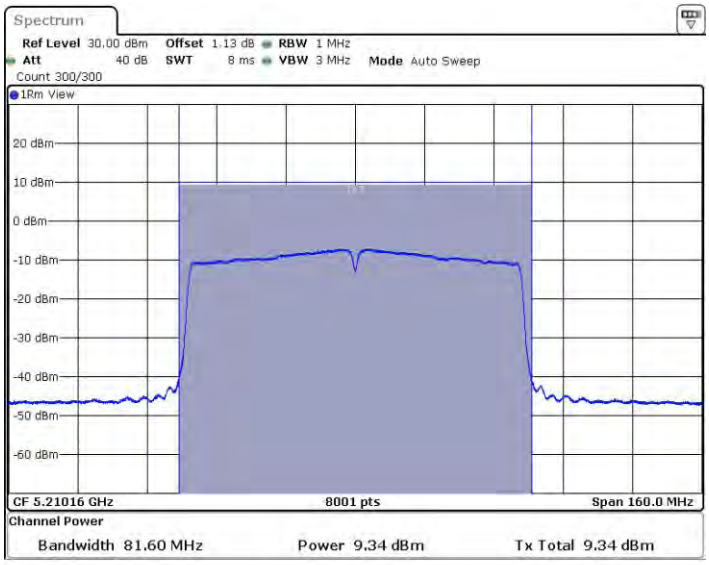


11AC80SISO_Ant1_5210



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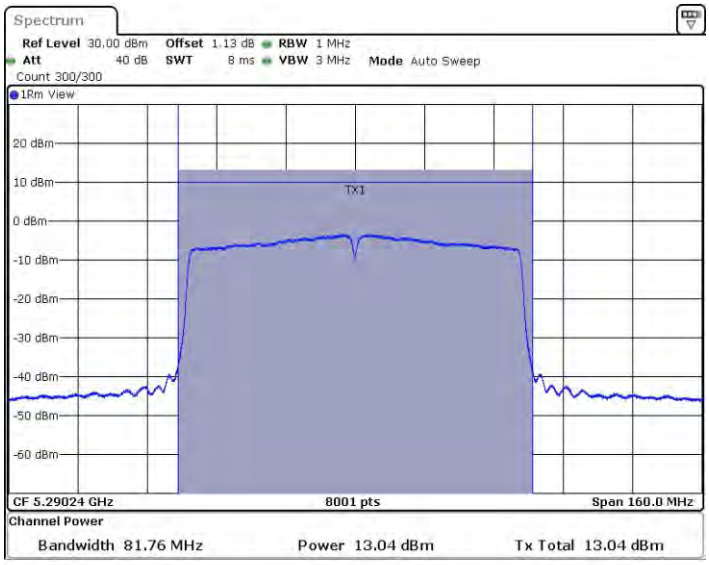

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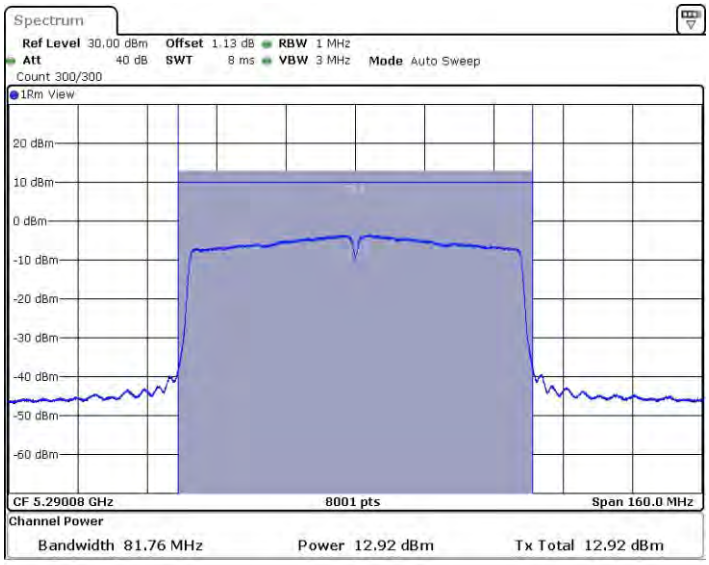

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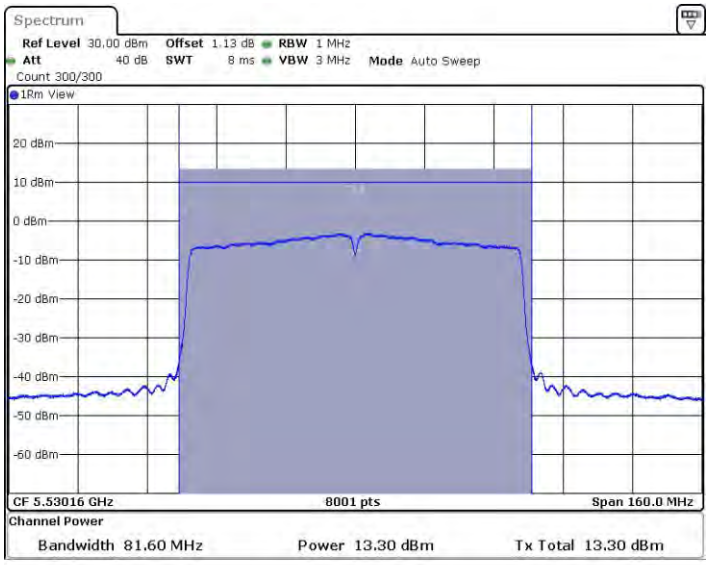

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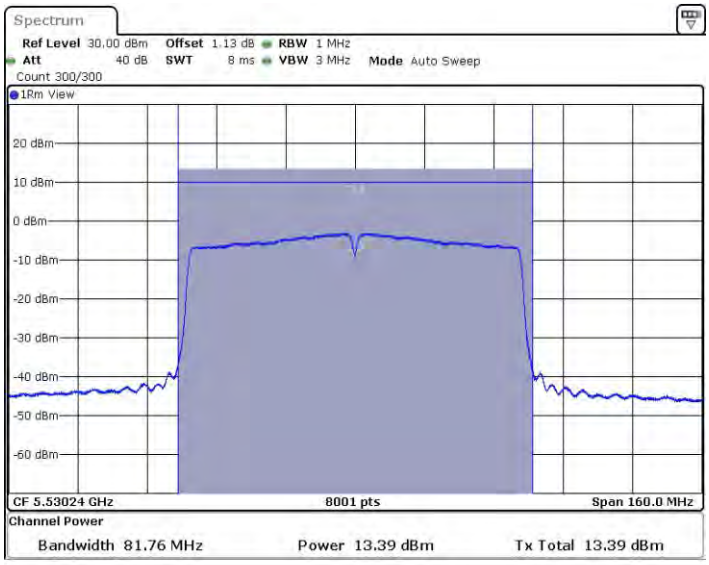

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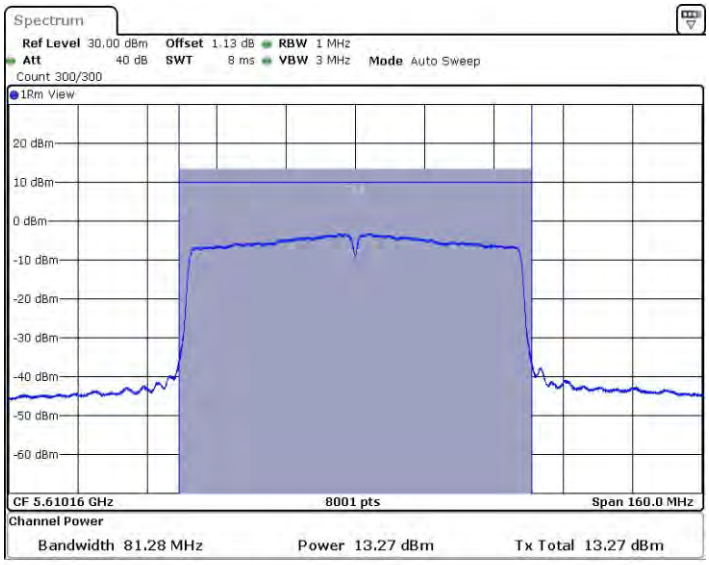

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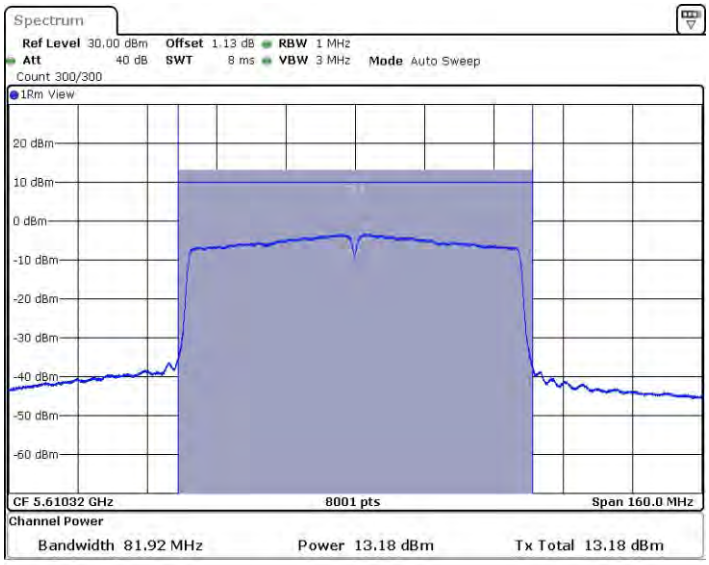

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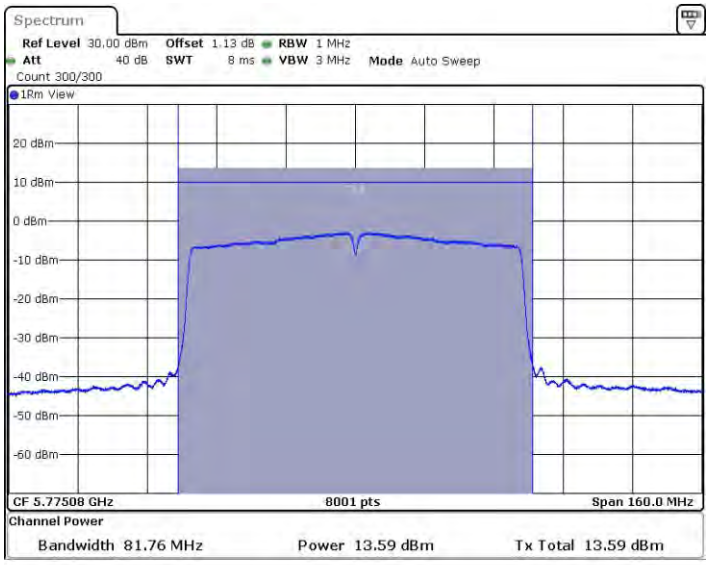

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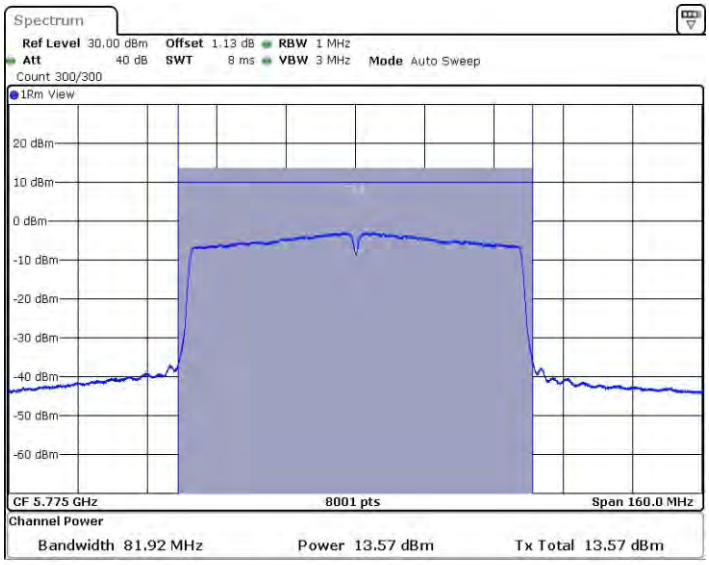

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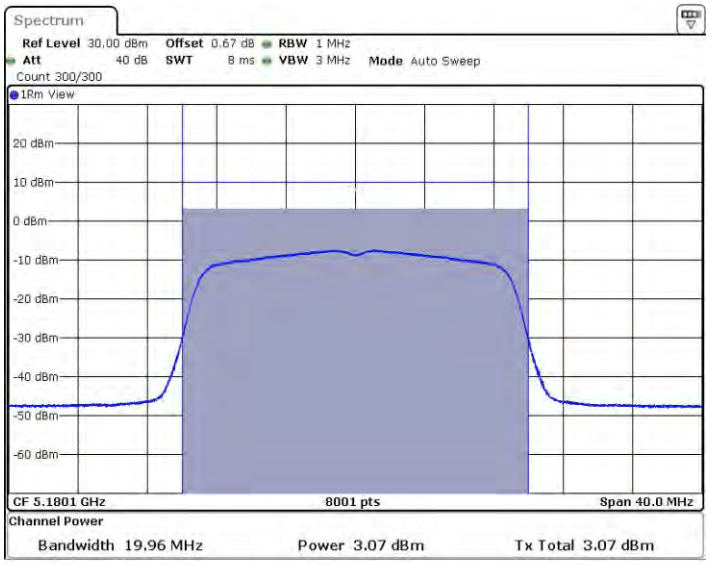

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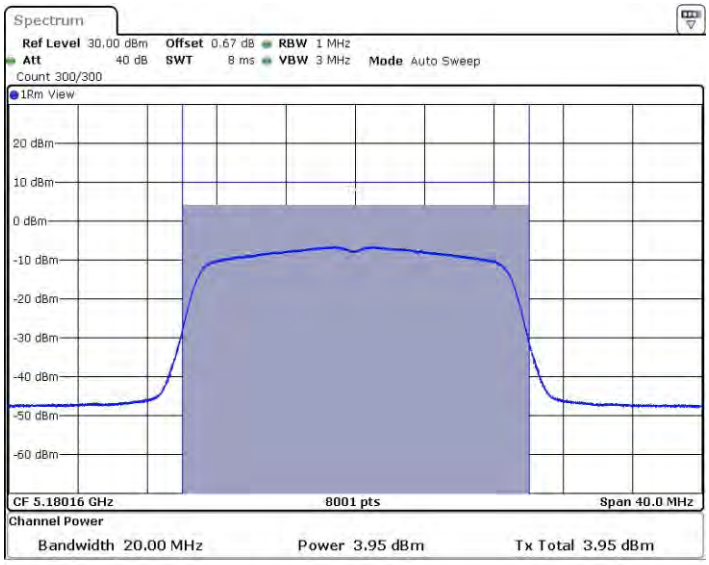

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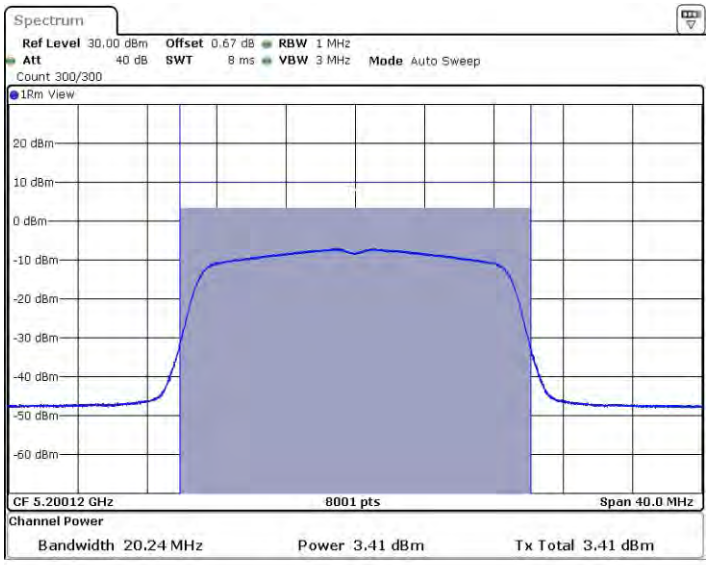

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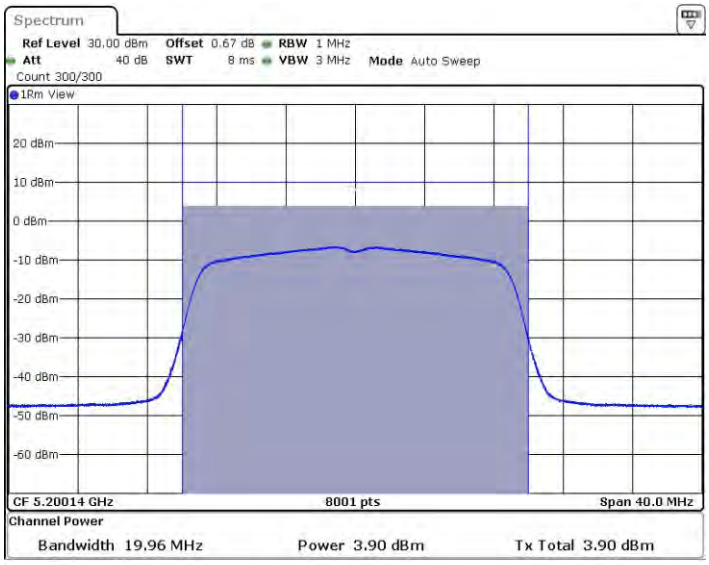

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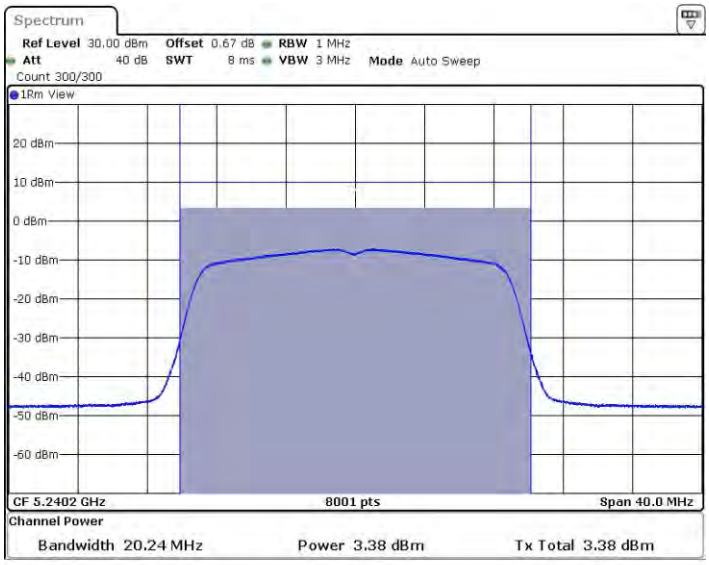

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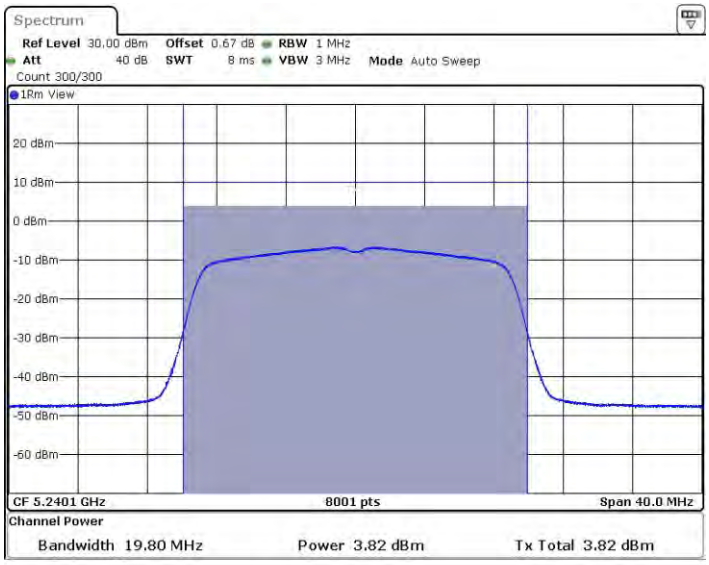

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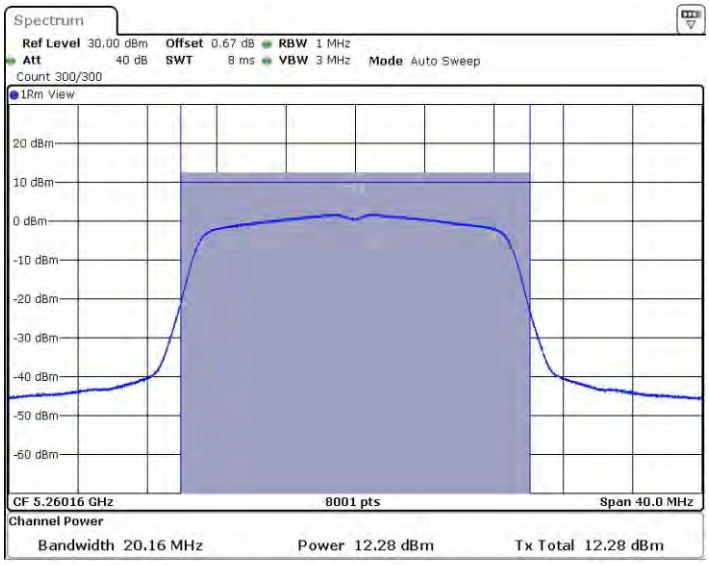

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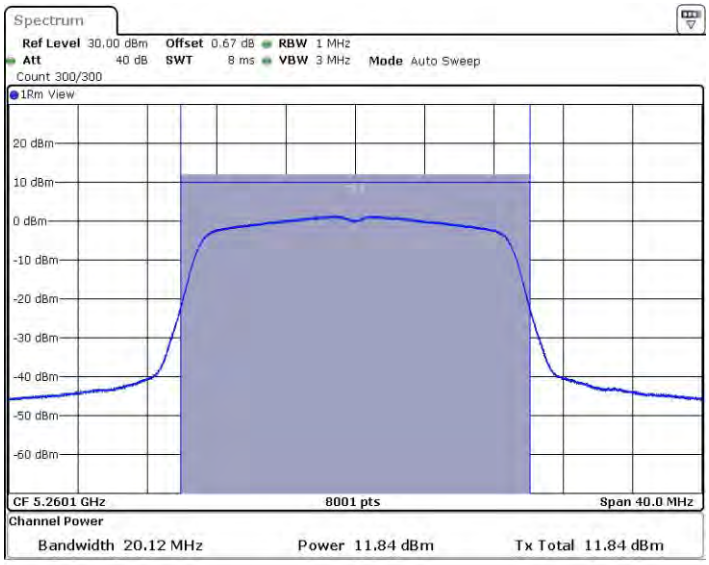

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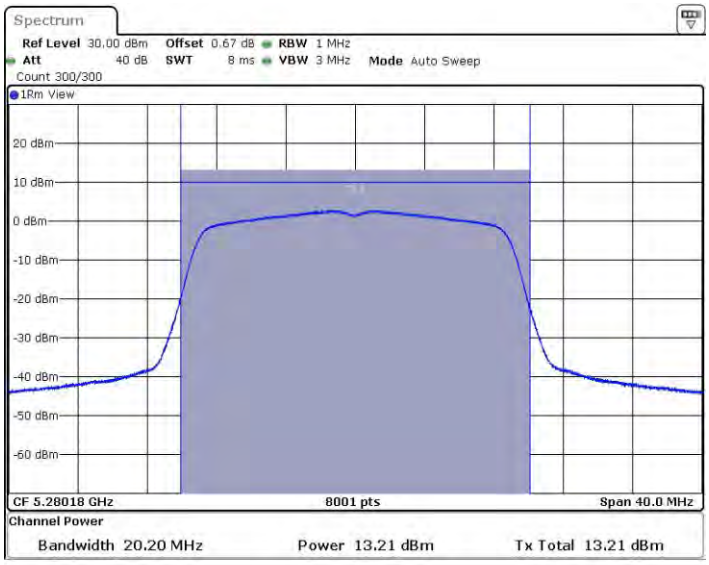

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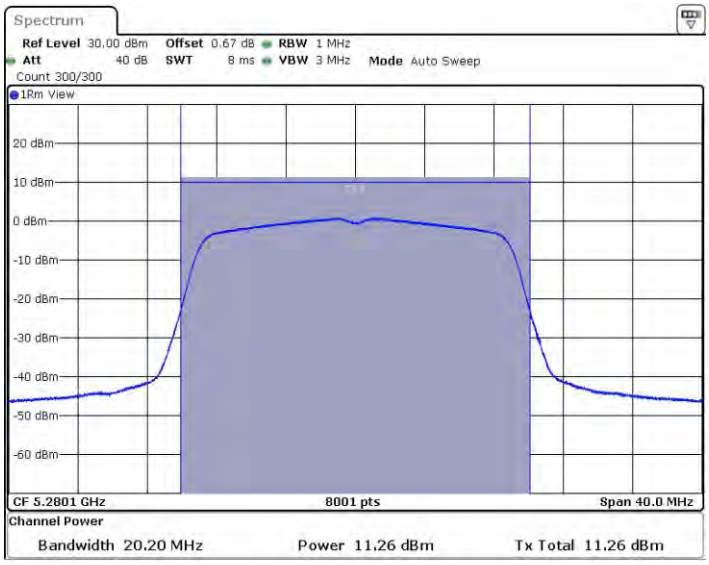

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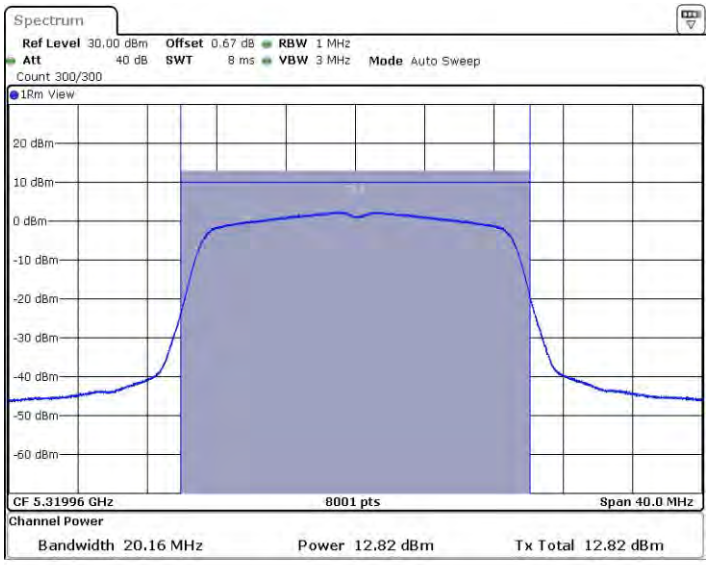

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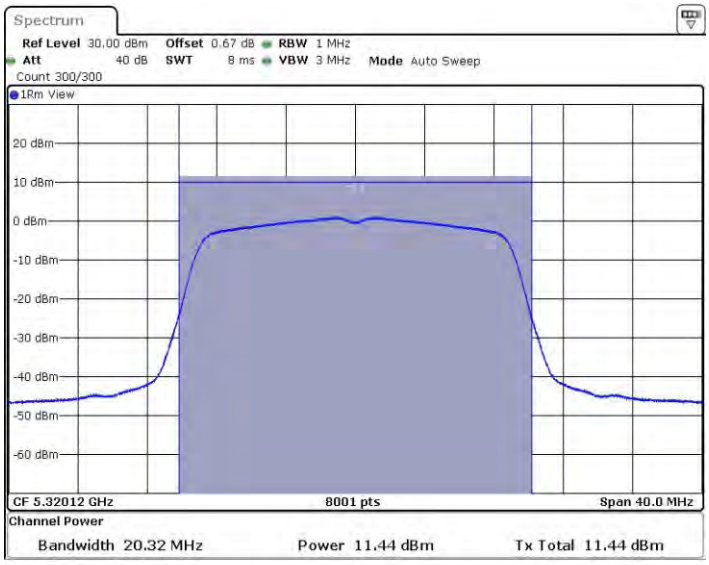

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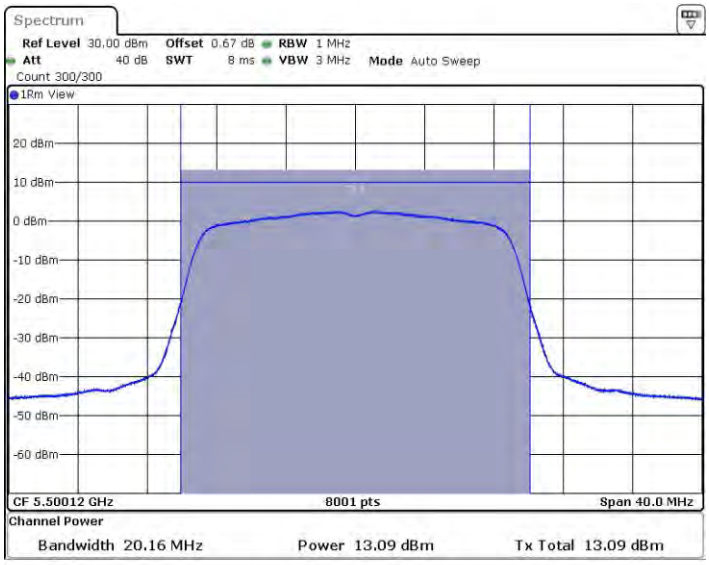

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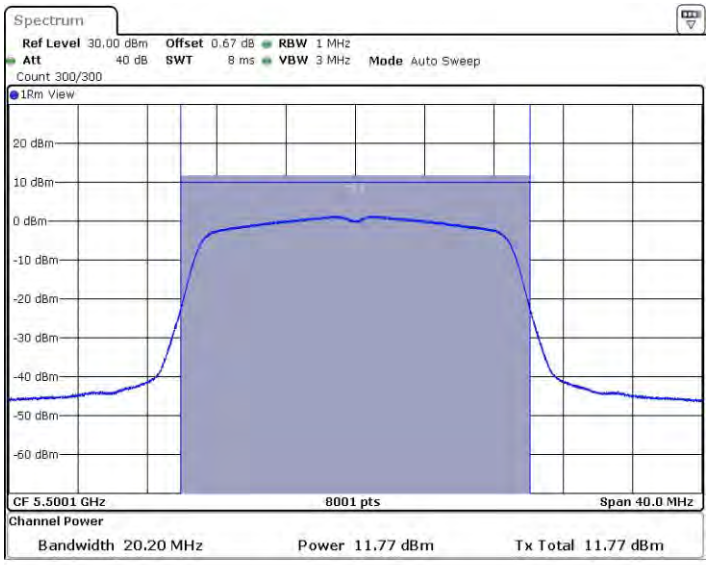
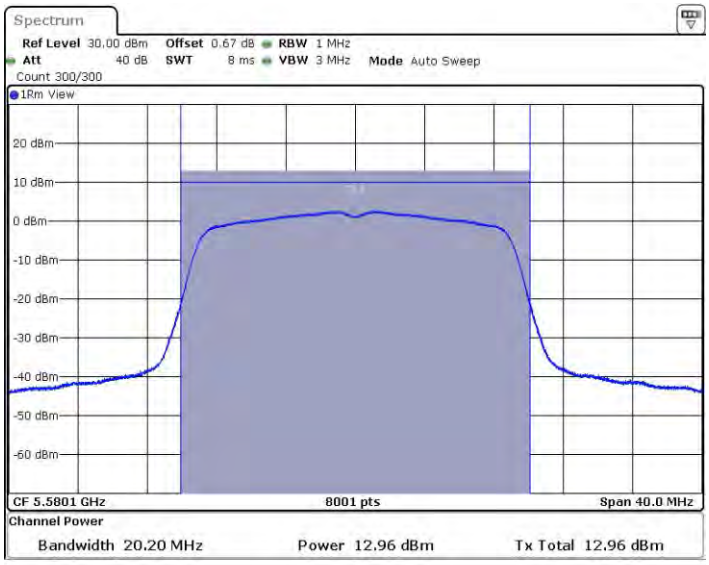
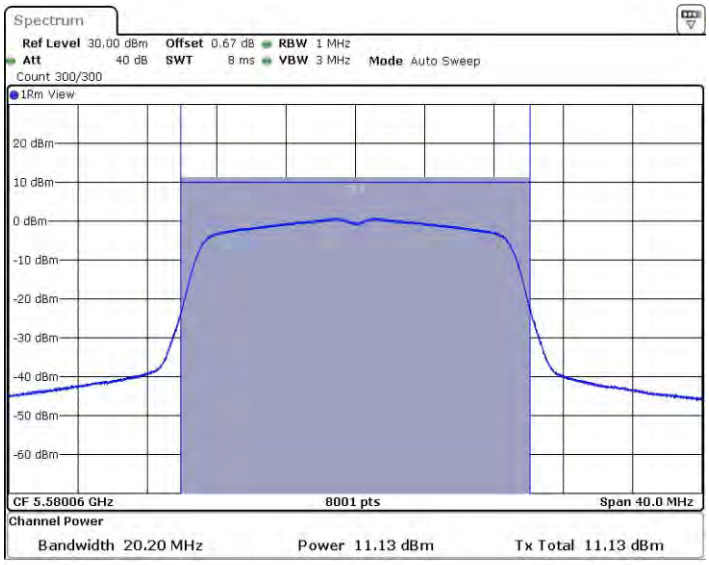


11N20MIMO_Ant2_5500



11N20MIMO_Ant1_5580



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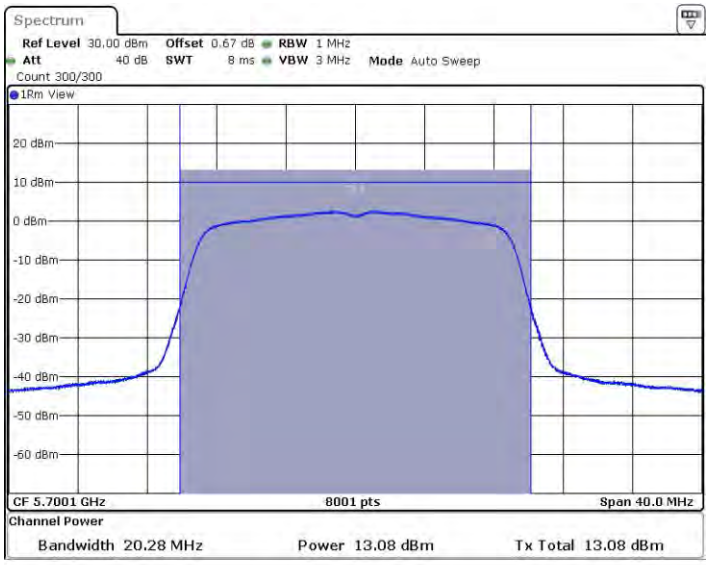

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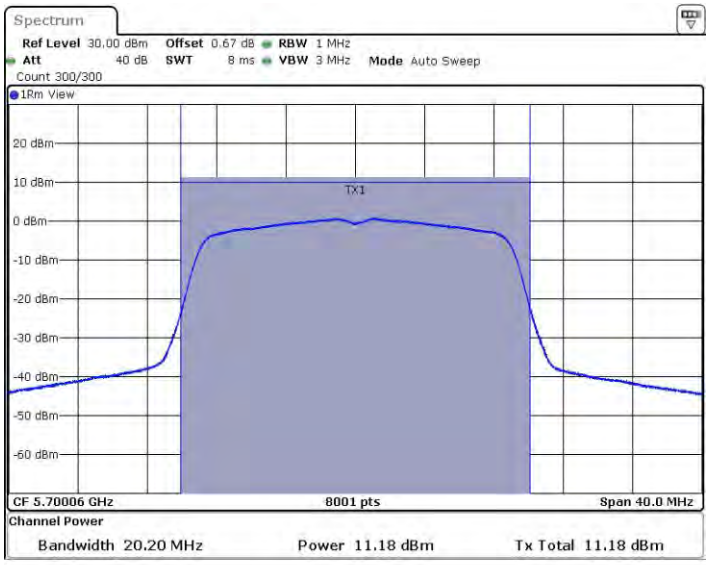

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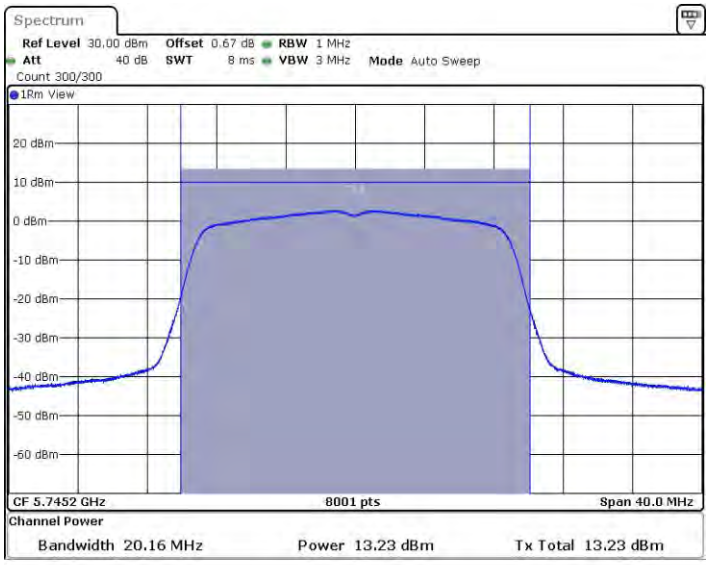

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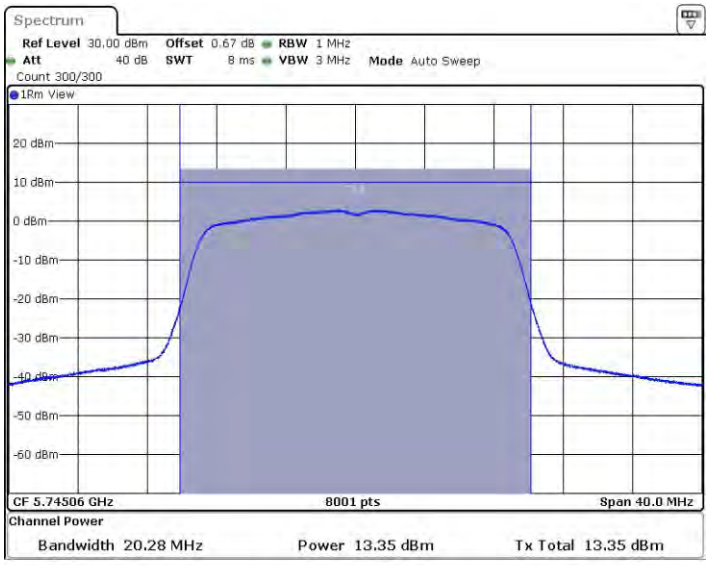

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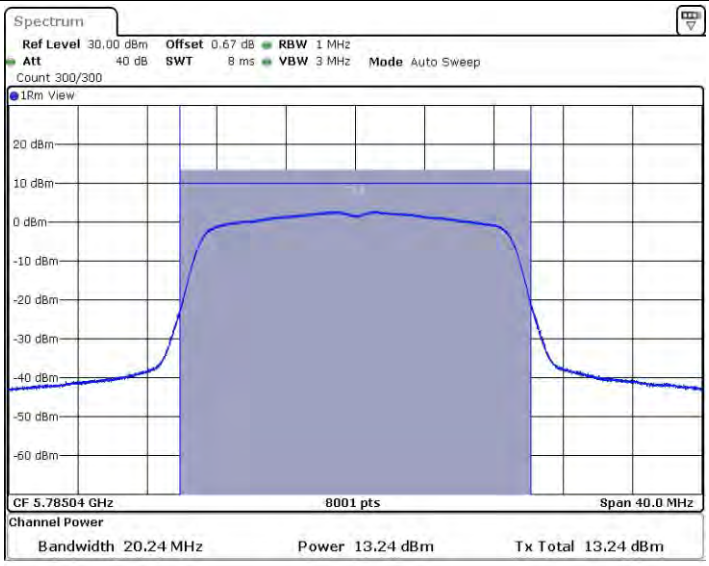
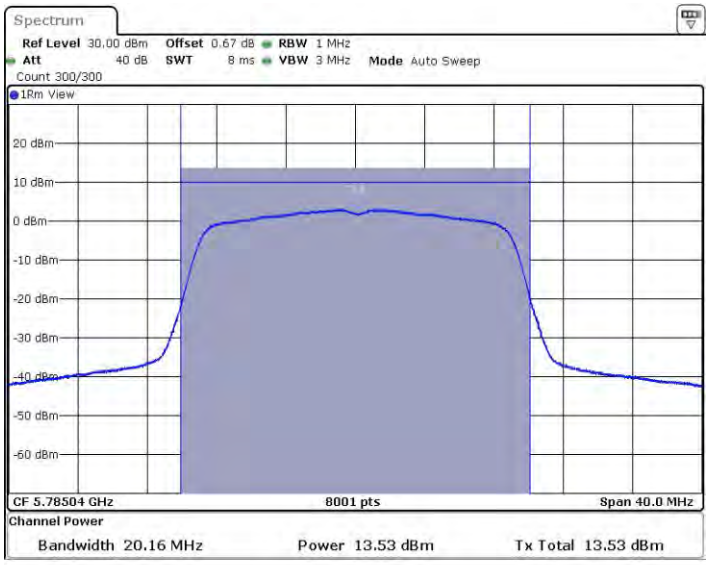


11N20MIMO_Ant1_5785



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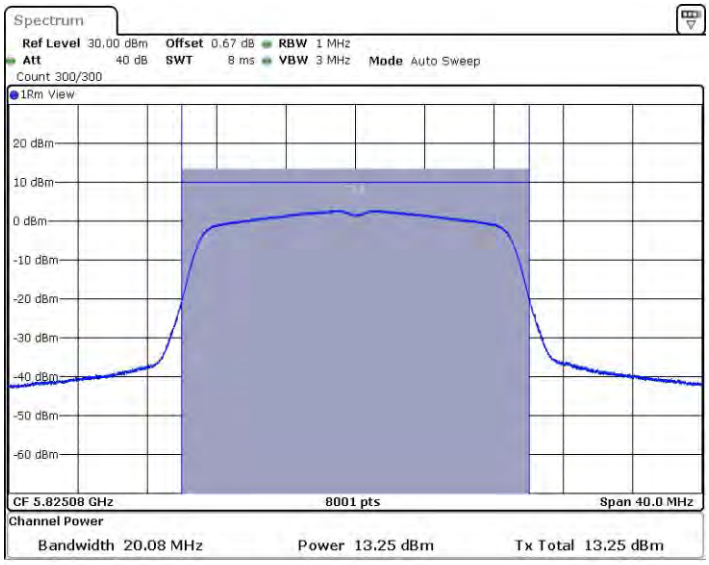

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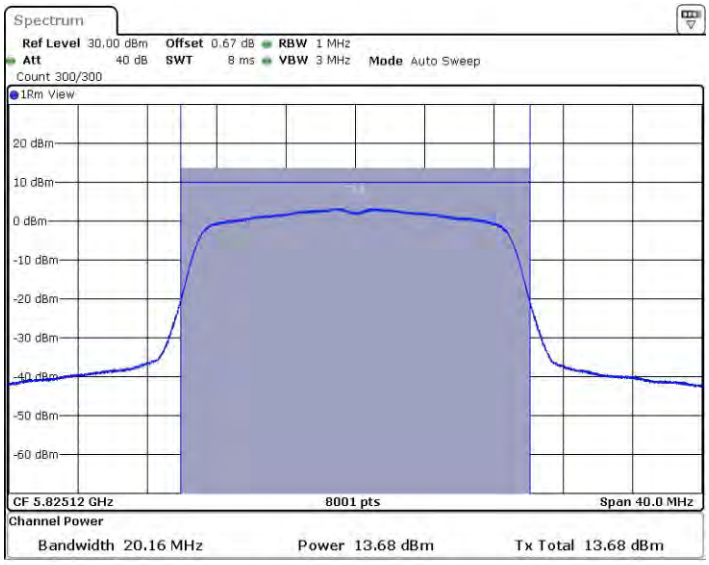

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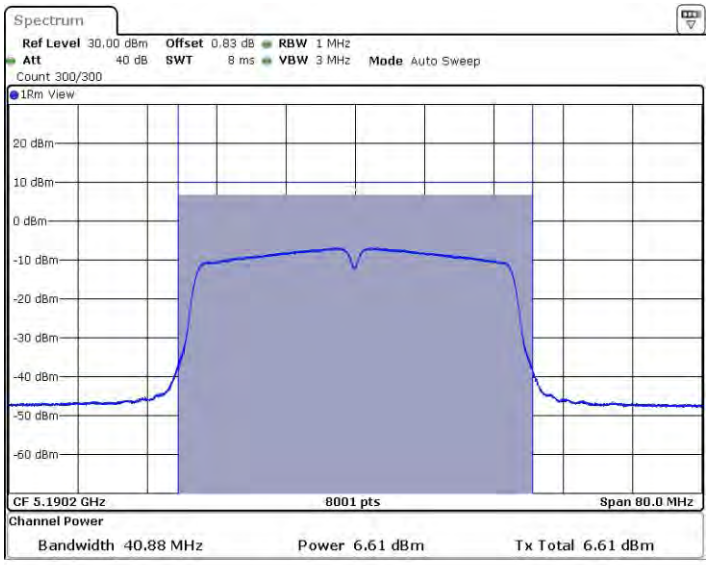
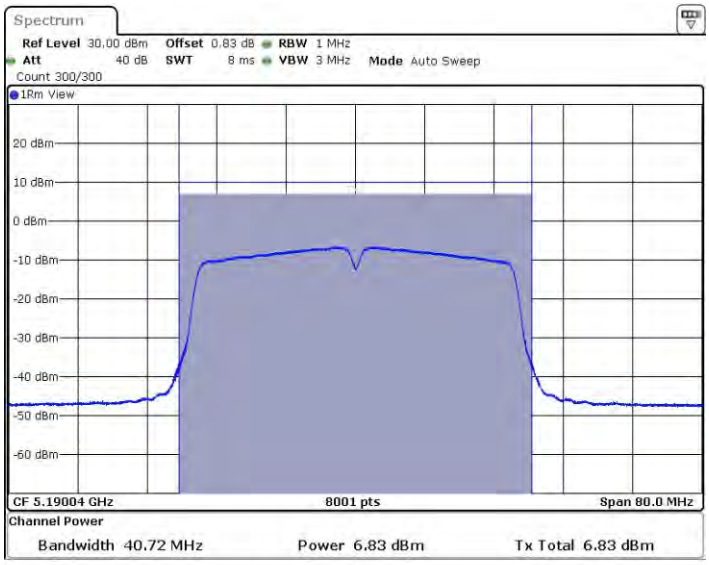


11N40MIMO_Ant1_5190



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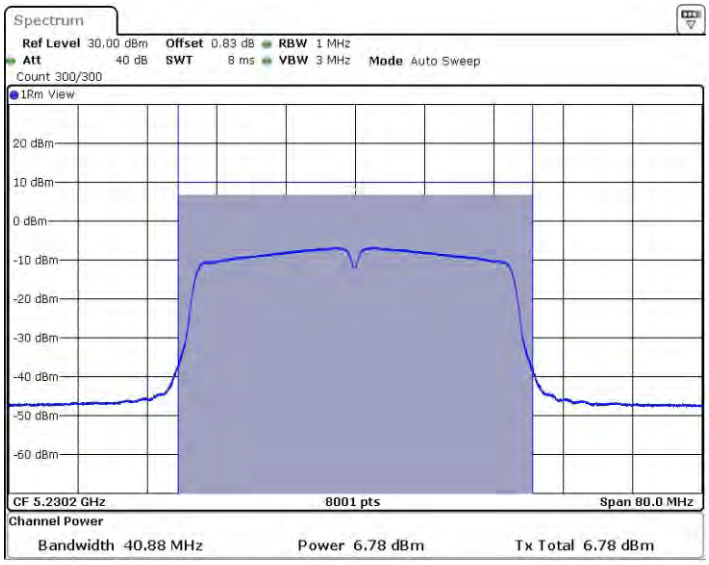
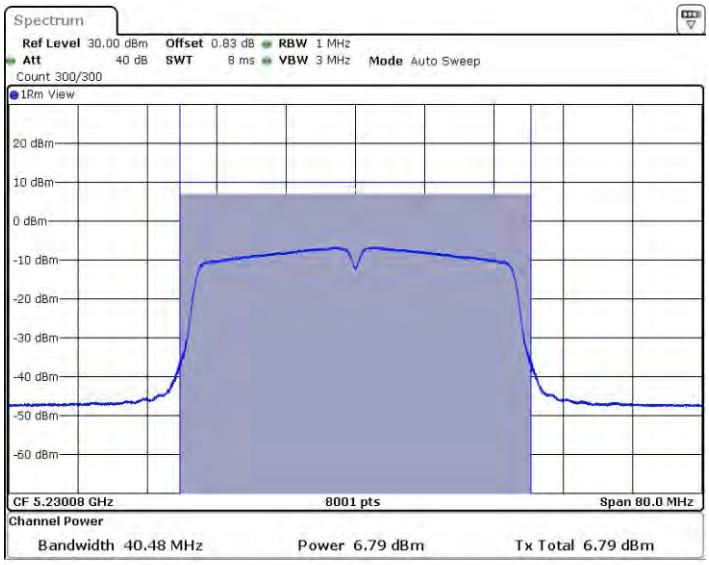


11N40MIMO_Ant1_5230



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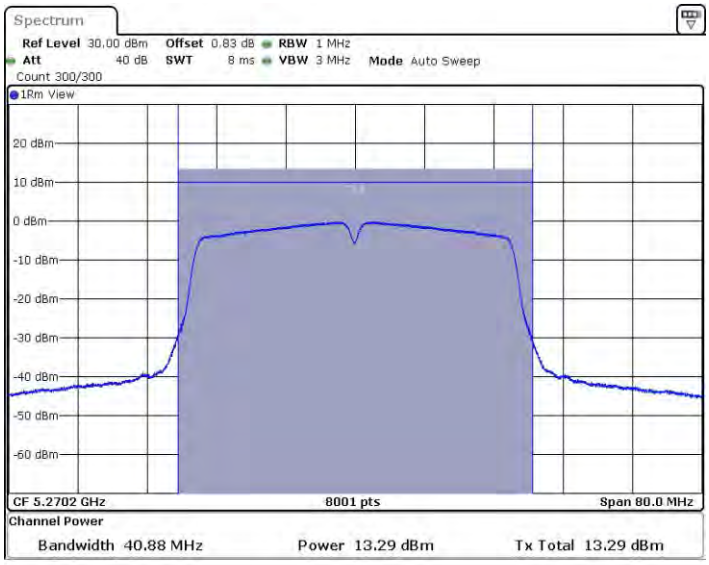

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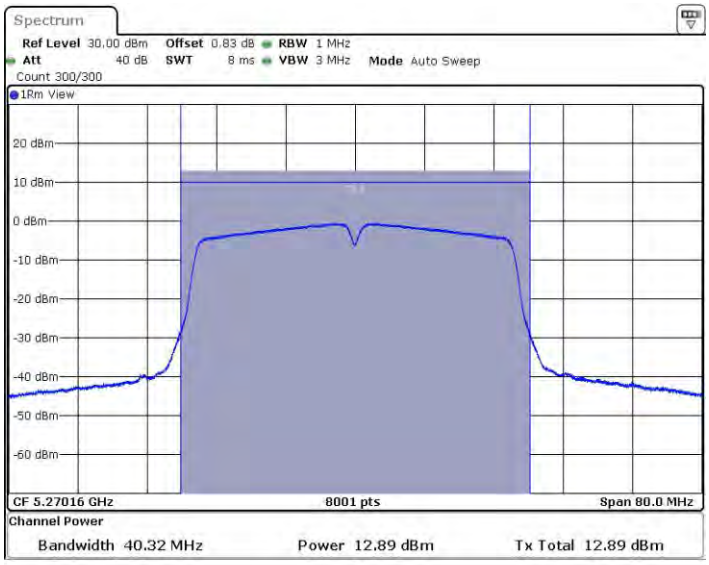

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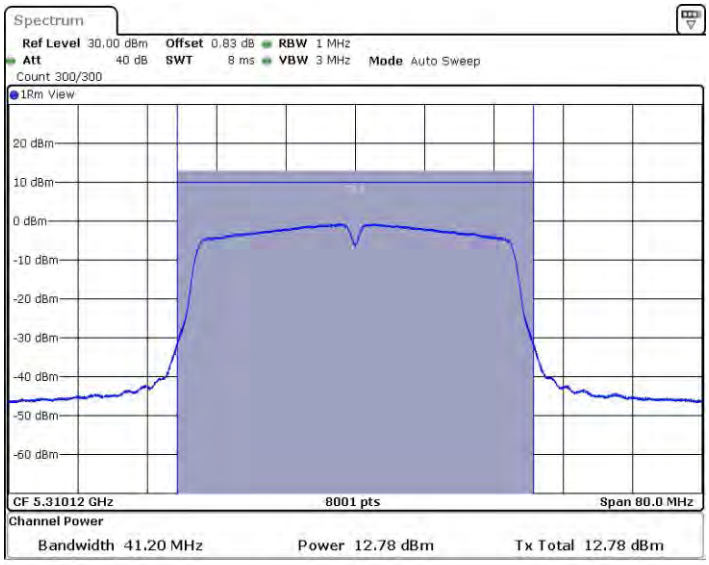

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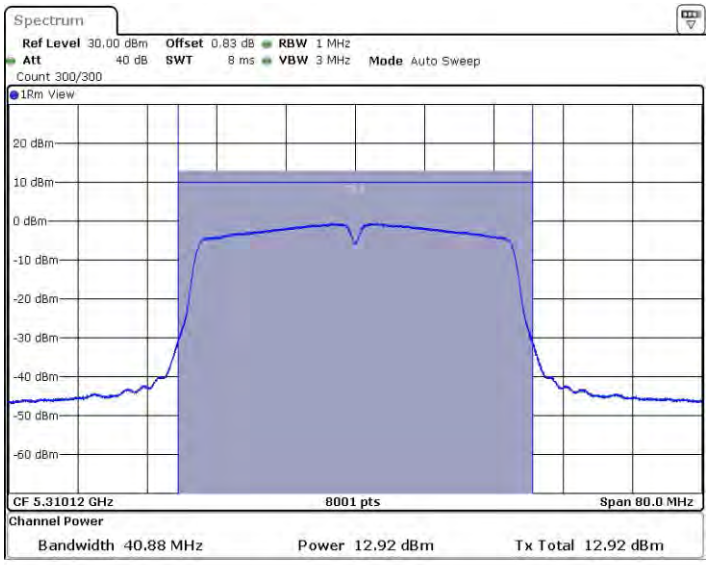

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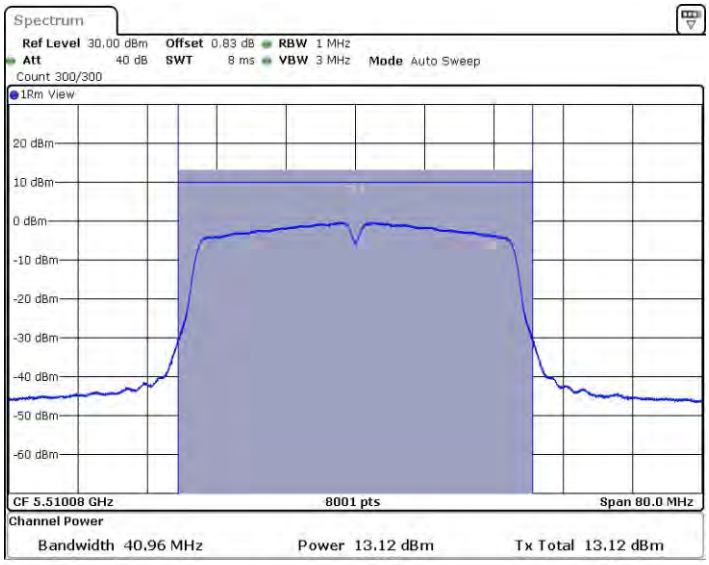

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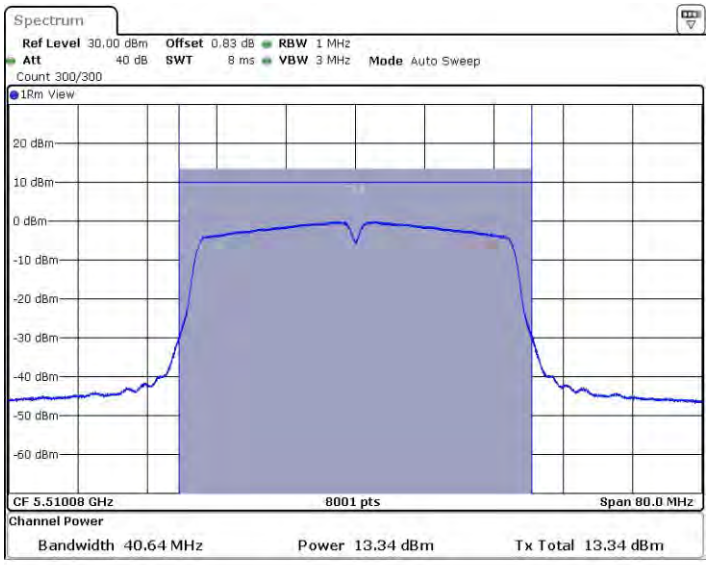

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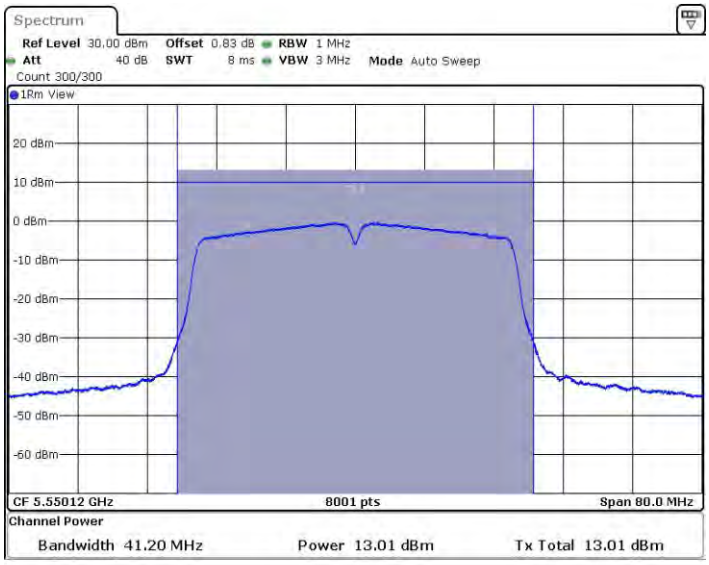

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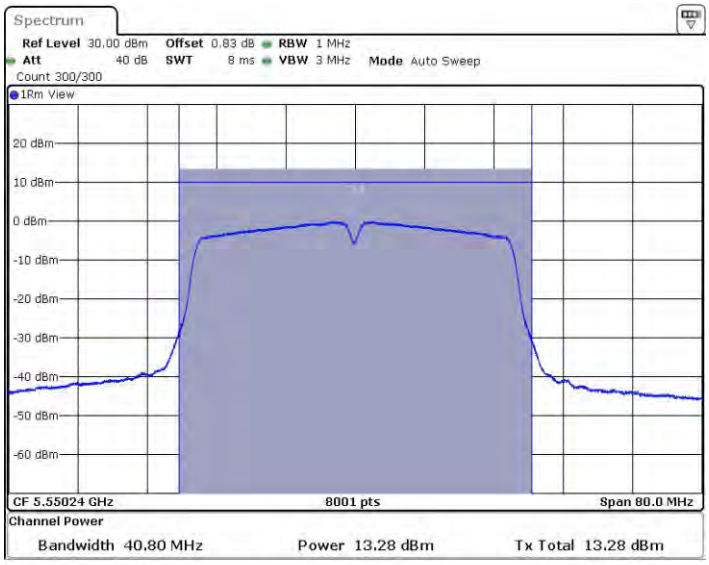

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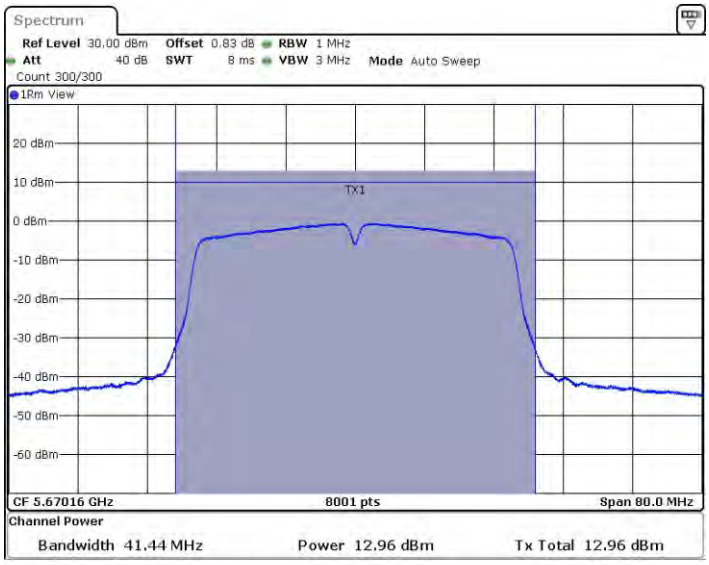

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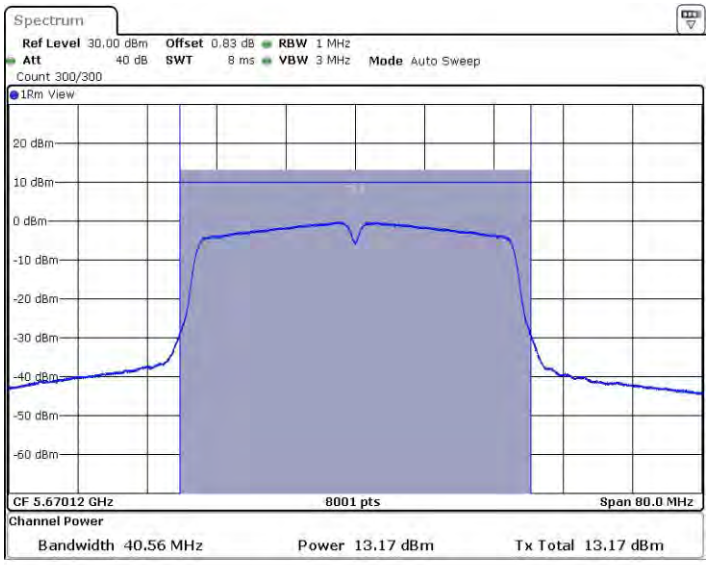

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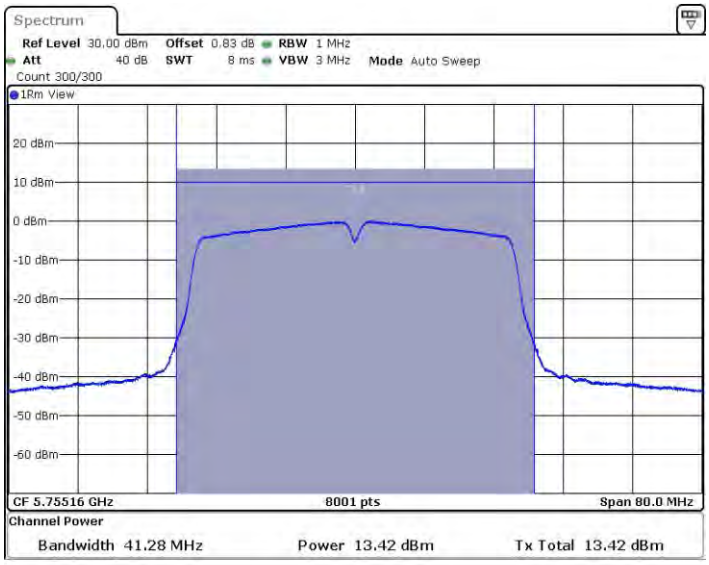

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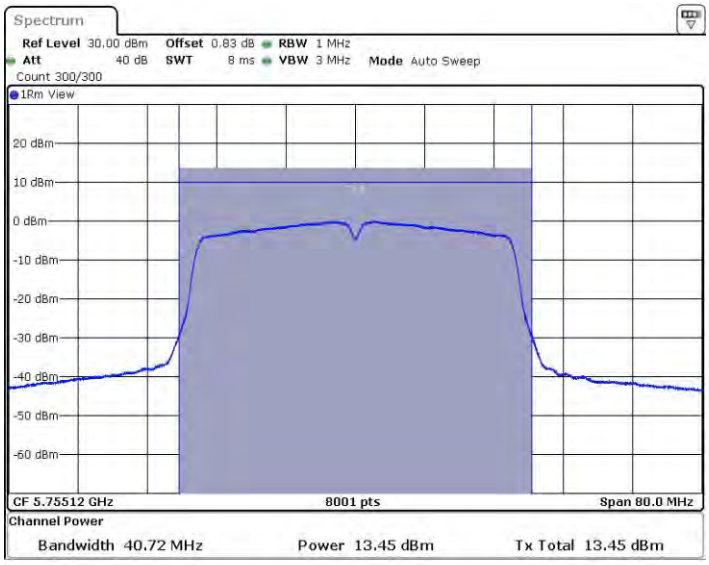

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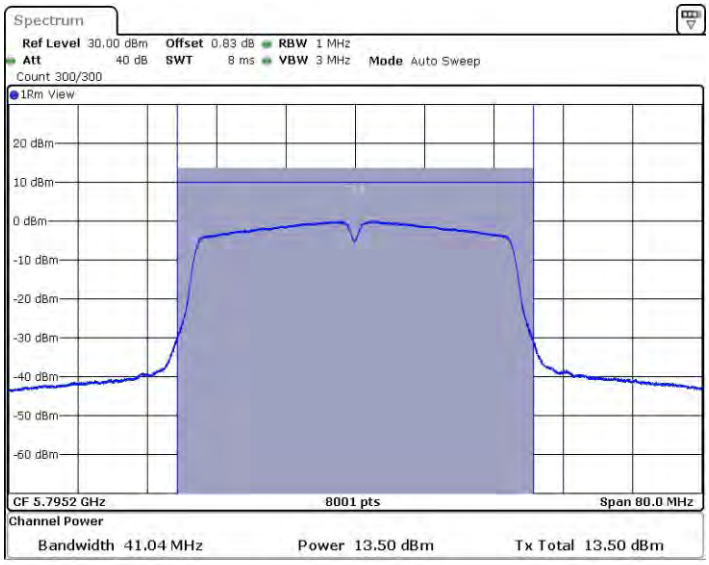

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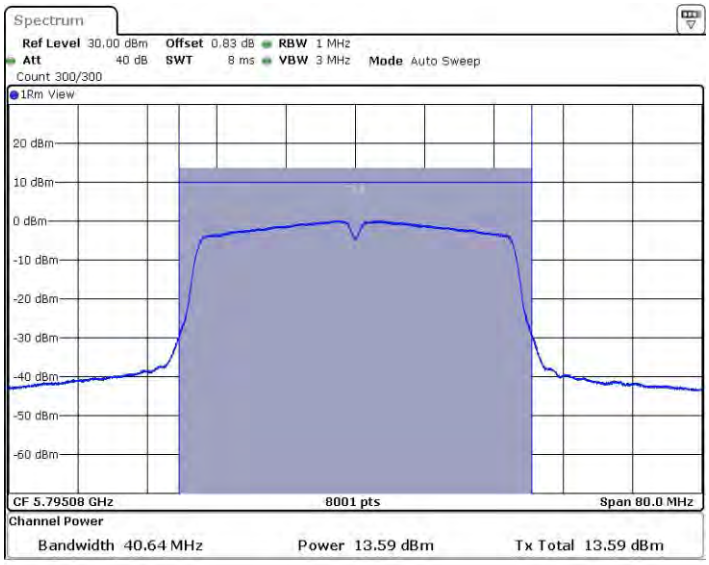

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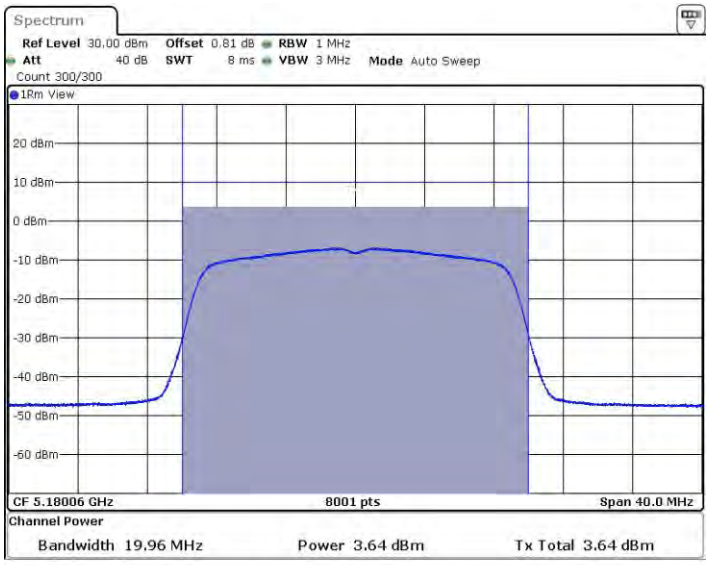

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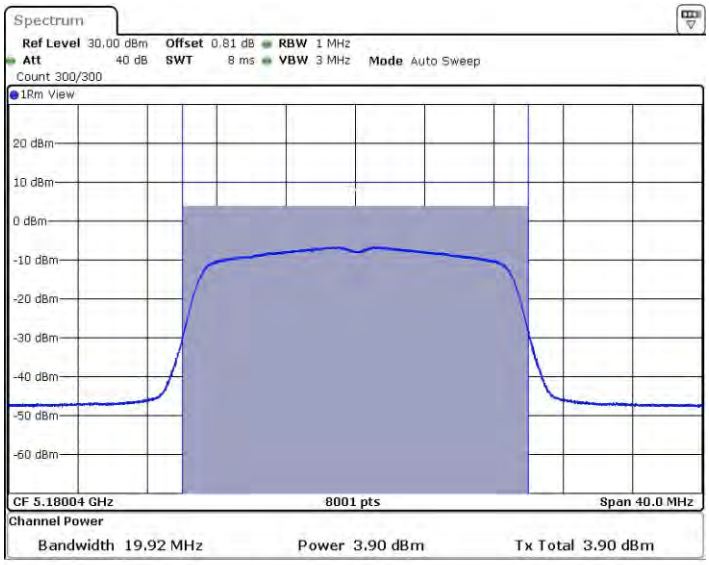

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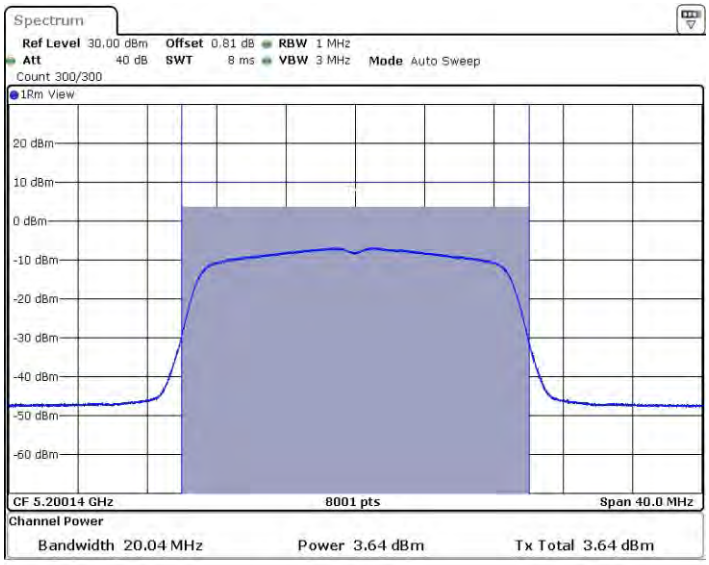

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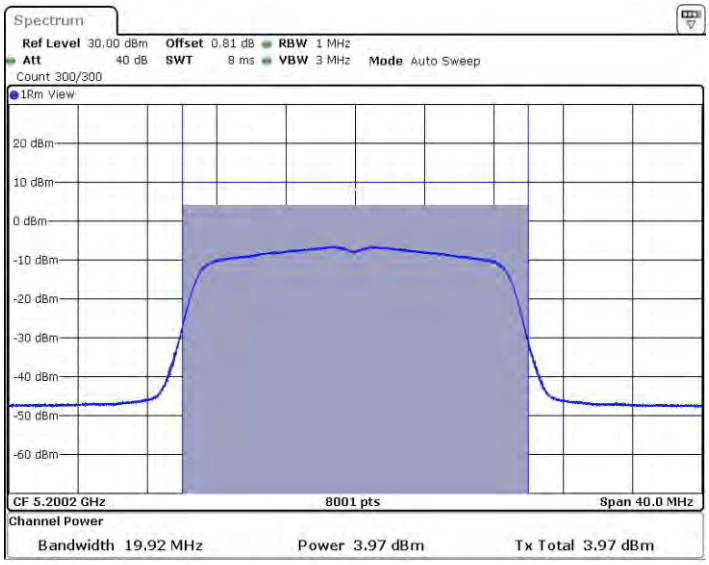

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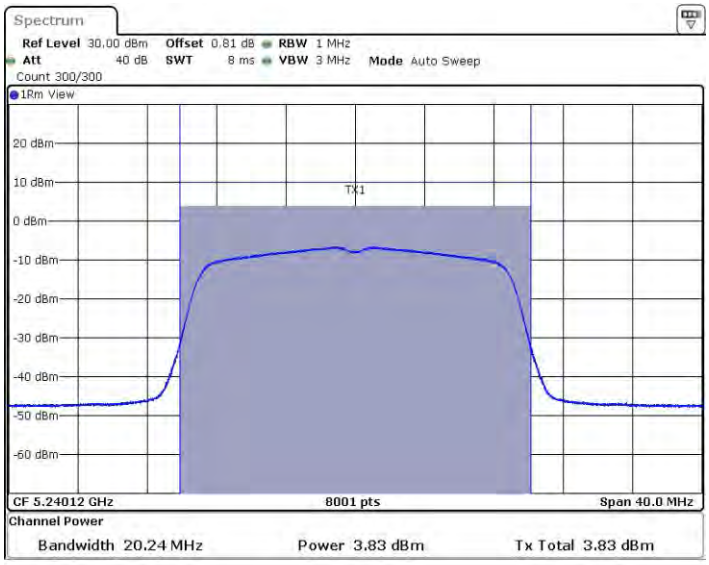

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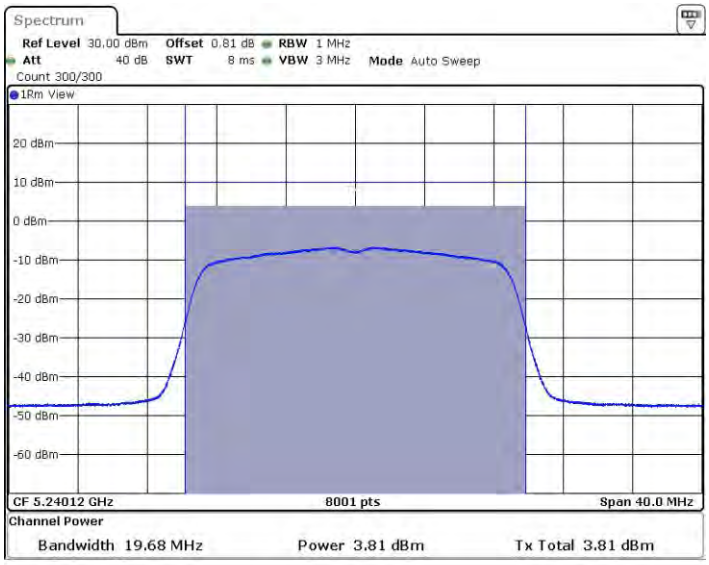

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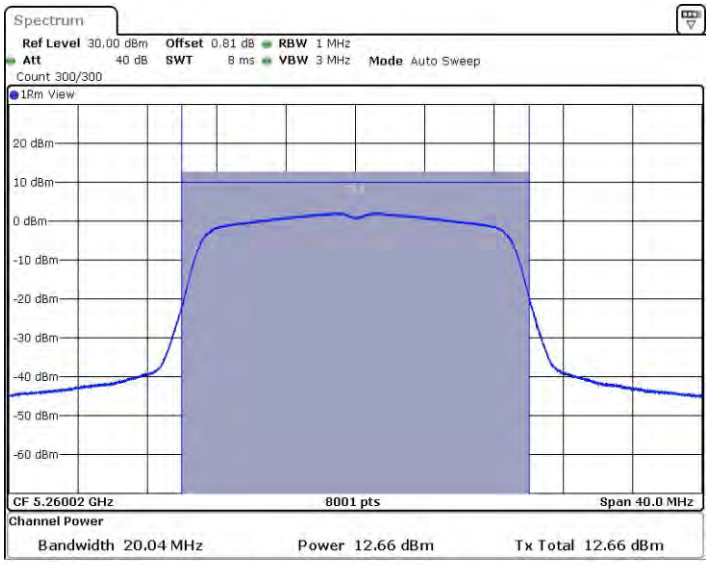

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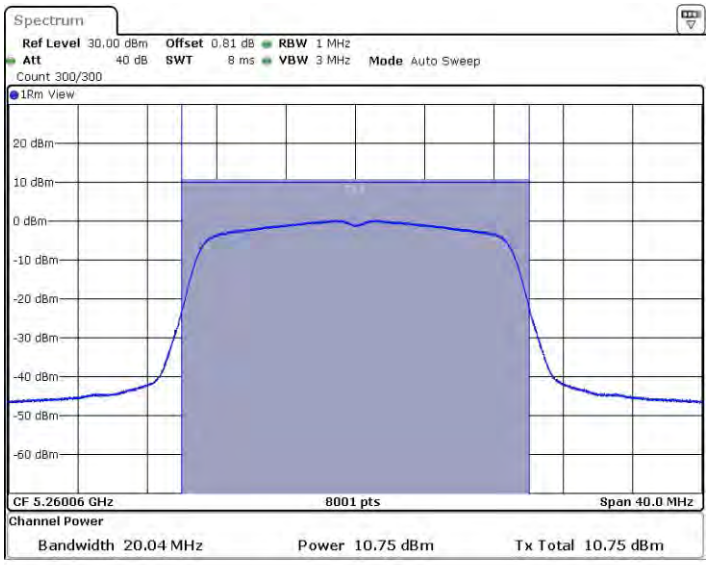

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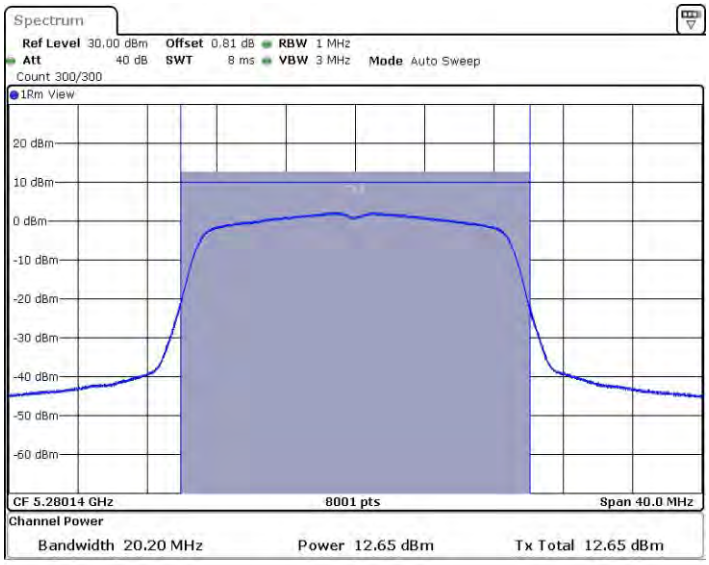

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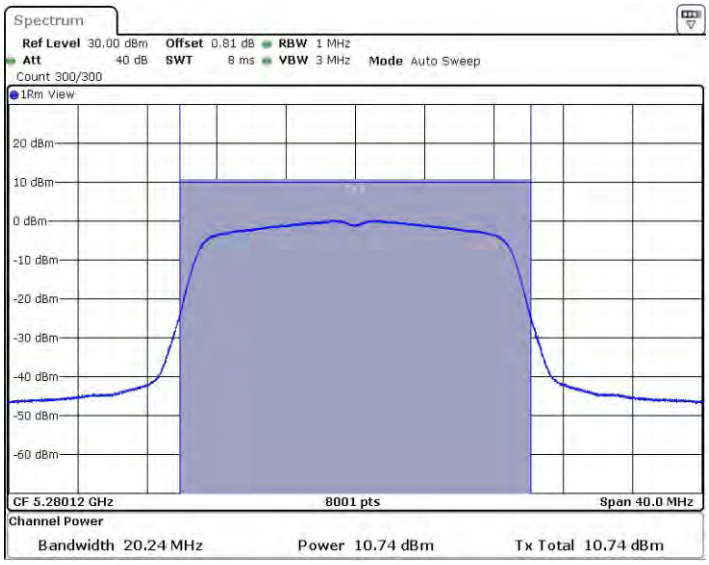

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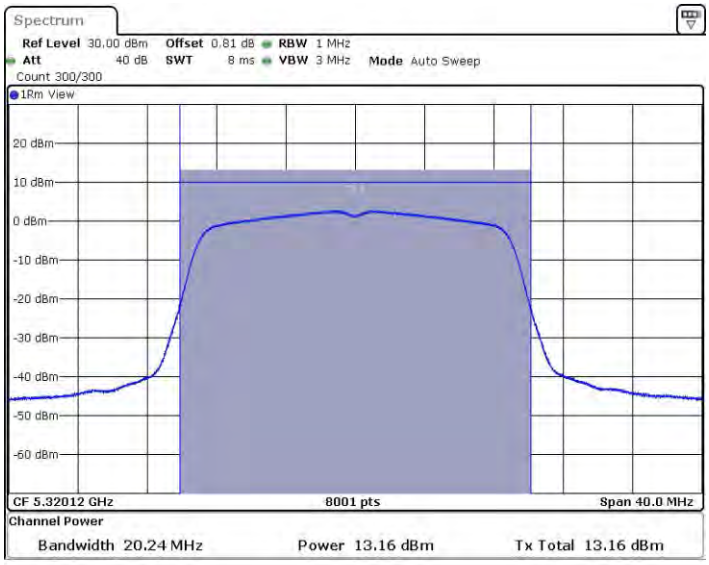

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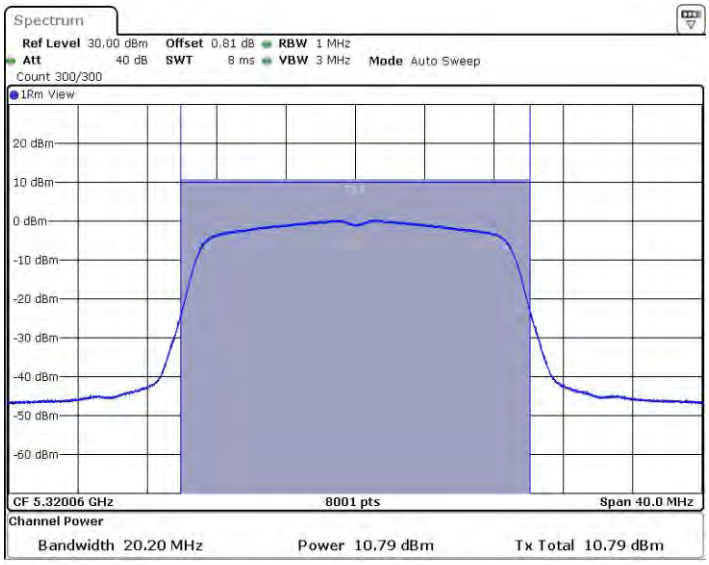

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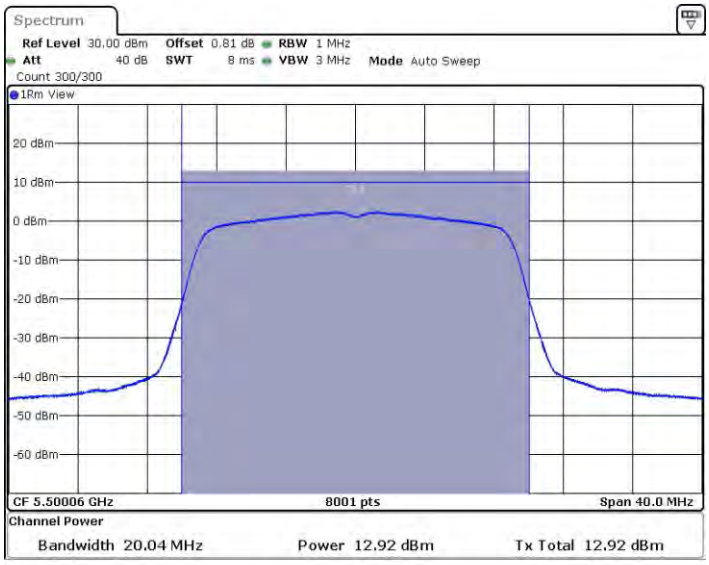

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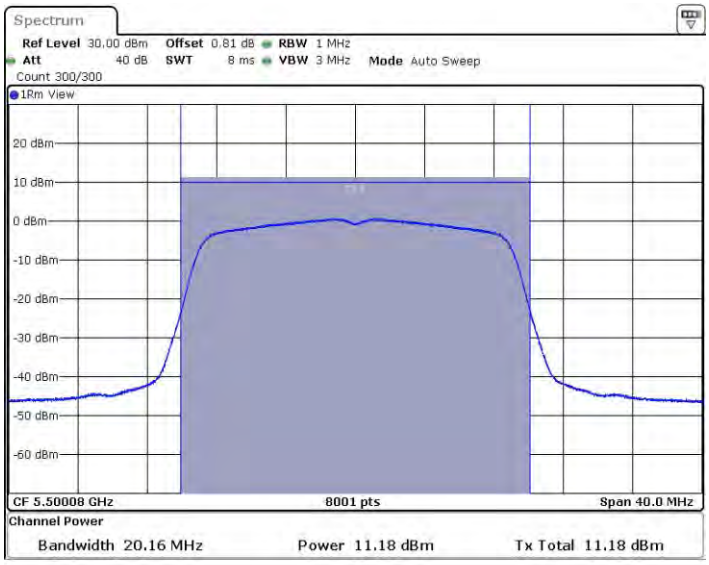

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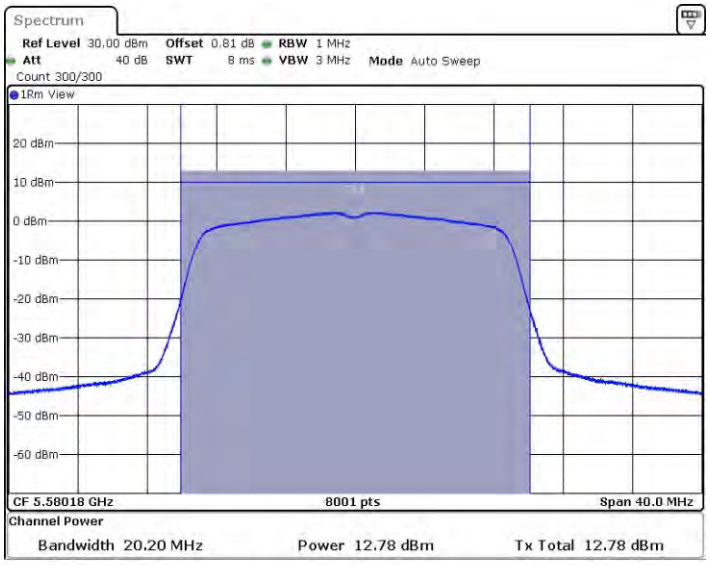

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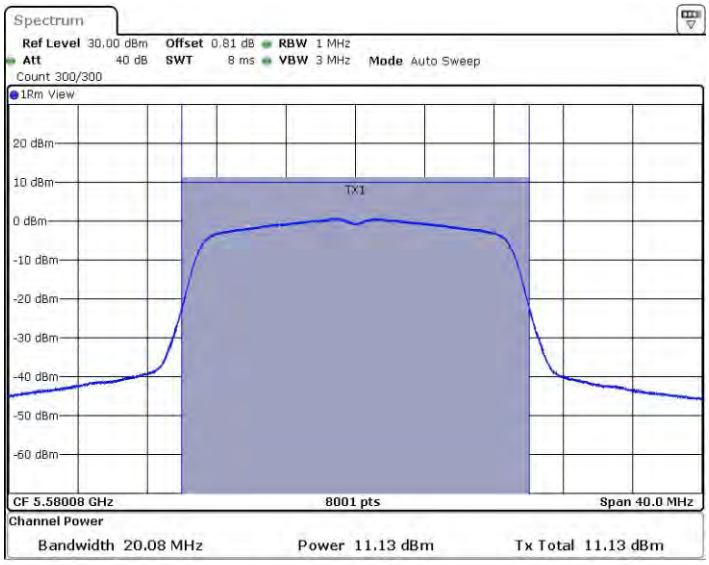

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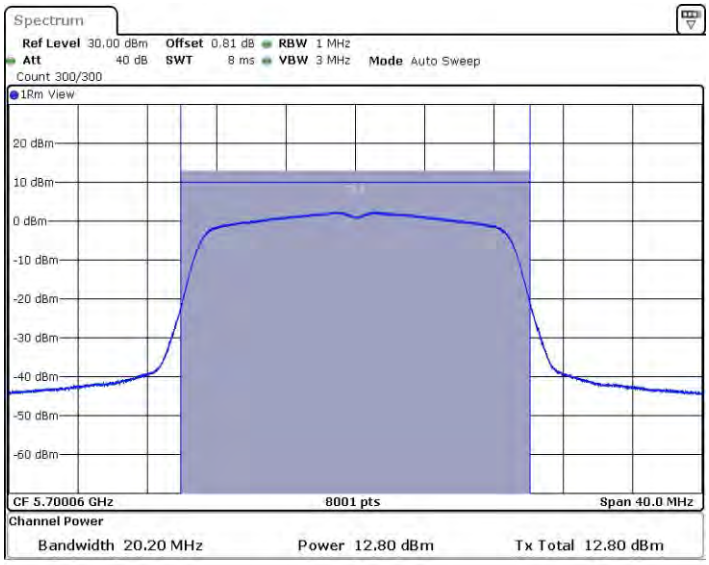

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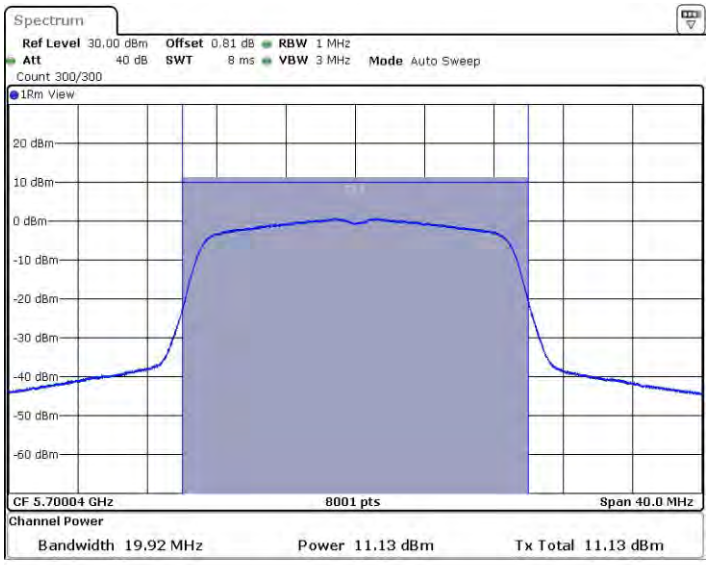

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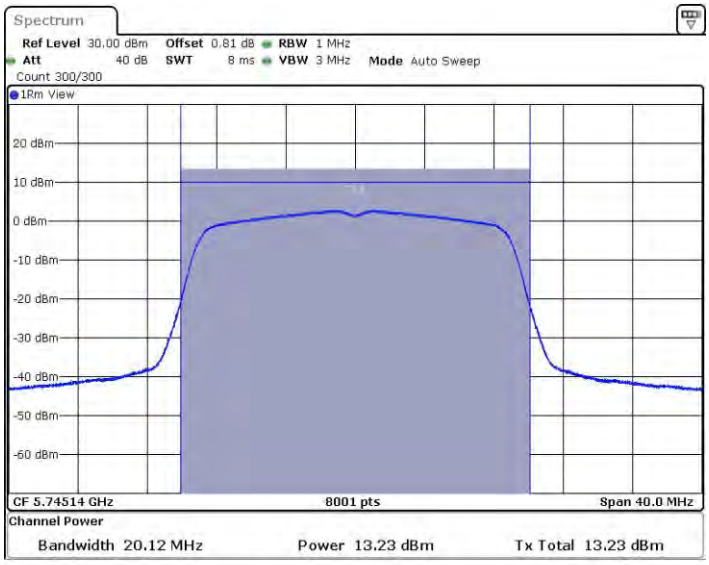

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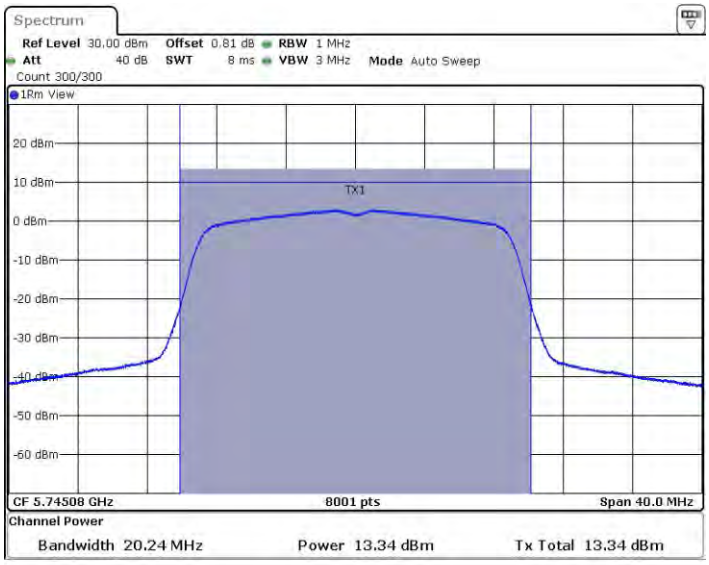

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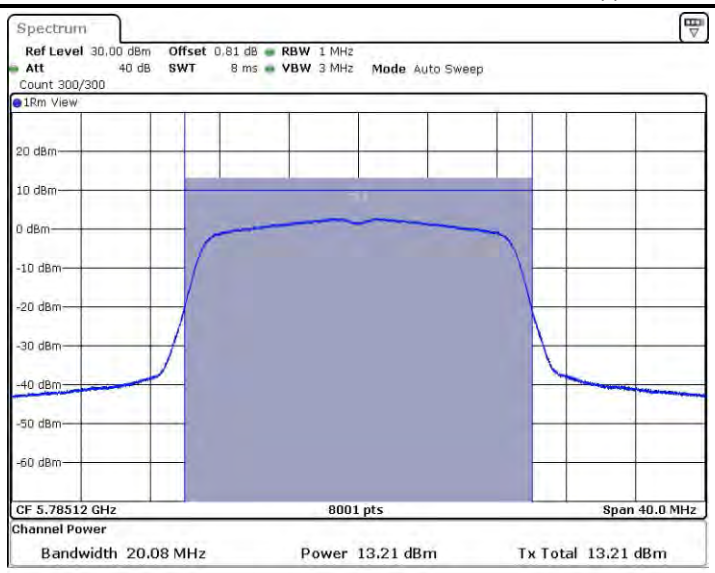

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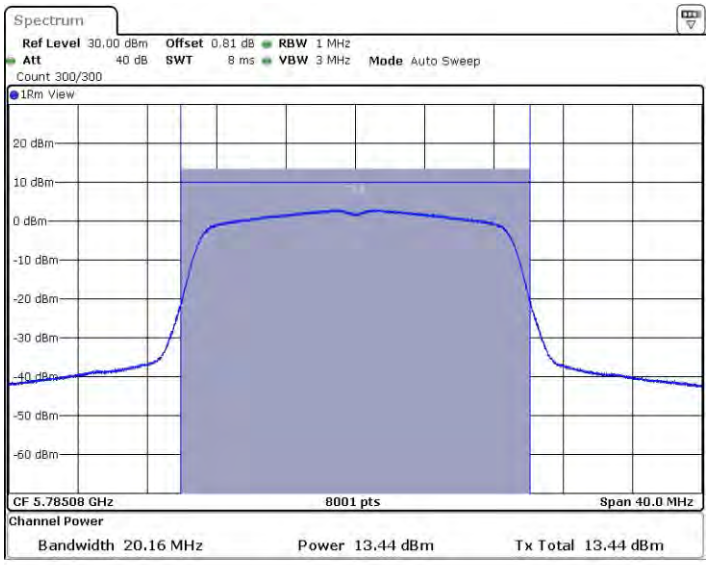

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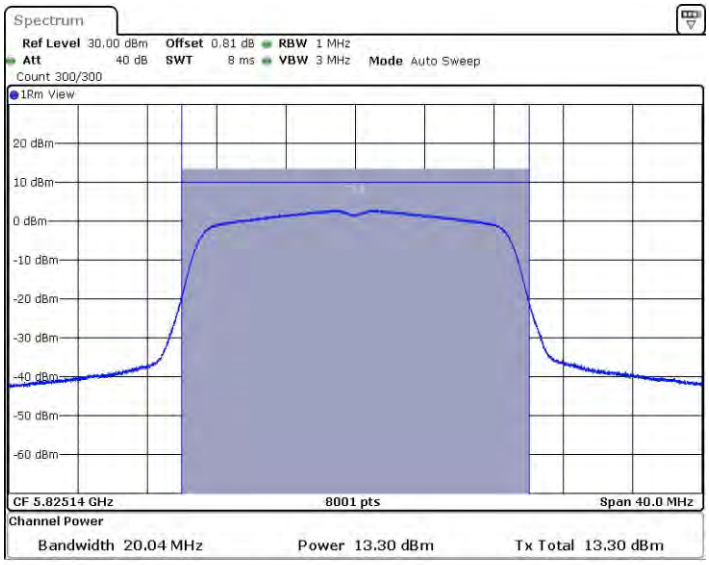

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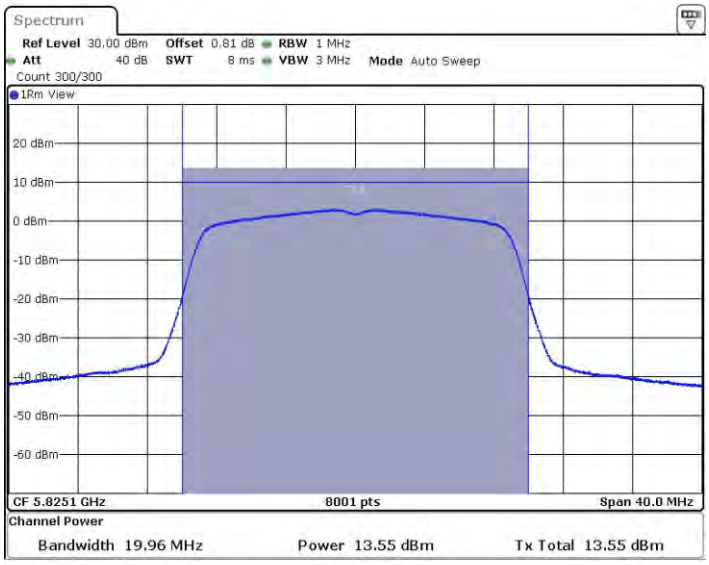

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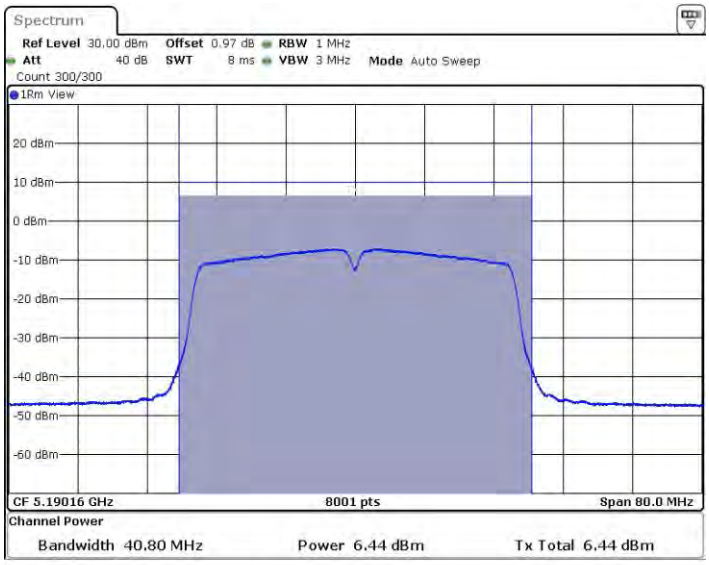

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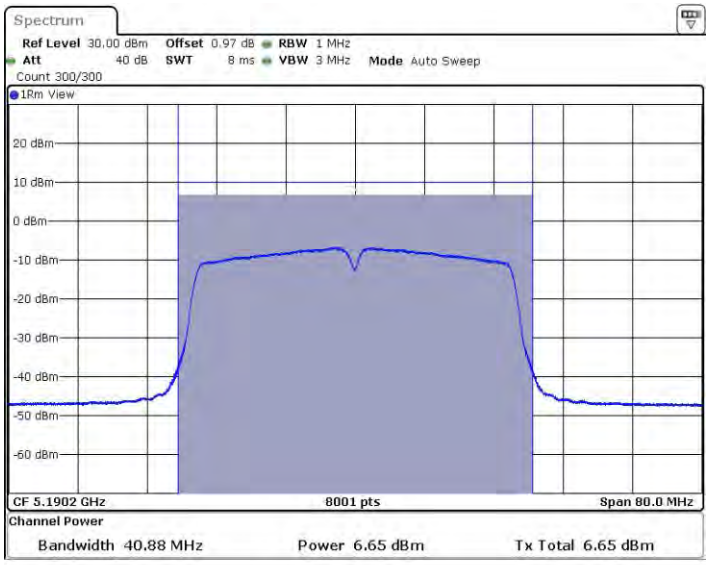

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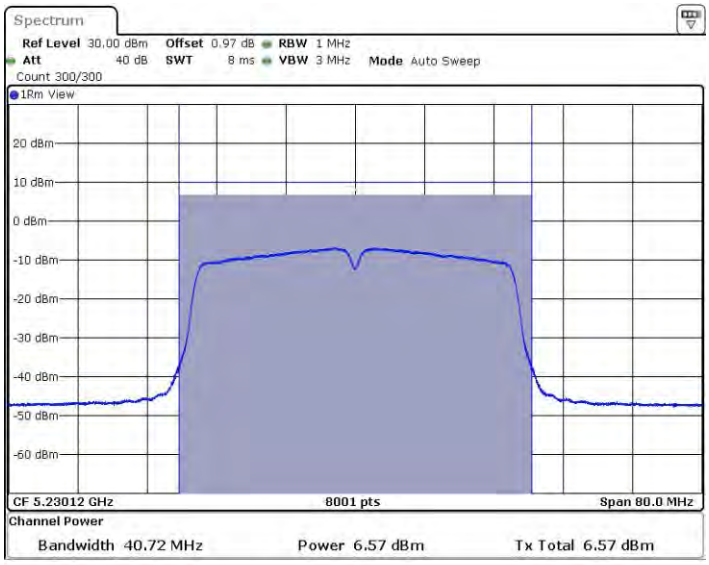

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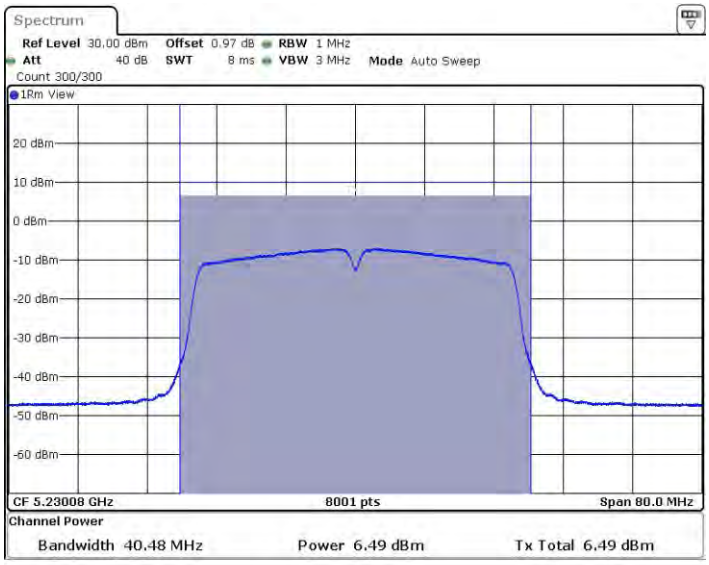
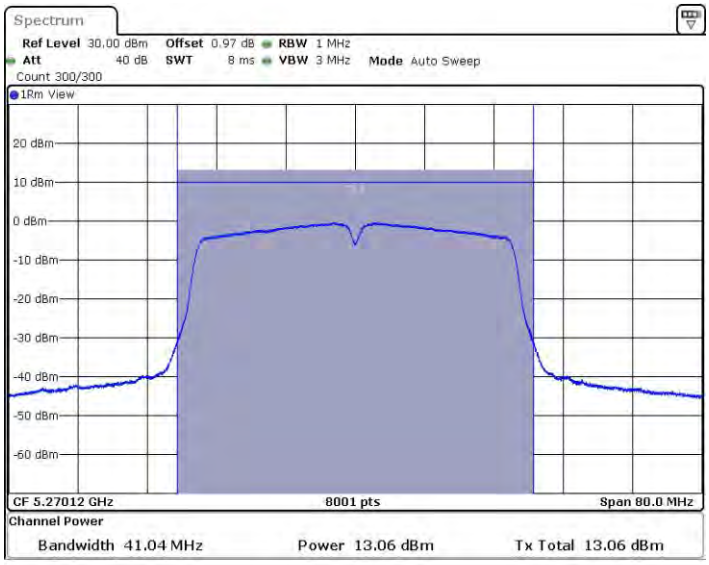


11AC40MIMO_Ant2_5230



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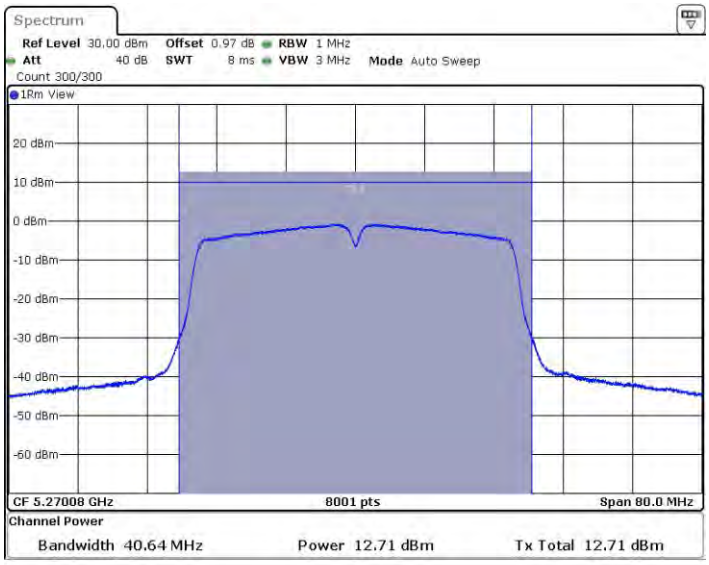

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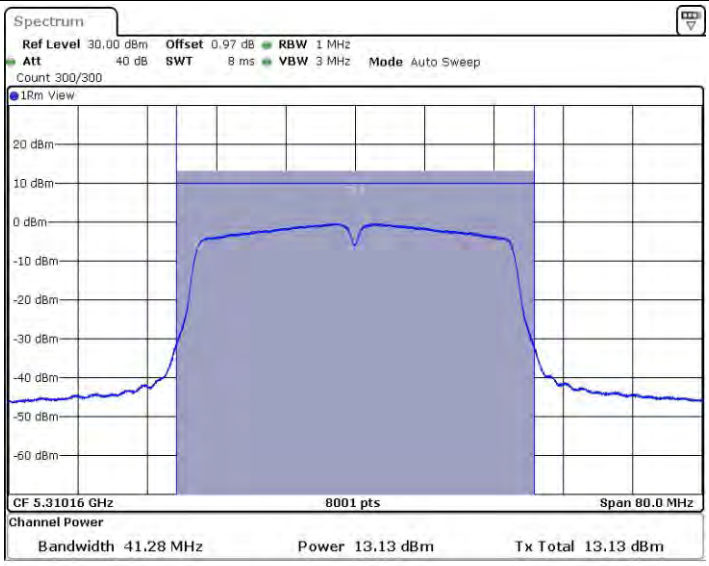

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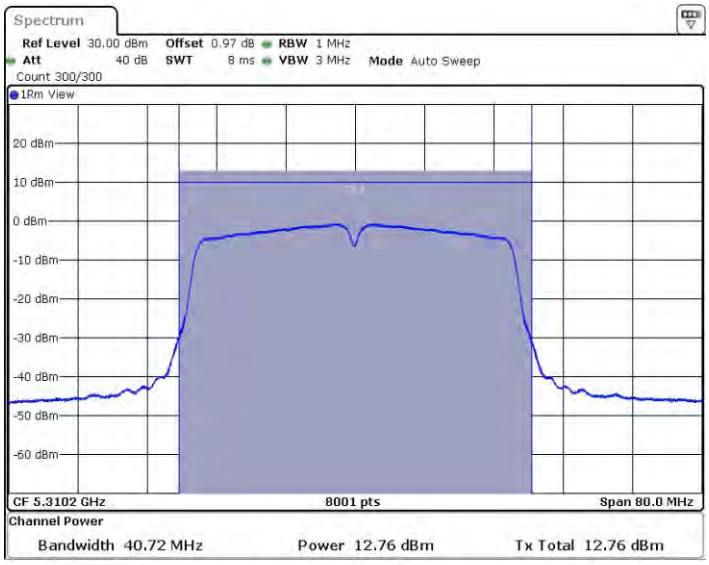

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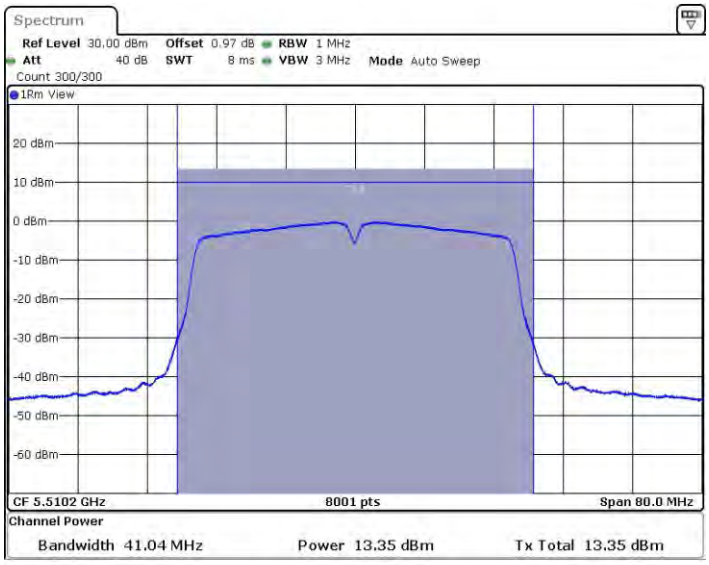

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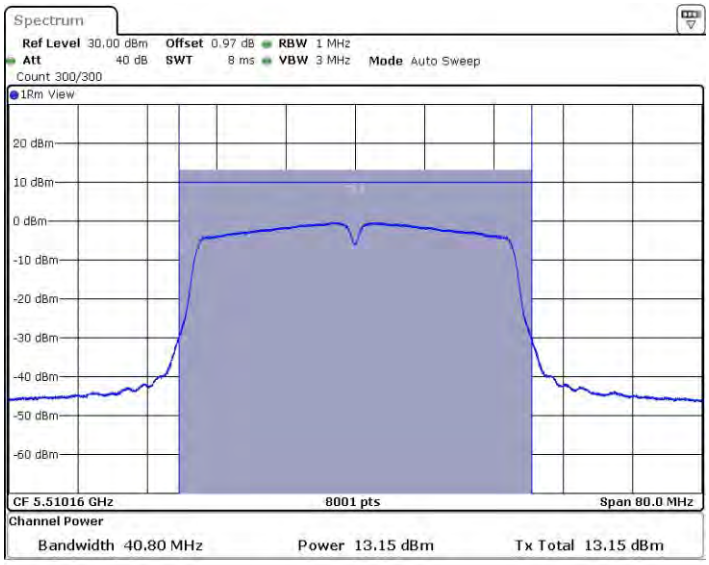

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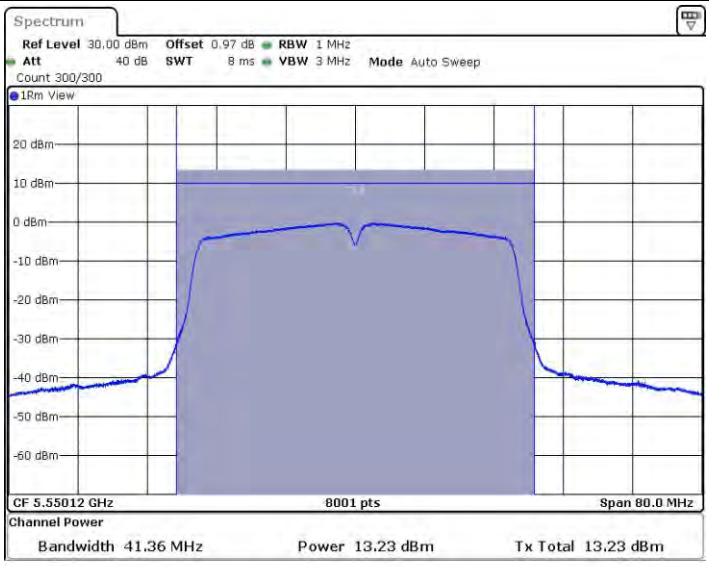

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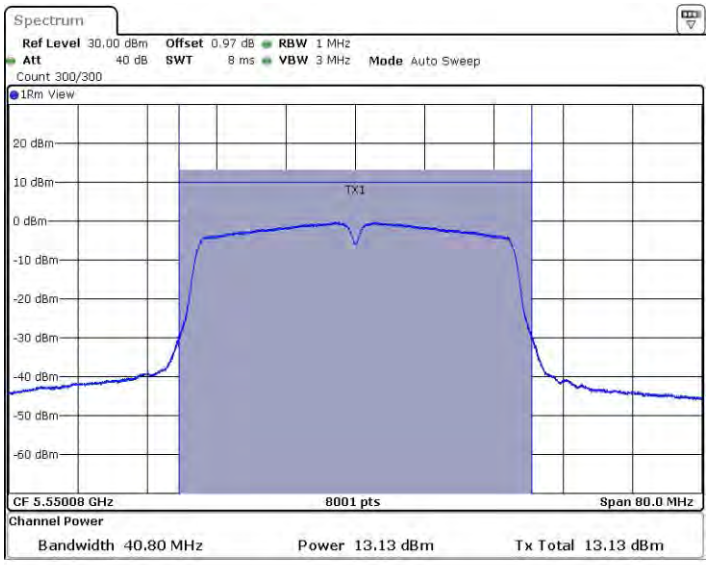

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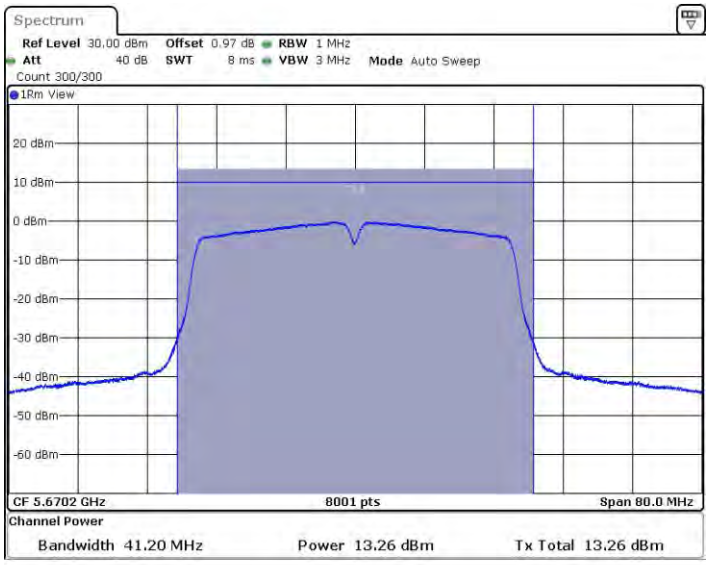

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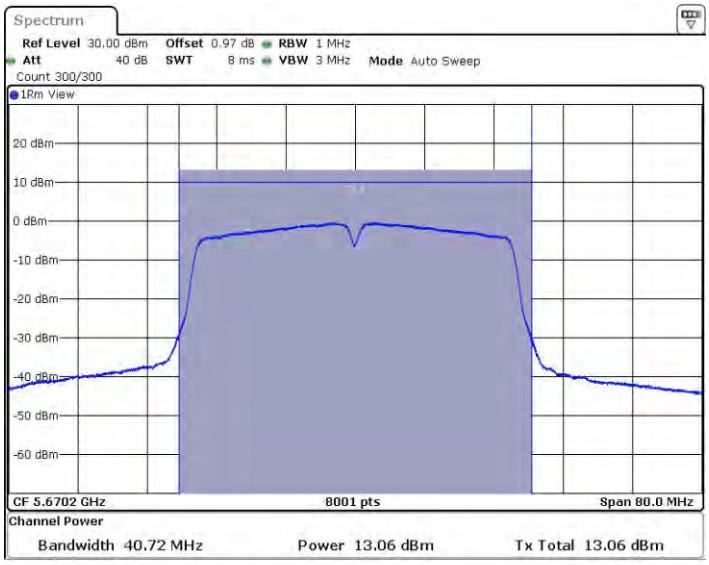

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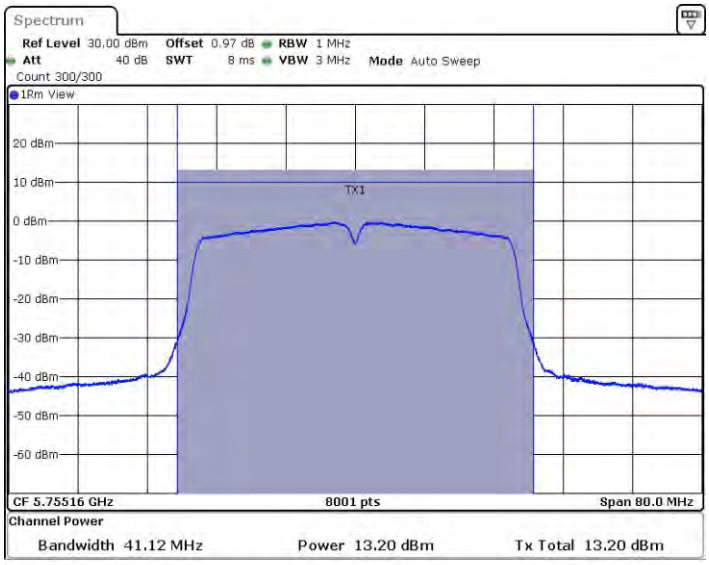

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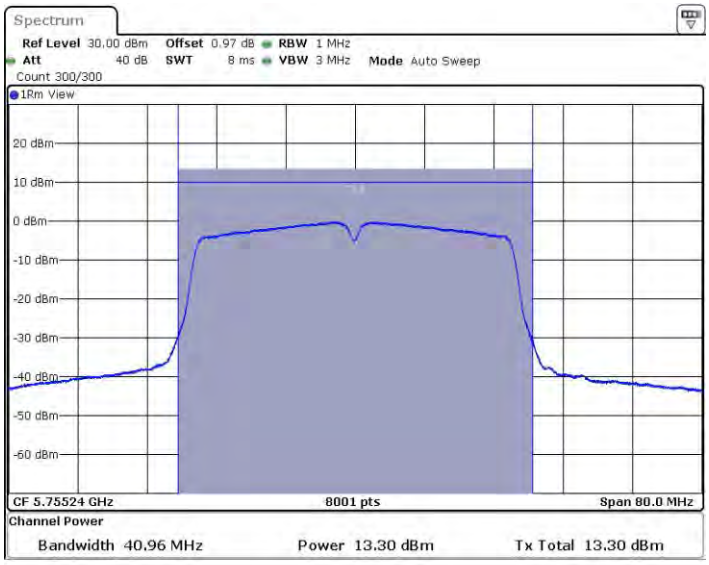

11AC40MIMO_Ant2_5755