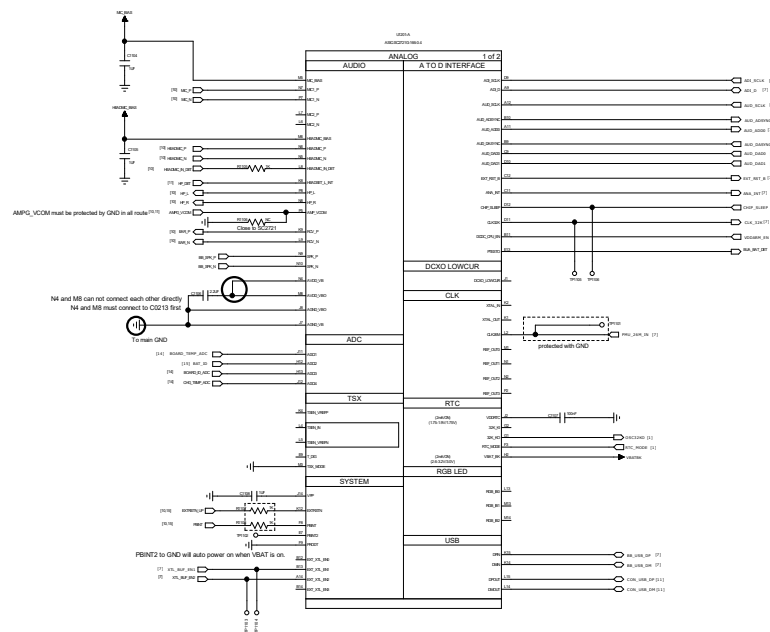


OK

PMU_ANALOG

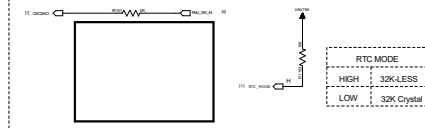


BKBT



32K less

RTC



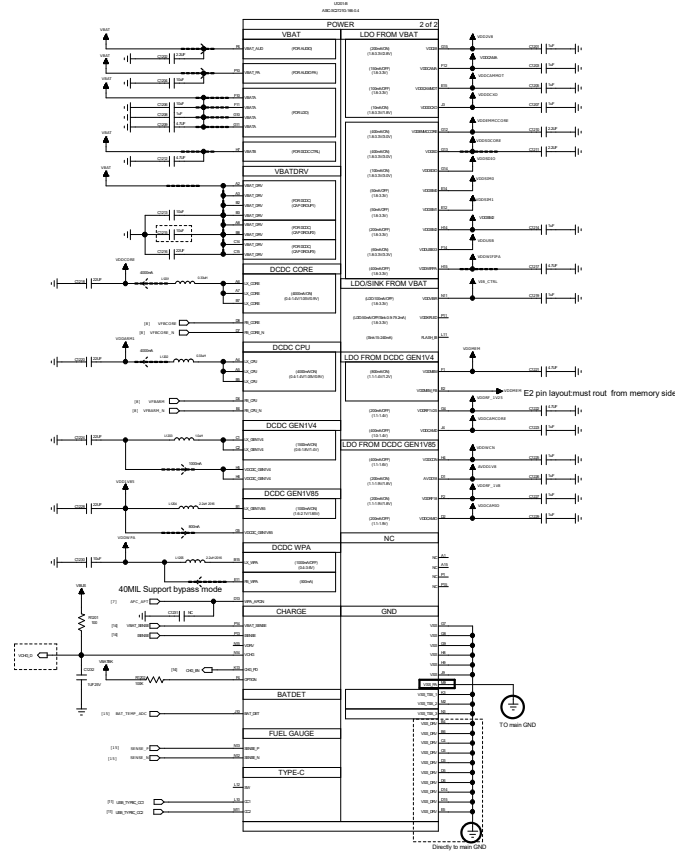
RTC Mode	Type	R103	R105	X101	C107	C108	R104	R106
32K Crystal	Option	0R	NF	Crystal	8.2pF	8.2pF	0R	NF
32K-less	Default	NF	0R	NF	NF	NF	NF	10K

Note: 32K-less only use in TSX mode, 32K crystal is only use in TCXO mode

OK

PMU_POWER

INPUT	Description
VBAT_ALD	VBAT power input for audio
VBAT_PA	VBAT power input for audio speaker PA
VBATA	VBAT power input for LDO
VBATB	VBAT power input for KP_LED,VIB,VBATBK,Flash LED
VBATC	VBAT power input for LDO
VBAT_DRV	VBAT power input for DCDC



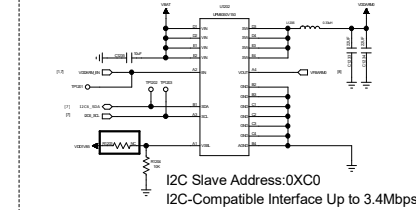
SC2721 DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCCORE	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCGEN1V4	0.6-1.8	1500mA	1.4V	ON
	DCDCGEN1V85	1.6-2.1	1000mA	1.85V	ON
	DCDCWPA	0.4-3.6	1000mA		OFF

SC2721 LDO output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO from VBAT	VDDCAMA	1.8-3.3	150mA	2.8V	OFF
	VDDCMMOT	1.8-3.3	100mA	2.8V	OFF
	VDDSIM0	1.8-3.3	50mA	1.8V	OFF
	VDDSIM1	1.8-3.3	50mA	1.8V	OFF
	VDDSIM2	1.8-3.3	200mA	1.8V	OFF
	VDD28	1.8-3.3	200mA	2.8V	ON
	VDDMMCCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDIO	1.8-3.3	100mA	3.0V	ON
	VBATBK	2.6-3.2	2mA	3.0V	ON
	VDDVIB	1.8-3.3	100mA	3.3V	OFF
	VDDKPLED	1.8-3.3	LDO 50mA/Sink 0.9-78.2mA		
	VDDWIFIPA	1.8-3.3	400mA	3.3V	OFF
	VDDDCXO	1.8-3.3	10mA	1.8V	ON
	VDDUSB33	1.8-3.3	60mA	3.3V	ON
LDO from DCDC GEN1V4	VDDCAMD	1.0-1.4	400mA		OFF
	VDDRF1V25	1.1-1.4	200mA		OFF
	VDD_MEM	1.1-1.4	800mA	1.2V	ON
LDO from DCDC GEN1V85	VDDCAMIO	1.1-1.9	200mA		OFF
	AVDD18	1.1-1.9	200mA	1.8V	ON
	VDDRF18	1.1-1.9	200mA	1.8V	ON
	VDD_CON	1.1-1.6	400mA		OFF

EXT DCDC ARM



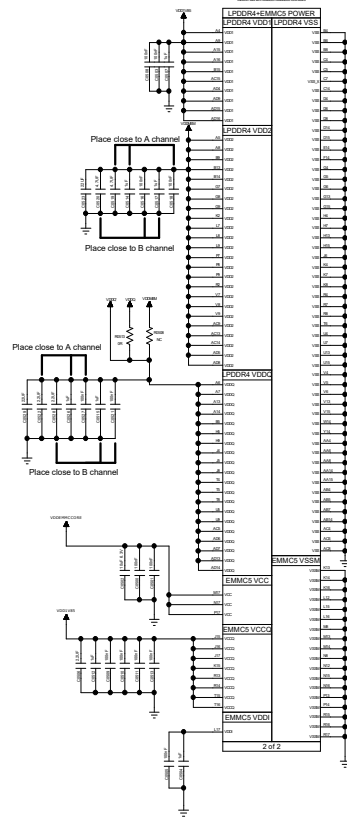
COMPANY: PRIZE

TITLE 12.ABB_POWER

SIZE: A0 SHEET 2 OF 21 VERSION: <Version number>
Date: 2024/6/25

Project: SC20H
Drawn By: Xueheng
PCB Checked By: XUE CHENGBO
Power Checked By: <Power checked by>
Audio Checked: <Audio checked by>
Sensor Checked By: <Sensor checked by>
Media Checked By: <Media checked by>
RELEASED

Please refer to QVL table to select LPDDR4(X) was verified by us.

[illegible]

BUCK 0.9V output for BB_VDDIOEMIO and Memory VDDQOLPDRXAX (Default)

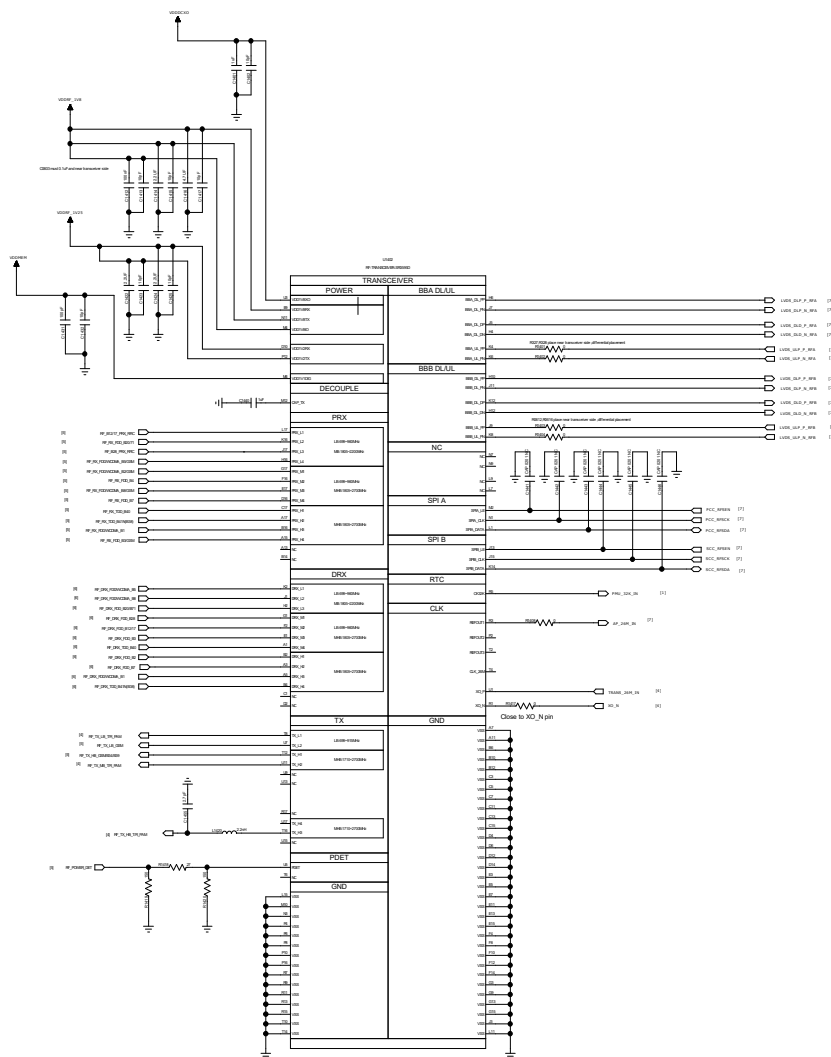
CO2625	R0518	R0512	CO3571	R0503	R05009	L05000	CO524	CO530	R0504	R0501	U0501	
10uF	0ohm	NF	0.1uF	NF	10K	NF	1uH	100pF	10uF	0ohm	NF	BUCK IC

BUCK 0.9V output for BB_VDDIOEMIO (reserve)

CO2625	R0518	CO3571	R0503	R05009	L05000	CO524	CO530	R0504	R0501	U0501		
10uF	0ohm	NF	0.1uF	NF	10K	NF	1uH	100pF	10uF	60K 1%	120K 1%	BUCK IC

R0504 value is half of R0501 to set output voltage as 0.9V.

LTE/WCDMA PAM

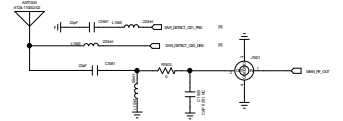
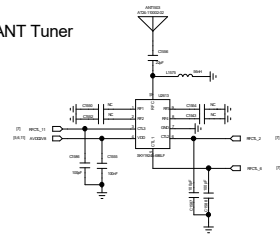
[illegible]

Priority	Mode	X1401	X2801	C1703	C1702	R1405	R1411	R1409	C2824	C2823	R1417
1ST	TSX	TSX	NF	1UF	100PF	100K	OR	NF	NF	NF	OR
2RD	TCXO	NF	TCXO	NF	NF	NF	NF	1000PF	1UF	22PF	NF

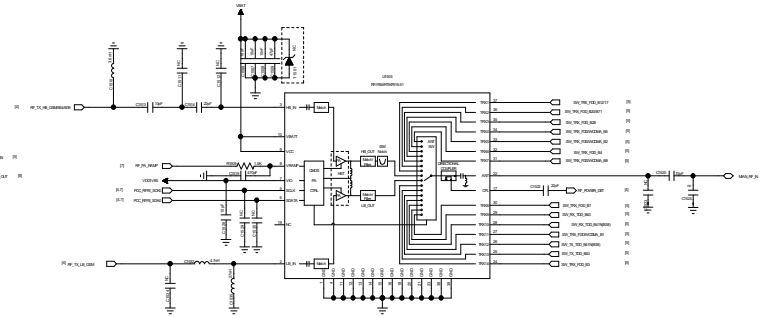
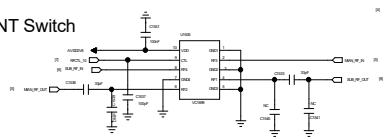
Project	KC02H	TITLE		14.RF_CSFB_TRANS	
Drawn By		SIZE: A0		SHEET 4 OF 21	VERSION: <Version number>
RF checked By	<RF checked by>	Date:		2024/6/25	
Power checked By	<Power checked by>				
Radio checked By	<Radio checked by>				
Sensor checked By	<Sensor checked by>				
Media checked By	<Media checked by>				
RELEASED					

SIZE: A0	SHEET 4 OF 21	VERSION: <Version number>
Date:	2024/6/25	

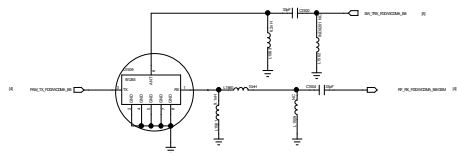
ANT Tuner



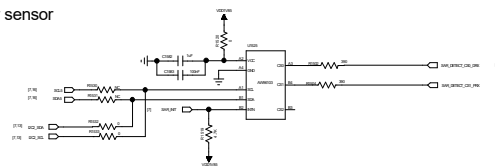
ANT Switch



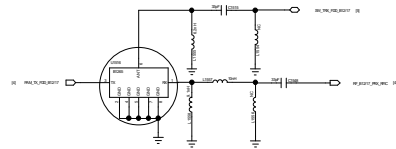
FDDLTE_B8



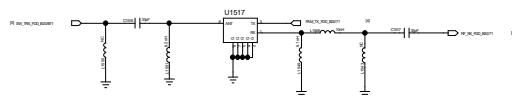
sar sensor



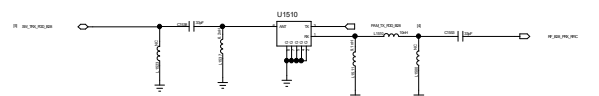
FDDLTE_B12/17



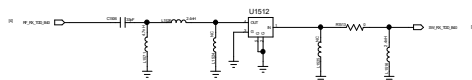
FDDLTE_B20&B71



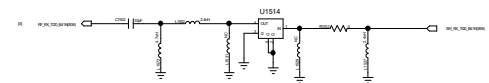
FDDLTE_B28AB



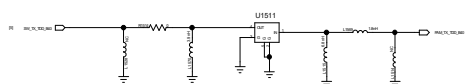
TDD_B40_RX



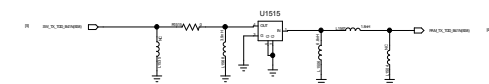
TDD_B41_RX



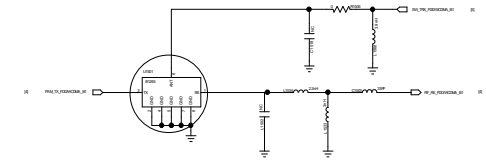
TDD_B40 TX



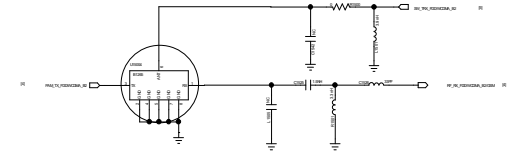
TDD_B41 TX



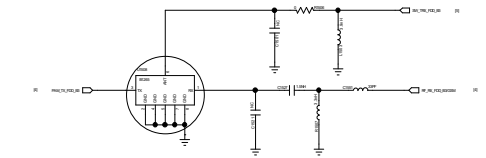
FDDLTE_B1



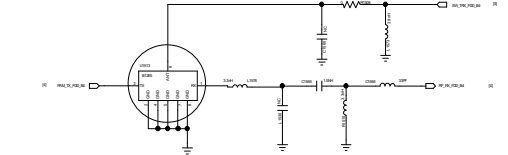
FDDLTE_B2/25



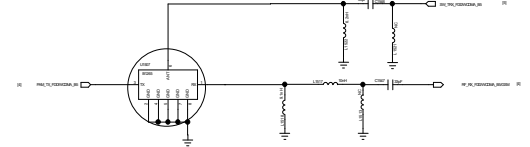
FDDLTE_B3



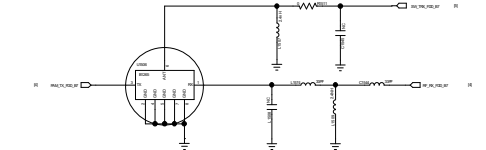
FDDLTE_B4/66



FDDLTE_B5/26



FDDLTE_B7



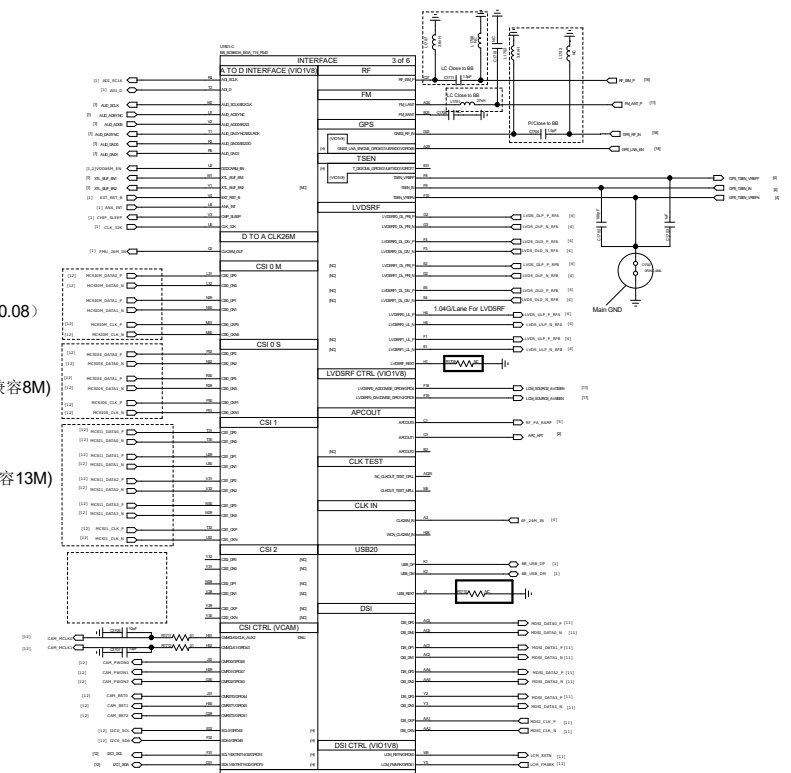
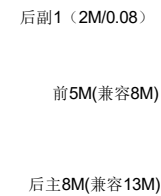
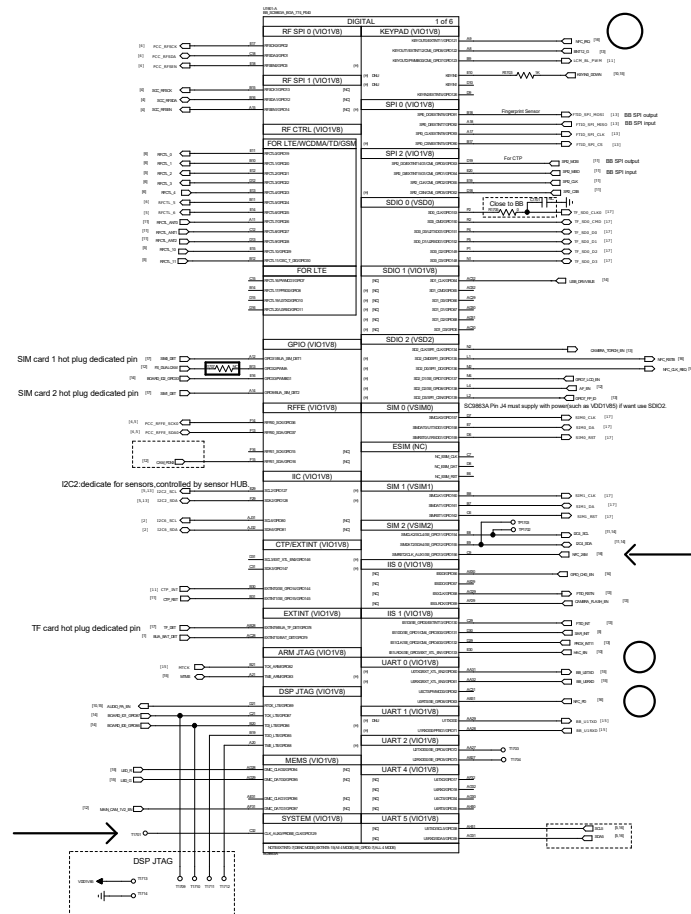
Project: K0301
 Drawn By:
 Design Checked By:
 Power Checked By:
 Audio Checked:
 Review Checked By:
 Media Checked By:
 RELEASED

COMPANY: **PRIZE**

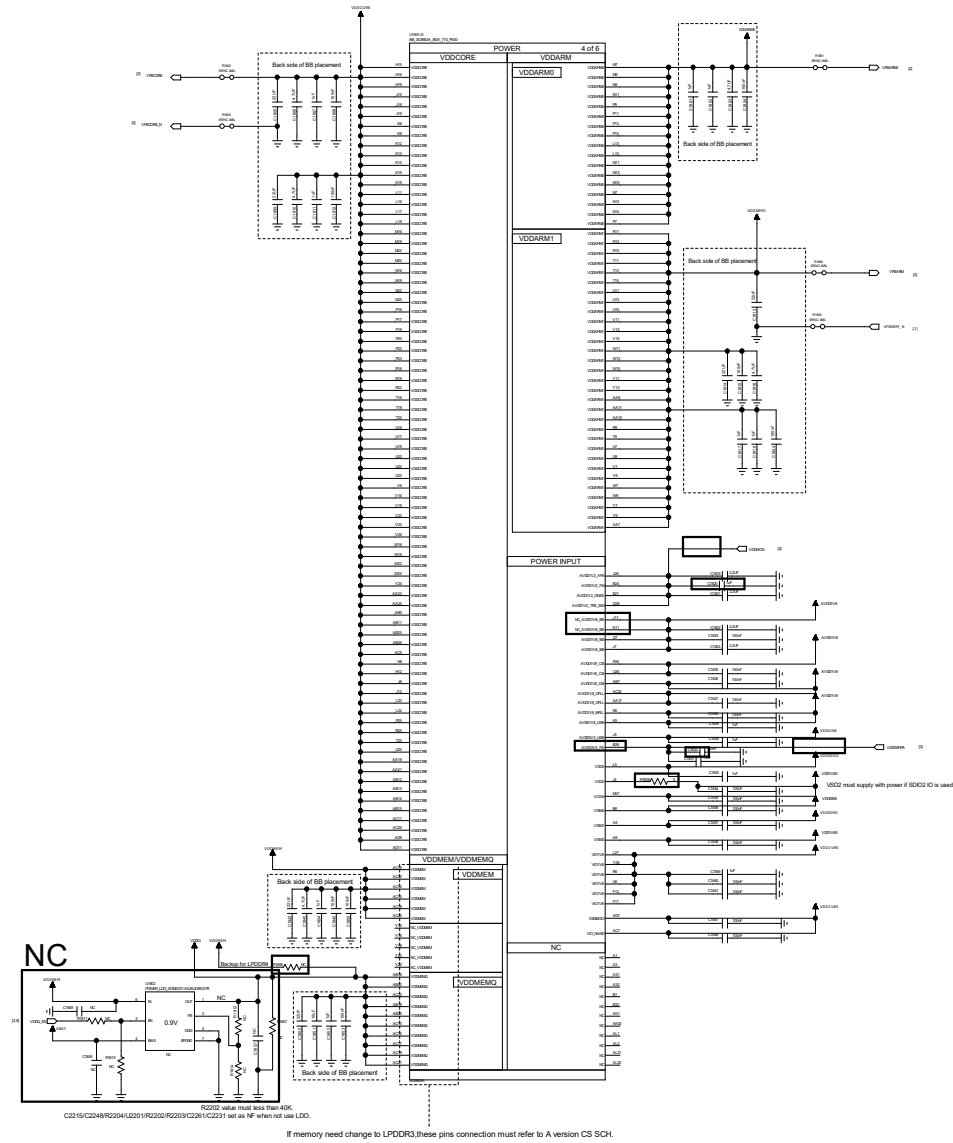
TITLE: 15.RF_PRIMARY

SIZE: A0 SHEET 5 OF 21 VERSION:
 Date: 2024/6/25

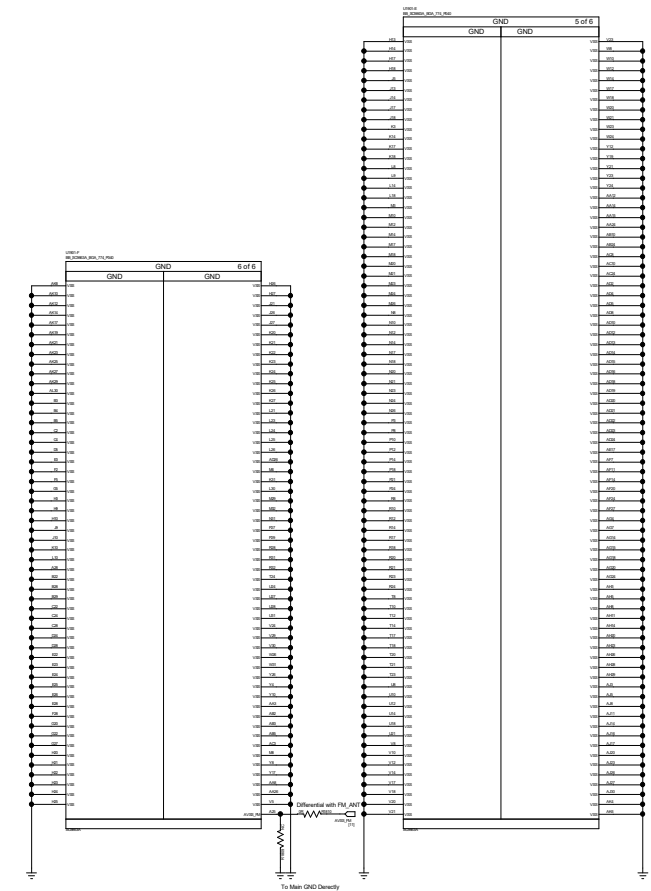
DIGITAL



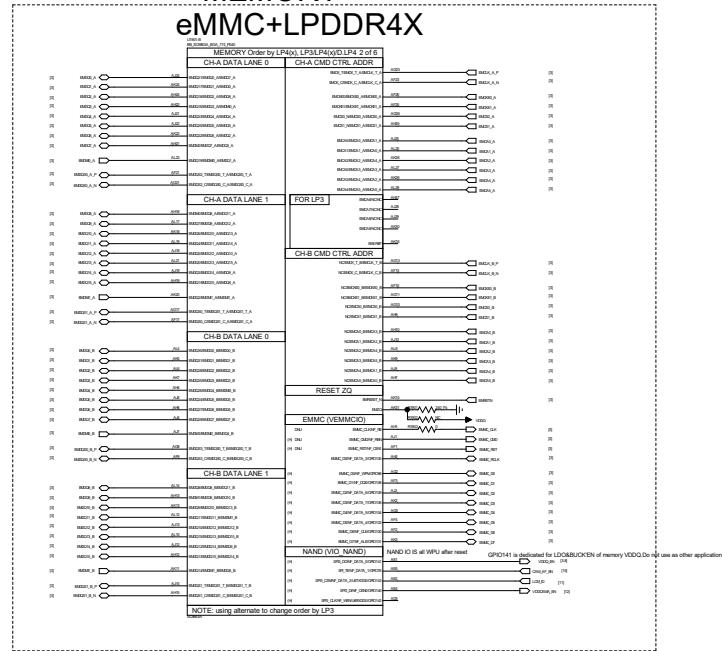
POWER



GND



MEMORY eMMC+LPDDR4X

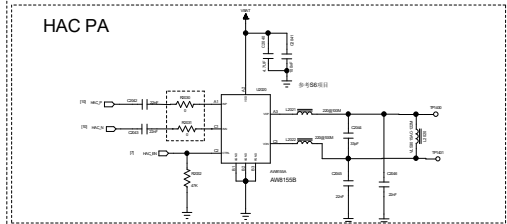
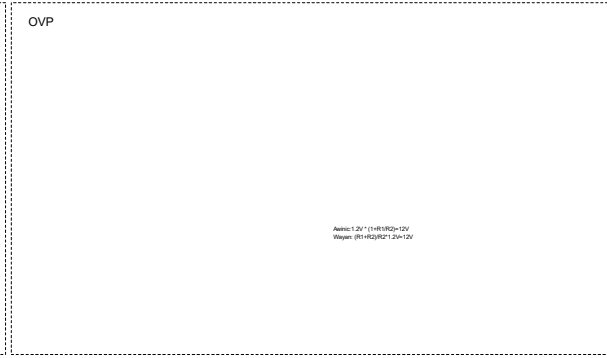
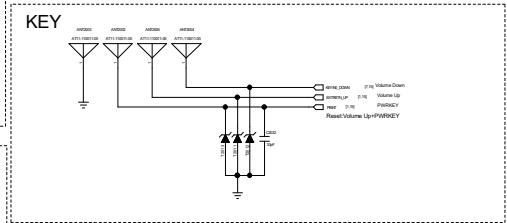
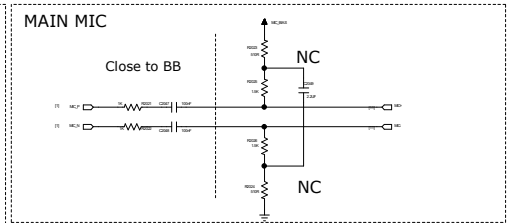
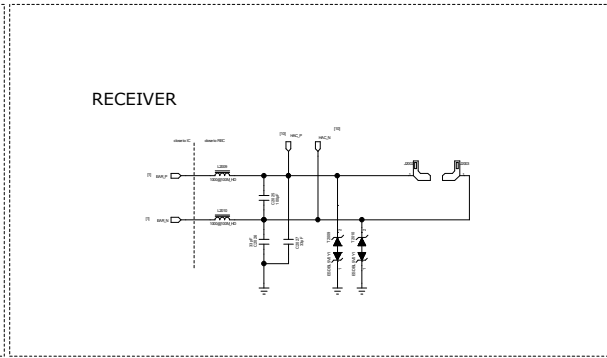
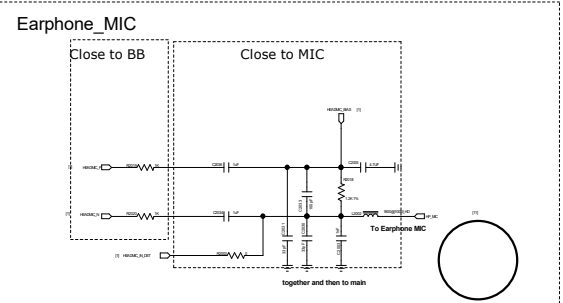
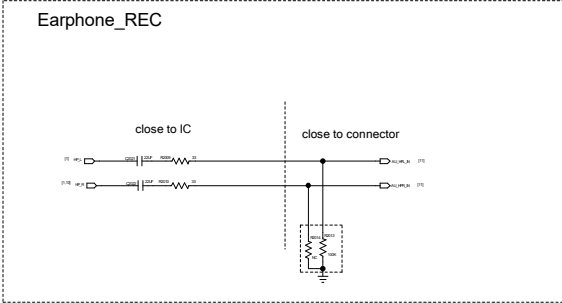
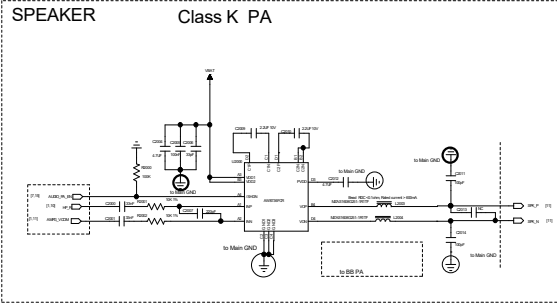


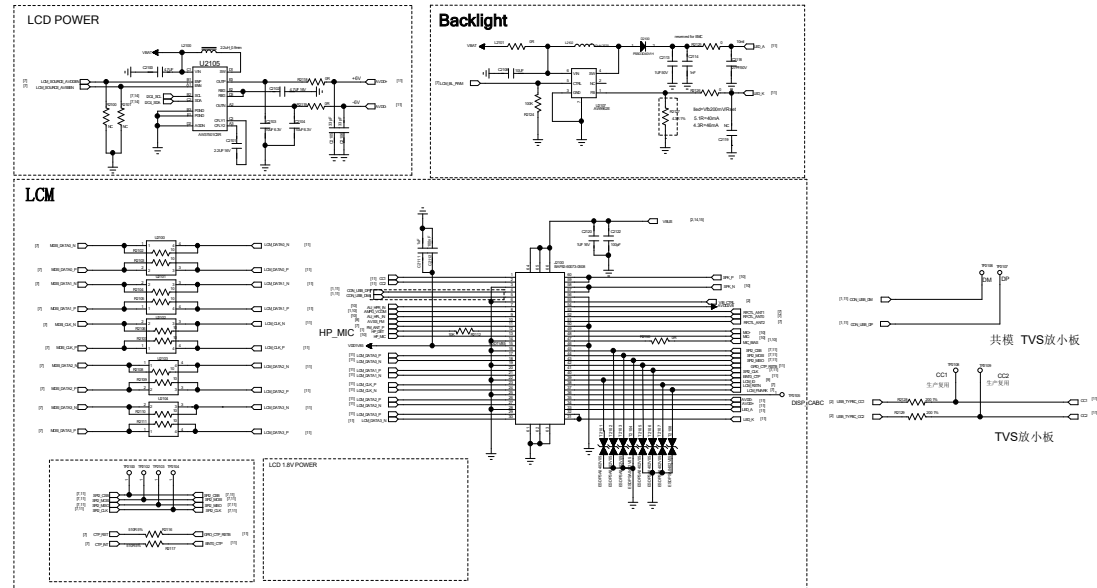
COMPANY: PRIZE

TITLE 19.DBB_MEMORY

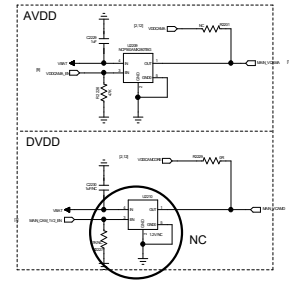
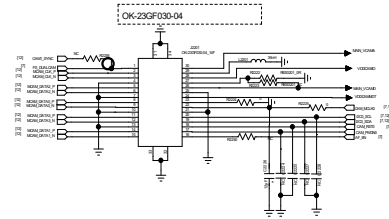
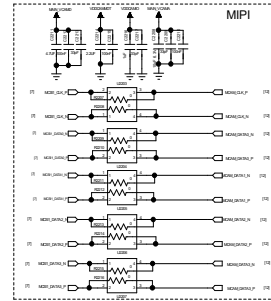
Project KCS001
Drawn By
EC Checked By
Power Checked By
Audio Checked
Sensor Checked By
Media Checked By
RELEASED

SIZE: A0
SHEET 9 OF 21
VERSION: <Version number>
Date: 2024/6/25

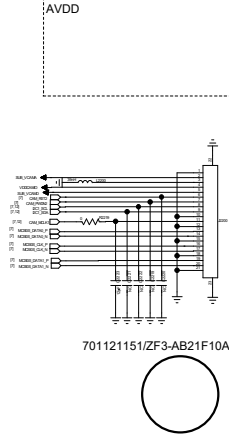
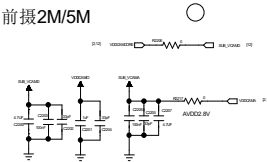




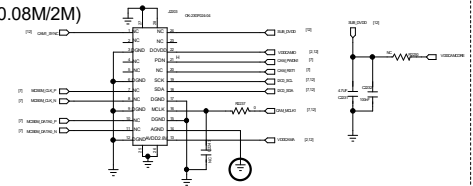
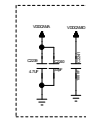
后摄5M/8M/13M



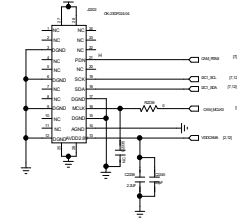
前摄2M/5M



副后摄二选一 副后摄1(0.08M/2M)



副后摄2(0.08M)

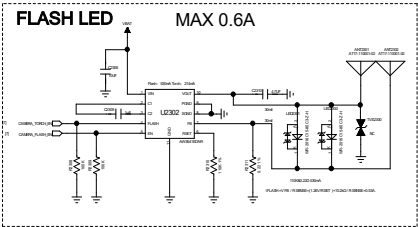
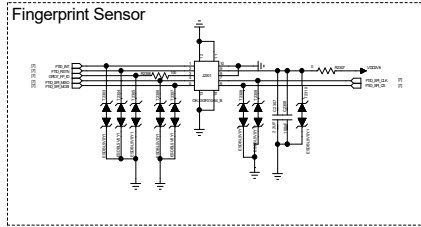
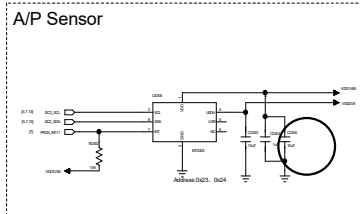
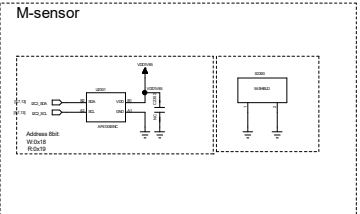
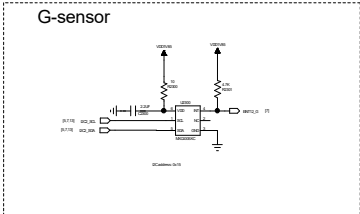


COMPANY: PRIZE

TITLE 22.CAMERA

Project: KC30H1
Drawn By:
2D Checked By:
Power Checked By:
Audio Checked:
Camera Checked By:
Media Checked By:
RELEASED

SIZE: A0 SHEET 12 OF 21 VERSION: <Version number>
Date: 2024/6/25

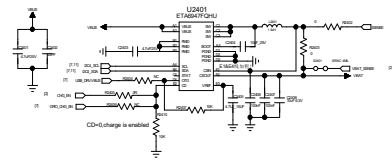


Project	KC301
Drawn By	
DT Checked By	<BPP checked by>
Power Checked By	<Power checked by>
Audio Checked	<Audio checked by>
Sensor Checked By	<Sensor checked by>
Media Checked By	<Media checked by>
RELEASED	

COMPANY:	PRIZE		
TITLE	23.SENSOR		
SIZE: A0	SHEET 13 OF 21	VERSION:	<Version number>
Date:	2024/6/25		

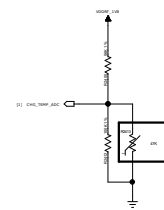
Switching charger

ETA6947 【 I2C Address: 0x6A (Write:0xD4, Read:0xD5) 】



CHARGE NTC

Switching charger:can reserved
Linear charger:must add



Attention

Clock Mode	BoardID3(GPIOB6)
TSX	0

TCXO +32K

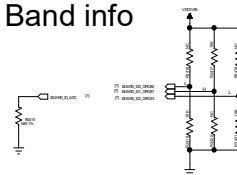
1

No SW compatibility for memory selection(Default)

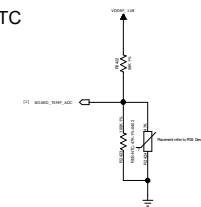
LPDDR Type	BoardID1(GPIOB7)	SW compatibility for memory selection
eMCP LPDDR3	0	add one GPIO
eMCP LPDDR4X	1	

LPDDR Type	BoardID1(GPIOB7)	BoardID2(GPIOC3)	SW compatibility for memory selection
eMCP/discrete LPDDR3	0	0	add two GPIOs
eMCP LPDDR4X	1	0	
Discrete LPDDR4	0	1	
Discrete LPDDR4X	1	1	

Band info



BB NTC

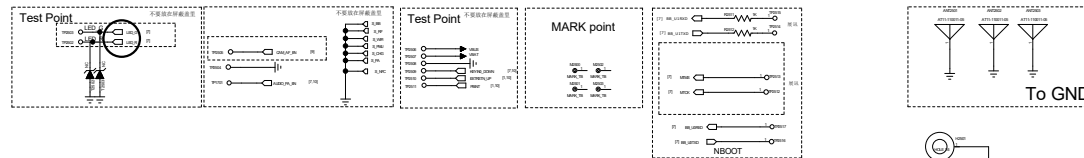
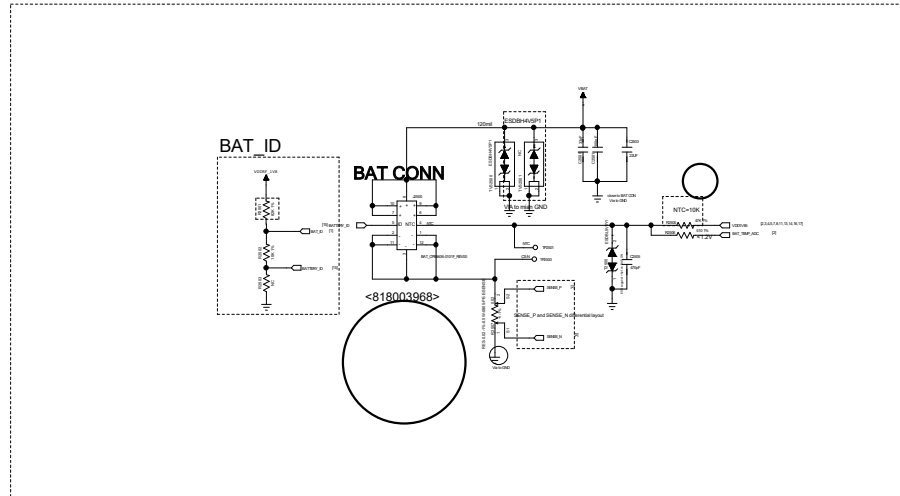


COMPANY: **PRIZE**

TITLE 24.CHARGE

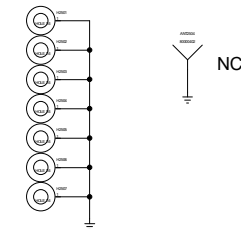
Project: KC30H
Drawn By:
All Checked By:
Power Checked By:
Audio Checked:
Sensor Checked By:
Media Checked By:
RELEASED

SIZE: A0
SHEET 14 OF 21
VERSION:
Date: 2024/6/25



Strapping pin

Ball name	Signal name in boot code	Value	Descriptions	Default vale
UTXND	USB_DLOAD_EN	0	USB download(enable)	1(enable)
		1	USB download(disable)	
AUD_DAD0	ARM_BOOT_MD0	000	Reserved	
		001	TLC NAND Toggle	
		010	MLC/BLC NAND Flash(CNF1)	11(enable)
		011	SD boot(enable)	
AUD_DAD0	ARM_BOOT_MD1	100	MLC/BLC NAND Flash Toggle	(MSB=msd, LSB=msl)
		101	Reserved	
ADJ_SCLK	ARM_BOOT_MD2	110	Reserved	
		111	eMMC boot	

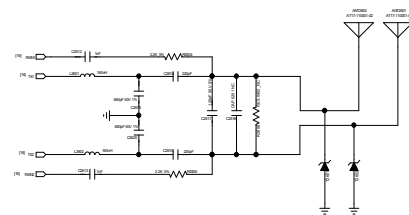
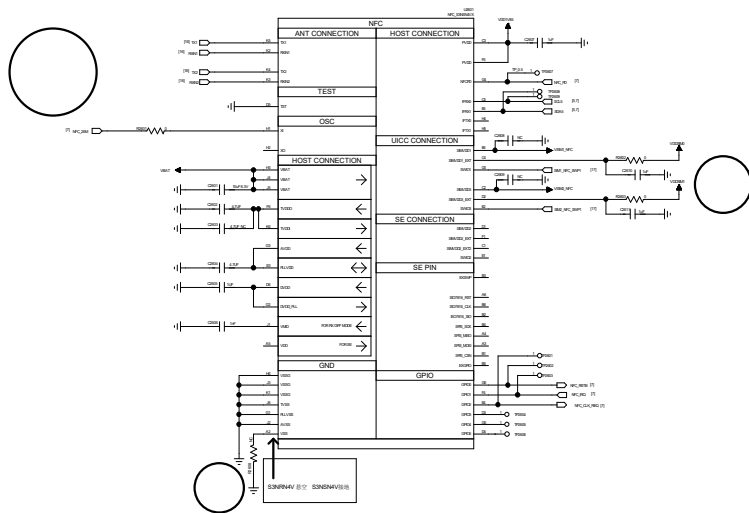


Project: KC30H1
 Drawn By:
 All Checked By:
 Power Checked By:
 Audio Checked:
 Sensor Checked By:
 Media Checked By:
 RELEASED

COMPANY: **PRIZE**

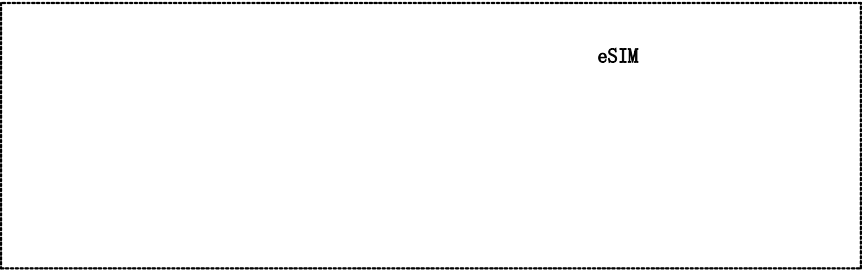
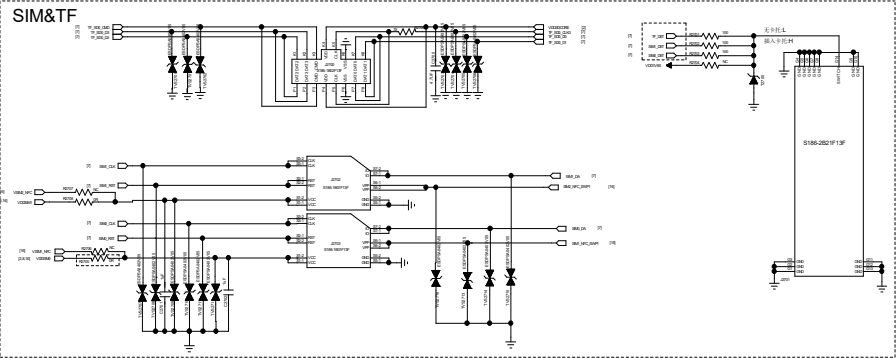
TITLE: 25.BATCONN_TSETPOINT

SIZE: A0 SHEET 15 OF 21 VERSION:
 Date: 2024/6/25



Project	EC3001
Drawn By	
DT Checked By	<RF CHECKED BY>
Power Checked By	<Power checked by>
Audio Checked	<Audio checked by>
Camera Checked By	<Camera checked by>
Media Checked By	<Media checked by>
RELEASED	

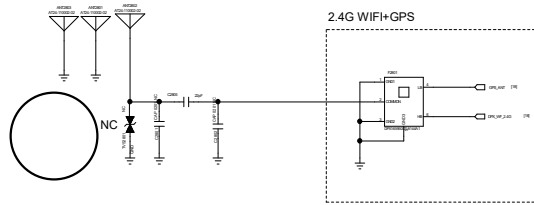
COMPANY:	PRIZE
TITLE	26.NFC
SIZE: A0	SHEET 16 OF 21
Date:	2024/6/25



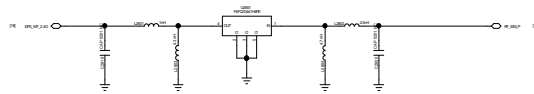
Project	KC3001
Drawn By	
Alt. Checked By	<BPP CHECKED BY>
Power Checked By	<Power checked by>
Audio Checked	<audio checked by>
Control Checked By	<Control checked by>
Media Checked By	<media checked by>
RELEASED	

COMPANY: PRIZE	
TITLE 27.T_FLASH_SIM	
SIZE: A0	SHEET 17 OF 21
Date: 2024/6/25	VERSION: <Version number>

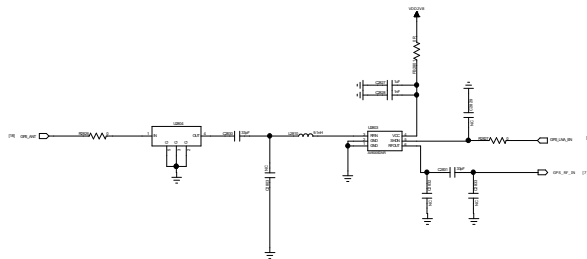
WCN



WIFI/BT SAW



GPS



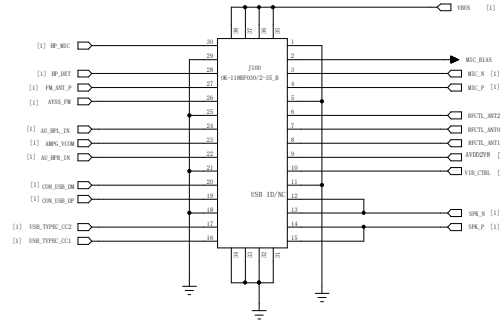
Project: K0301
 Drawn By:
 All Checked By:
 Power Checked By:
 Audio Checked:
 Sensor Checked By:
 Media Checked By:
 RELEASED

COMPANY: **PRIZE**

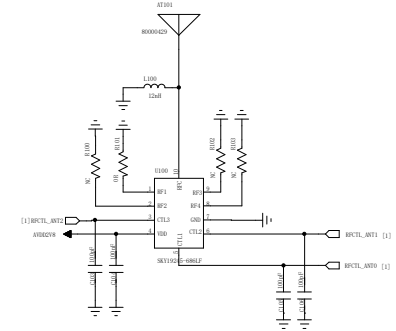
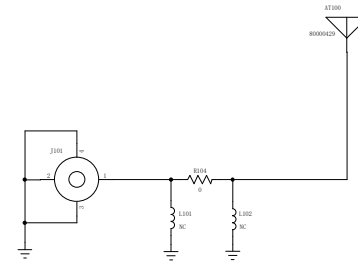
TITLE: 28.BT/WIFI/FM/GPS

SIZE: A0
 SHEET 18 OF 21
 VERSION:
 Date: 2024/6/25

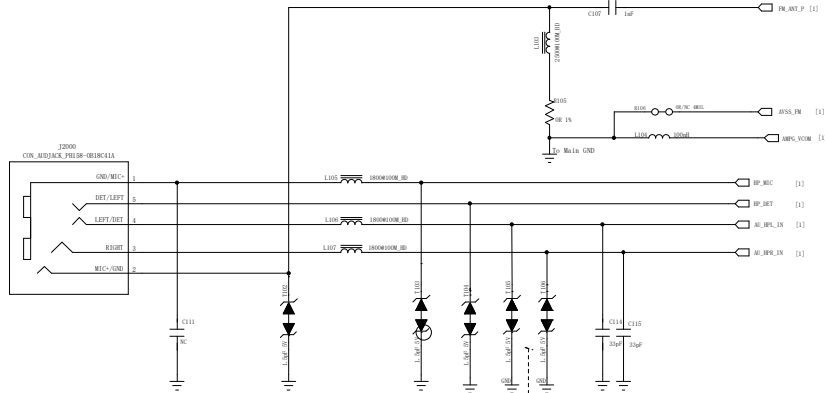
TO Main PCB



ANT

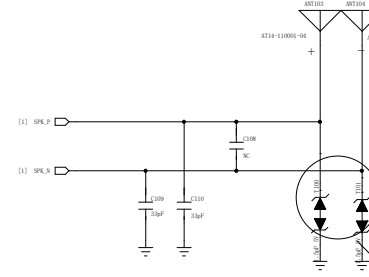


Earphone



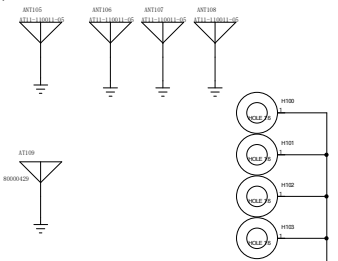
TVS CAP < 0.5pf

Speaker

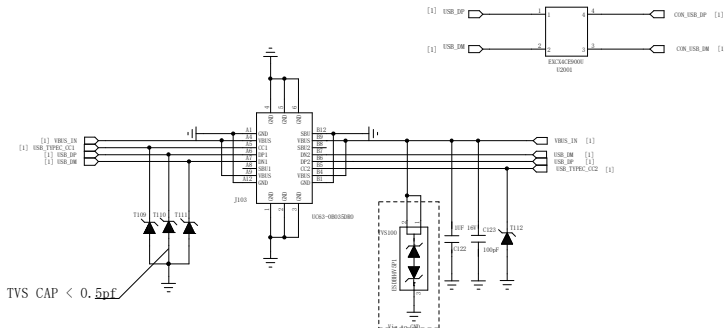


TVS管耐压值 >12V
701060077&ESD9V12BA

接地弹片

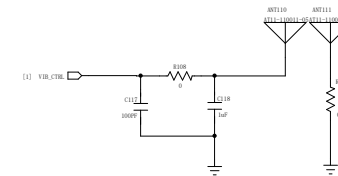


MICRO USB

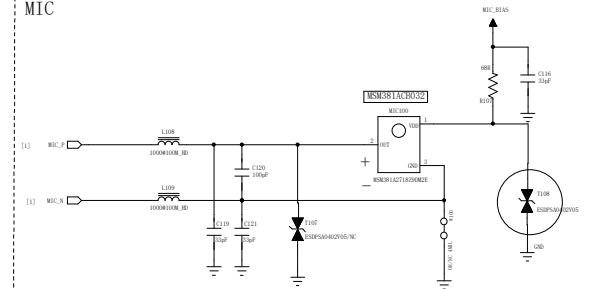


TVS CAP < 0.5pf

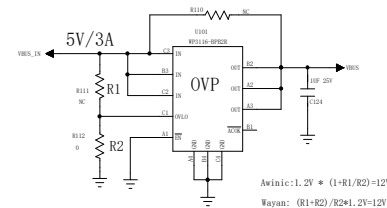
Vibr



MIC



OVP



Awinc:1.2V * (1+R1/R2)=12V
Wayan: (R1+R2)/R2=1.2V=12V

ShieldingCase

COMPANY:

PRIZE

TITLE 1_FPC_MIC_ANT_USB_OVP

SIZE: A1 SHEET 1 OF 2 VERSION: V10

Date: 2024/6/25

Project	C20_SUB_01
Drawn By	DONG
RF Checked By	
Power Checked By	
Audio Checked	
Sensor Checked By	
Media Checked By	
RELEASED	DCC