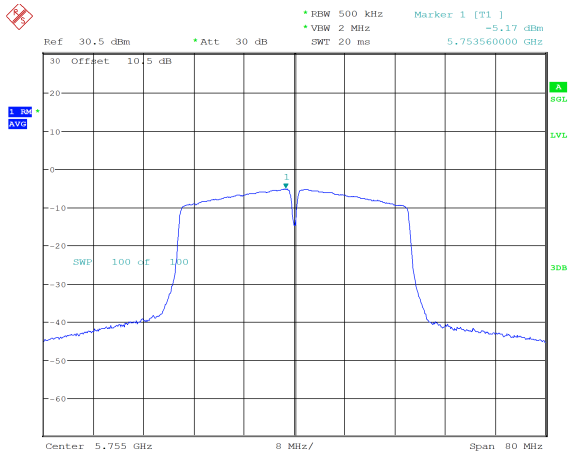
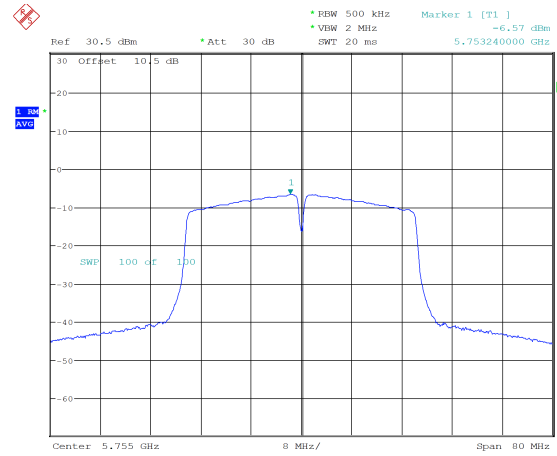


n40_5755MHz_Chain 0 -5.17 dBm



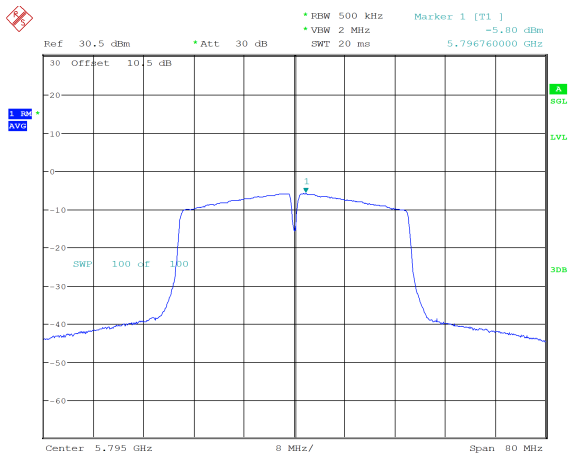
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 18:41:13

n40_5755MHz_Chain 1 -6.57 dBm



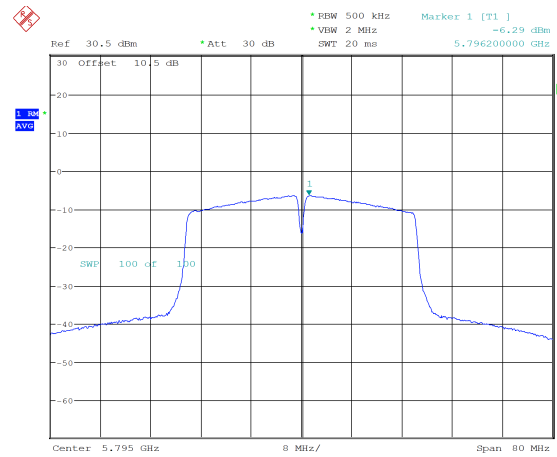
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 20:10:50

n40_5795MHz_Chain 0 -5.80 dBm



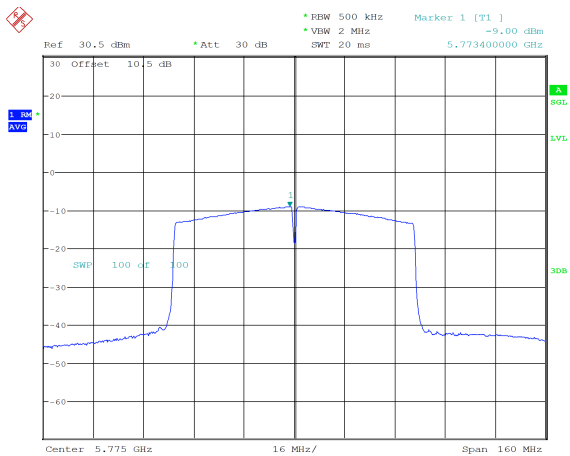
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 18:44:17

n40_5795MHz_Chain 1 -6.29 dBm



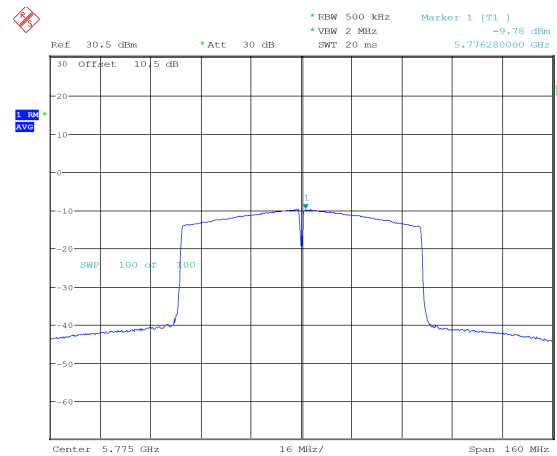
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 20:13:44

ac80_5775MHz_Chain 0 -9.00 dBm



ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 18:46:32

ac80_5775MHz_Chain 1 -9.78 dBm



ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 20:16:00

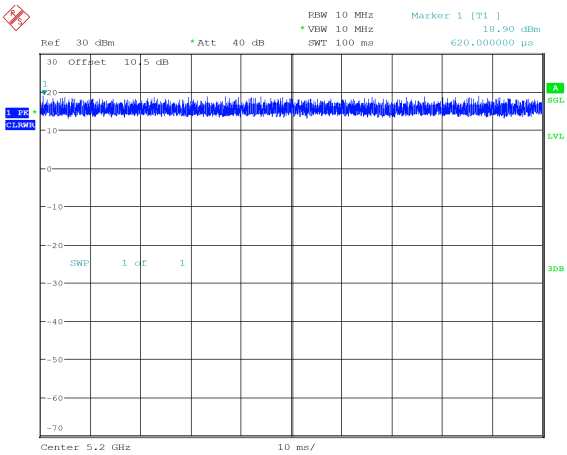
Duty Cycle**5.2G**

Mode	Ton (ms)	Ton+Toff (ms)	Duty Cycle (%)	Duty Cycle Factor (dB)	1/Ton (Hz)	VBW Setting (kHz)
a_5200MHz_Chain 0	100	100	100	0	/	0.01
n20_5200MHz_Chain 0	100	100	100	0	/	0.01
n40_5190MHz_Chain 0	100	100	100	0	/	0.01
ac80_5210MHz_Chain 0	100	100	100	0	/	0.01

Duty Cycle = Ton/(Ton+Toff)*100%

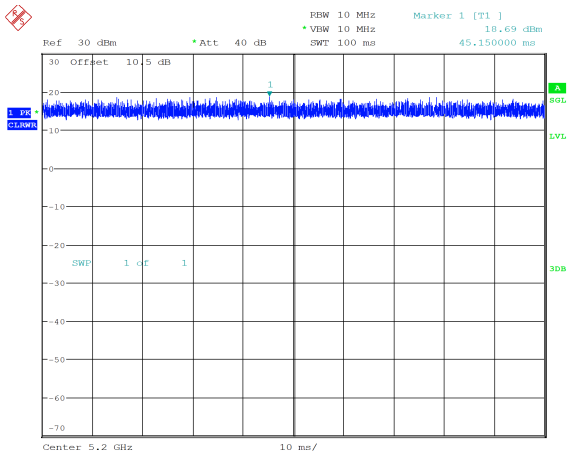
5.2G

a_5200MHz_Chain 0
100.000 ms



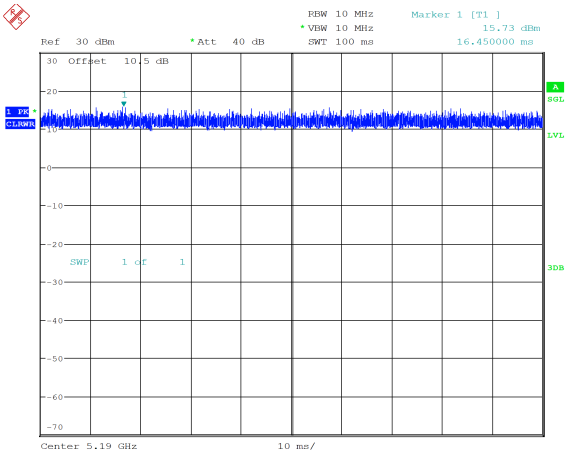
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 17:21:56

n20_5200MHz_Chain 0
100.000 ms



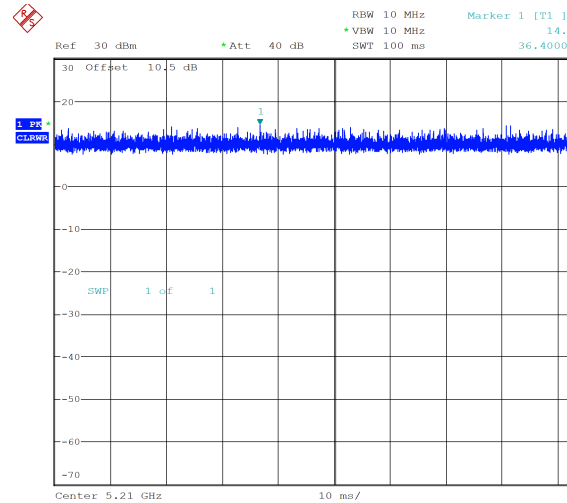
ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 17:29:39

n40_5190MHz_Chain 0
100.000 ms



ProjectNo.:2403S72396E-RF Tester:Chin Qin
Date: 9.MAY.2024 17:36:23

ac80_5210MHz_Chain 0
100.000 ms



Comment: ProjectNo.:2403S72396E-RF Tester:Arthur Su
Date: 30.APR.2024 11:18:20