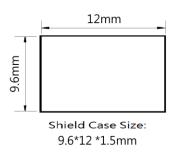
## <u>HY-40R204PC / MD40R204SR6PC BLE 5.0 Bluetooth module</u> (PCB Helix Antenna , 16 pin, with shield case) specifications:

(1).IC: TTC2640R2 4\*4mm 32pin package IC; Use TI CC2640R2 die chip

(2) .HY-40R204PC FCC ID & IC ID Print Format on the Shield Case:





1st row: Mode: HY-40R204PC

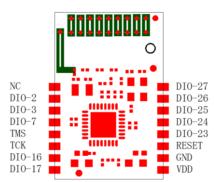
2nd row: FCC ID: 2ADXE-HY-40R204PC

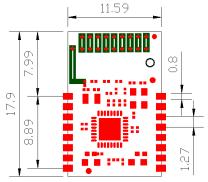
3rd row: IC: 23267-HY40R204PC

(3): HY-40R204PC / WMD40R204SR6PC PCB Helix Antenna, with shield case; (PCBA dimension size: 17.9\*11.59\*2.6 mm).









(4). Pin function table (input and output ports description)

Pin	Name	Type	Function Description		
1	NC	NC	Not connect		
2	DIO_2	Digital I/O	UART RX; GPIO, Sensor Controller (I:4mA max),		
3	DIO_3	Digital I/O	UART TX; GPIO, Sensor Controller (I:4mA max)		
4	DIO_7	Digital I/O	Wake Up , Don't floating. GPIO, Sensor Controller. high-drive capability (8mA max).		
5	JTAG TMSC	Digital I/O	JTAG TMSC; high-drive capability		
6	JTAG TCKC	Digital I/O	JTAG TCKC		

7	DIO_16	D: -: + -1 T/O	GPIO, JTAG_TDO,		
7	TDO	Digital I/O	high-drive capability(8mA max).		
0	DIO_17	D::. 1 I/O	GPIO, JTAG_TDI.		
8	TDI	Digital I/O	high-drive capability(8mA max).		
9	VDD	Power	+2.4V to +3.0V Power Supply		
10	GND	Power GND	Ground		
11	RESET_N	Digital input	Reset, active-low. Module have pull up.		
12	DIO_23	Digital I/O	GPIO, Sensor Controller, Analog(I: 4mA max)		
13	DIO_24	Digital I/O	GPIO, Sensor Controller, Analog(I: 4mA max)		
14	DIO_25	Digital I/O	GPIO, Sensor Controller, Analog(I: 4mA max)		
15	DIO_26	Digital I/O	GPIO, Sensor Controller, Analog(I: 4mA max)		
16	DIO_27	Digital I/O	GPIO, Sensor Controller, Analog(I: 4mA max)		

### (5). Electrical characteristics

(With Ta = 25 °C, VDD = 3.0V with internal DC-DC converter, standard measure:1Mbps GFSK modulation, FRF = 2440MHz Bluetooth Low energy mode.)

- 10-1. Modulation Mode: GFSK
- 10-2. Frequency range: 2402~2480MHZ (2.4G ISM band)
- 10-3. Transmit power setting Range: -21  $^{\sim}$  +5 dBm typical (programmable by software)
- 10-4. Operating ambient temperature range: -40 °C  $^{\sim}$  + 85 °C
- 10-5. The storage temperature range: -40 °C  $^{\sim}$  + 100 °C
- 10-6. The power supply voltage: 2.4  $^{\sim}$  3.3 VDC
- 10-7. Receiver sensitivity: 95dBm typical, SMA connector PER 30.8 %.
- 10-8. Receiving mode current (high gain setting): 6.1 mA (typical)
- 10-9. Transmit mode current (at +5 dBm output setting): 9.1 mA (typical)
- 10-10. Idle. Supply Systems and RAM powered. : 550uA (Typical)
- 10-11. Stand by . With Cache, RTC, CPU, RAM and partial register retention. XOSC\_LF: 3.0 uA (typical).
- 10-12. Shut down. No clocks running, no retention: 150 nA (Typical)

## (6). GPIO DC Characteristics TA = $25^{\circ}$ C, VDDS = 3.0 V

PARAMETER	TEST CONDITIONS	Typical	UNIT
GPIO VOH at 8-mA load	IOCURR = 2, high-drive GPIOs only	2.68	V
GPIO VOL at 8-mA load	IOCURR = 2, high-drive GPIOs only	0. 33	V
GPIO VOH at 4-mA load	IOCURR = 1	2.72	V
GPIO VOL at 4-mA load	IOCURR = 1	0. 28	V

### (7). Timing Requirements

Description			NOM	MAX	UNIT	
Rising supply-voltage slew rate				100	mV/uS	
Falling supply-voltage slew rate				20	mV/uS	
Falling supply-voltage slew rate, with low-power flash settings(1)				3	mV/uS	
Positive temperature gradient in standby(2)	No limitation for negative temperature gradient, or outside standby mode			5	° C/s	
CONTROL INPUT AC CHARACTERISTICS (3)						
RESET_N low duration					uS	

- (1) For smaller coin cell batteries, with high worst-case end-of-life equivalent source resistance, a  $22-\mu$  F VDDS input capacitor must be used to ensure compliance with this slew rate.
- (2) Applications using RCOSC\_LF as sleep timer must also consider the drift in frequency caused by a change in temperature .

## (8). Switching Characteristics

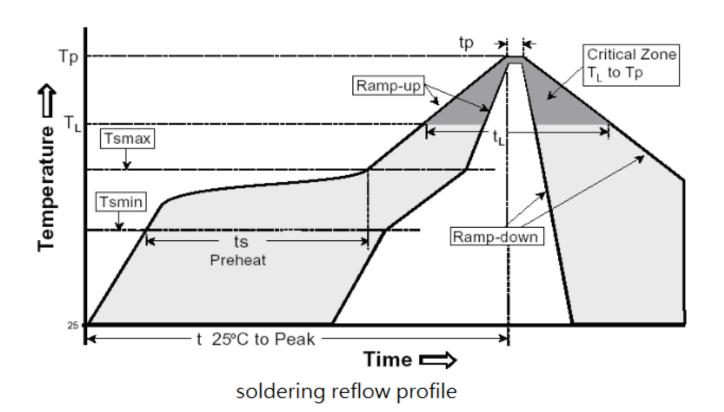
Measured with  $Tc = 25^{\circ} C$ , VDDS = 3.0 V, unless otherwise noted.

PARAMETER	TEST CONDITIONS	MIN	Тур	MAX	UNIT
WAKEUP AND TIMING					
Idle → Active			14		uS
Standby → Active			151		uS
Shutdown → Active			1015		uS

(9). Recommend Reflow profile (Use Leadless: Sn 96.5%, Ag 3%, Cu 0.5%)

# soldering reflow profile

	Sn-Pb Eutect	tic Assembly	Pb-Free Assembly		
Profile Feature	Large Body	Small Body	Large Body	Small Body	
Average ramp-up rate (T <sub>L</sub> to Tp)	3°C/seco	ond max.	3°C/second max.		
Preheat  - Temperature Min (Ts <sub>min</sub> )  - Temperature Max (Ts <sub>max</sub> )  - Time (min to max) (ts)	100 150 60-120 :	)°C	200	0°C 0°C seconds	
Tsmax to T <sub>L</sub> – Ramp-up Rate			3°C/second max		
Time maintained above:  - Temperature (T <sub>L</sub> )  - Time (t <sub>L</sub> )	183°C 60-150 seconds		2		
Peak Temperature (Tp)	225 +0/-5°C	240 +0/-5°C	245 +0/-5°C	250 +0/-5°C	
Time within 5°C of actual Peak Temperature (tp)	10-30 seconds	10-30 seconds	10-30 seconds	20-40 seconds	
Ramp-down Rate	6°C/seco	ond max.	6°C/second max.		
Time 25°C to Peak Temperatur	ur 6 minutes max. 8 minutes max.		es max.		



#### (10). FCC/IC Statements

(OEM) Integrator has to assure compliance of the entire end-product incl. the integrated RF Module. For 15 B (§ 15.107 and if applicable § 15.109) compliance, the host manufacturer is required to show compliance with 15 while the module is installed and operating.

Furthermore the module should be transmitting and the evaluation should confirm that the module's intentional emissions (15C) are compliant (fundamental / out-of-band). Finally the integrator has to apply the appropriate equipment authorization (e.g. Verification) for the new host device per definition in § 15.101.

Integrator is reminded to assure that these installation instructions will not be made available to the end-user of the final host device.

The final host device, into which this RF Module is integrated" has to be labeled with an auxiliary label stating the FCC ID of the RF Module, such as "Contains FCC ID: 2ADXE-HY-40R204PC

"This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions:

- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation."

"Changes or modifications to this unit not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment."

the Integrator will be responsible to satisfy SAR/ RF Exposure requirements, when the module integrated into the host device.

The final host device, into which this RF Module is integrated" has to be labeled with an auxiliary label stating the IC of the RF Module, such as" Contains transmitter module IC: 23267-HY40R204PC

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and(2) this device must accept any interference, including interference that may cause undesired operation of the device.

Le présent appareil est conforme aux CNR d'Industrie Canada applicables auxappareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes :(1) l'appareil ne doit pas produire de brouillage, et(2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre lefonctionnement.

#### Module statement

The single-modular transmitter is a self-contained, physically delineated, component for which compliance can be demonstrated independent of the host operating conditions, and which complies with all eight requirements of § 15.212(a)(1) as summarized below.

- 1) The radio elements have the radio frequency circuitry shielded.
- 2) The module has buffered modulation/data inputs to ensure that the device will comply with Part 15 requirements with any type of input signal.
  - 3) The module contains power supply regulation on the module.
  - 4) The module contains a permanently attached antenna.
  - 5) The module demonstrates compliance in a stand-alone configuration.
  - 6) The module is labeled with its permanently affixed FCC ID label.
- 7) The module complies with all specific rules applicable to the transmitter, including all the conditions provided in the integration instructions by the grantee.
  - 8) The module complies with RF exposure requirements.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- -Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- -Consult the dealer or an experienced radio/TV technician for help