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SIZE:A3

SCHEMATIC

MODEL: HUB63A
TITLE: HUB63A MAIN BOARD
VERSION: V1.0
DRAWING NO.:

SHEET 1 OF 21

DRAWN:
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NORMALIZED:
APPROVED:

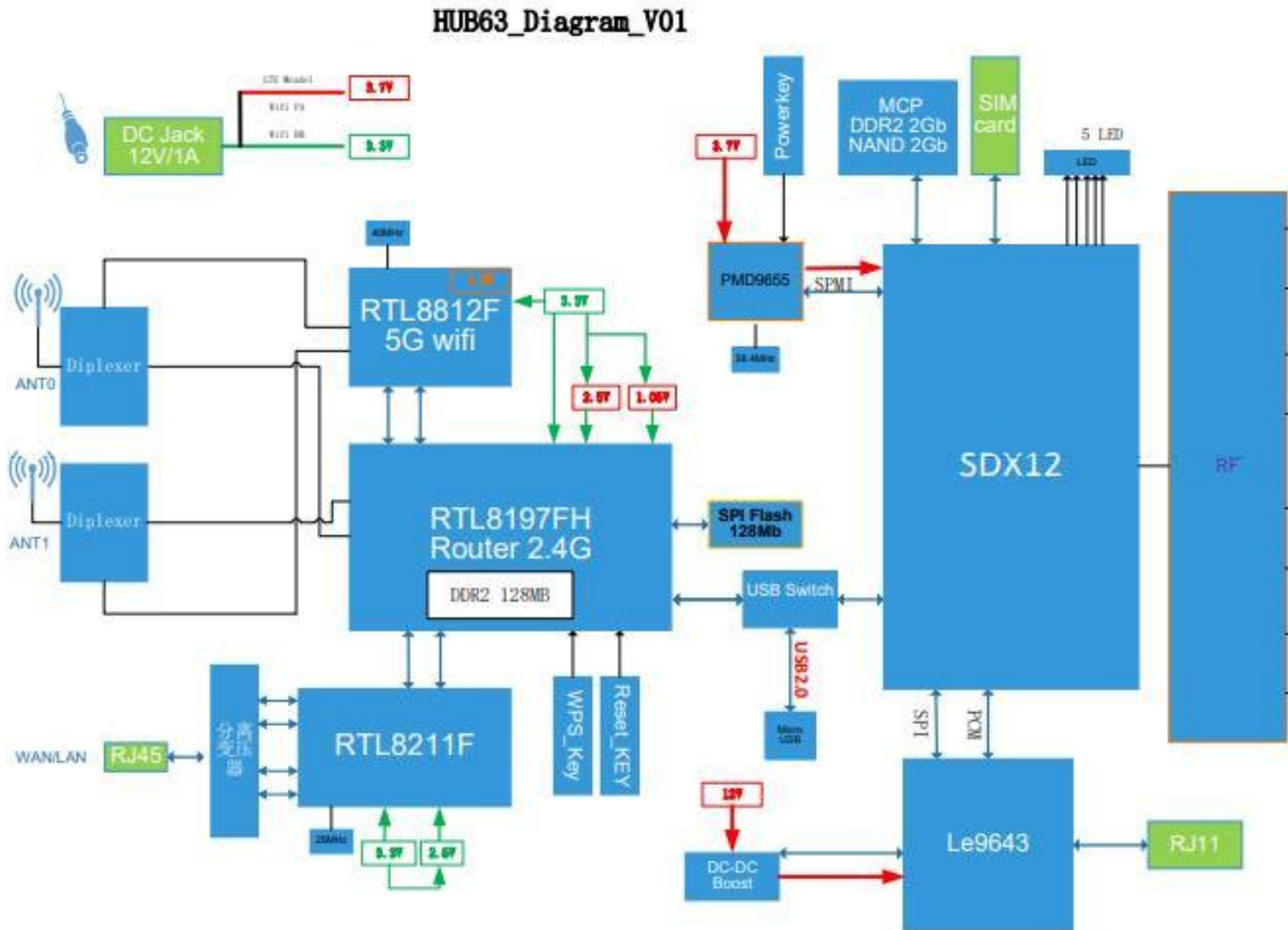
MK CORPORATION

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MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A		SHEET OF 21	
DRAWN		TITLE: MAIN BOARD		VERSION:	V1.0
CHECKED		DRAWING NO.:			

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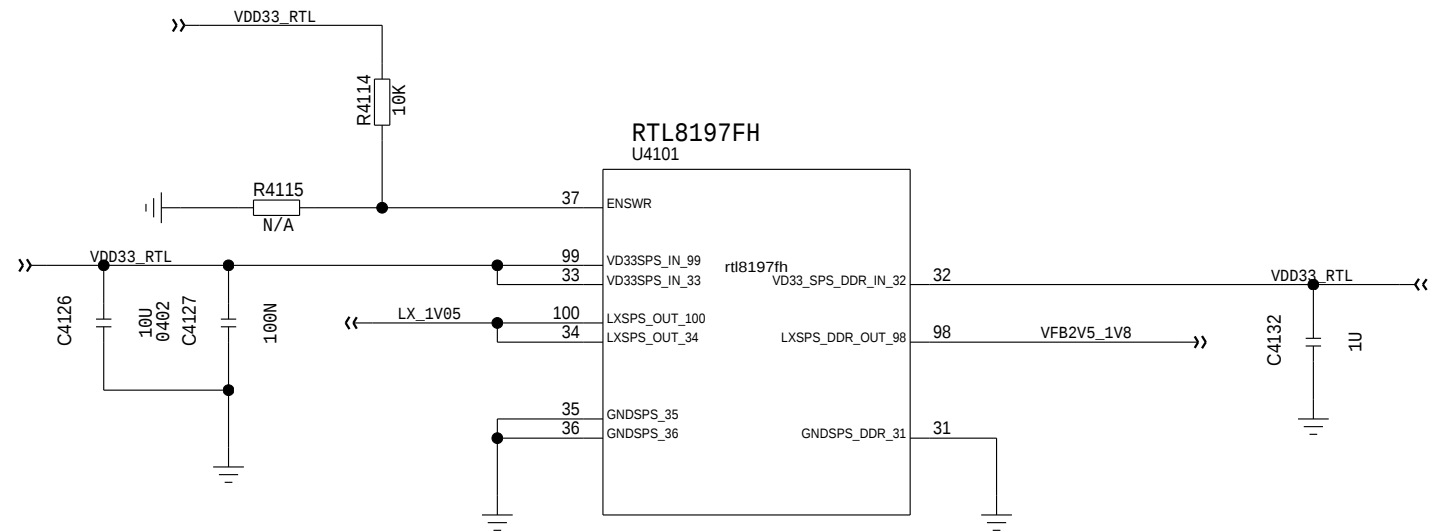
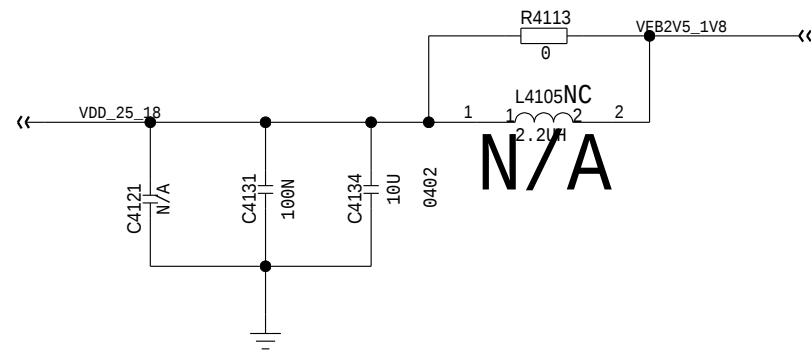
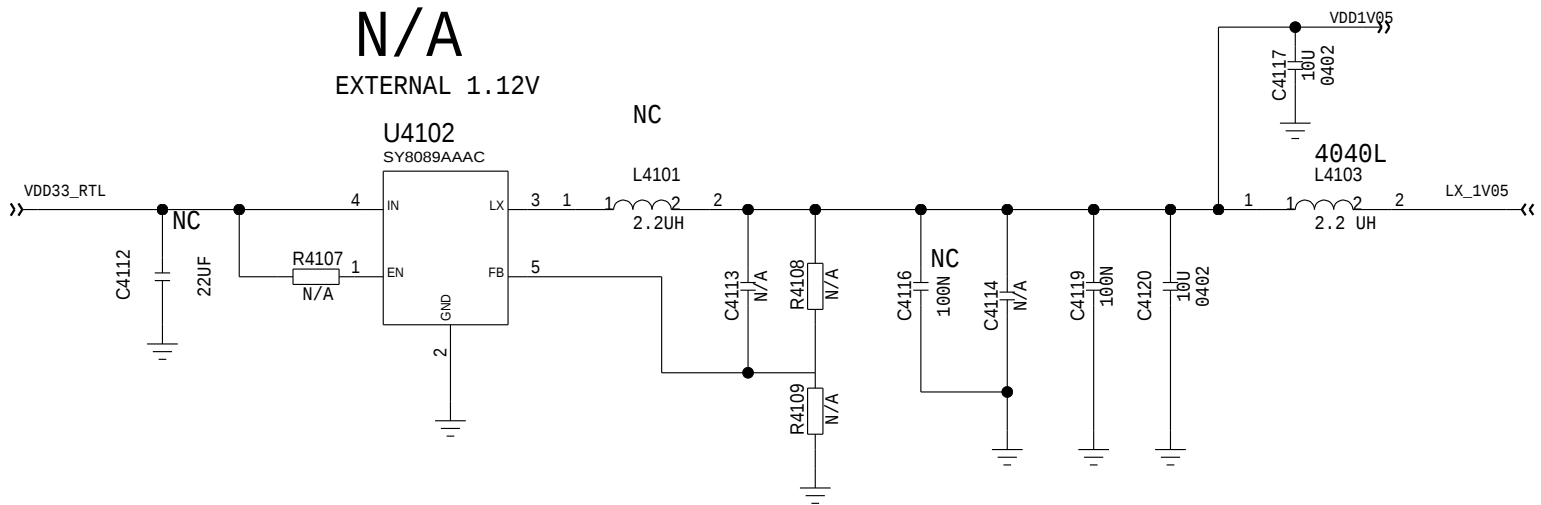
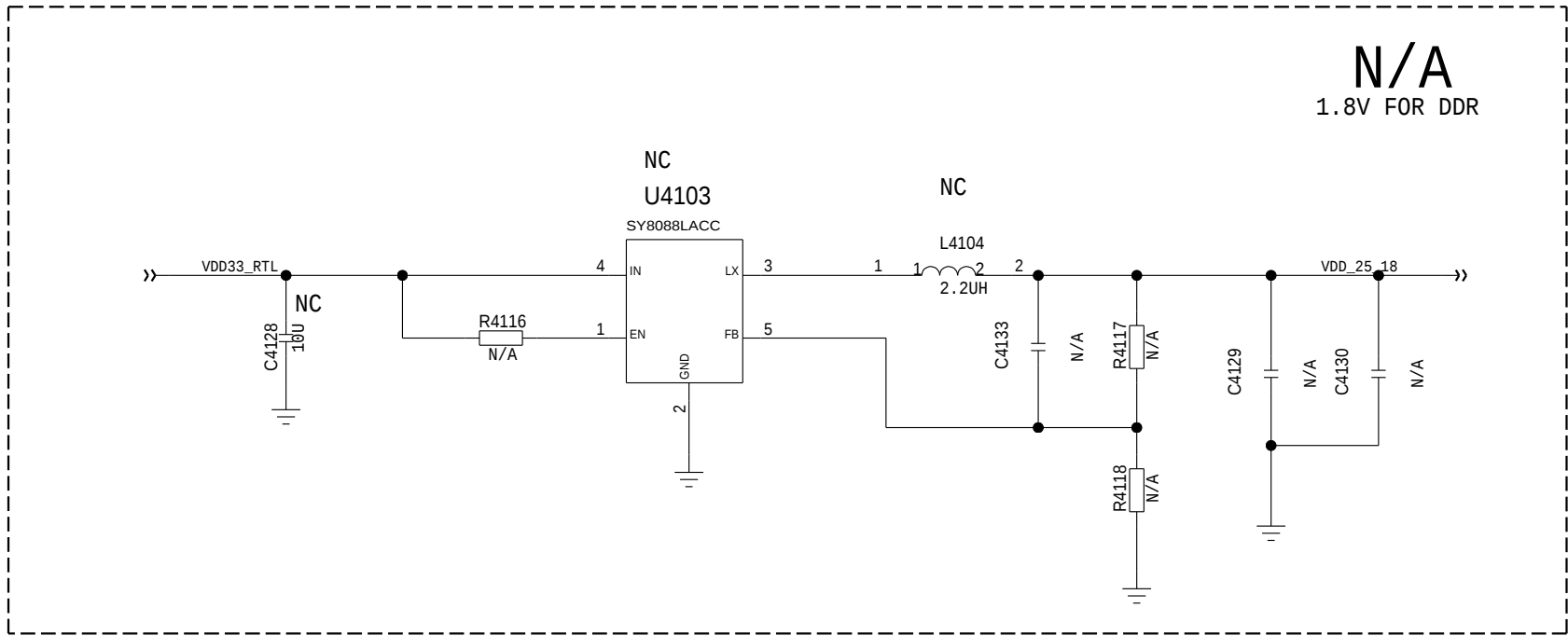
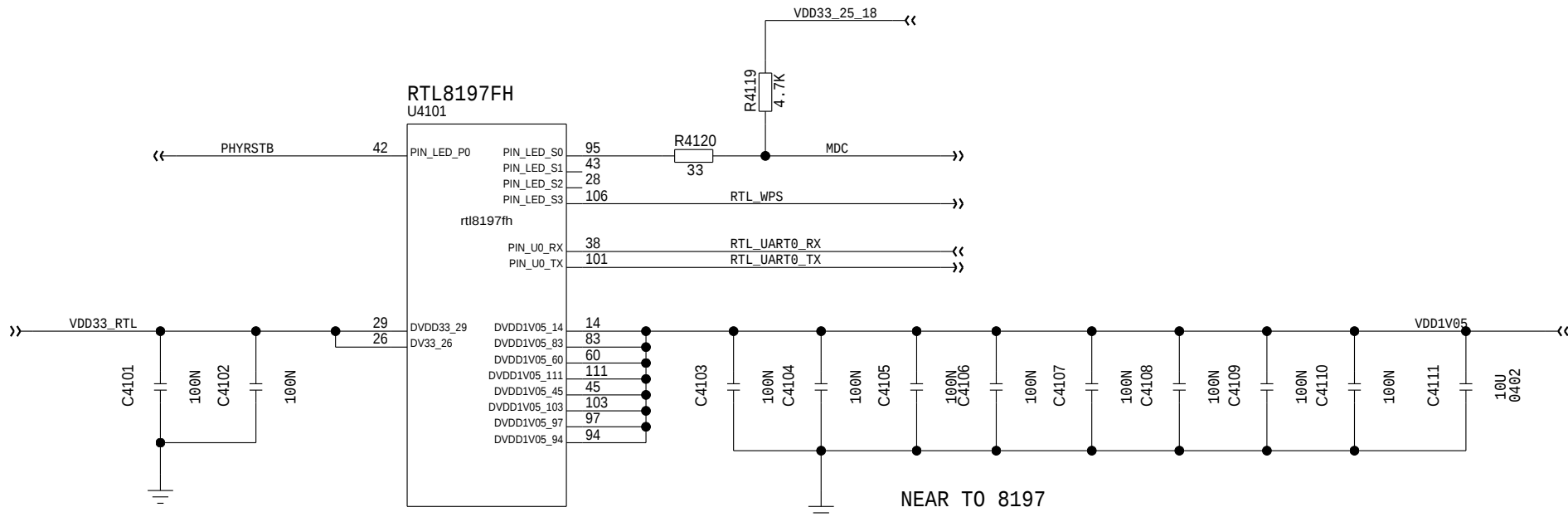
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MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A	SHEET 21	
DRAWN		TITLE: MAIN BOARD		
CHECKED		DRAWING NO.:	VERSION:	V1.0

POWER

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MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A	SHEET 21	
DRAWN		TITLE: MAIN BOARD	VERSION:	V1.0
CHECKED		DRAWING NO.:		

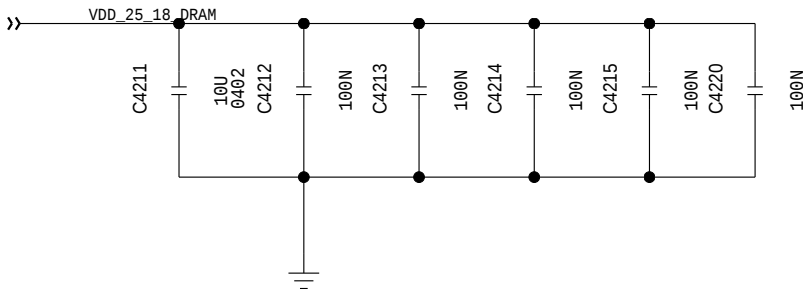
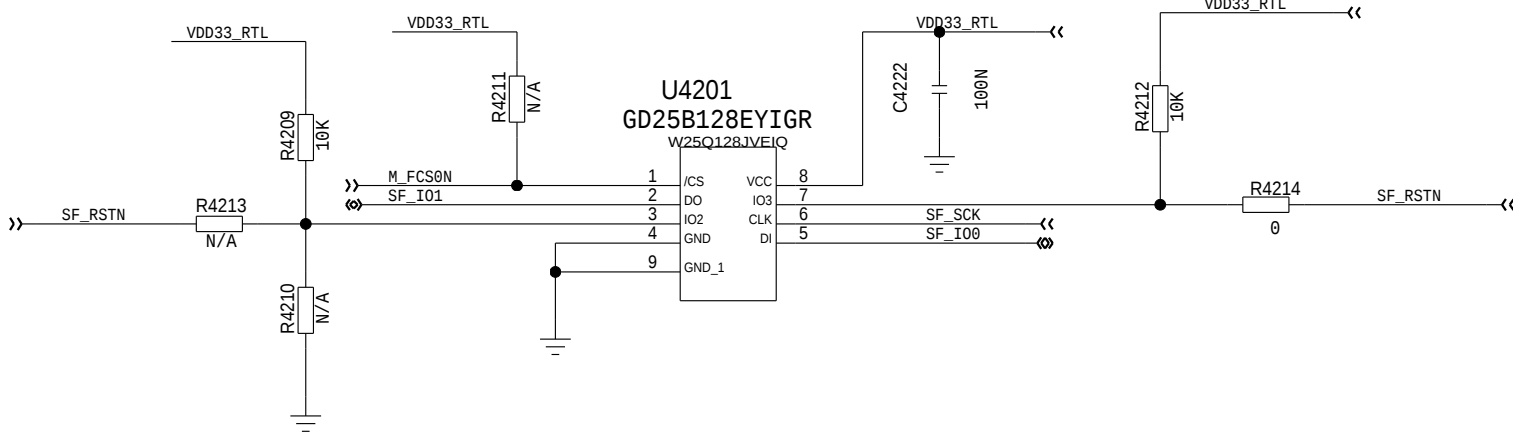
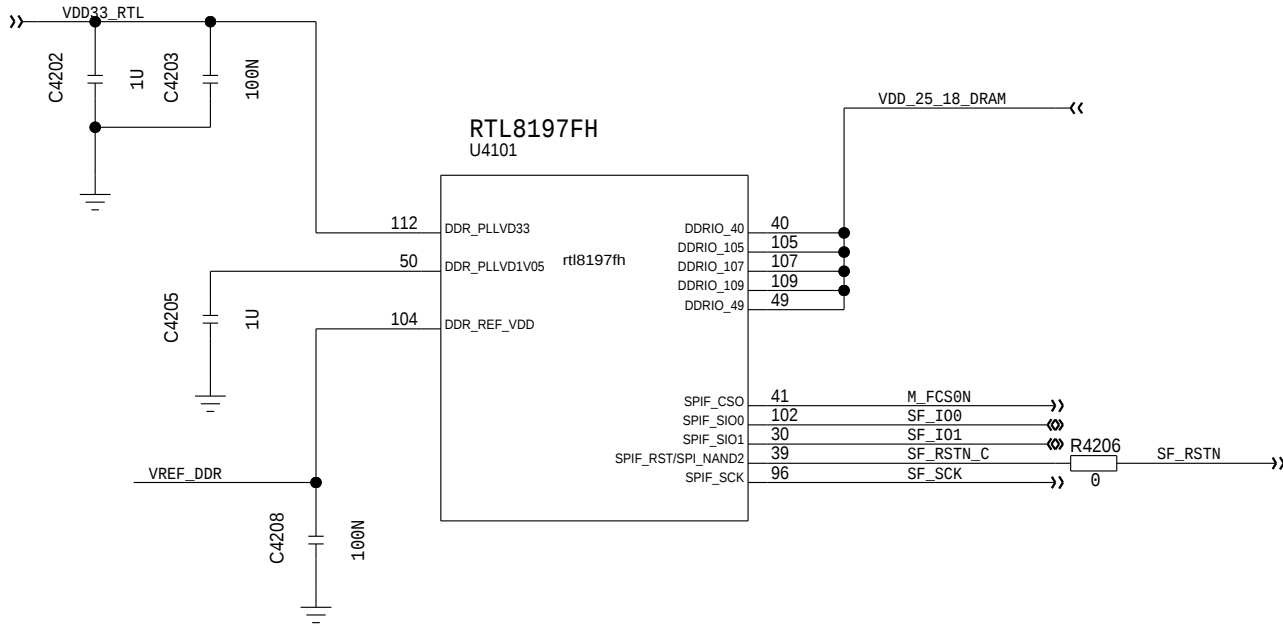
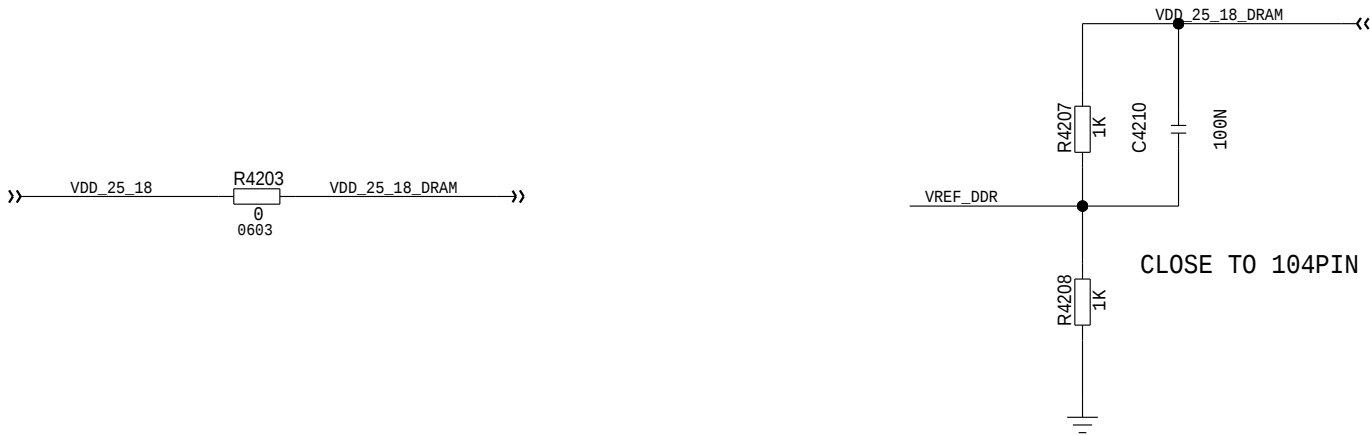
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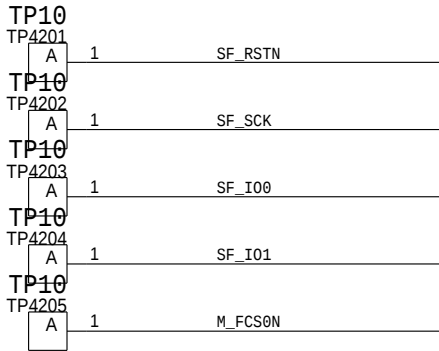
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NOR FLASH



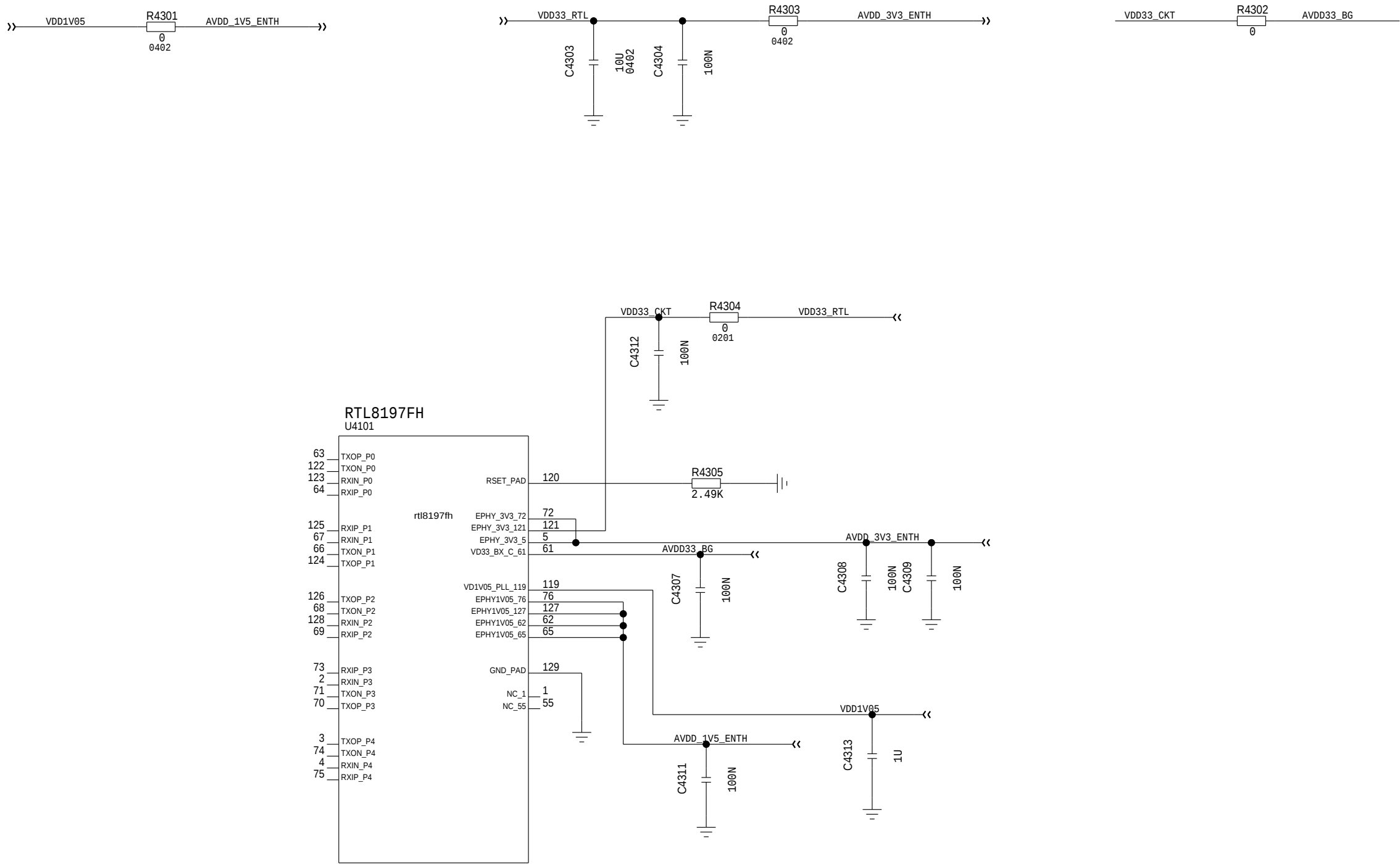
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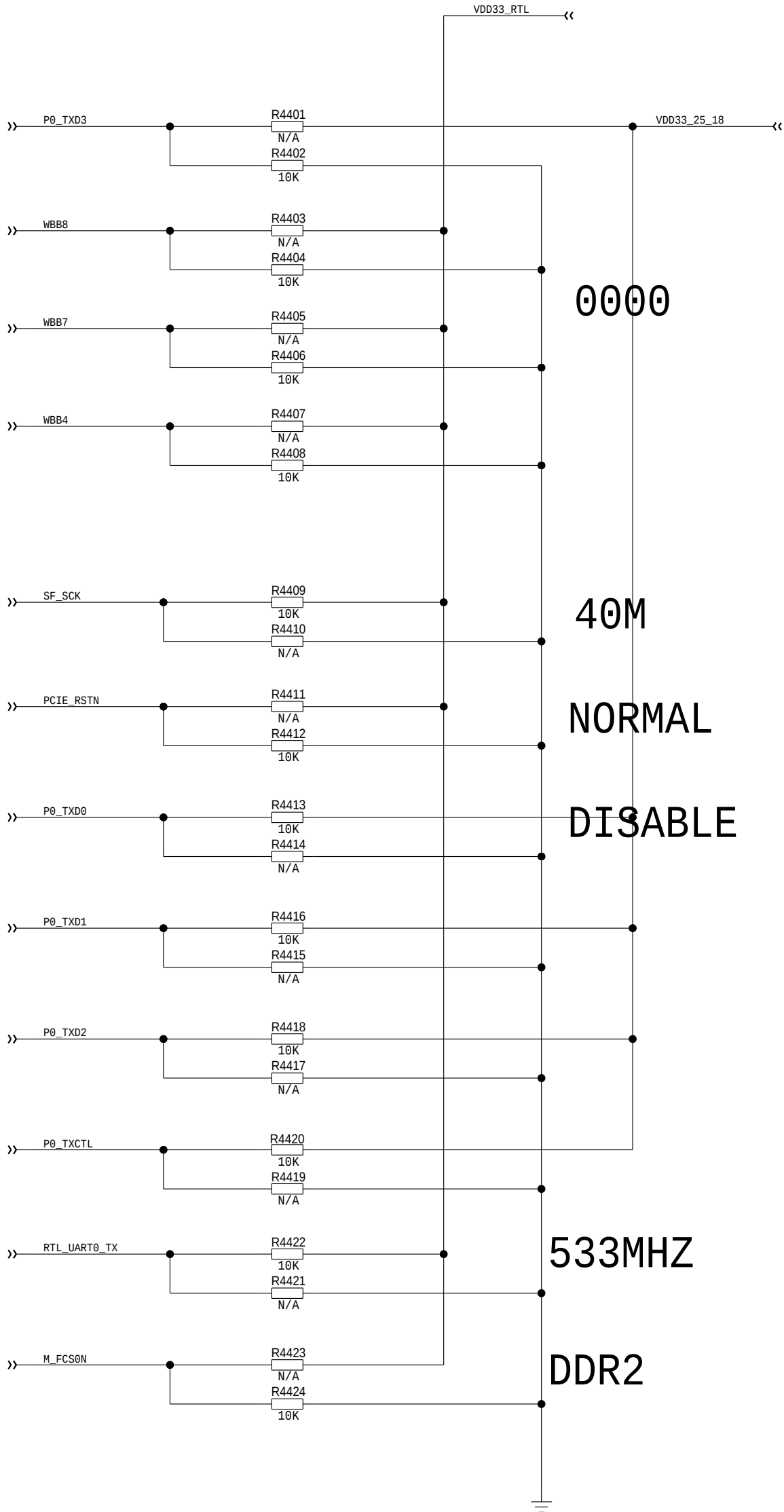
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MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A		SHEET 0F 21	
DRAWN		TITLE: MAIN BOARD		VERSION:	V1.0
CHECKED		DRAWING NO.:			

RTL8197



pin name	function name	function detail
P0_TXD3	boot_sel[3]	boot_select[3:0] 0000: SPI-NOR-Flash booting (3 byte addressing) 0001: [ROM] SPI-NOR-Flashing(3 byte addressing) 0010: [ROM] SPI-NAND-Flash booting 0011: [ROM] Parallel-NAND-Flash booting 0100: [ROM] eMMC 1.8V booting 0101: [ROM] SD booting 0110: [ROM] iNIC USB mode 0111: [ROM] SW-define 1000: [ROM] Switch (Ethernet-Port) + 1GPIO 1001: [ROM] RGMII mode + 1GPIO 1010: SRAM booting: NFBI + RGMII/Ethernet 1011: [ROM] SW-define 1100: [ROM] eMMC 3.3V booting 1101: [ROM] SW-define 1110: SPI-NOR-Flash booting for OLT 1111: loop mode for debug
PIN_WBB8	boot_sel[2]	
PIN_WBB7	boot_sel[1]	
PIN_WBB4	boot_sel[0]	

pad_flash_ck_io	Sel_40m	sel_40m 1=40MHZ 0=25MHZ(default)
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PIN_PCIE_RSTN	Test_mode	strap_test_mode 1=TEST mode 0=Normal mode(default)
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PIN_P0_TXD0	DISABLE_EXT_RSTN	external reset: 0=enable, 1=disable
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PIN_P0_TXD1	Load_Efuse_Value	Load_Efuse_Value 1: disable Load Efuse 0: enable Load Efuse (Referenced by SW)
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PIN_P0_TXD2	ddr_ldo_sel	ddr_ldo_sel 1: LDO, 0:SWR
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PIN_P0_TXCTL	HW_DBG_DISABLE	1: Disable, 0: Enable
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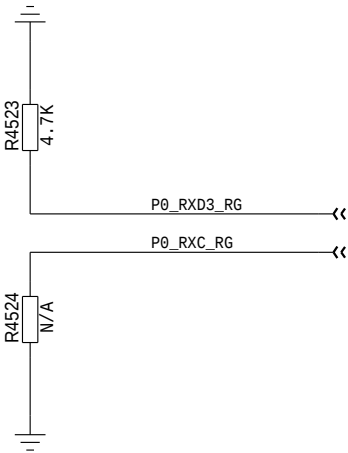
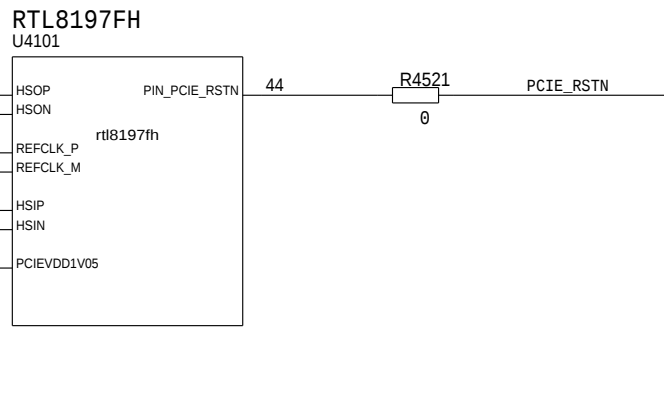
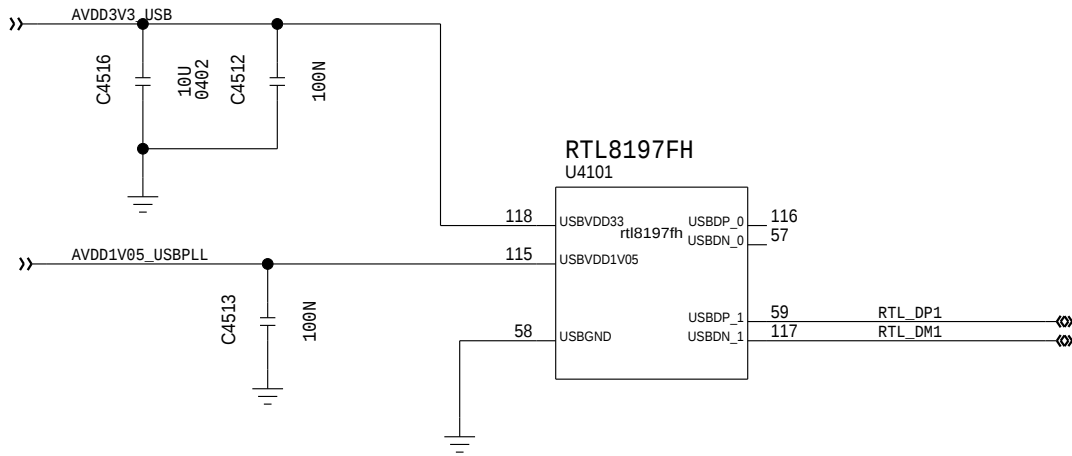
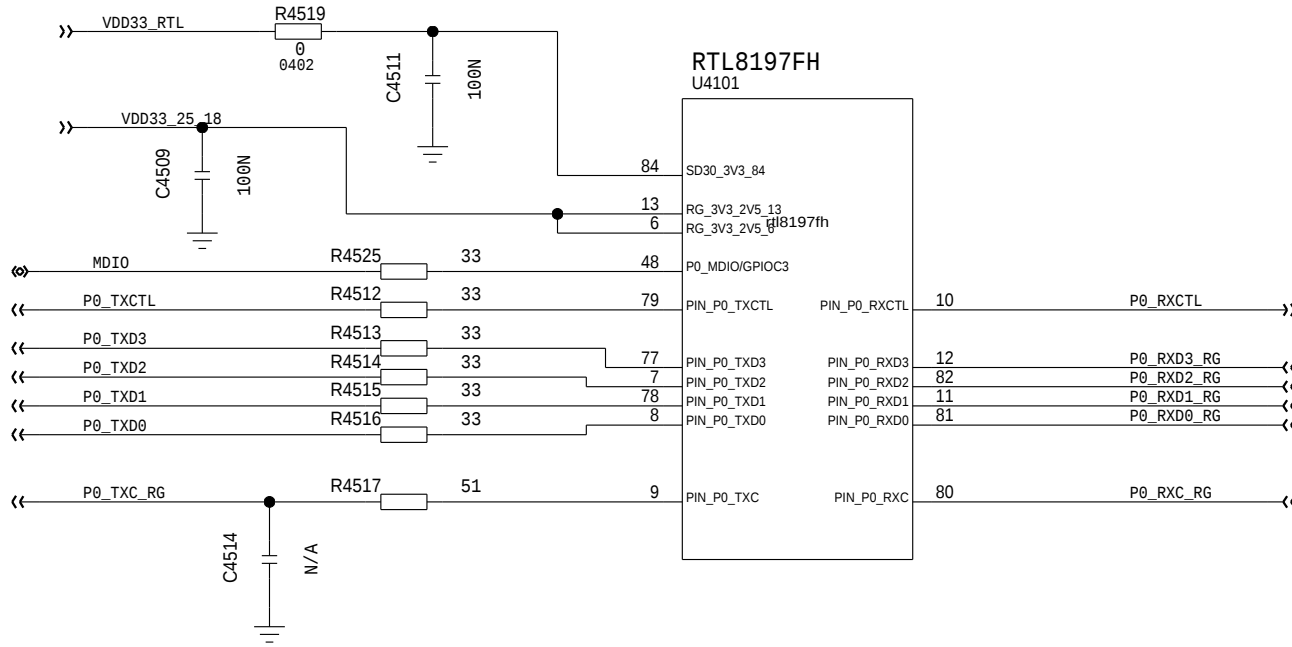
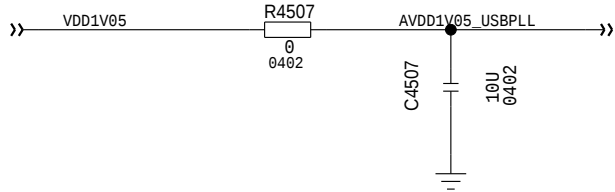
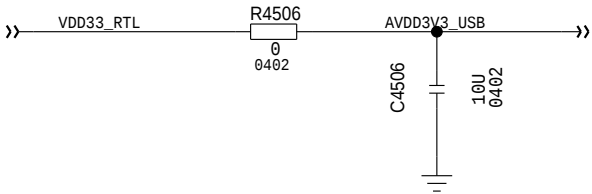
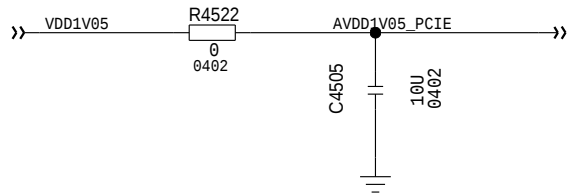
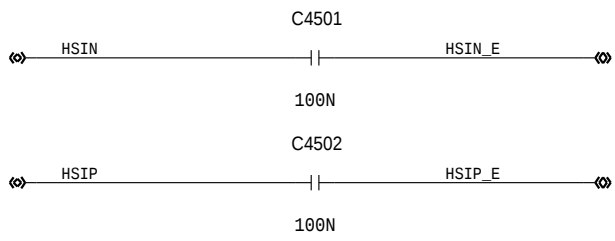
When set ROM booting, P0_TXD1 should set High
= enable Load Efuse

If use external DDR regulator, P0_TXD2 HW strap pin set LDO mode,

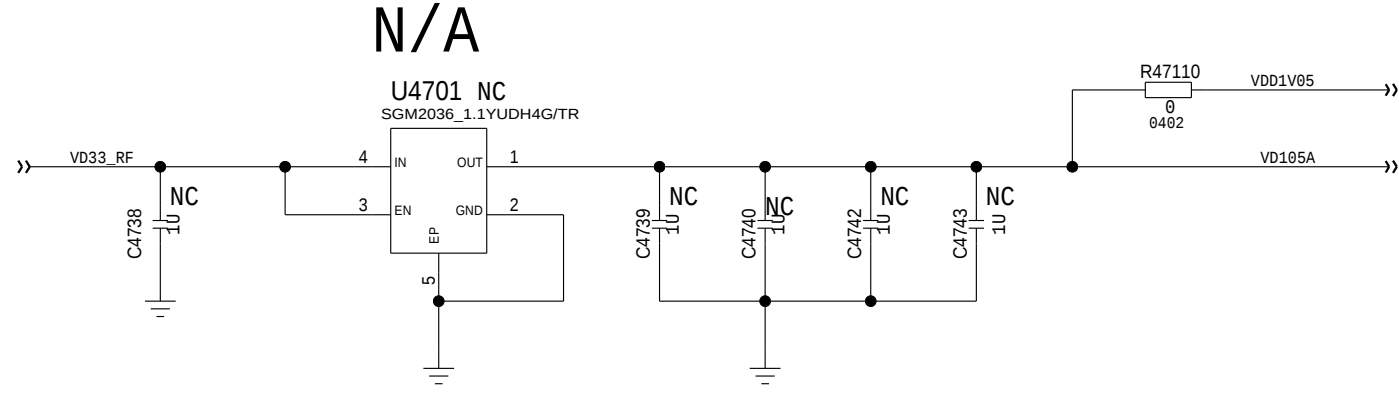
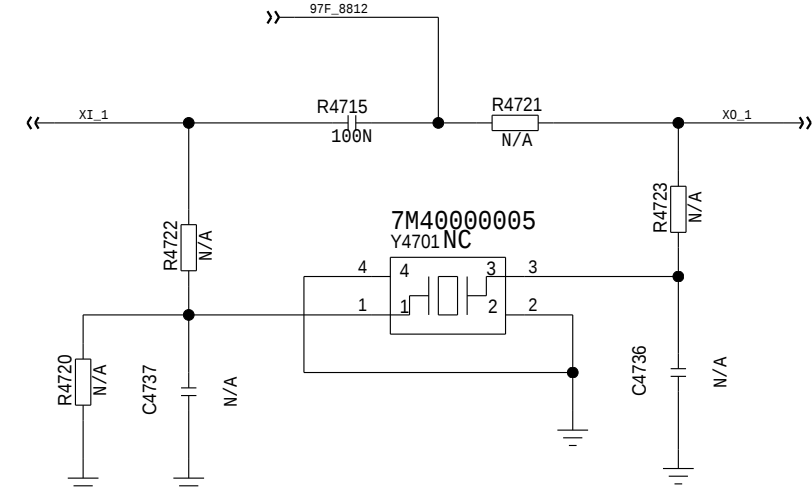
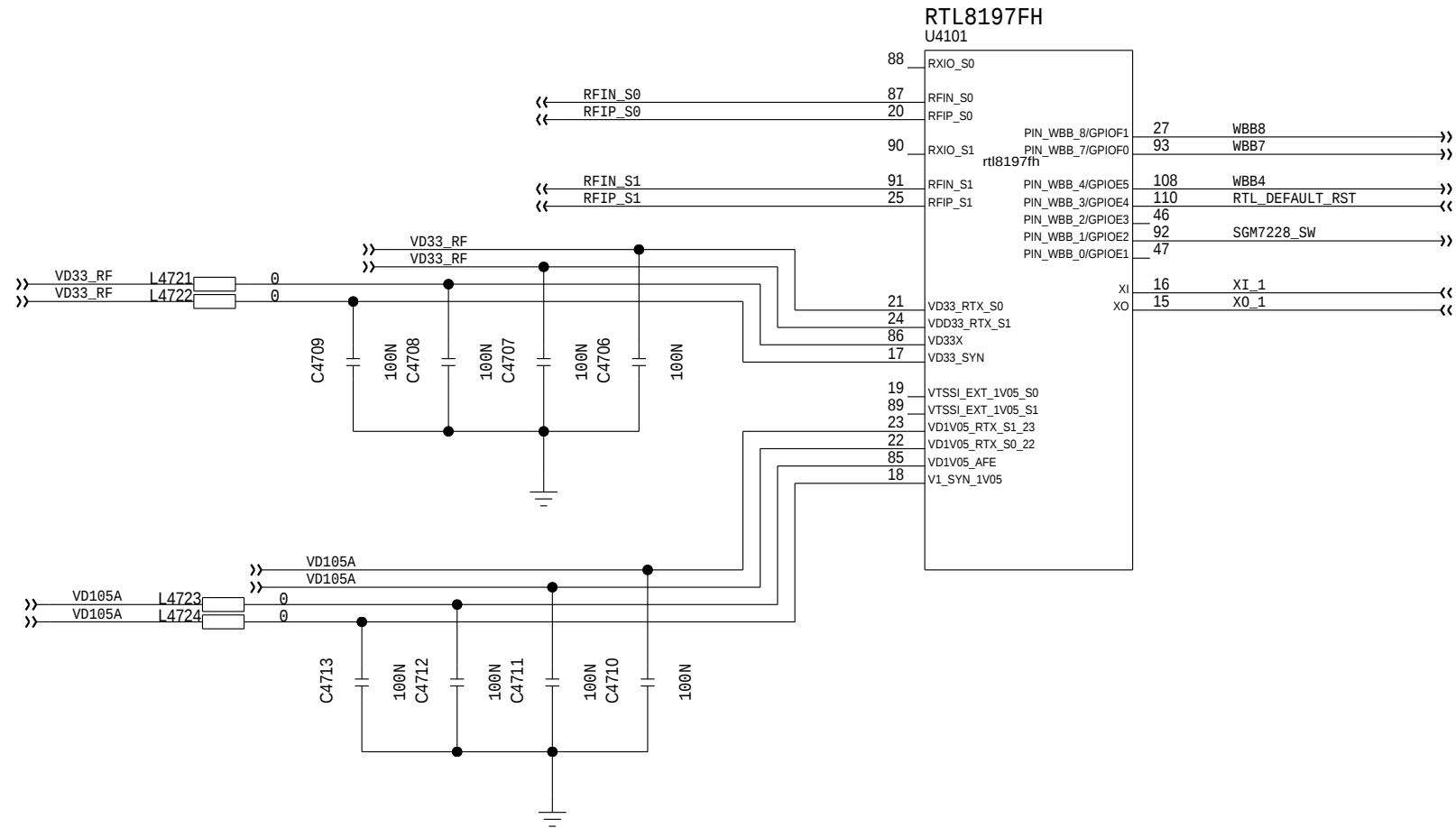
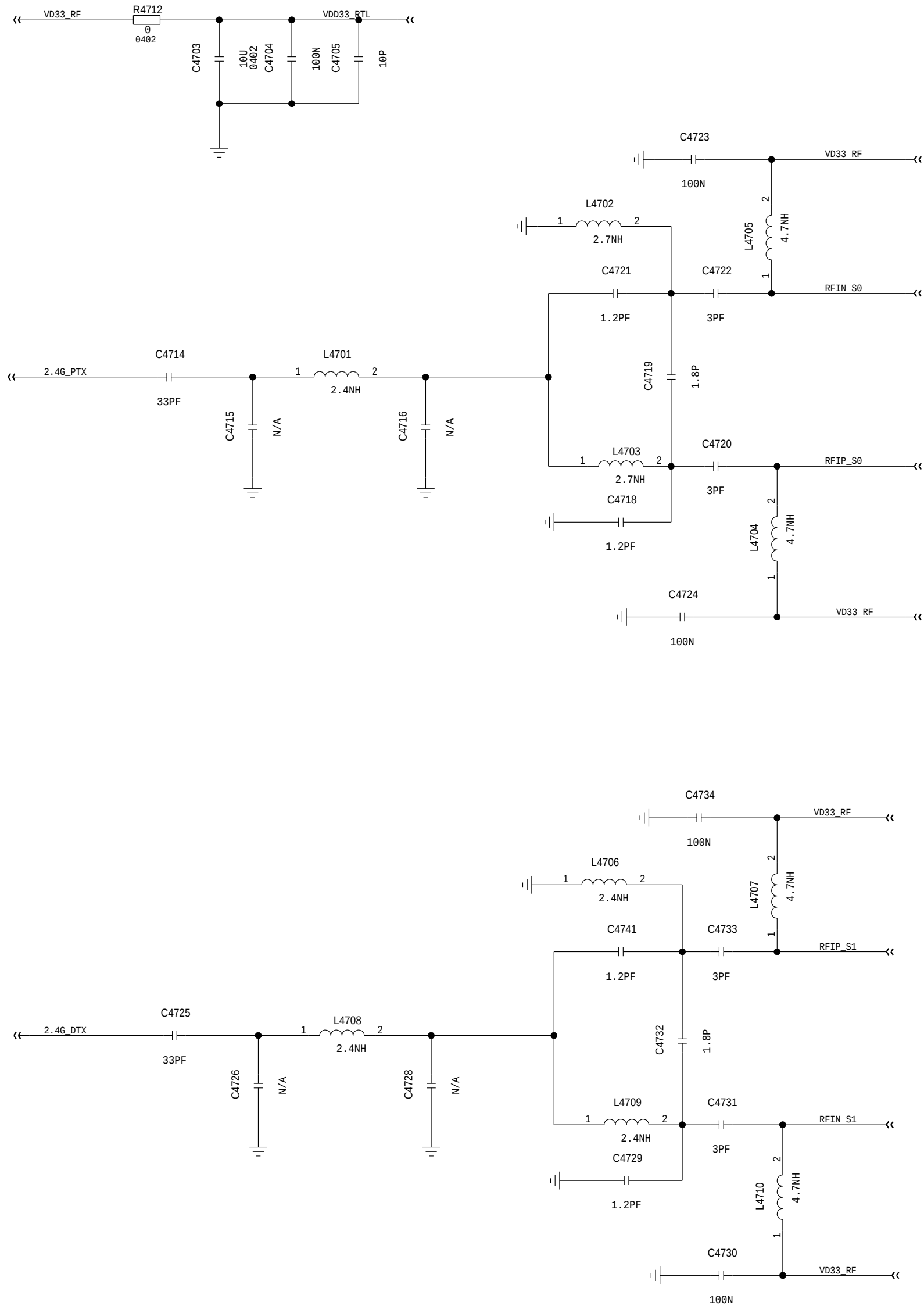
pad_uart0_tx_io	DRAM_FEQ / HW_DBG_OPTION	DRAM_TYPE IF HW_DBG_DISABLE= 1,DRAM_FEQ: 0: DDR2_400MHz, DDR1_200MHz, 1: DDR2_533MHz, DDR1_250MHz (Only Referenced by SW)
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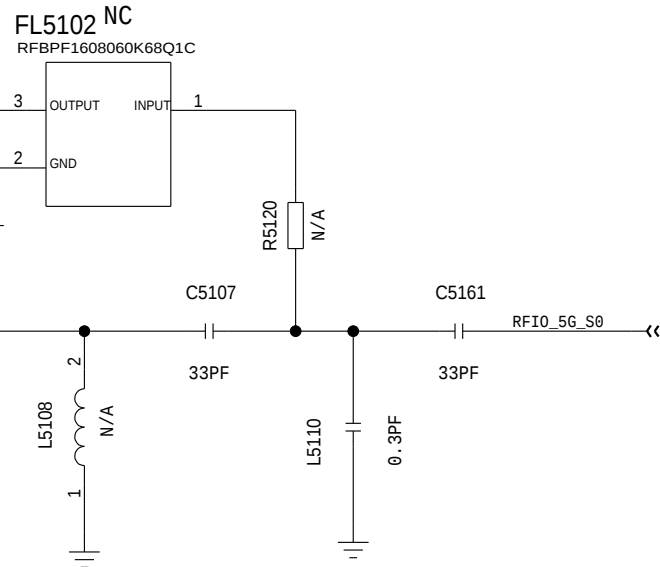
PIN_FCS0_N	DRAM_TYPE[0]_INV	DRAM_TYPE[0]_INV 1=DDR1 (Default) 0=DDR2
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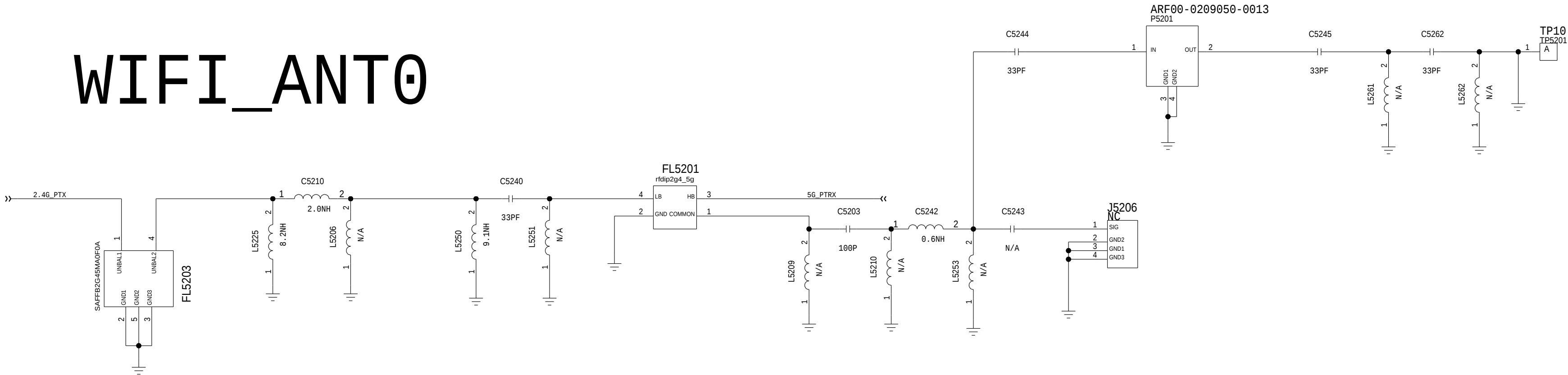


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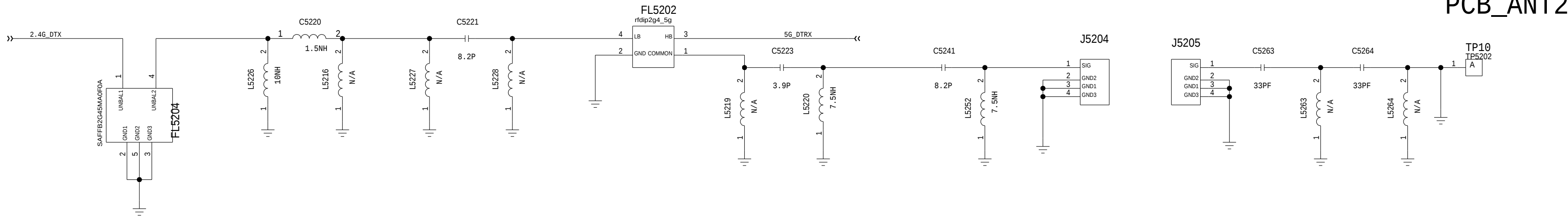
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WIFI_ANT0

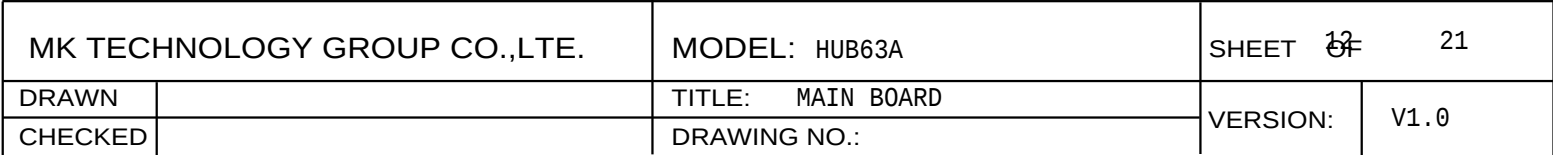


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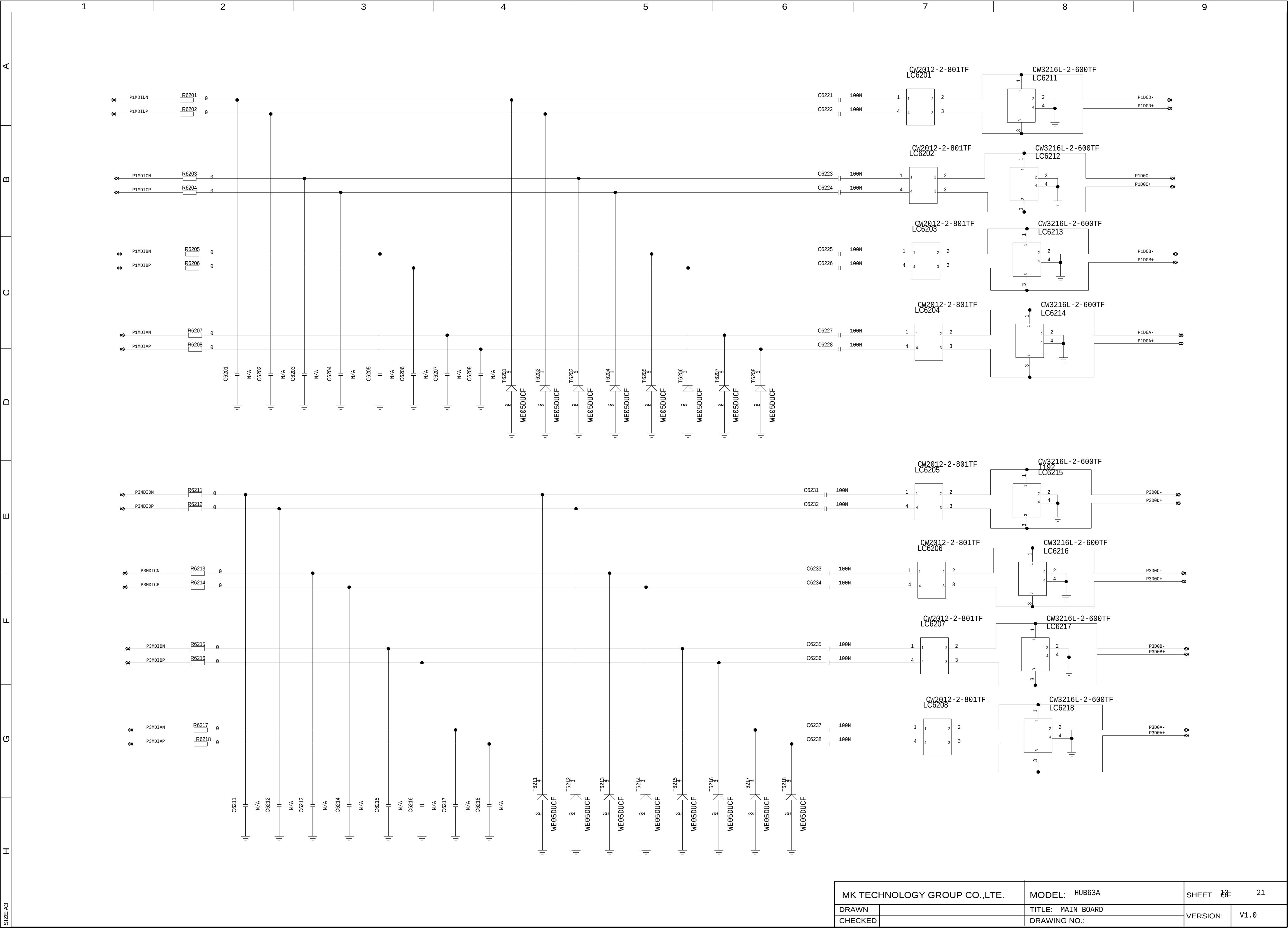
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DRAWN		TITLE: MAIN BOARD	VERSION:	V1.0
CHECKED		DRAWING NO.:		

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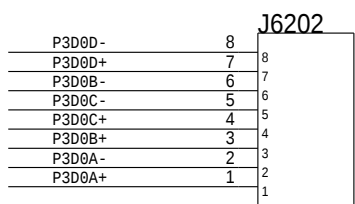
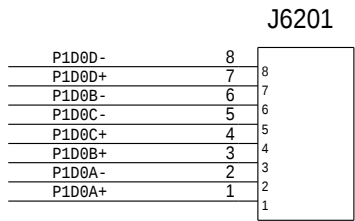
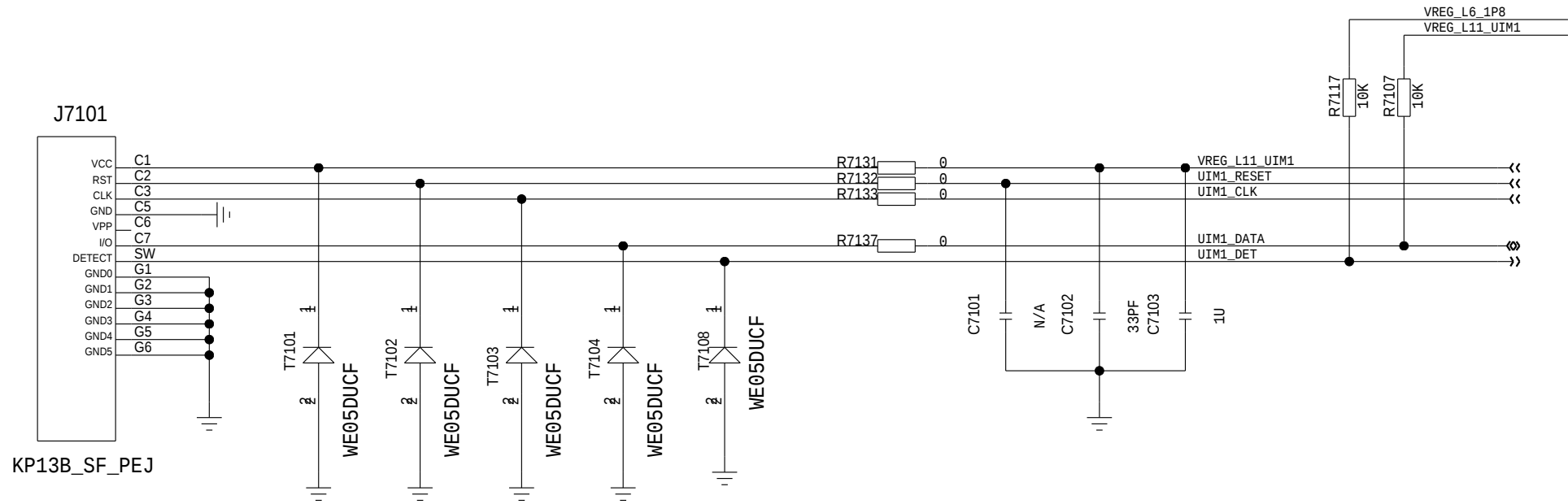
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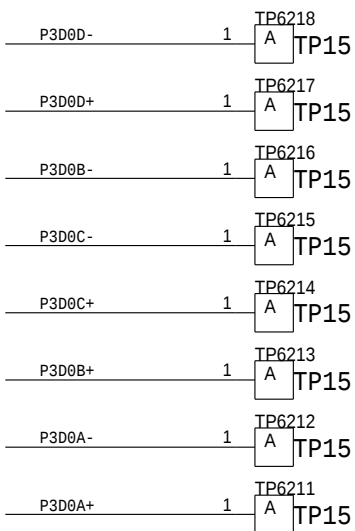
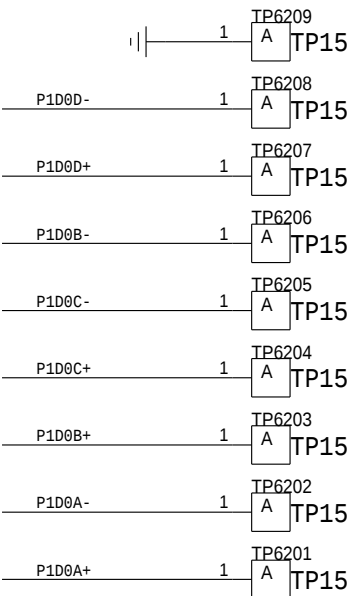
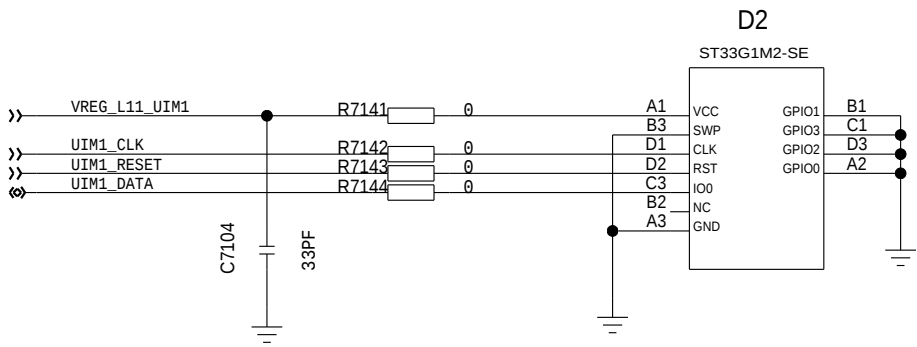
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SIM CARD

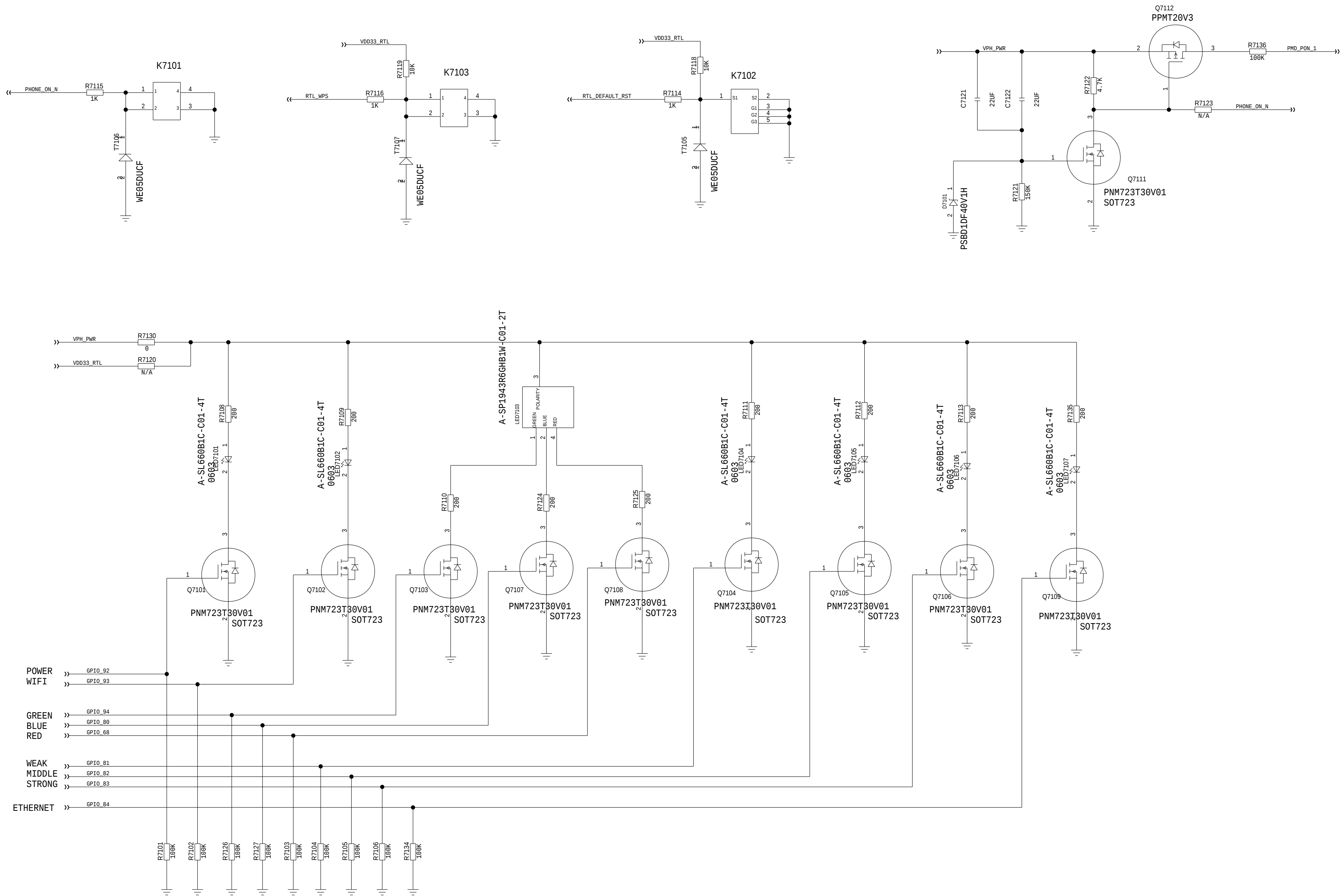
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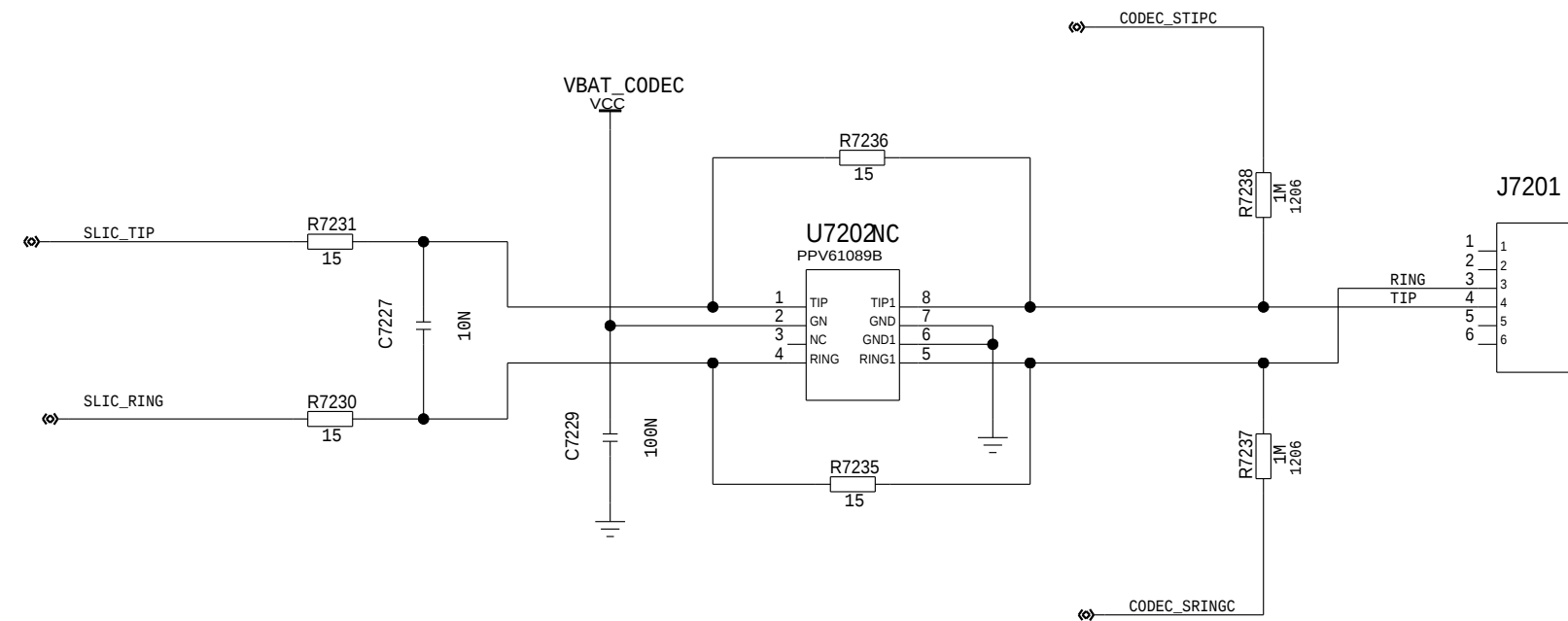


LED & KEYS



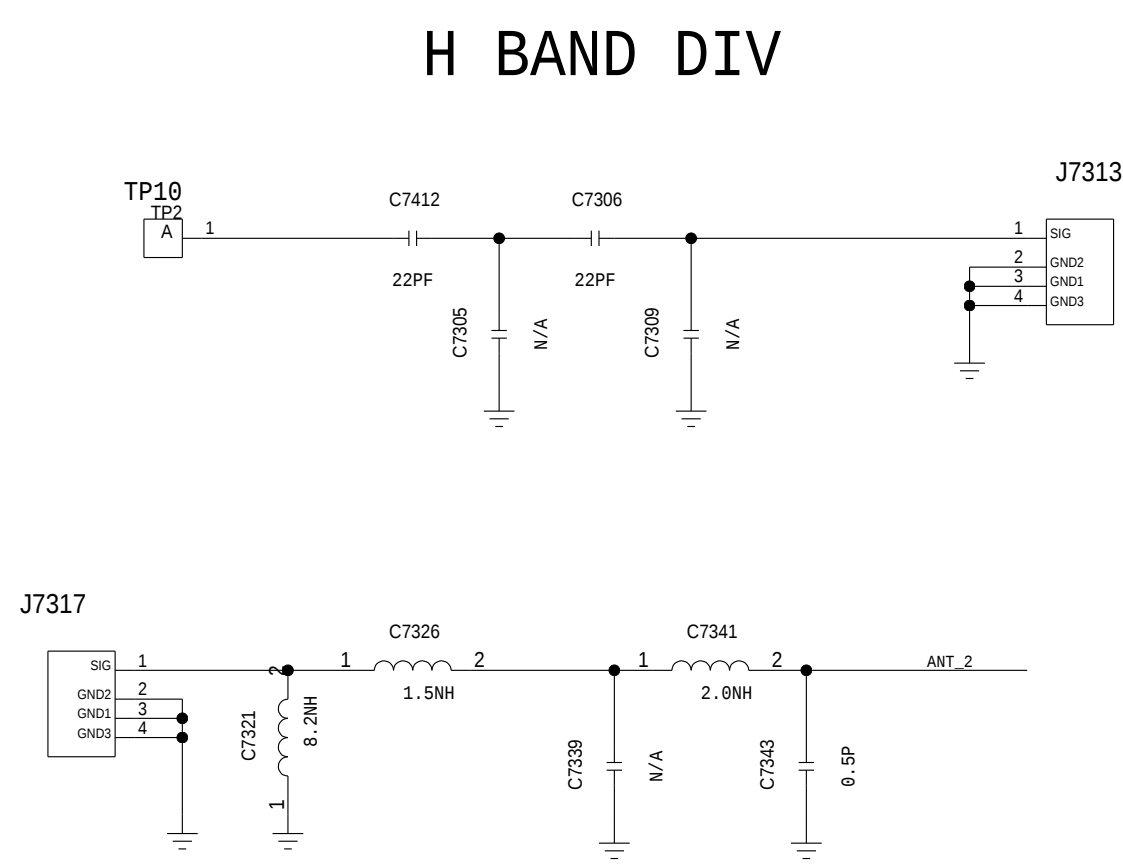
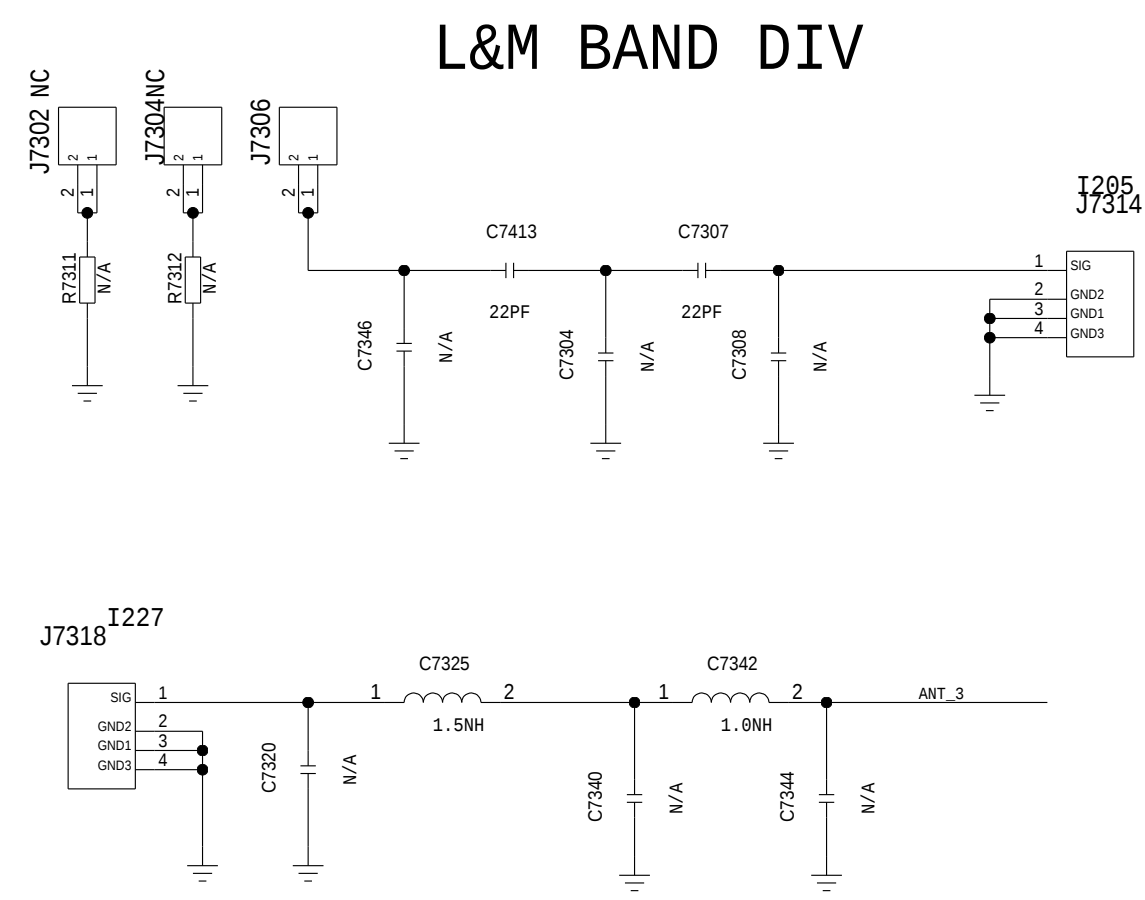
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DRAWN		TITLE: MAIN BOARD	VERSION:	V1.0
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SIZE A3	H	G	F	E	D	C	B	A
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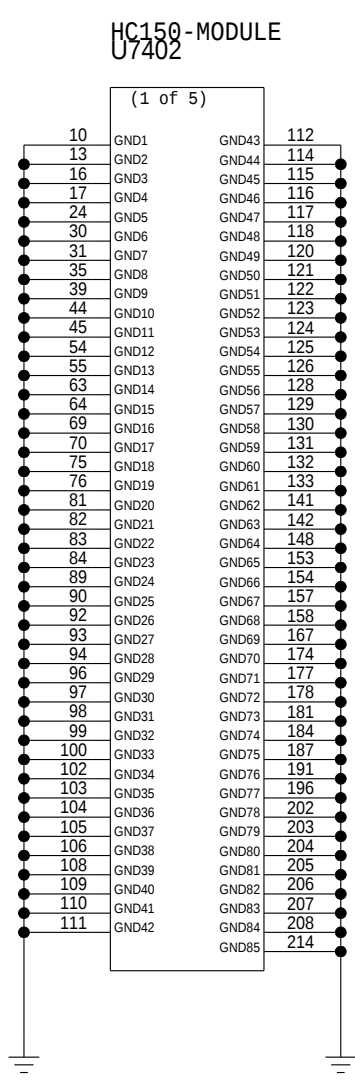
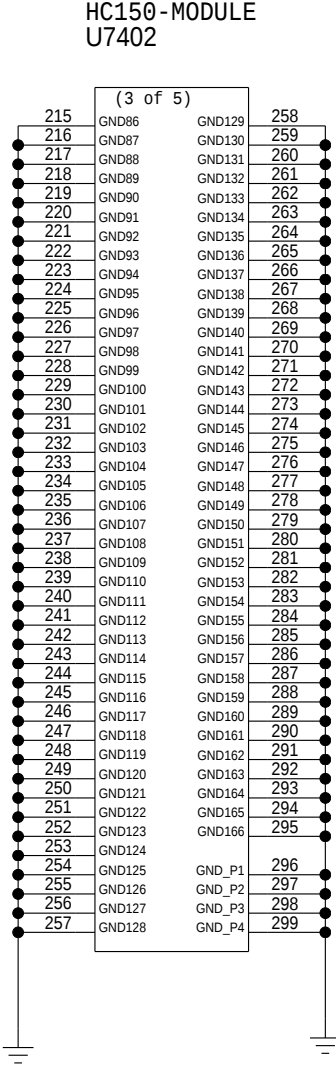
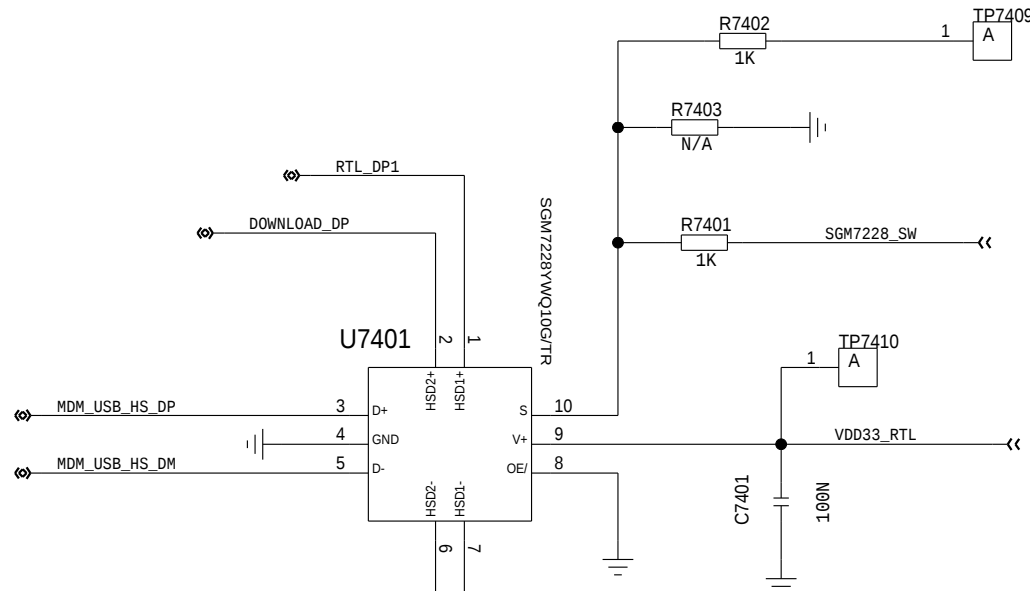
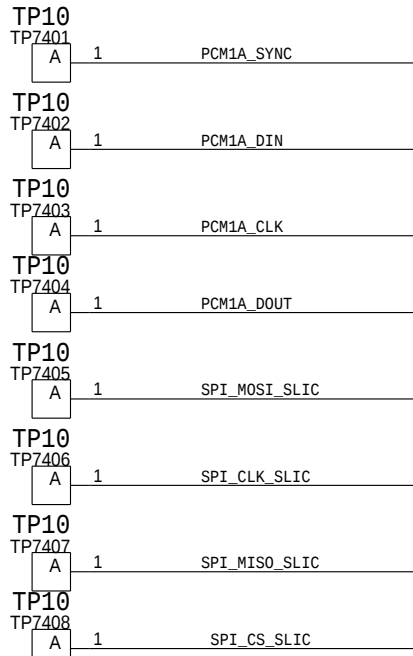
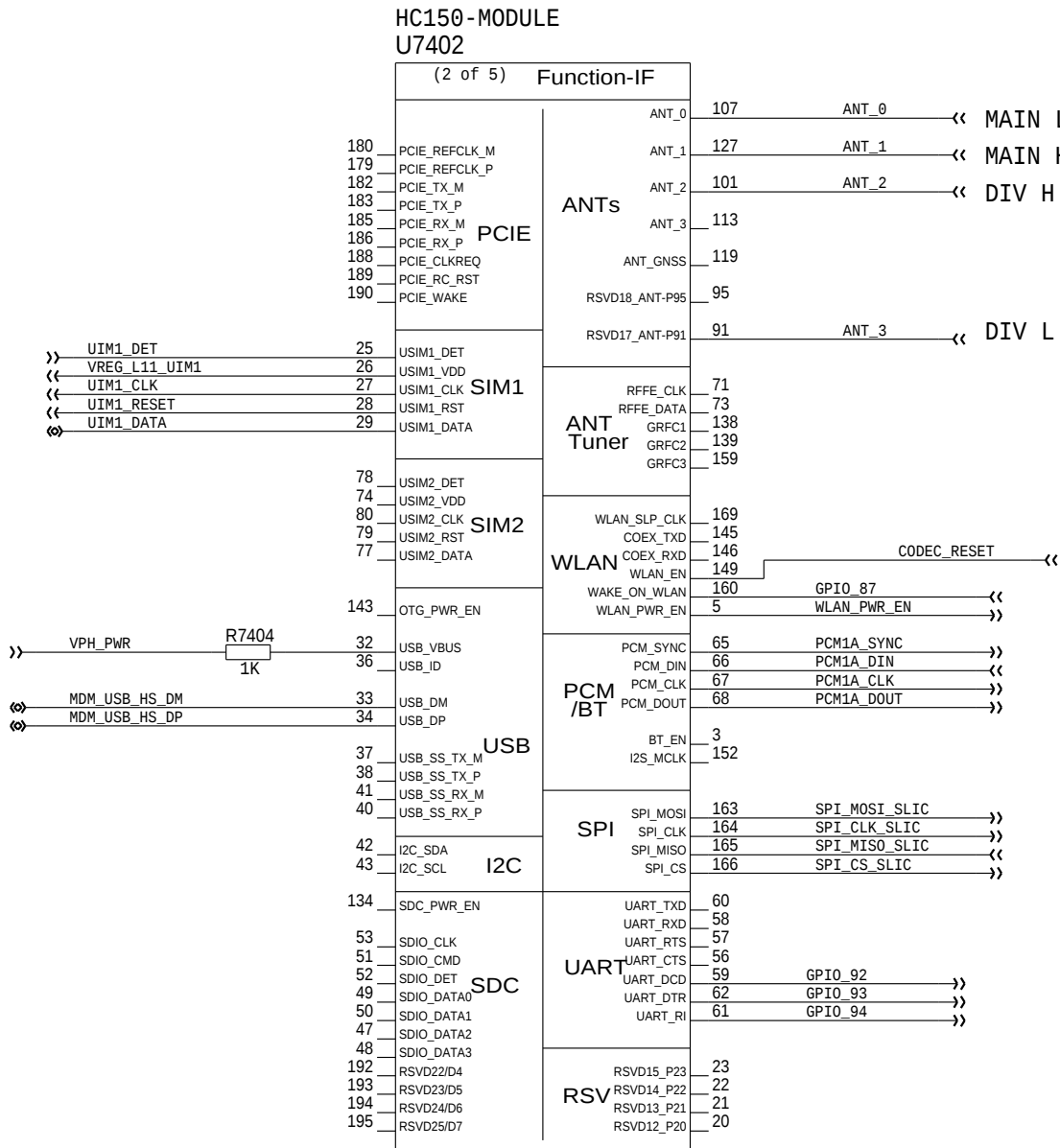
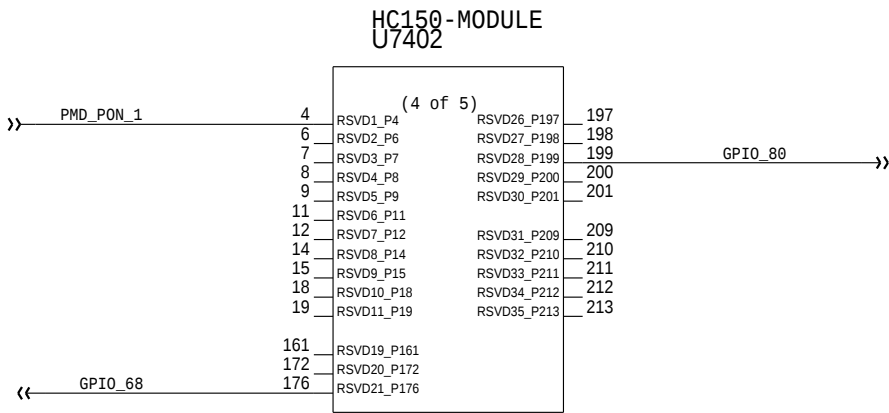
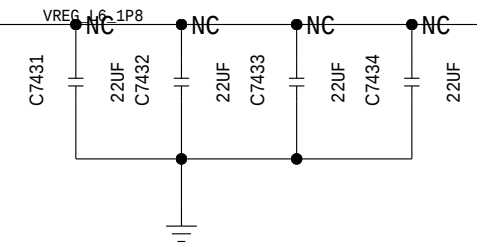
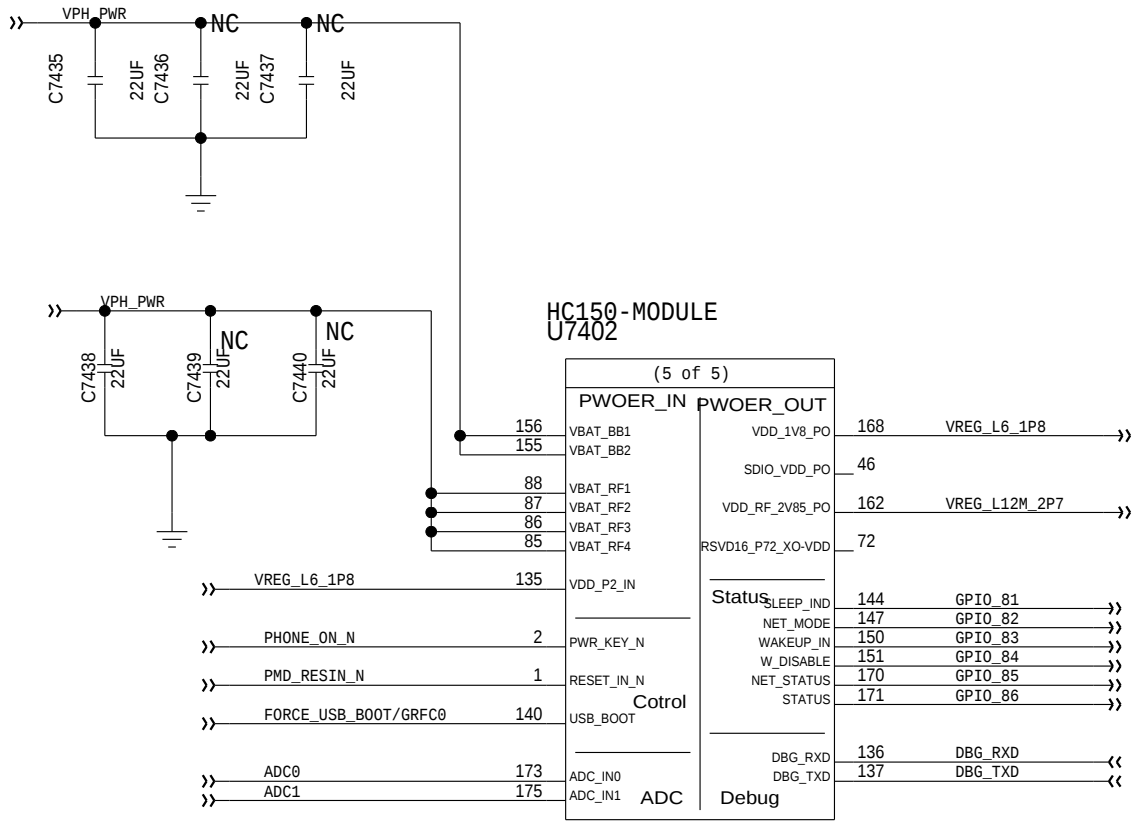


MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A	SHEET 17 OF 21	
DRAWN		TITLE: MAIN BOARD	VERSION:	V1.0
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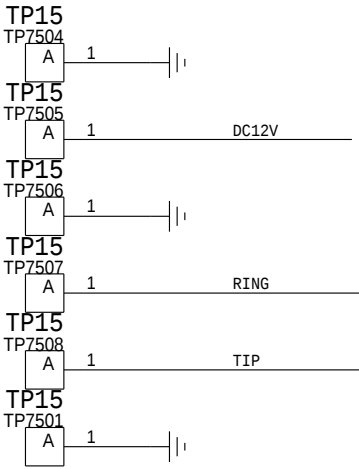
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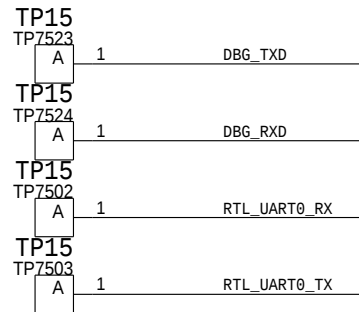
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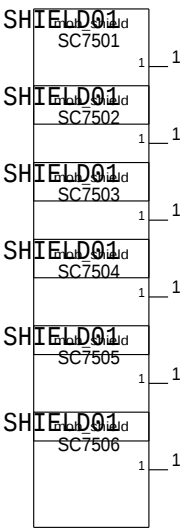
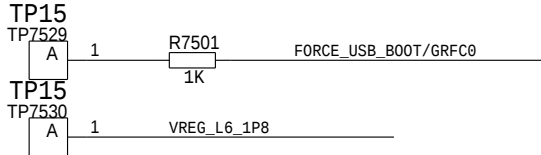
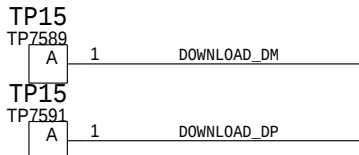
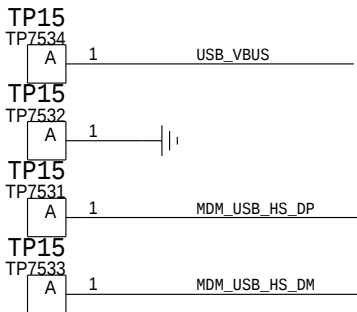
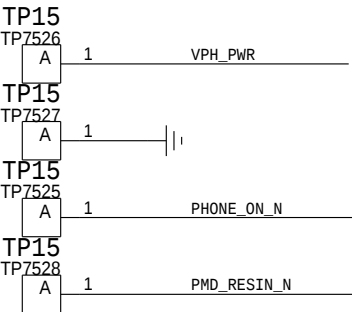


DEBUG UART



MODEM

ROUTER



MK TECHNOLOGY GROUP CO.,LTE.

MODEL: HUB63A

SHEET 21

DRAWN

TITLE: SHIELDING & TP

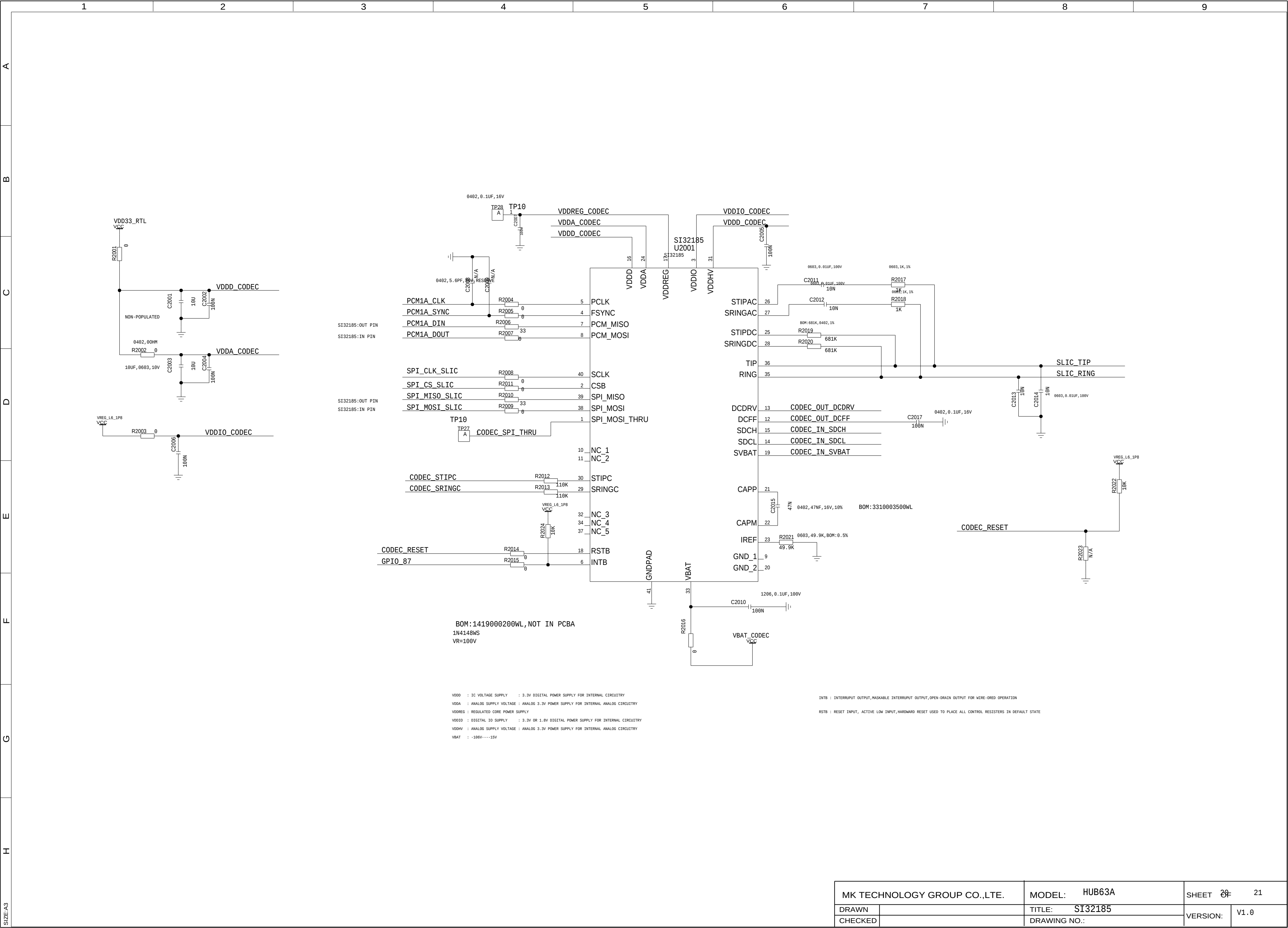
VERSION:

V1.0

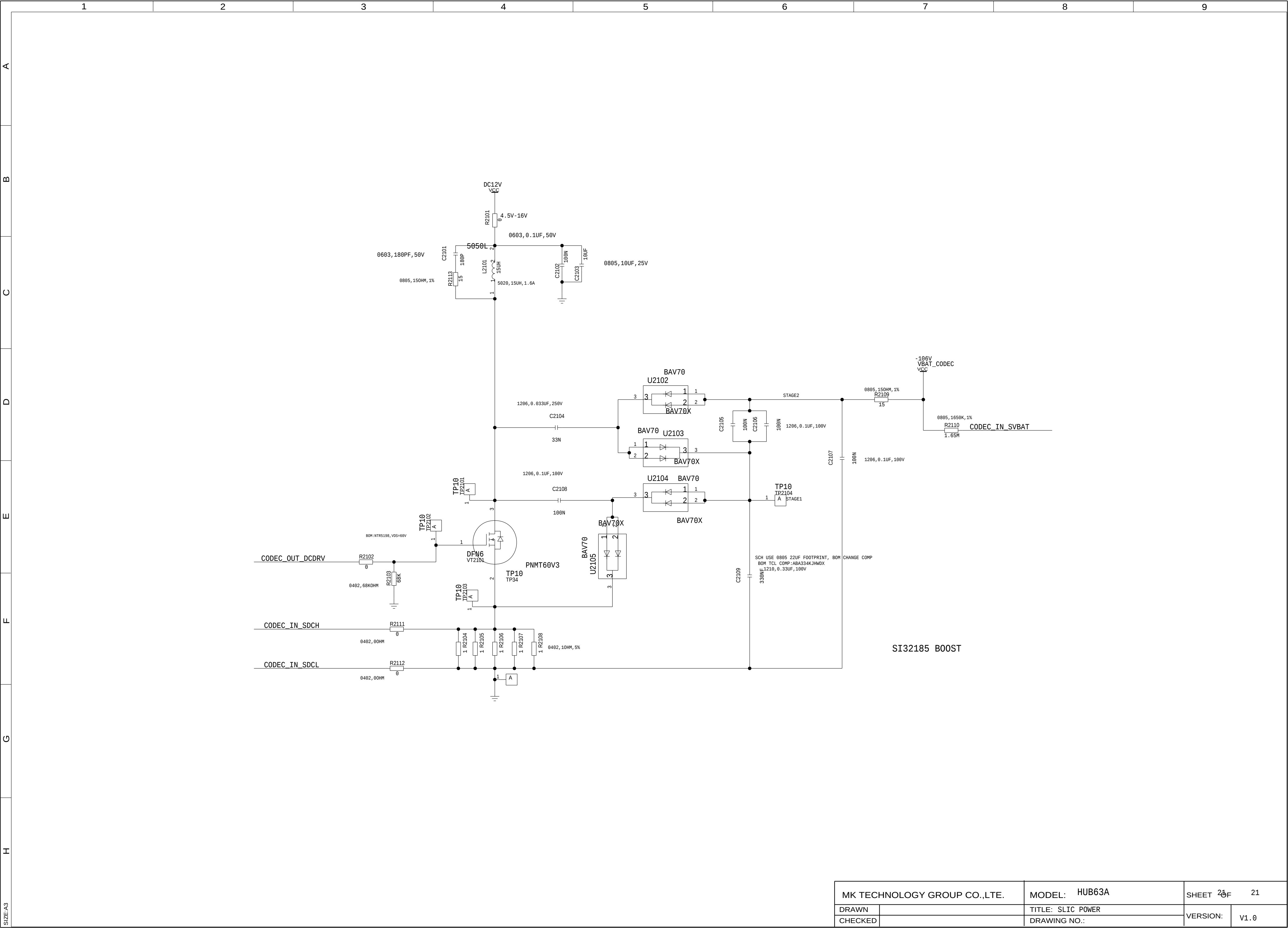
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MK TECHNOLOGY GROUP CO.,LTE.		MODEL: HUB63A		SHEET 20F 21	
DRAWN		TITLE: SLIC POWER		VERSION:	V1.0
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