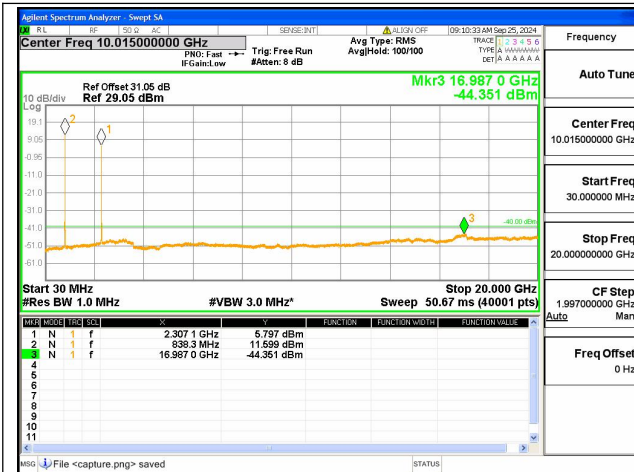
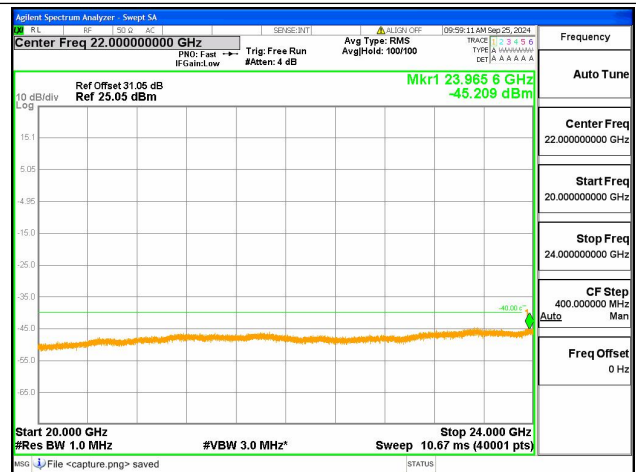
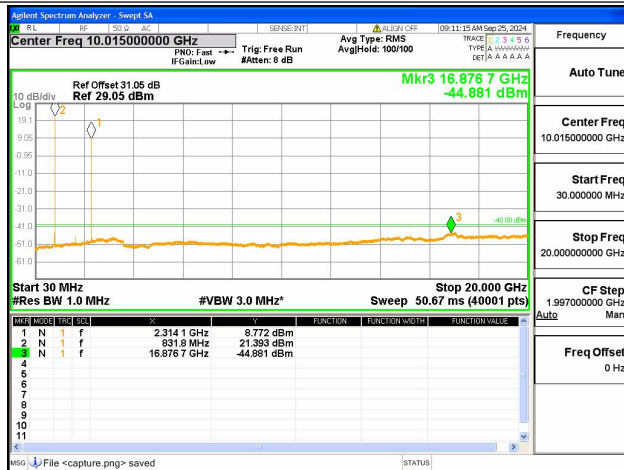




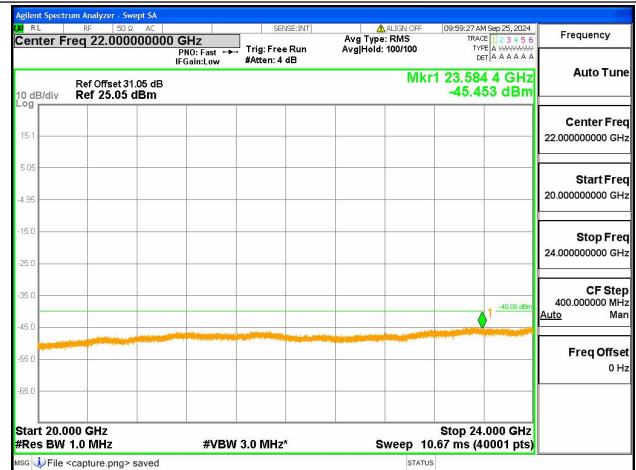
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Mid+Low CH / 50@0-50@0



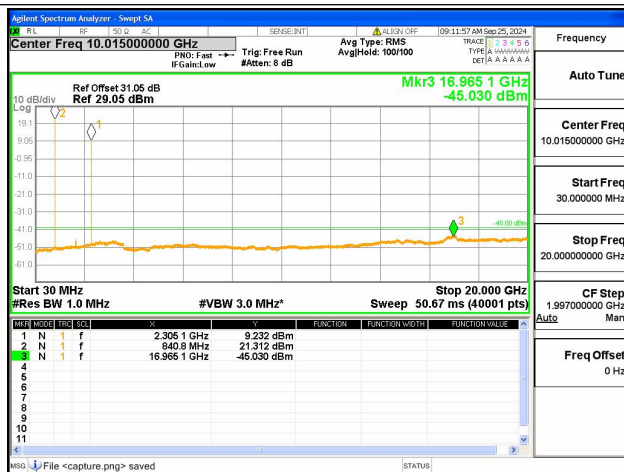
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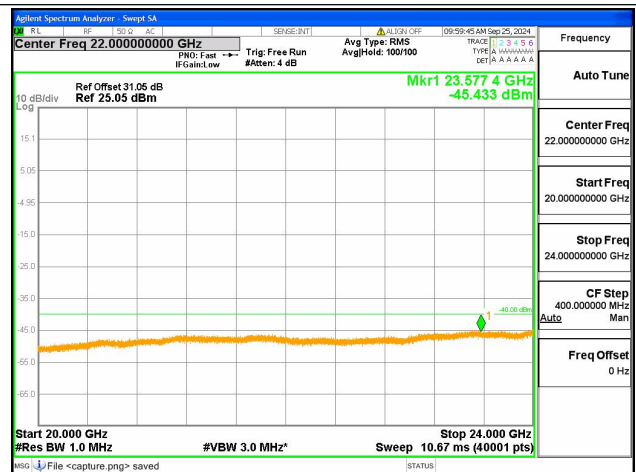
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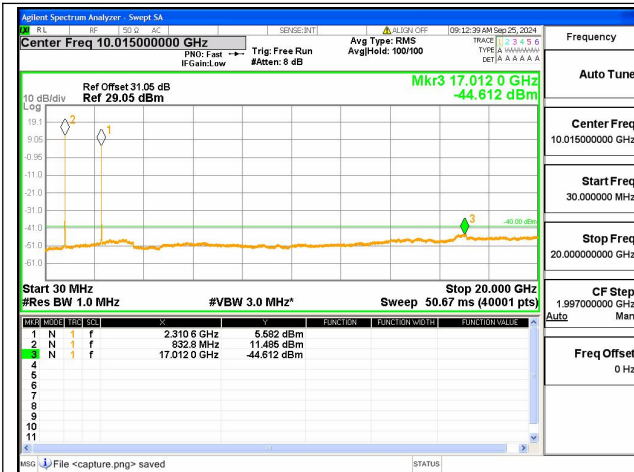
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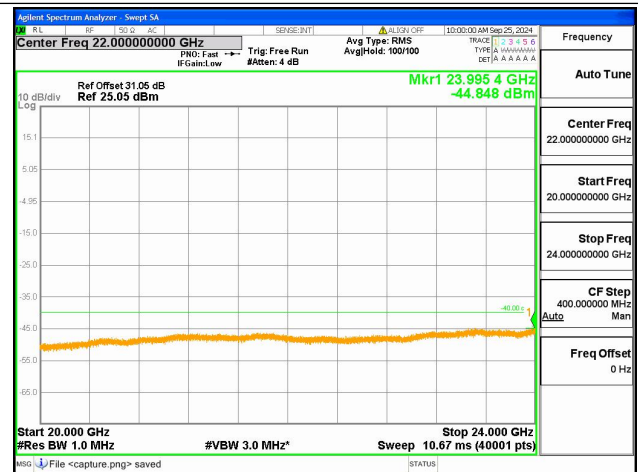
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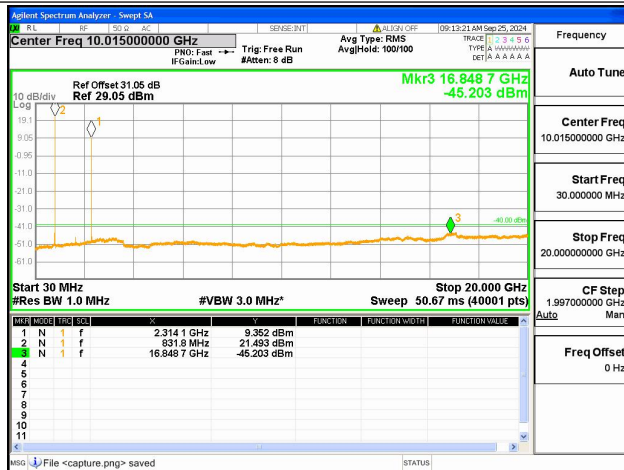
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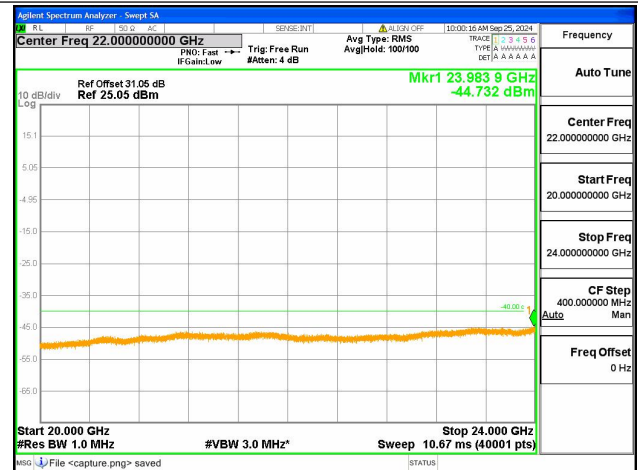
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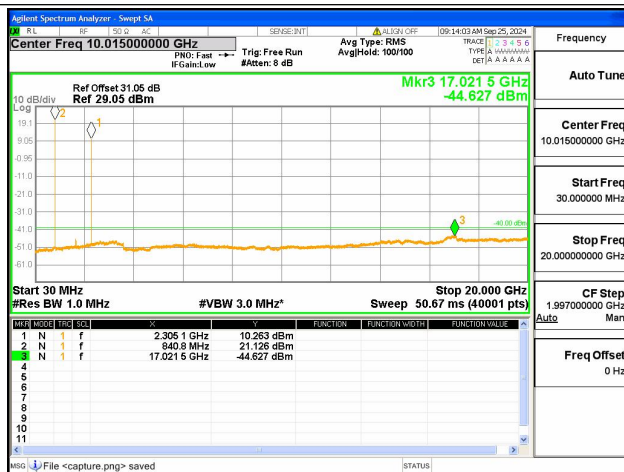
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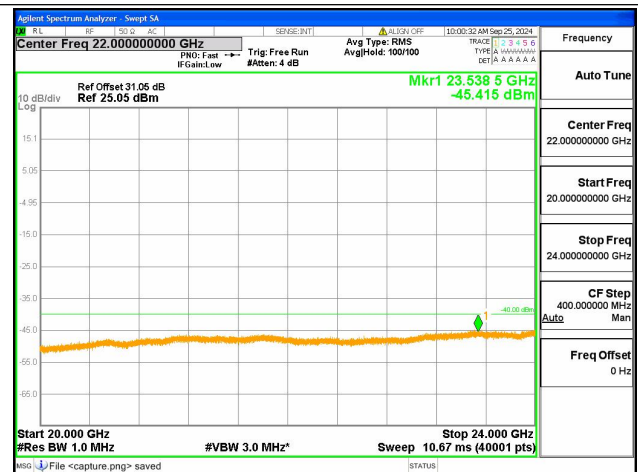
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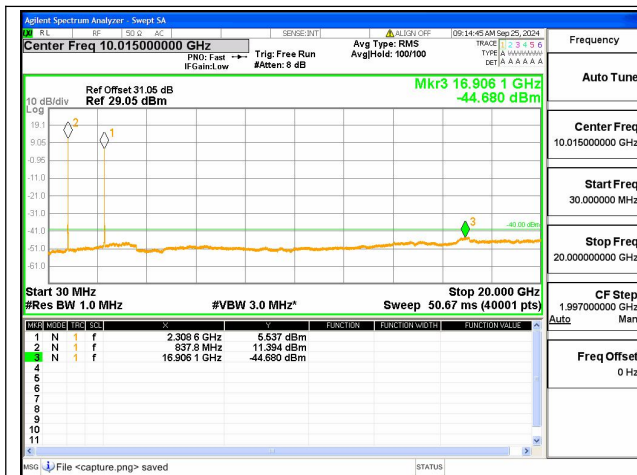
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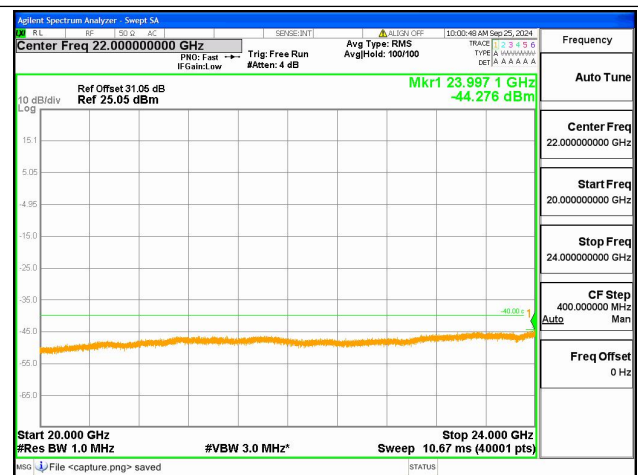
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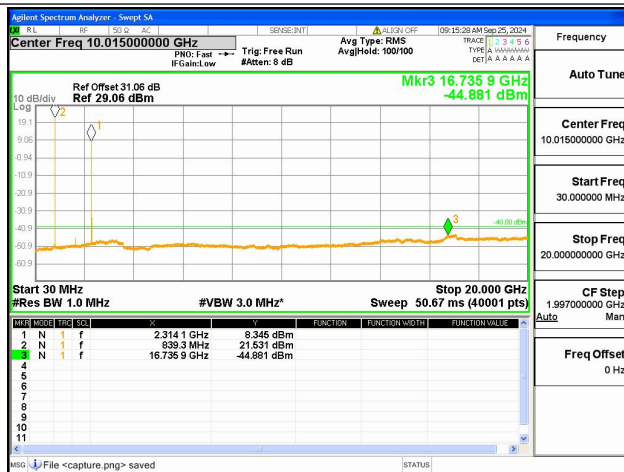
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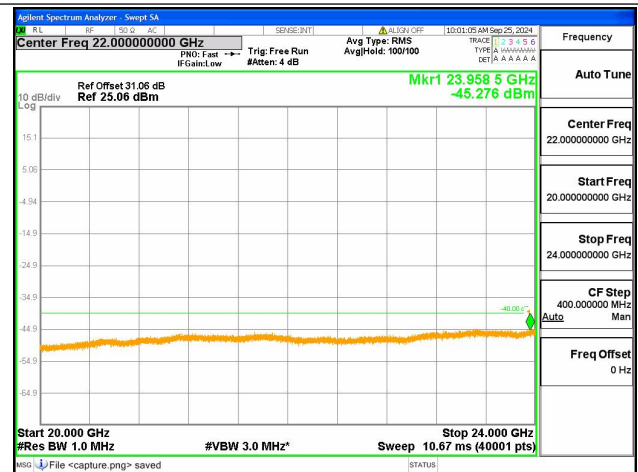
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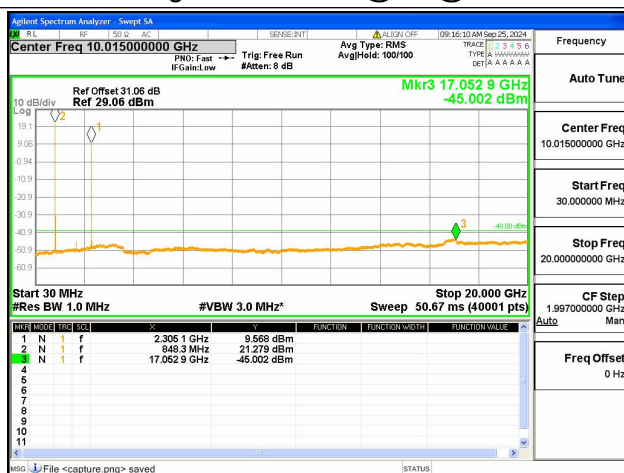
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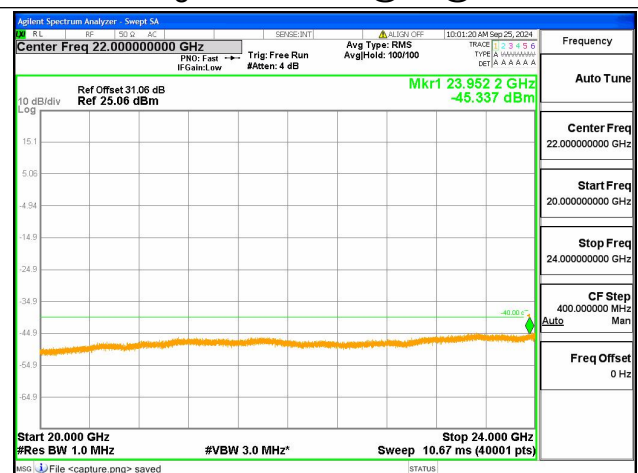
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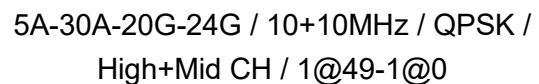
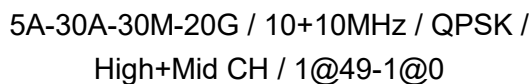
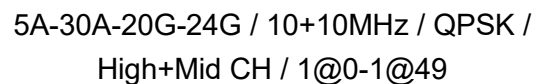
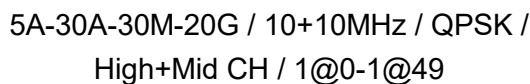
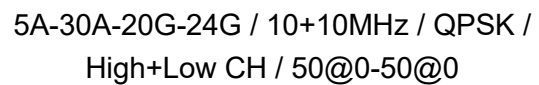
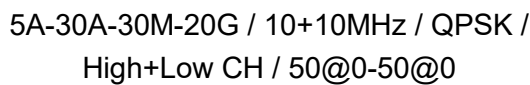
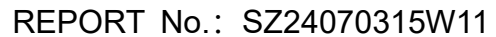
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High+Low CH / 1@0-1@49

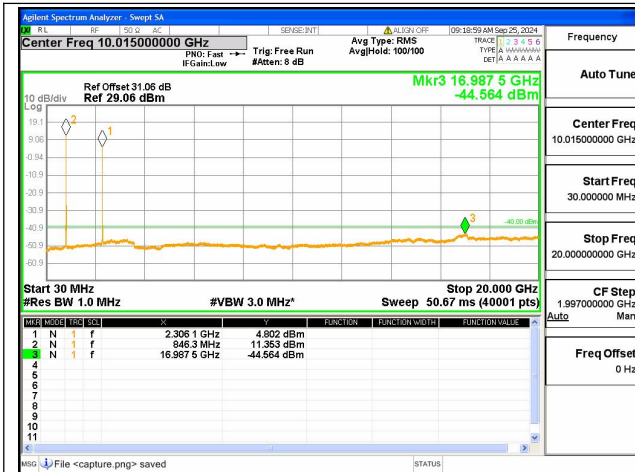


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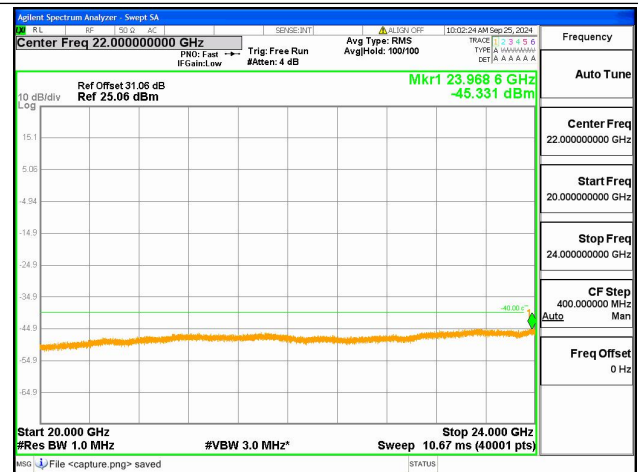


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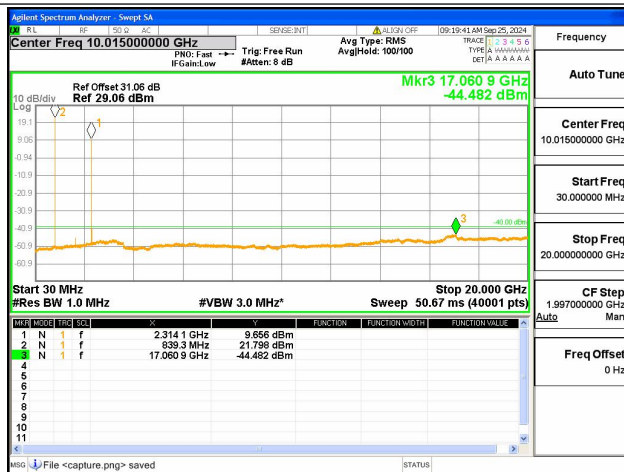




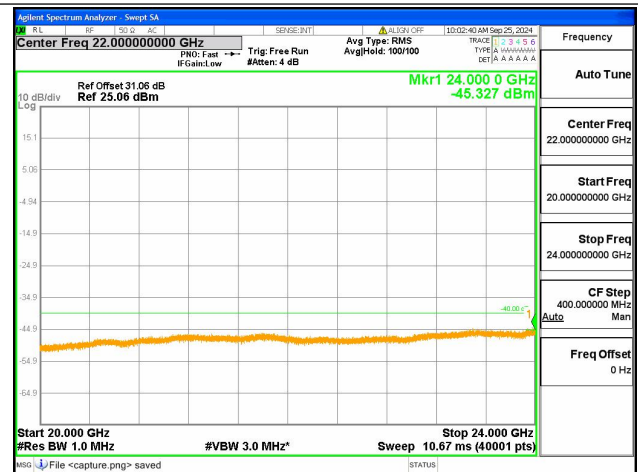
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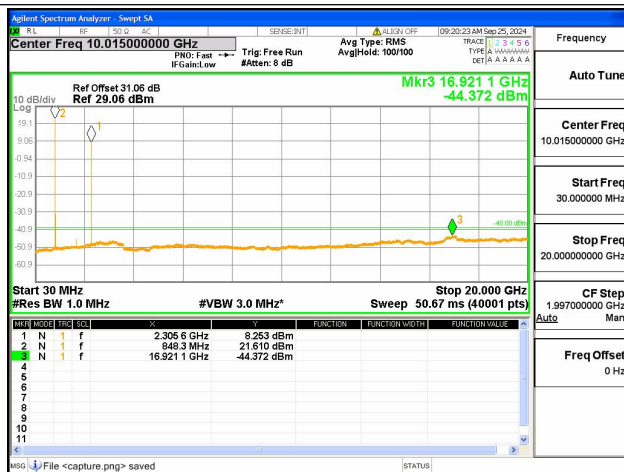
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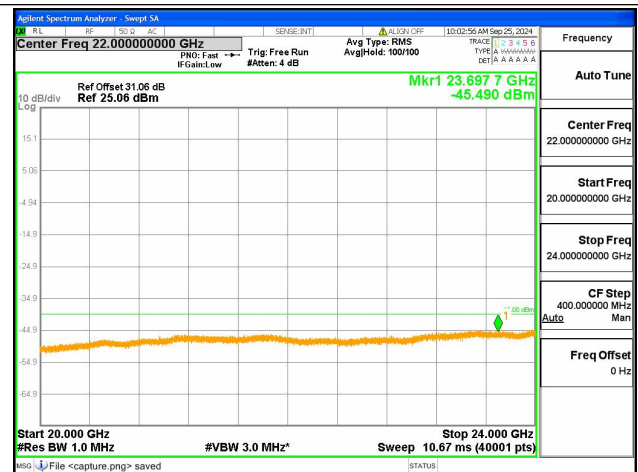
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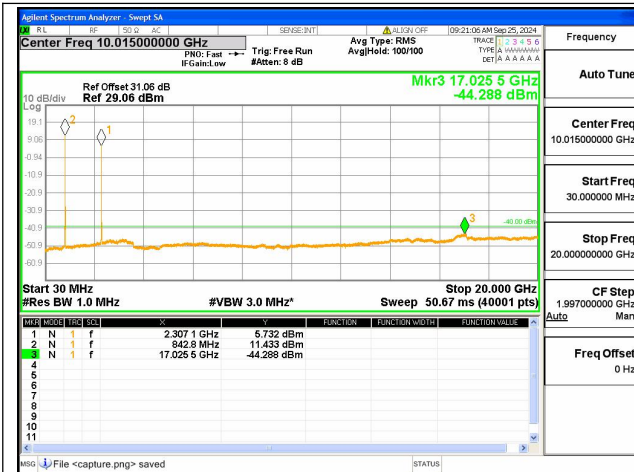
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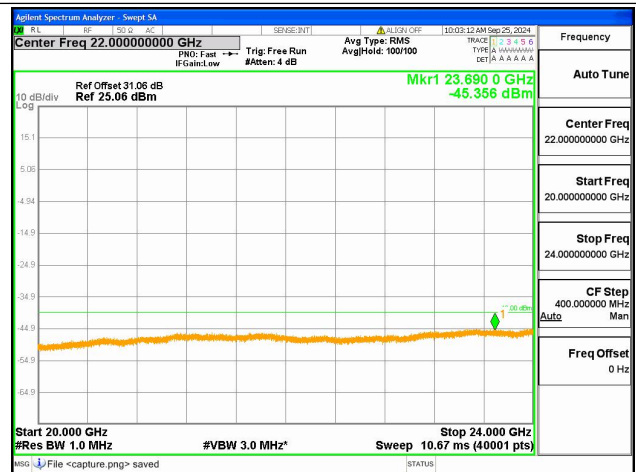
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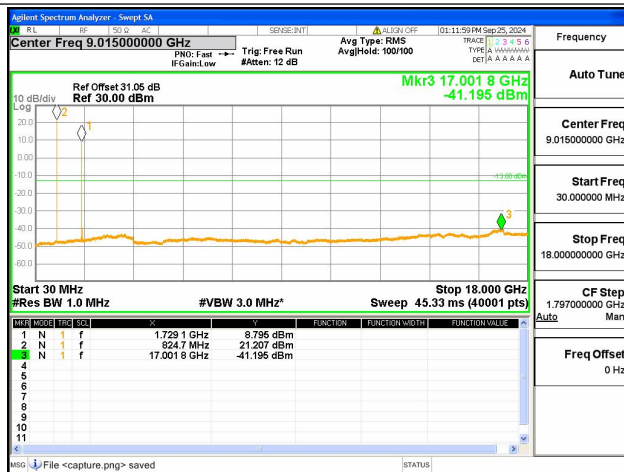
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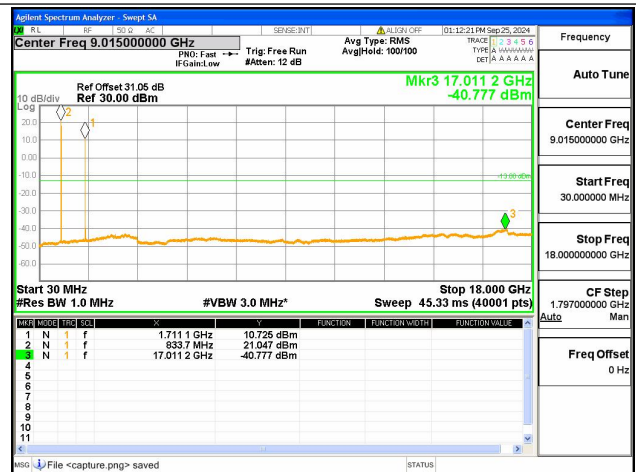
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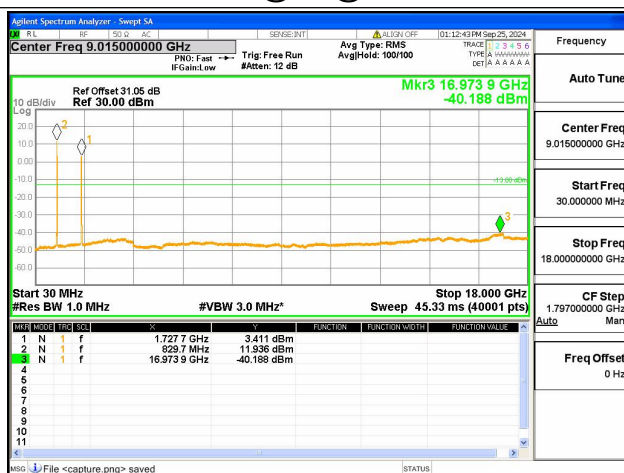
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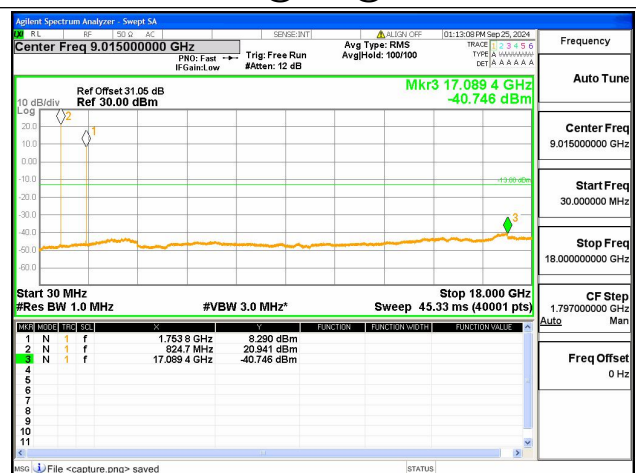
5A-66A / 10+20MHz / QPSK / Low+Low CH /
1@0-1@99



5A-66A / 10+20MHz / QPSK / Low+Low CH /
1@49-1@0



5A-66A / 10+20MHz / QPSK / Low+Low CH /
50@0-100@0



5A-66A / 10+20MHz / QPSK / Low+Mid CH /
1@0-1@99