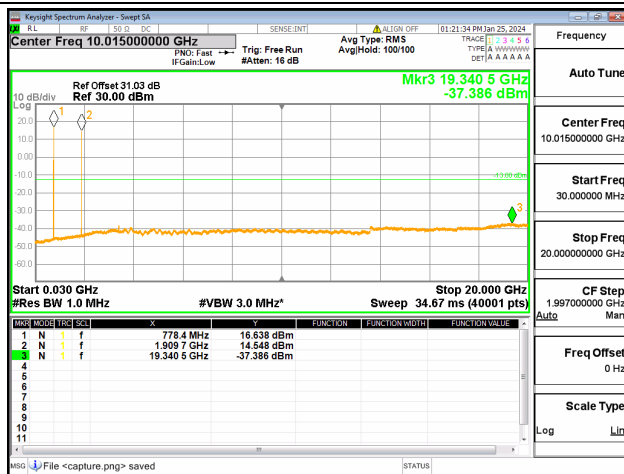
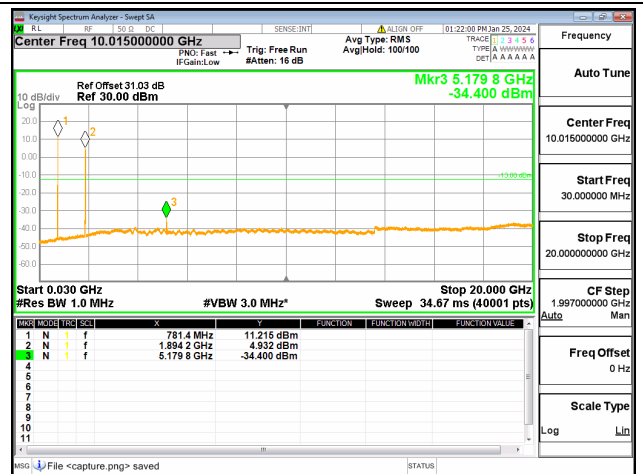
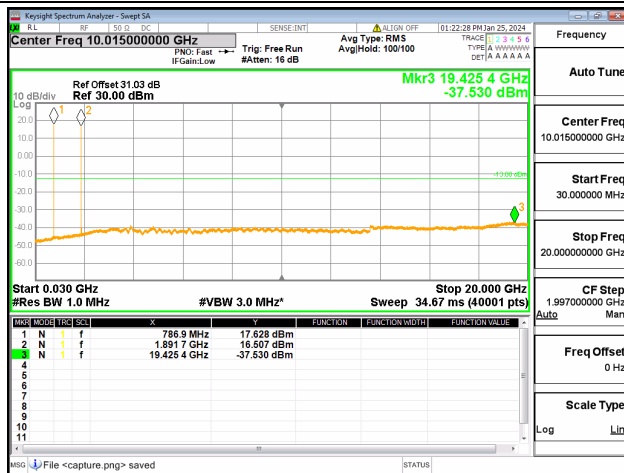




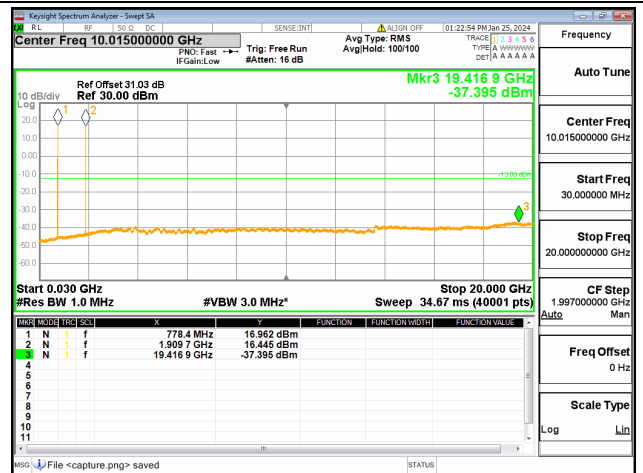
2A-13A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@0



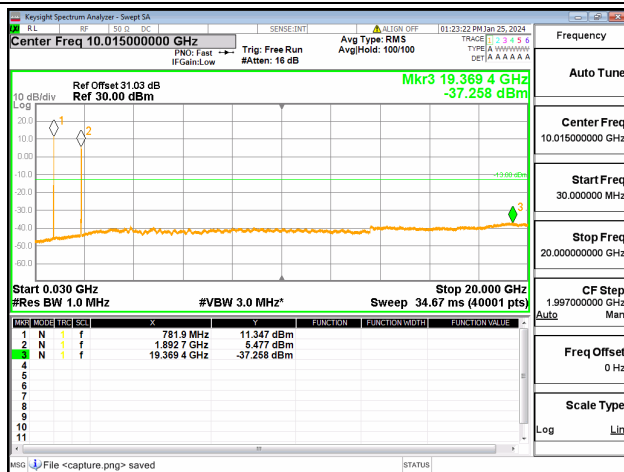
2A-13A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@0



2A-13A / 20+10MHz / QPSK / High+High CH /
1@0-1@49

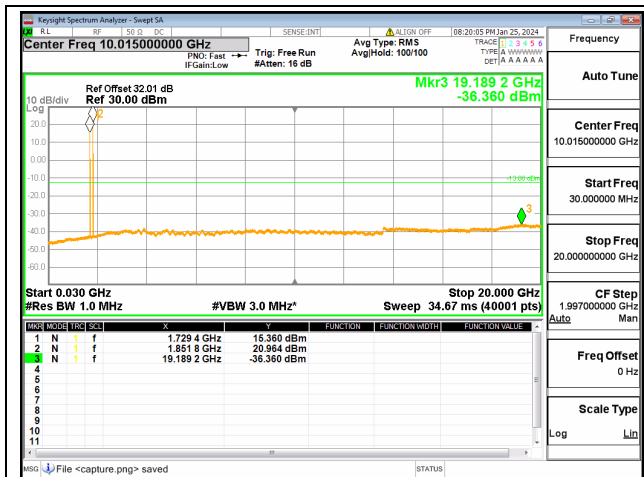
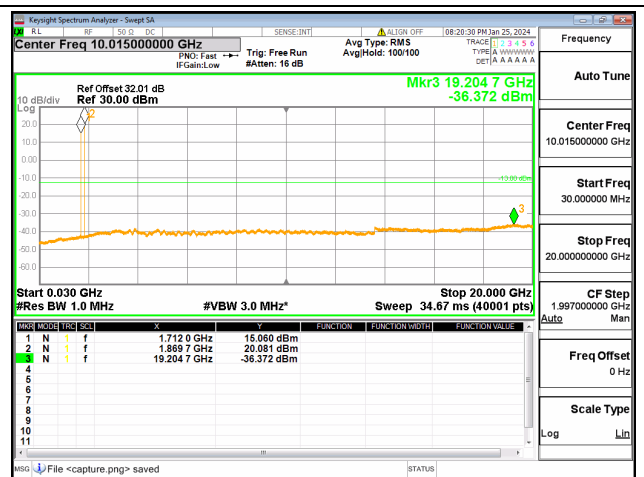
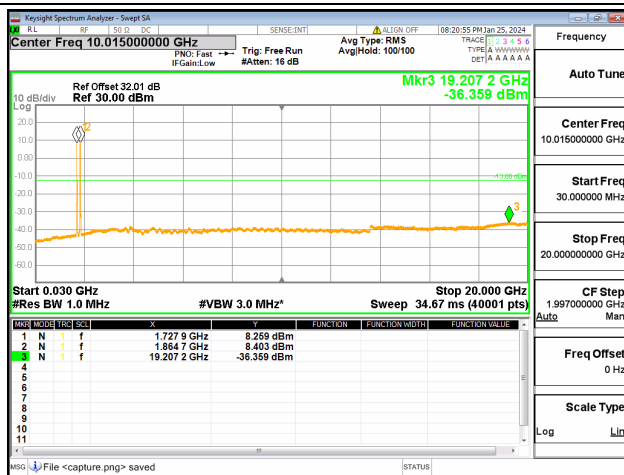
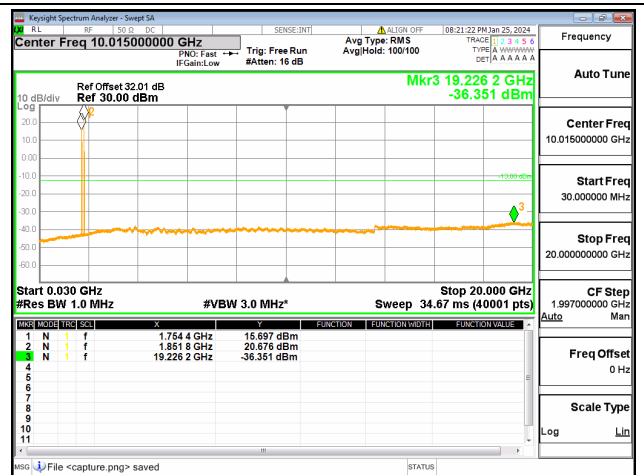
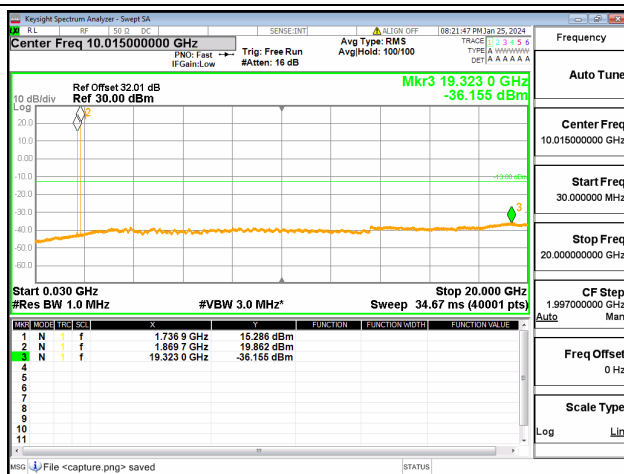
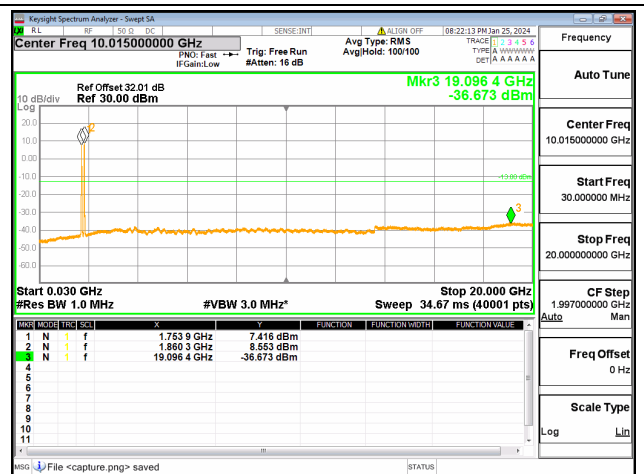


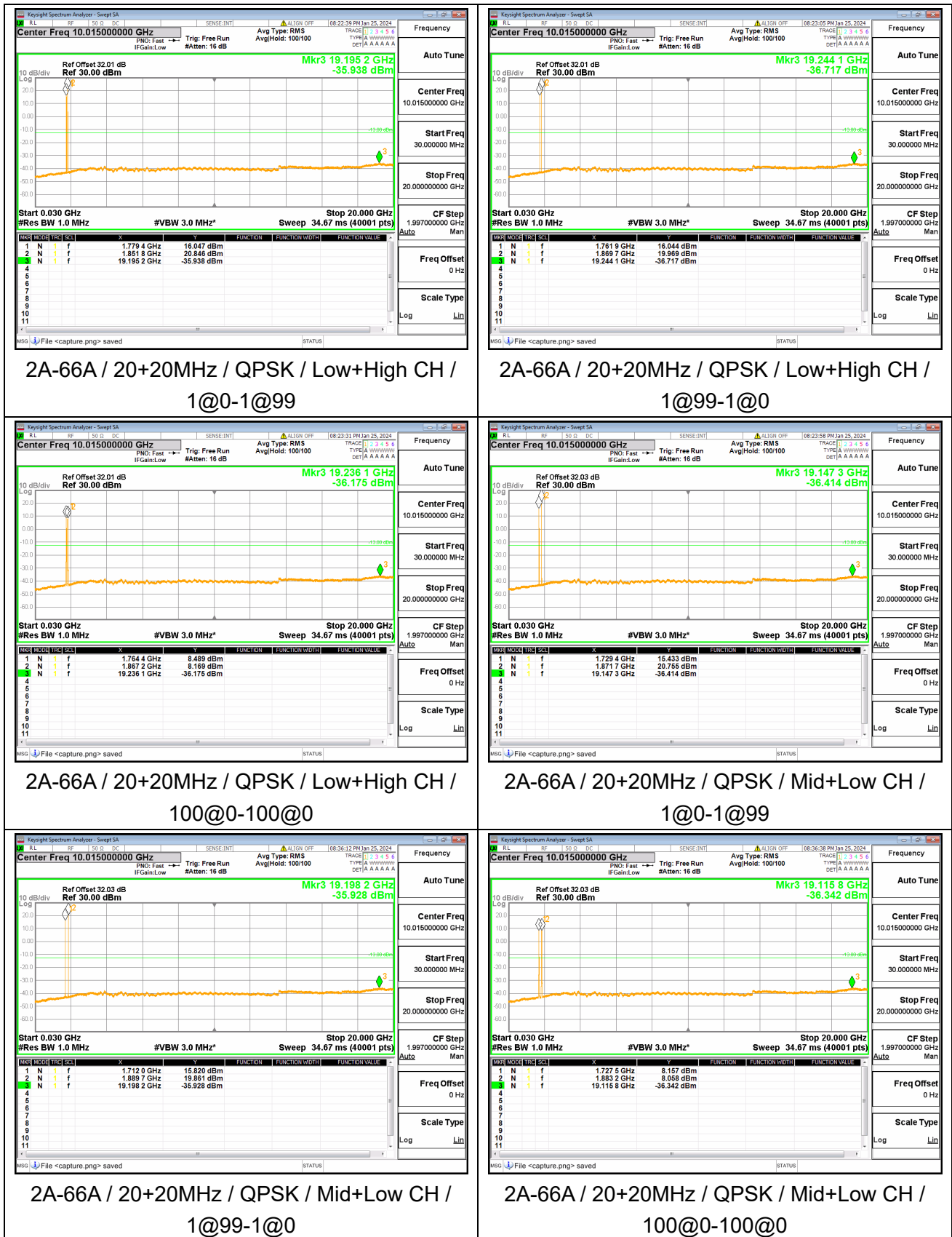
2A-13A / 20+10MHz / QPSK / High+High CH /
1@99-1@0

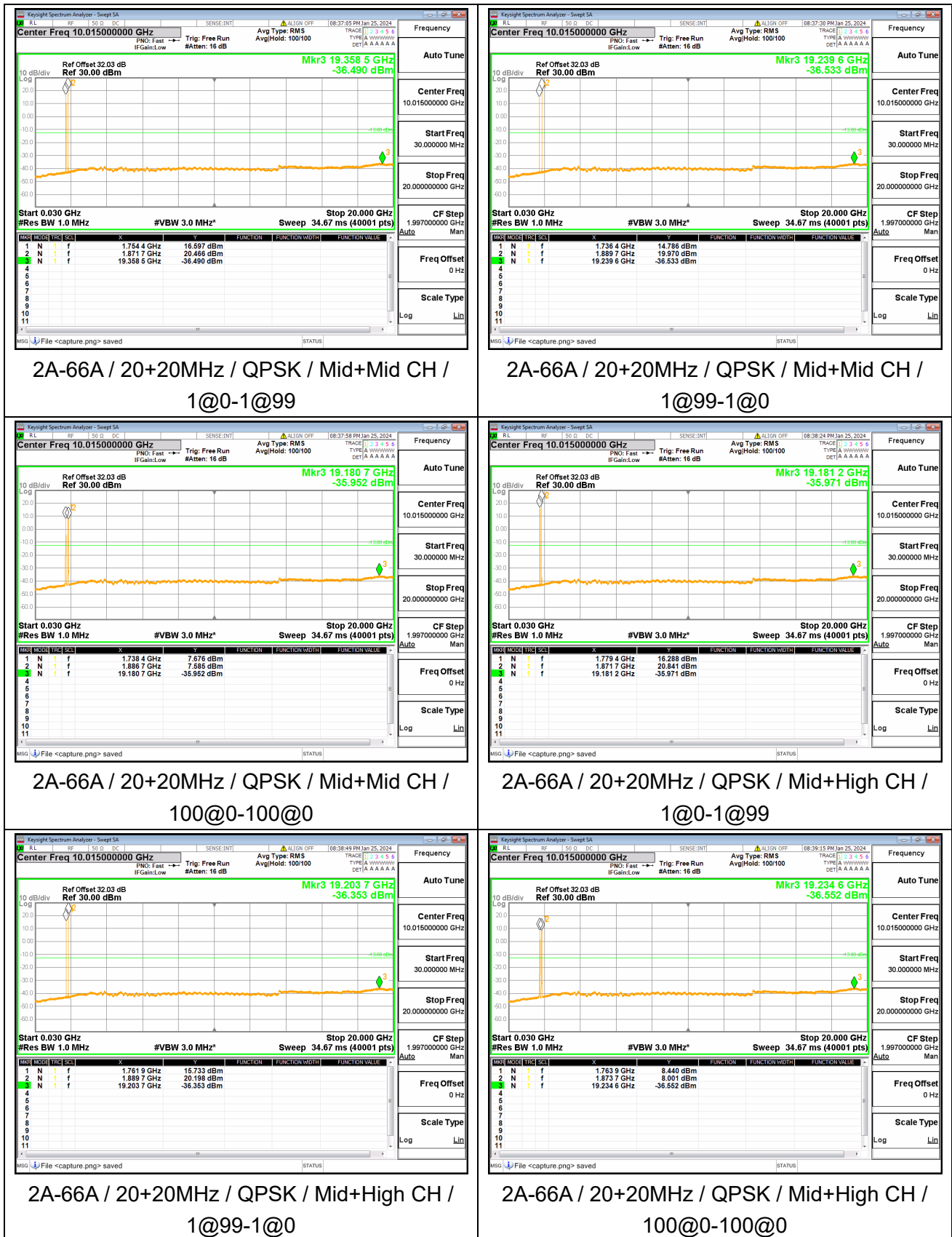


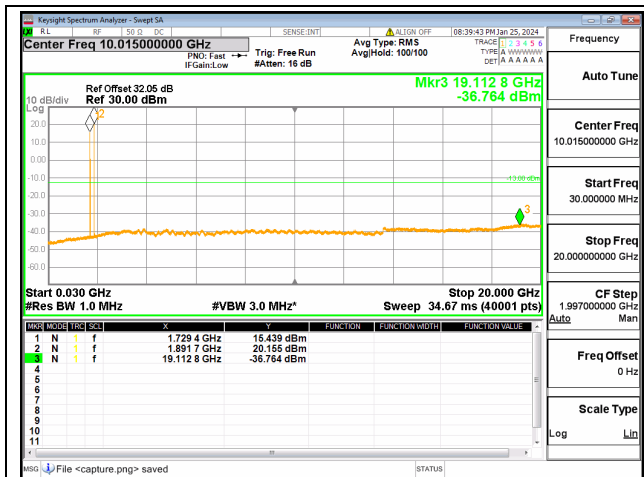
2A-13A / 20+10MHz / QPSK / High+High CH /
100@0-50@0

N/A

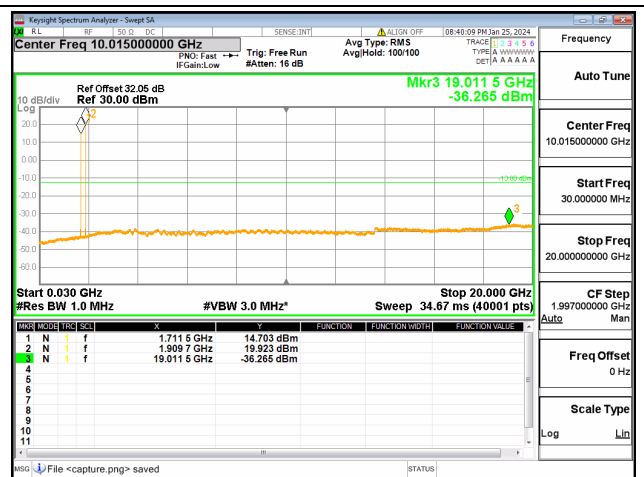
2A-66A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@02A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@0



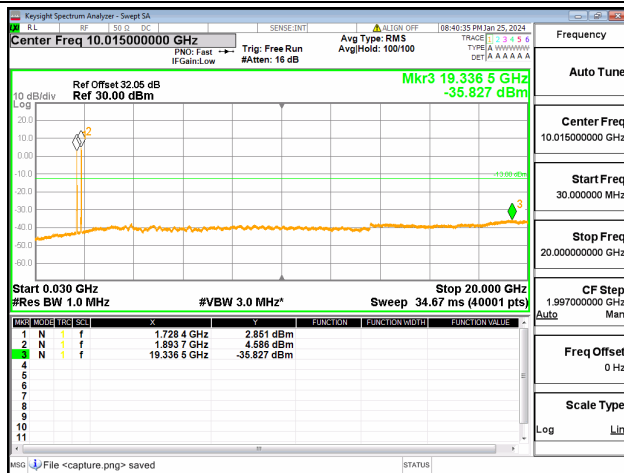




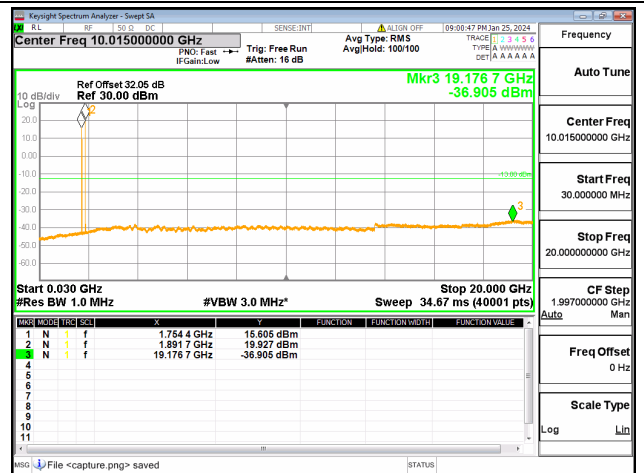
2A-66A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99



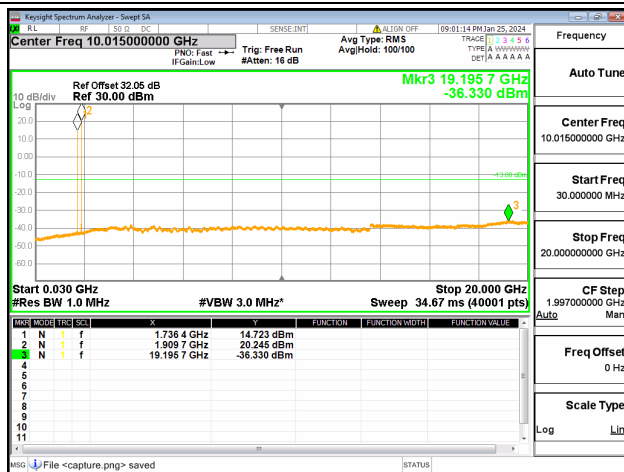
2A-66A / 20+20MHz / QPSK / High+Low CH /
1@99-1@0



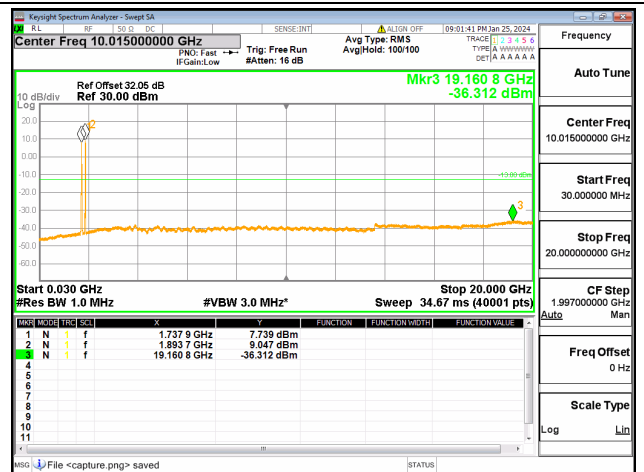
2A-66A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0



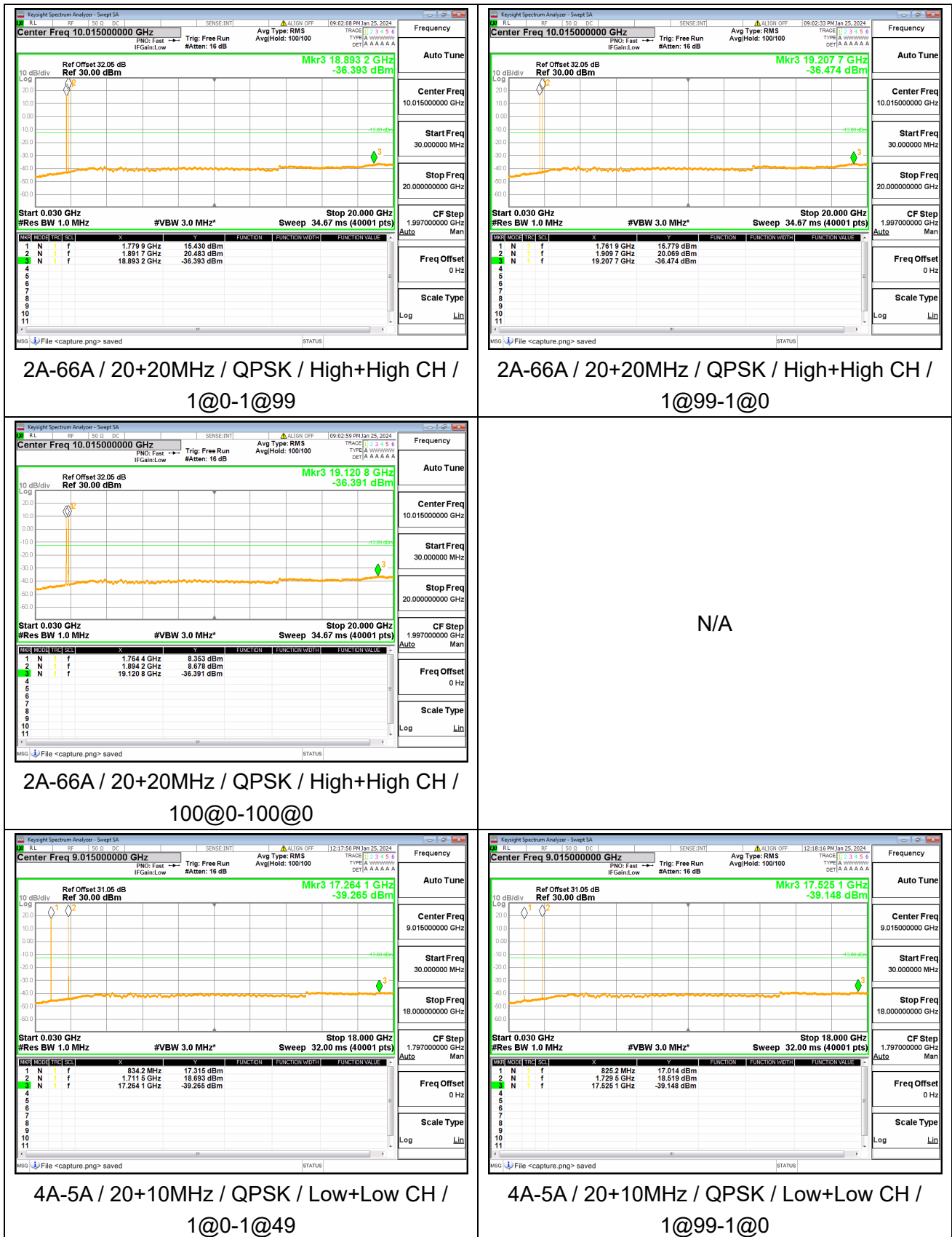
2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@99

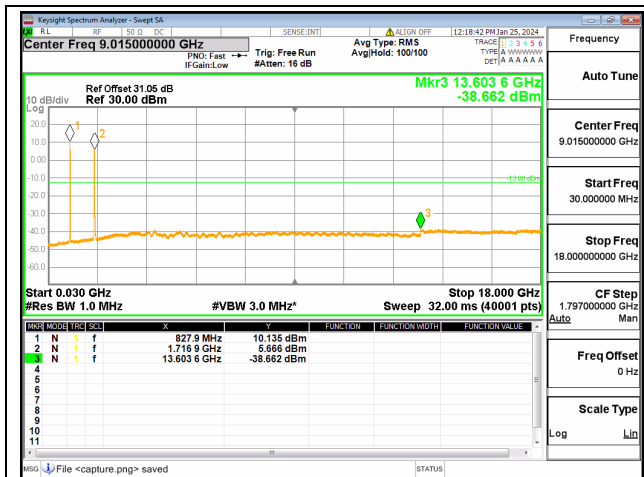
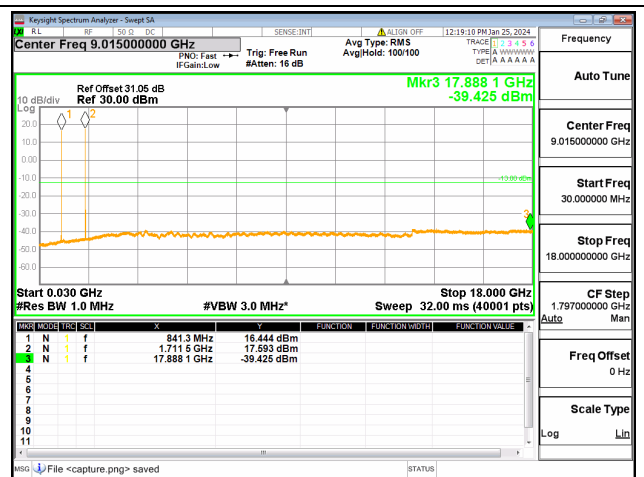
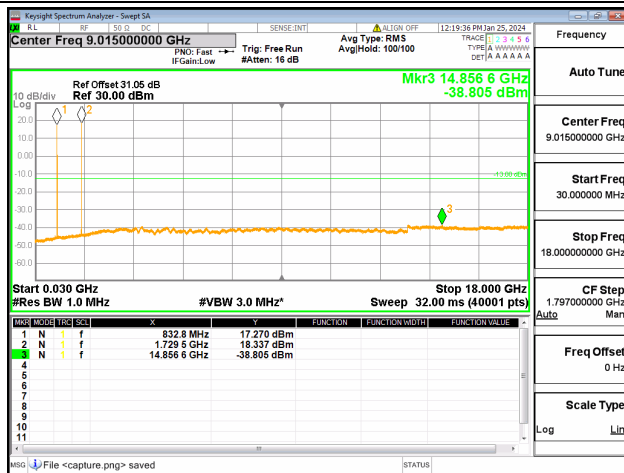
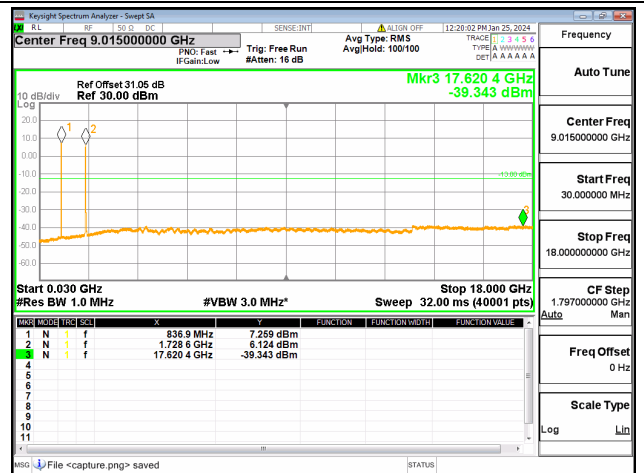
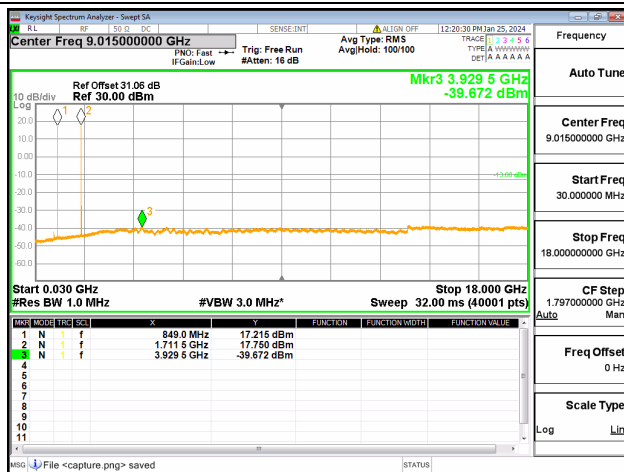
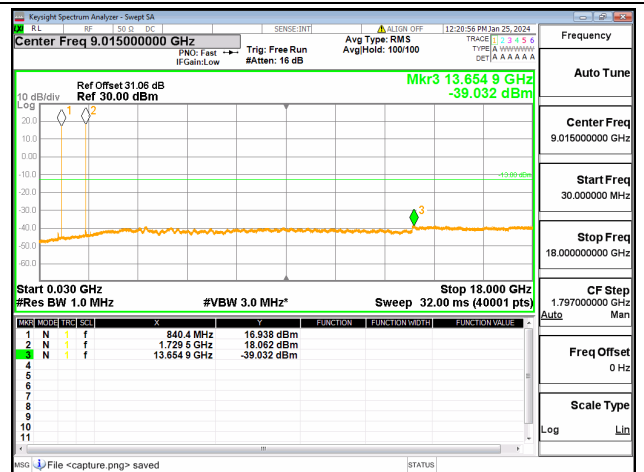


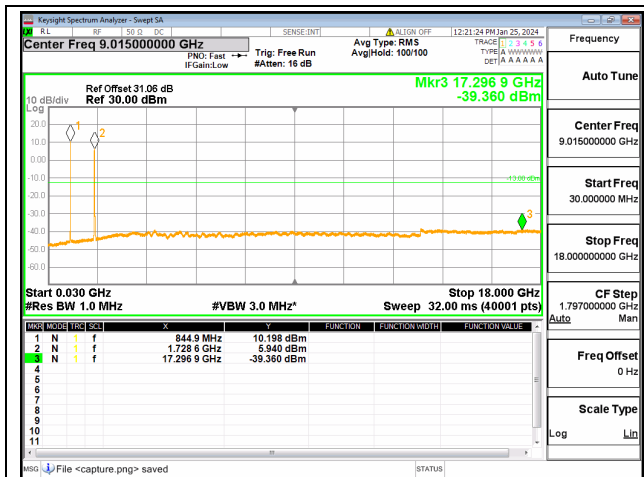
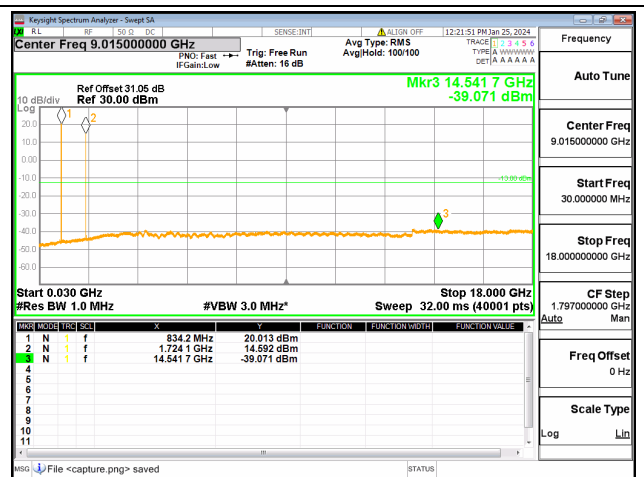
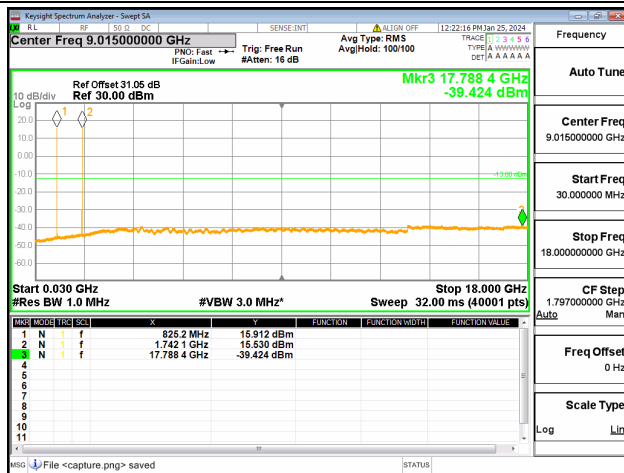
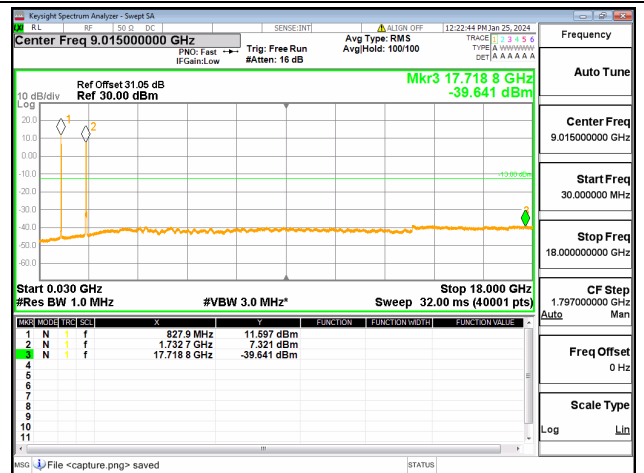
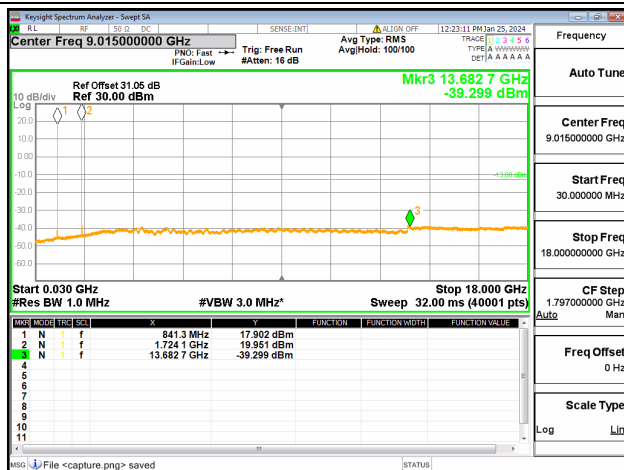
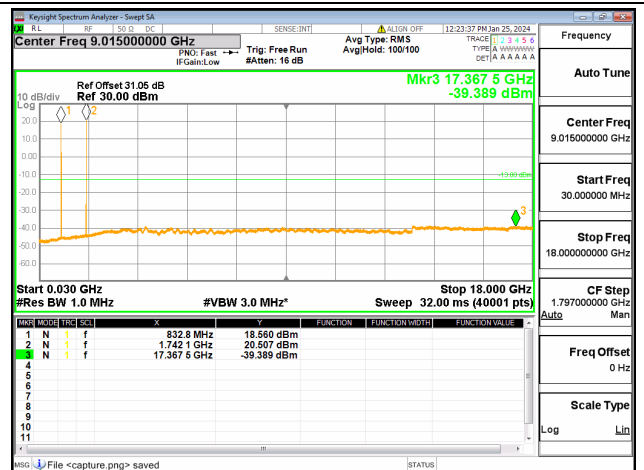
2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@0

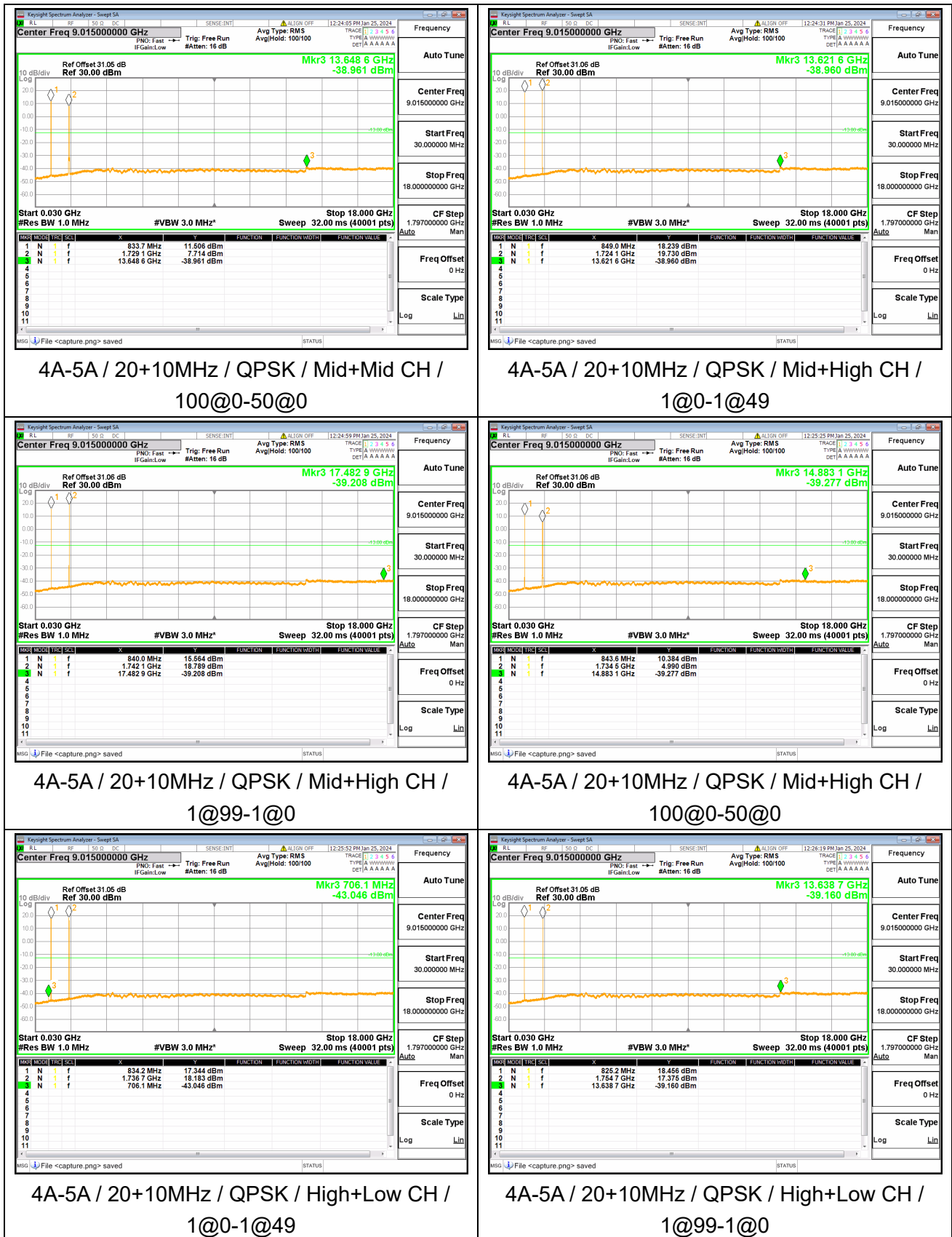


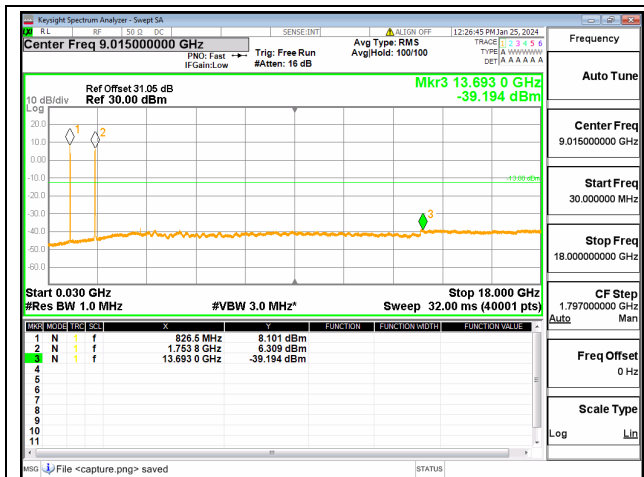
2A-66A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0



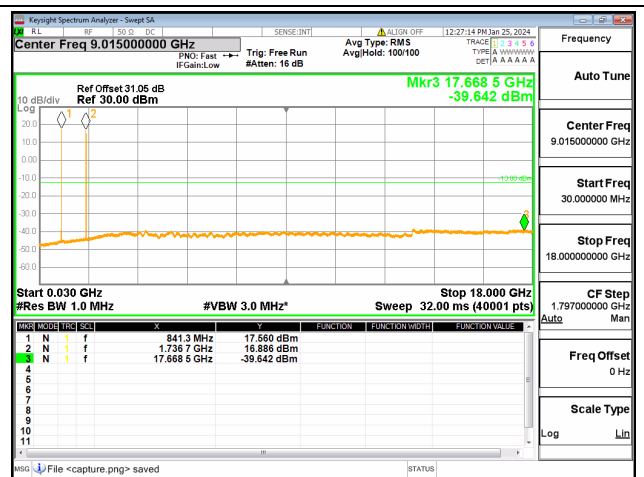
4A-5A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@04A-5A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Low+High CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Low+High CH /
1@99-1@0

4A-5A / 20+10MHz / QPSK / Low+High CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Mid+Low CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@04A-5A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0

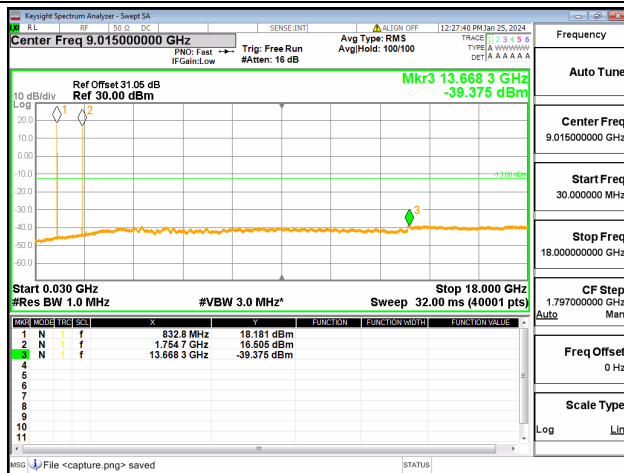




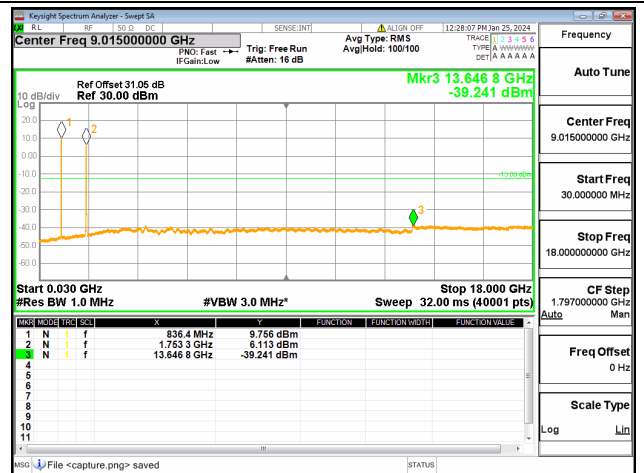
4A-5A / 20+10MHz / QPSK / High+Low CH /
100@0-50@0



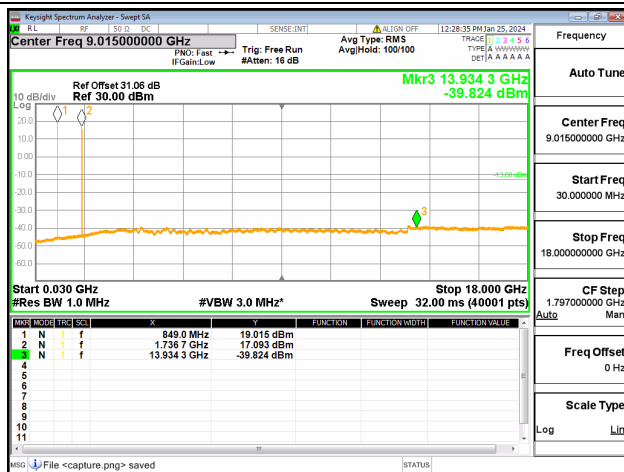
4A-5A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@49



4A-5A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@0



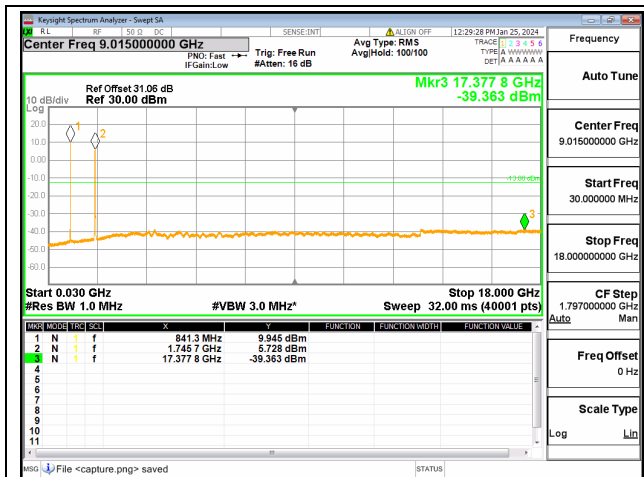
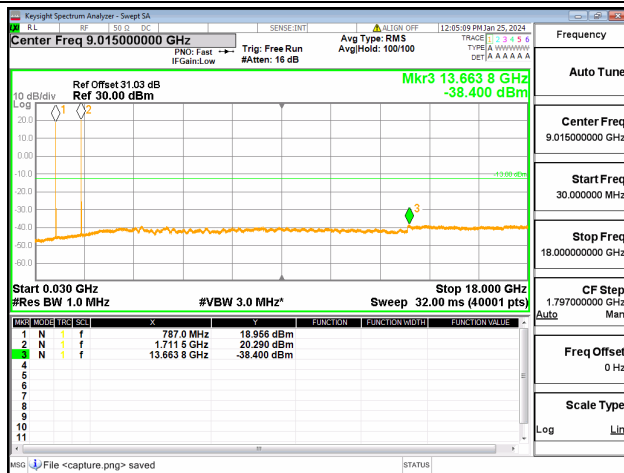
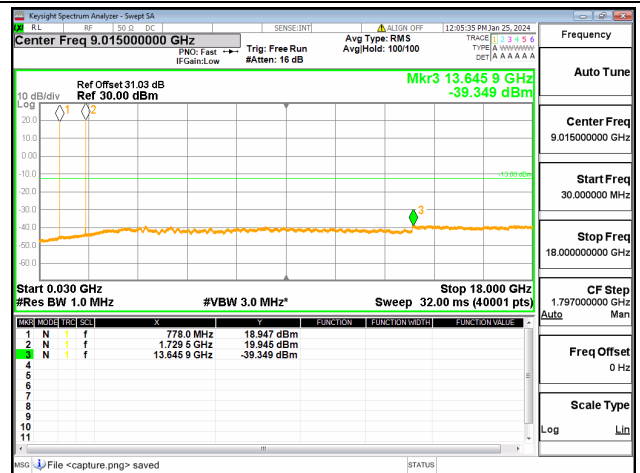
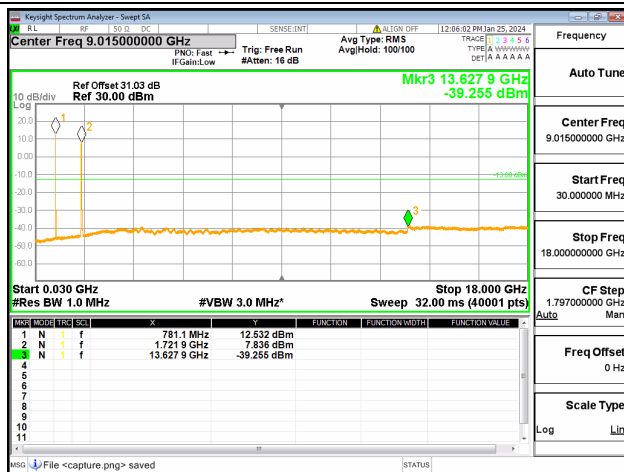
4A-5A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@0

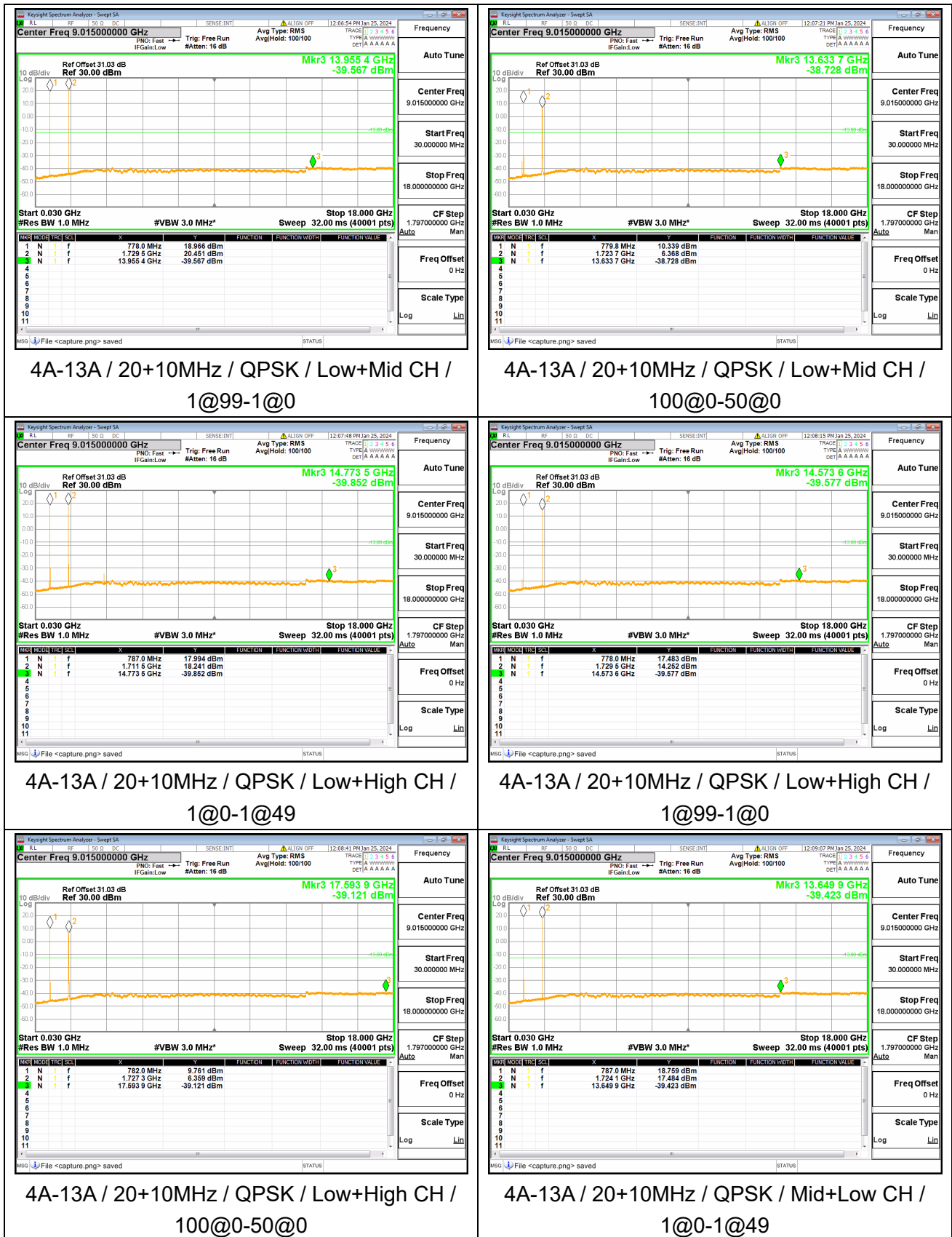


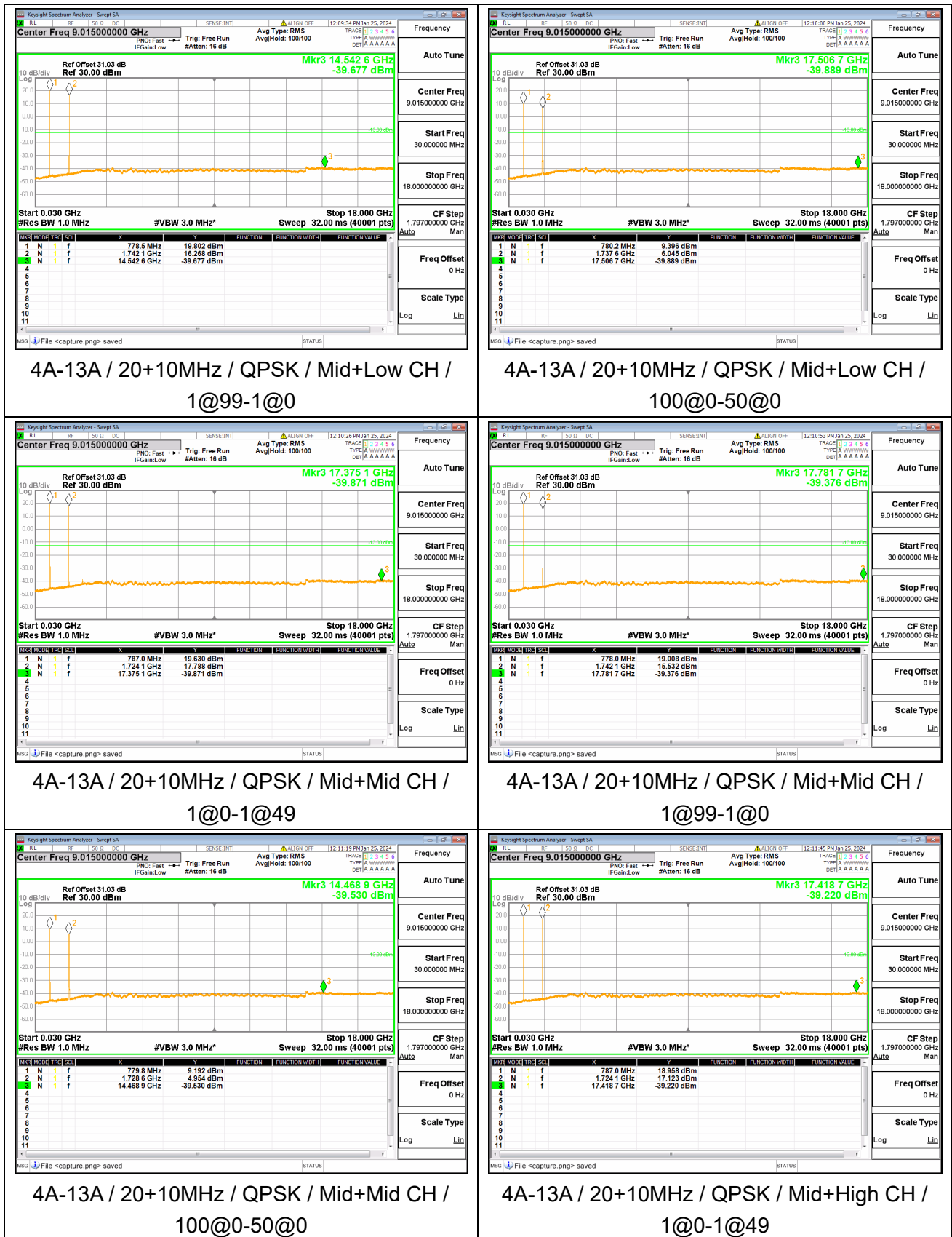
4A-5A / 20+10MHz / QPSK / High+High CH /
1@0-1@49

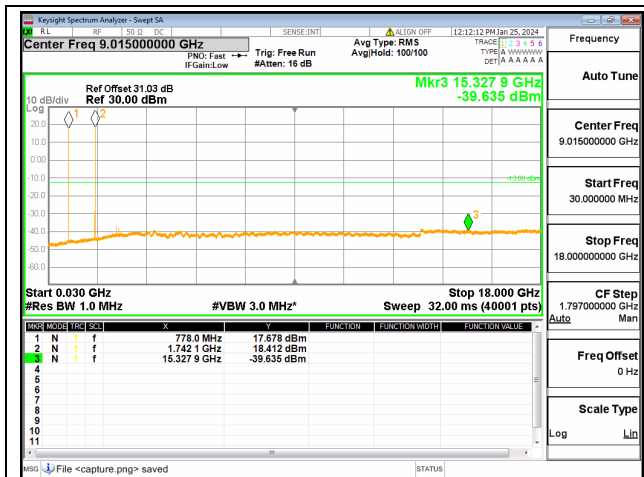
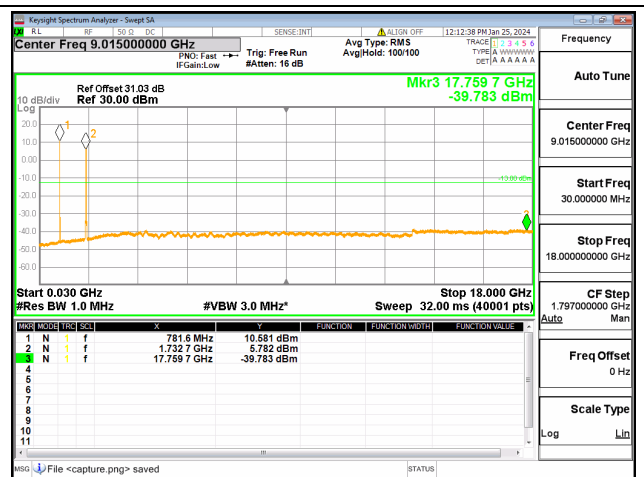
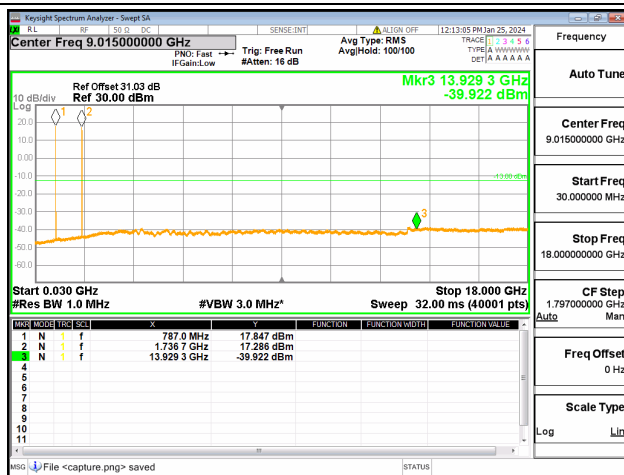
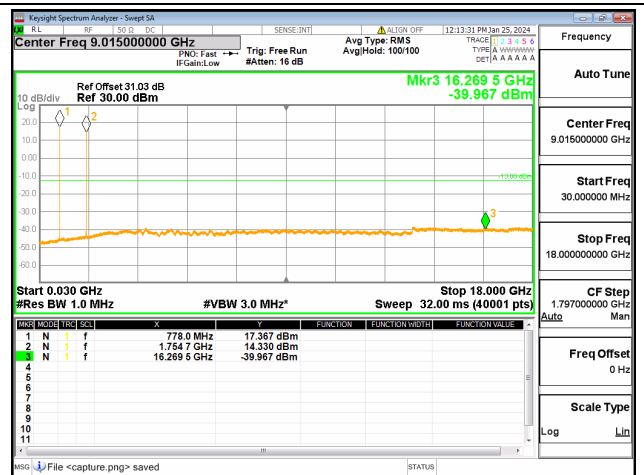
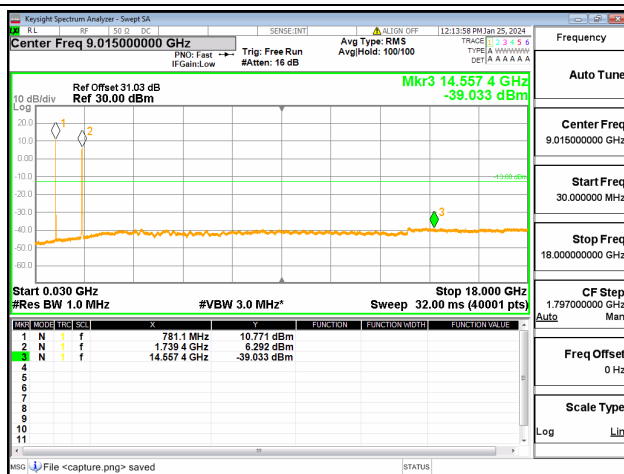
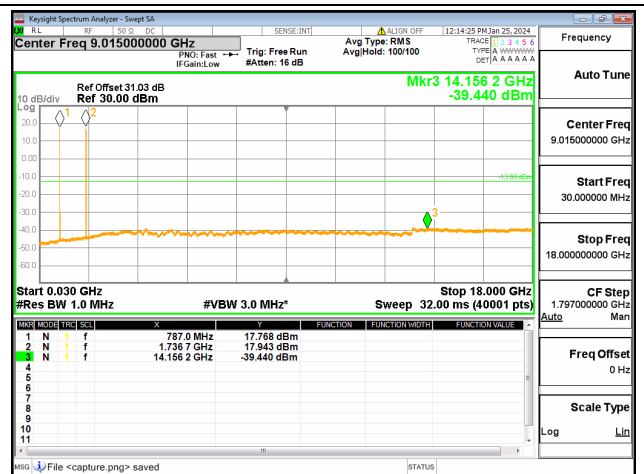


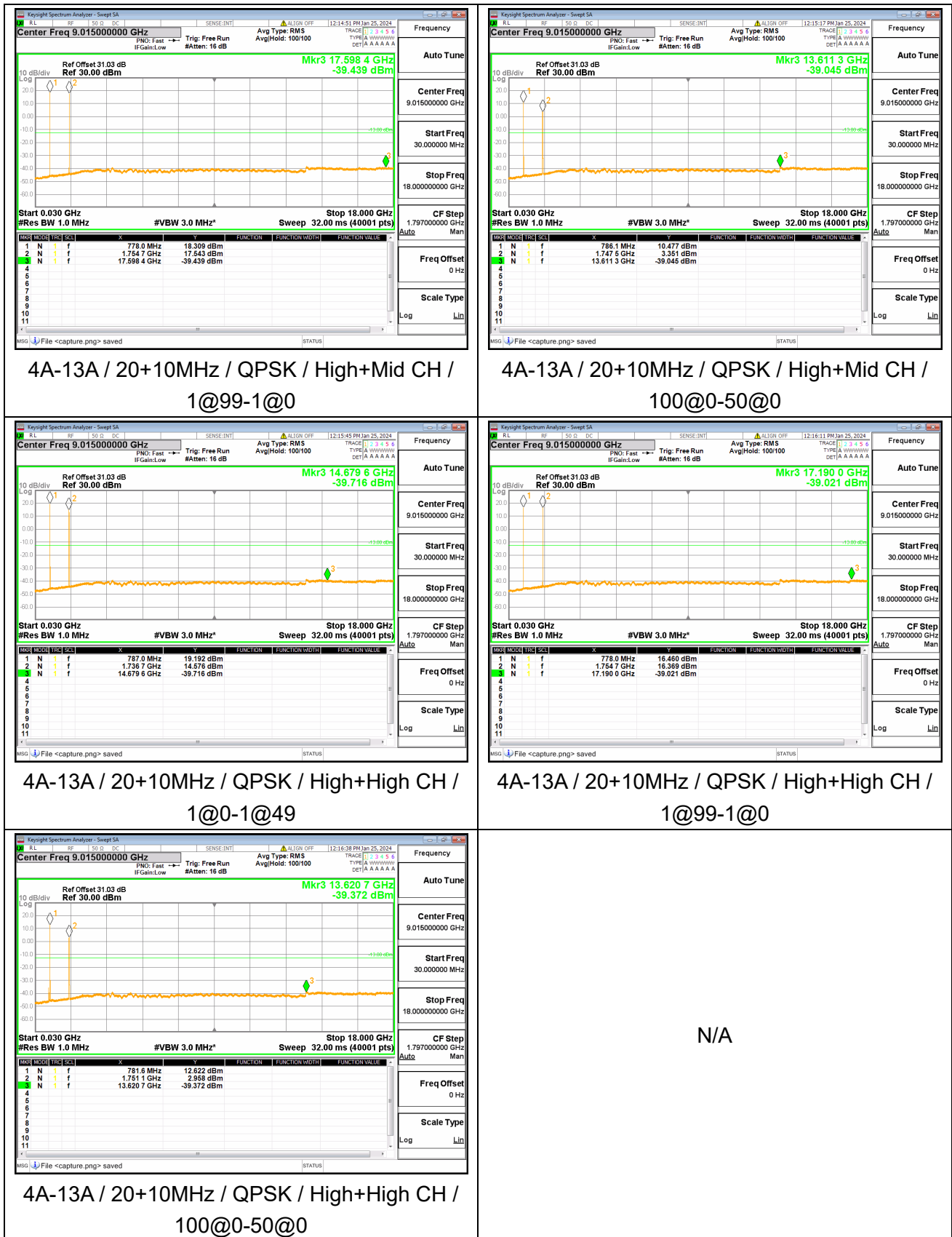
4A-5A / 20+10MHz / QPSK / High+High CH /
1@99-1@0

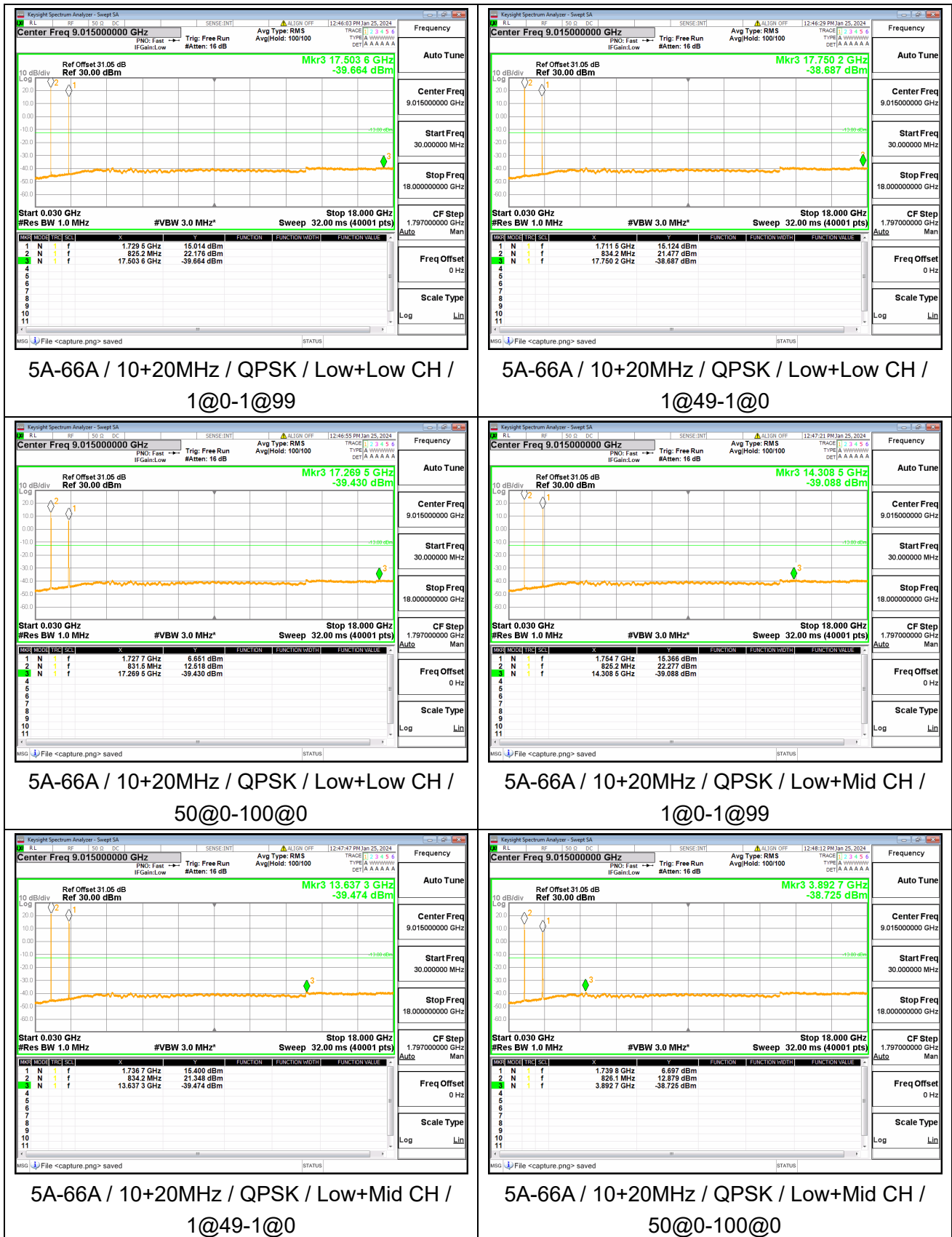
4A-5A / 20+10MHz / QPSK / High+High CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@49

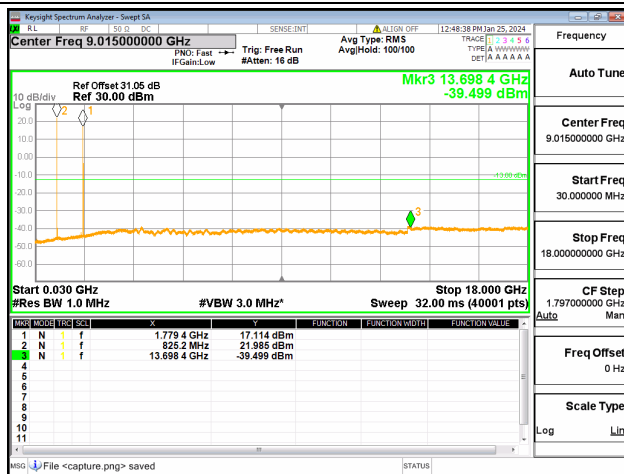




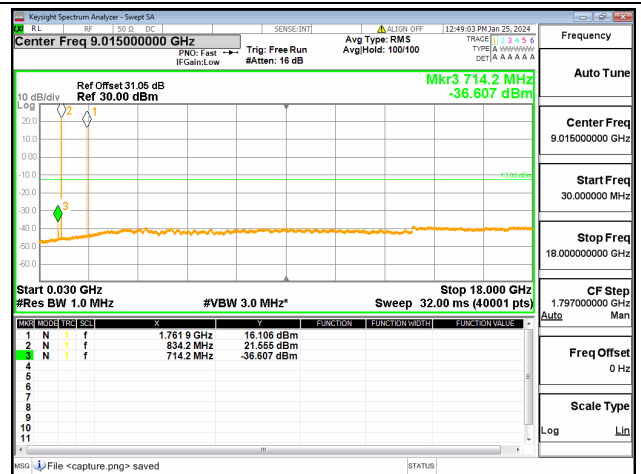
4A-13A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@04A-13A / 20+10MHz / QPSK / High+Low CH /
1@0-1@494A-13A / 20+10MHz / QPSK / High+Low CH /
1@99-1@04A-13A / 20+10MHz / QPSK / High+Low CH /
100@0-50@04A-13A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@49



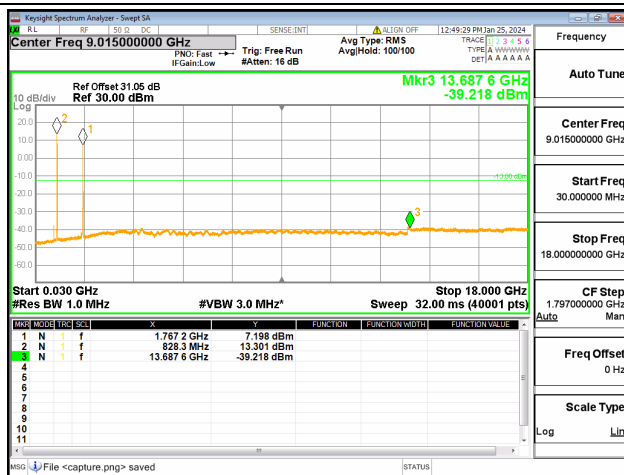




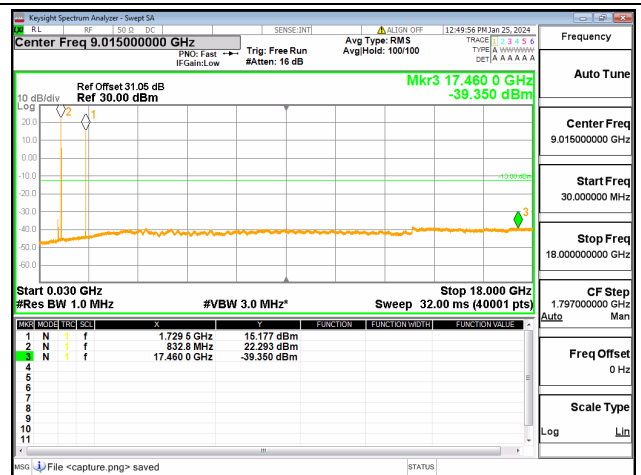
5A-66A / 10+20MHz / QPSK / Low+High CH /
1@0-1@99



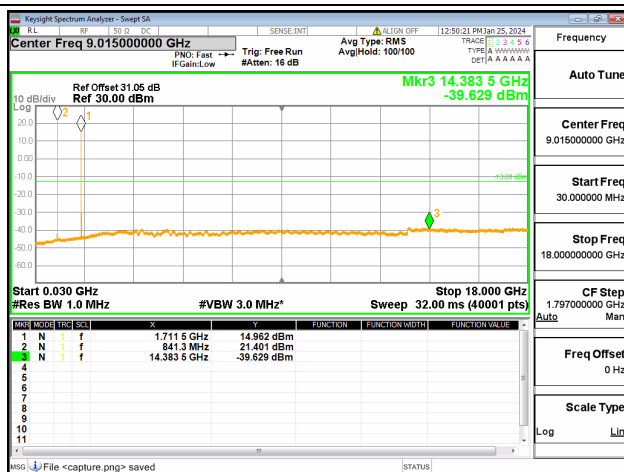
5A-66A / 10+20MHz / QPSK / Low+High CH /
1@49-1@0



5A-66A / 10+20MHz / QPSK / Low+High CH /
50@0-100@0



5A-66A / 10+20MHz / QPSK / Mid+Low CH /
1@0-1@99



5A-66A / 10+20MHz / QPSK / Mid+Low CH /
1@49-1@0



5A-66A / 10+20MHz / QPSK / Mid+Low CH /
50@0-100@0

